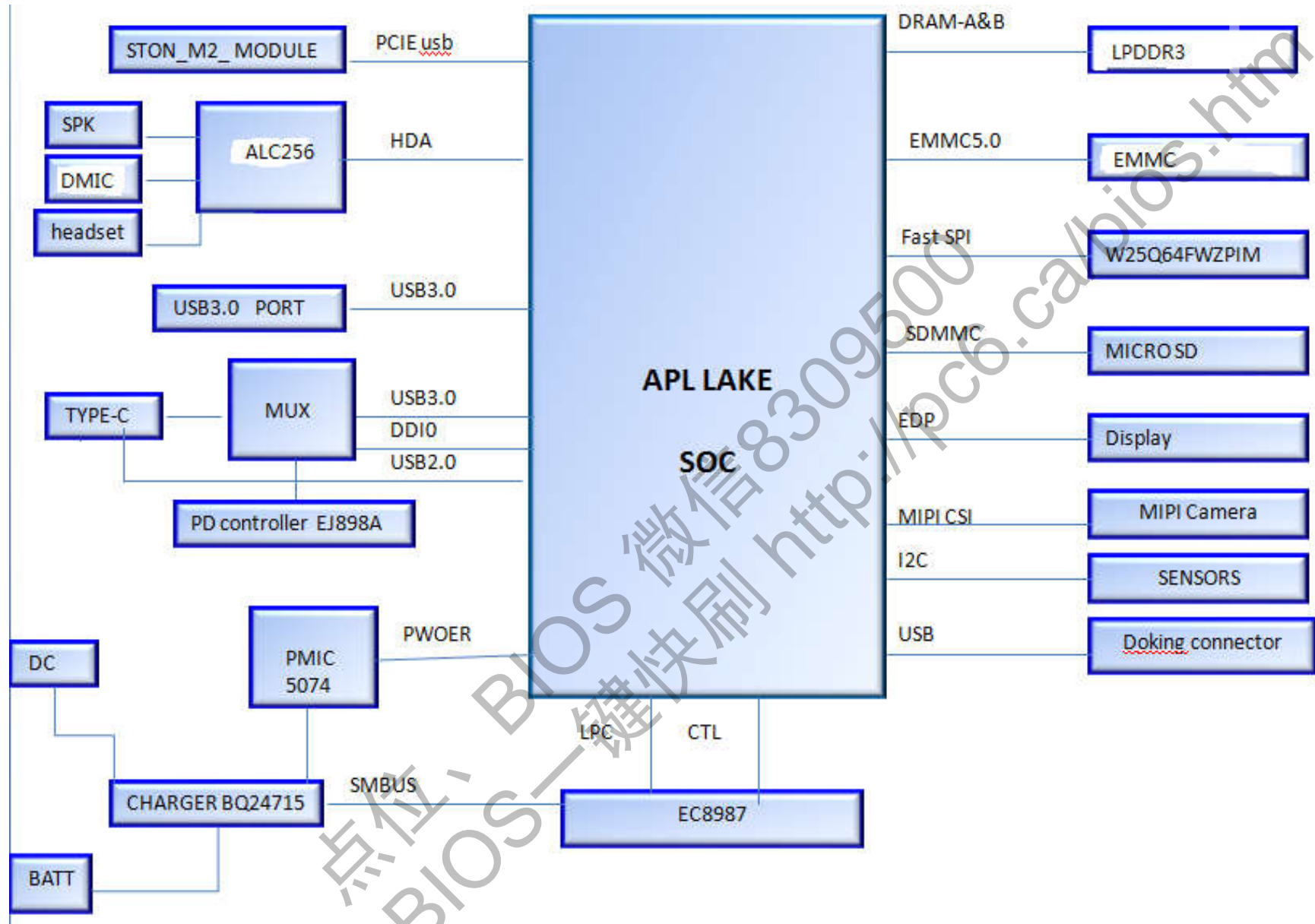



# APL HUAQIN

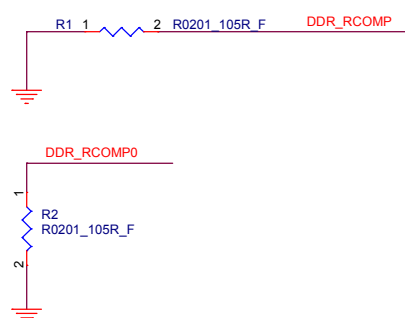
图纸、点位、BIOS 微信 83095006  
百优思.BIOS—键快刷 <http://pc6.caiyousi.com>

 HUAQIN 华勤通信		Huaqin Telecom Technology Com.,Ltd.	
Page name:		Title	
Size: A4	Project Name:	XM AIR 12.5 INDIA	REV: V1.0
Date:	Friday, April 14, 2017	Sheet: 1	of 44



 <b>HUAQIN</b> 华勤通讯		Huaqin Telecom Technology Com.,Ltd.	
Page name:		Title	
Size: A4	Project Name:	<b>XM AIR 12.5 INDIA</b>	REV: V1.0
Date:	Friday, April 14, 2017	Sheet:	2 of 44

14,17	LPDDR3_CH0_CAB7	LPDDR3_CH0_CAB7	BG50	DDR3L_CH0_MA0 / LPDDR3_CH0_CAB7	DDR3L_CH0_D00 / LPDDR3_CH0_D0A0	LPDDR3_CH0_D0A0	LPDDR3_CH0_D0A0	13
14,17	LPDDR3_CH0_CAB9	LPDDR3_CH0_CAB9	BG51	DDR3L_CH0_MA1 / LPDDR3_CH0_CAB9	DDR3L_CH0_D01 / LPDDR3_CH0_D0A1	LPDDR3_CH0_D0A1	LPDDR3_CH0_D0A1	13
14,17	LPDDR3_CH0_CAB5	LPDDR3_CH0_CAB5	BH51	DDR3L_CH0_MA2 / LPDDR3_CH0_CAB5	DDR3L_CH0_D02 / LPDDR3_CH0_D0A2	LPDDR3_CH0_D0A2	LPDDR3_CH0_D0A2	13
			BD41	DDR3L_CH0_MA3 / LPDDR3_CH0_NC	DDR3L_CH0_D03 / LPDDR3_CH0_D0A3	LPDDR3_CH0_D0A3	LPDDR3_CH0_D0A3	13
			BE41	DDR3L_CH0_MA4 / LPDDR3_CH0_NC	DDR3L_CH0_D04 / LPDDR3_CH0_D0A4	LPDDR3_CH0_D0A4	LPDDR3_CH0_D0A4	13
13,17	LPDDR3_CH0_CAA2	LPDDR3_CH0_CAA2	BJ52	DDR3L_CH0_MA5 / LPDDR3_CH0_CAA2	DDR3L_CH0_D05 / LPDDR3_CH0_D0A5	LPDDR3_CH0_D0A5	LPDDR3_CH0_D0A5	13
13,17	LPDDR3_CH0_CAA0	LPDDR3_CH0_CAA0	BG53	DDR3L_CH0_MA6 / LPDDR3_CH0_CAA0	DDR3L_CH0_D06 / LPDDR3_CH0_D0A6	LPDDR3_CH0_D0A6	LPDDR3_CH0_D0A6	13
13,17	LPDDR3_CH0_CAA3	LPDDR3_CH0_CAA3	BG55	DDR3L_CH0_MA7 / LPDDR3_CH0_CAA3	DDR3L_CH0_D07 / LPDDR3_CH0_D0A7	LPDDR3_CH0_D0A7	LPDDR3_CH0_D0A7	13
13,17	LPDDR3_CH0_CAA1	LPDDR3_CH0_CAA1	BH53	DDR3L_CH0_MA8 / LPDDR3_CH0_CAA1	DDR3L_CH0_D08 / LPDDR3_CH0_D0A8	LPDDR3_CH0_D0A8	LPDDR3_CH0_D0A8	13
13,17	LPDDR3_CH0_CAA4	LPDDR3_CH0_CAA4	BG52	DDR3L_CH0_MA9 / LPDDR3_CH0_CAA4	DDR3L_CH0_D09 / LPDDR3_CH0_D0A9	LPDDR3_CH0_D0A9	LPDDR3_CH0_D0A9	13
14,17	LPDDR3_CH0_CAB6	LPDDR3_CH0_CAB6	BH49	DDR3L_CH0_MA10 / LPDDR3_CH0_CAB6	DDR3L_CH0_DQ10 / LPDDR3_CH0_D0A10	LPDDR3_CH0_D0A10	LPDDR3_CH0_D0A10	13
13,17	LPDDR3_CH0_CAA6	LPDDR3_CH0_CAA6	BH55	DDR3L_CH0_MA11 / LPDDR3_CH0_CAA6	DDR3L_CH0_DQ11 / LPDDR3_CH0_D0A11	LPDDR3_CH0_D0A11	LPDDR3_CH0_D0A11	13
13,17	LPDDR3_CH0_CAA5	LPDDR3_CH0_CAA5	BG54	DDR3L_CH0_MA12 / LPDDR3_CH0_CAA5	DDR3L_CH0_DQ12 / LPDDR3_CH0_D0A12	LPDDR3_CH0_D0A12	LPDDR3_CH0_D0A12	13
14,17	LPDDR3_CH0_CAB0	LPDDR3_CH0_CAB0	BG46	DDR3L_CH0_MA13 / LPDDR3_CH0_CAB0	DDR3L_CH0_DQ13 / LPDDR3_CH0_D0A13	LPDDR3_CH0_D0A13	LPDDR3_CH0_D0A13	13
14,17	LPDDR3_CH0_CAB8	LPDDR3_CH0_CAB8	BG56	DDR3L_CH0_MA14 / LPDDR3_CH0_CAB8	DDR3L_CH0_DQ14 / LPDDR3_CH0_D0A14	LPDDR3_CH0_D0A14	LPDDR3_CH0_D0A14	13
13,17	LPDDR3_CH0_CAA8	LPDDR3_CH0_CAA8	BG57	DDR3L_CH0_MA15 / LPDDR3_CH0_CAA8	DDR3L_CH0_DQ15 / LPDDR3_CH0_D0A15	LPDDR3_CH0_D0A15	LPDDR3_CH0_D0A15	13
13,17	LPDDR3_CH0_CAA9	LPDDR3_CH0_CAA9	BG57	DDR3L_CH0_MA15 / LPDDR3_CH0_CAA9	DDR3L_CH0_DQ16 / LPDDR3_CH0_D0A16	LPDDR3_CH0_D0A16	LPDDR3_CH0_D0A16	13
14,17	LPDDR3_CH0_CAB2	LPDDR3_CH0_CAB2	BJ48	DDR3L_CH0_BA0 / LPDDR3_CH0_CAB2	DDR3L_CH0_DQ17 / LPDDR3_CH0_D0A17	LPDDR3_CH0_D0A17	LPDDR3_CH0_D0A17	13
14,17	LPDDR3_CH0_CAB8	LPDDR3_CH0_CAB8	BG49	DDR3L_CH0_BA1 / LPDDR3_CH0_CAB8	DDR3L_CH0_DQ18 / LPDDR3_CH0_D0A18	LPDDR3_CH0_D0A18	LPDDR3_CH0_D0A18	13
13,17	LPDDR3_CH0_CAB8	LPDDR3_CH0_CAB8	BH57	DDR3L_CH0_BA2 / LPDDR3_CH0_CAB8	DDR3L_CH0_DQ19 / LPDDR3_CH0_D0A19	LPDDR3_CH0_D0A19	LPDDR3_CH0_D0A19	13
14,17	LPDDR3_CH0_CAB1	LPDDR3_CH0_CAB1	BH47	DDR3L_CH0_CASN / LPDDR3_CH0_CAB1	DDR3L_CH0_DQ20 / LPDDR3_CH0_D0A20	LPDDR3_CH0_D0A20	LPDDR3_CH0_D0A20	13
14,17	LPDDR3_CH0_CAB4	LPDDR3_CH0_CAB4	BG48	DDR3L_CH0_WEN / LPDDR3_CH0_CAB4	DDR3L_CH0_DQ21 / LPDDR3_CH0_D0A21	LPDDR3_CH0_D0A21	LPDDR3_CH0_D0A21	13
14,17	LPDDR3_CH0_CAB3	LPDDR3_CH0_CAB3	BG47	DDR3L_CH0_RASN / LPDDR3_CH0_CAB3	DDR3L_CH0_DQ22 / LPDDR3_CH0_D0A22	LPDDR3_CH0_D0A22	LPDDR3_CH0_D0A22	13
13	LPDDR3_CH0_CSA0_N	LPDDR3_CH0_CSA0_N	AR43	DDR3L_CH0_CSN / LPDDR3_CH0_CSB0A	DDR3L_CH0_DQ23 / LPDDR3_CH0_D0A23	LPDDR3_CH0_D0A23	LPDDR3_CH0_D0A23	13
14	LPDDR3_CH0_CSB0_N	LPDDR3_CH0_CSB0_N	BA41	DDR3L_CH0_NC / LPDDR3_CH0_CSB0B	DDR3L_CH0_DQ24 / LPDDR3_CH0_D0A24	LPDDR3_CH0_D0A24	LPDDR3_CH0_D0A24	13
14	LPDDR3_CH0_CSB1_N	LPDDR3_CH0_CSB1_N	BA41	DDR3L_CH0_CSN1 / LPDDR3_CH0_CSB1B	DDR3L_CH0_DQ25 / LPDDR3_CH0_D0A25	LPDDR3_CH0_D0A25	LPDDR3_CH0_D0A25	13
13	LPDDR3_CH0_CSA1_N	LPDDR3_CH0_CSA1_N	AT43	DDR3L_CH0_NC1 / LPDDR3_CH0_CSB1A	DDR3L_CH0_DQ26 / LPDDR3_CH0_D0A26	LPDDR3_CH0_D0A26	LPDDR3_CH0_D0A26	13
13	LPDDR3_CH0_CKEA0_N	LPDDR3_CH0_CKEA0_N	BH61	DDR3L_CH0_CKE0 / LPDDR3_CH0_CKE0A	DDR3L_CH0_DQ27 / LPDDR3_CH0_D0A27	LPDDR3_CH0_D0A27	LPDDR3_CH0_D0A27	13
13	LPDDR3_CH0_CKEA1_N	LPDDR3_CH0_CKEA1_N	BH60	DDR3L_CH0_CKE1 / LPDDR3_CH0_CKE1A	DDR3L_CH0_DQ28 / LPDDR3_CH0_D0A28	LPDDR3_CH0_D0A28	LPDDR3_CH0_D0A28	13
13	LPDDR3_CH0_CKEB0_N	LPDDR3_CH0_CKEB0_N	BH58	DDR3L_CH0_NC / LPDDR3_CH0_CKE0B	DDR3L_CH0_DQ29 / LPDDR3_CH0_D0A29	LPDDR3_CH0_D0A29	LPDDR3_CH0_D0A29	13
14	LPDDR3_CH0_CKEB1_N	LPDDR3_CH0_CKEB1_N	BJ58	DDR3L_CH0_NC / LPDDR3_CH0_CKE1B	DDR3L_CH0_DQ30 / LPDDR3_CH0_D0A30	LPDDR3_CH0_D0A30	LPDDR3_CH0_D0A30	13
14	LPDDR3_CH0_ODT_B	LPDDR3_CH0_ODT_B	AW41	DDR3L_CH0_ODT1 / LPDDR3_CH0_ODTB	DDR3L_CH0_DQ31 / LPDDR3_CH0_D0A31	LPDDR3_CH0_D0A31	LPDDR3_CH0_D0A31	13
13	LPDDR3_CH0_ODT_A	LPDDR3_CH0_ODT_A	AW43	DDR3L_CH0_ODT0 / LPDDR3_CH0_ODTA	DDR3L_CH0_DQ32 / LPDDR3_CH0_D0A32	LPDDR3_CH0_D0A32	LPDDR3_CH0_D0A32	14
13	+VREFDQ0_SOC	+VREFDQ0_SOC	AT34	DDR3L_CH0_VREFDQ / LPDDR3_CH0_VREFDQ	DDR3L_CH0_DQ33 / LPDDR3_CH0_D0B1	LPDDR3_CH0_D0B1	LPDDR3_CH0_D0B1	14
13	+VREFCA0_SOC	+VREFCA0_SOC	AR35	DDR3L_CH0_VREFCA / LPDDR3_CH0_VREFCA	DDR3L_CH0_DQ34 / LPDDR3_CH0_D0B2	LPDDR3_CH0_D0B2	LPDDR3_CH0_D0B2	14
	DDR_RCOMP0	DDR_RCOMP0	AV34	DDR3L_CH0_RPDEXT / LPDDR3_CH0_RPDEXT	DDR3L_CH0_DQ35 / LPDDR3_CH0_D0B3	LPDDR3_CH0_D0B3	LPDDR3_CH0_D0B3	14
14,17	LPDDR3_CH0_CLKB_P	LPDDR3_CH0_CLKB_P	BD45	DDR3L_CH0_CKP0 / LPDDR3_CH0_CKP0B	DDR3L_CH0_DQ36 / LPDDR3_CH0_D0B4	LPDDR3_CH0_D0B4	LPDDR3_CH0_D0B4	14
14,17	LPDDR3_CH0_CLKB_N	LPDDR3_CH0_CLKB_N	BE45	DDR3L_CH0_CKN0 / LPDDR3_CH0_CKN0B	DDR3L_CH0_DQ37 / LPDDR3_CH0_D0B5	LPDDR3_CH0_D0B5	LPDDR3_CH0_D0B5	14
13,17	LPDDR3_CH0_CLKA_P	LPDDR3_CH0_CLKA_P	BB48	DDR3L_CH0_CKP1 / LPDDR3_CH0_CKP1A	DDR3L_CH0_DQ38 / LPDDR3_CH0_D0B6	LPDDR3_CH0_D0B6	LPDDR3_CH0_D0B6	14
13,17	LPDDR3_CH0_CLKA_N	LPDDR3_CH0_CLKA_N	BD48	DDR3L_CH0_CKN1 / LPDDR3_CH0_CKN1A	DDR3L_CH0_DQ39 / LPDDR3_CH0_D0B7	LPDDR3_CH0_D0B7	LPDDR3_CH0_D0B7	14
			AR30	DDR3L_CH1_RESETB	DDR3L_CH0_DQ40 / LPDDR3_CH0_D0B8	LPDDR3_CH0_D0B8	LPDDR3_CH0_D0B8	14
			AR34	DDR3L_CH0_RESETB	DDR3L_CH0_DQ41 / LPDDR3_CH0_D0B9	LPDDR3_CH0_D0B9	LPDDR3_CH0_D0B9	14
			AV30	DDR1RPDEXT_NC	DDR3L_CH0_DQ42 / LPDDR3_CH0_D0B10	LPDDR3_CH0_D0B10	LPDDR3_CH0_D0B10	14
					DDR3L_CH0_DQ43 / LPDDR3_CH0_D0B11	LPDDR3_CH0_D0B11	LPDDR3_CH0_D0B11	14
					DDR3L_CH0_DQ44 / LPDDR3_CH0_D0B12	LPDDR3_CH0_D0B12	LPDDR3_CH0_D0B12	14
					DDR3L_CH0_DQ45 / LPDDR3_CH0_D0B13	LPDDR3_CH0_D0B13	LPDDR3_CH0_D0B13	14
					DDR3L_CH0_DQ46 / LPDDR3_CH0_D0B14	LPDDR3_CH0_D0B14	LPDDR3_CH0_D0B14	14
					DDR3L_CH0_DQ47 / LPDDR3_CH0_D0B15	LPDDR3_CH0_D0B15	LPDDR3_CH0_D0B15	14
					DDR3L_CH0_DQ48 / LPDDR3_CH0_D0B16	LPDDR3_CH0_D0B16	LPDDR3_CH0_D0B16	14
					DDR3L_CH0_DQ49 / LPDDR3_CH0_D0B17	LPDDR3_CH0_D0B17	LPDDR3_CH0_D0B17	14
					DDR3L_CH0_DQ50 / LPDDR3_CH0_D0B18	LPDDR3_CH0_D0B18	LPDDR3_CH0_D0B18	14
					DDR3L_CH0_DQ51 / LPDDR3_CH0_D0B19	LPDDR3_CH0_D0B19	LPDDR3_CH0_D0B19	14
					DDR3L_CH0_DQ52 / LPDDR3_CH0_D0B20	LPDDR3_CH0_D0B20	LPDDR3_CH0_D0B20	14
					DDR3L_CH0_DQ53 / LPDDR3_CH0_D0B21	LPDDR3_CH0_D0B21	LPDDR3_CH0_D0B21	14
					DDR3L_CH0_DQ54 / LPDDR3_CH0_D0B22	LPDDR3_CH0_D0B22	LPDDR3_CH0_D0B22	14
					DDR3L_CH0_DQ55 / LPDDR3_CH0_D0B23	LPDDR3_CH0_D0B23	LPDDR3_CH0_D0B23	14
					DDR3L_CH0_DQ56 / LPDDR3_CH0_D0B24	LPDDR3_CH0_D0B24	LPDDR3_CH0_D0B24	14
					DDR3L_CH0_DQ57 / LPDDR3_CH0_D0B25	LPDDR3_CH0_D0B25	LPDDR3_CH0_D0B25	14
					DDR3L_CH0_DQ58 / LPDDR3_CH0_D0B26	LPDDR3_CH0_D0B26	LPDDR3_CH0_D0B26	14
					DDR3L_CH0_DQ59 / LPDDR3_CH0_D0B27	LPDDR3_CH0_D0B27	LPDDR3_CH0_D0B27	14
					DDR3L_CH0_DQ60 / LPDDR3_CH0_D0B28	LPDDR3_CH0_D0B28	LPDDR3_CH0_D0B28	14
					DDR3L_CH0_DQ61 / LPDDR3_CH0_D0B29	LPDDR3_CH0_D0B29	LPDDR3_CH0_D0B29	14
					DDR3L_CH0_DQ62 / LPDDR3_CH0_D0B30	LPDDR3_CH0_D0B30	LPDDR3_CH0_D0B30	14
					DDR3L_CH0_DQ63 / LPDDR3_CH0_D0B31	LPDDR3_CH0_D0B31	LPDDR3_CH0_D0B31	14
					DDR3L_CH0_DQSN0 / LPDDR3_CH0_DQSPA0	LPDDR3_CH0_DQSPA0	LPDDR3_CH0_DQSPA0	13
					DDR3L_CH0_DQSP0 / LPDDR3_CH0_DQSPNA0	LPDDR3_CH0_DQSPNA0	LPDDR3_CH0_DQSPNA0	13
					DDR3L_CH0_DQSN1 / LPDDR3_CH0_DQSPA1	LPDDR3_CH0_DQSPA1	LPDDR3_CH0_DQSPA1	13
					DDR3L_CH0_DQSP1 / LPDDR3_CH0_DQSPNA1	LPDDR3_CH0_DQSPNA1	LPDDR3_CH0_DQSPNA1	13
					DDR3L_CH0_DQSN2 / LPDDR3_CH0_DQSPA2	LPDDR3_CH0_DQSPA2	LPDDR3_CH0_DQSPA2	13
					DDR3L_CH0_DQSP2 / LPDDR3_CH0_DQSPNA2	LPDDR3_CH0_DQSPNA2	LPDDR3_CH0_DQSPNA2	13
					DDR3L_CH0_DQSN3 / LPDDR3_CH0_DQSPA3	LPDDR3_CH0_DQSPA3	LPDDR3_CH0_DQSPA3	13
					DDR3L_CH0_DQSP3 / LPDDR3_CH0_DQSPNA3	LPDDR3_CH0_DQSPNA3	LPDDR3_CH0_DQSPNA3	13
					DDR3L_CH0_DQSN4 / LPDDR3_CH0_DQSPA4	LPDDR3_CH0_DQSPA4	LPDDR3_CH0_DQSPA4	13
					DDR3L_CH0_DQSP4 / LPDDR3_CH0_DQSPNA4	LPDDR3_CH0_DQSPNA4	LPDDR3_CH0_DQSPNA4	13
					DDR3L_CH0_DQSN5 / LPDDR3_CH0_DQSPA5	LPDDR3_CH0_DQSPA5	LPDDR3_CH0_DQSPA5	13
					DDR3L_CH0_DQSP5 / LPDDR3_CH0_DQSPNA5	LPDDR3_CH0_DQSPNA5	LPDDR3_CH0_DQSPNA5	13
					DDR3L_CH0_DQSN6 / LPDDR3_CH0_DQSPA6	LPDDR3_CH0_DQSPA6	LPDDR3_CH0_DQSPA6	14
					DDR3L_CH0_DQSP6 / LPDDR3_CH0_DQSPNA6	LPDDR3_CH0_DQSPNA6	LPDDR3_CH0_DQSPNA6	14
					DDR3L_CH0_DQSN7 / LPDDR3_CH0_DQSPA7	LPDDR3_CH0_DQSPA7	LPDDR3_CH0_DQSPA7	14
					DDR3L_CH0_DQSP7 / LPDDR3_CH0_DQSPNA7	LPDDR3_CH0_DQSPNA7	LPDDR3_CH0_DQSPNA7	14



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Page name: Title


Size: A4	Project Name: XM AIR 12.5 INDIA	REV: V1.0
Date: Friday, April 14, 2017	Sheet: 3	of 44

SOC: LPDDR3L CH1	U1B	BXT_P_SOC_1295	DDR3L_CH1_DQ0 / LPDDR3_CH1_DQ0A0	BJ26	LPDDR3_CH1_DQ0A0	LPDDR3_CH1_DQ0A0	15
16,18 LPDDR3_CH1_CAB7	LPDDR3_CH1_CAB7	BG9	DDR3L_CH1_DQ1 / LPDDR3_CH1_DQ0A1	BG30	LPDDR3_CH1_DQ0A1	LPDDR3_CH1_DQ0A1	15
16,18 LPDDR3_CH1_CAB8	LPDDR3_CH1_CAB8	BG10	DDR3L_CH1_DQ2 / LPDDR3_CH1_DQ0A2	BH31	LPDDR3_CH1_DQ0A2	LPDDR3_CH1_DQ0A2	15
16,18 LPDDR3_CH1_CAB5	LPDDR3_CH1_CAB5	BH9	DDR3L_CH1_DQ3 / LPDDR3_CH1_DQ0A3	BG31	LPDDR3_CH1_DQ0A3	LPDDR3_CH1_DQ0A3	15
		BD16	DDR3L_CH1_DQ4 / LPDDR3_CH1_DQ0A4	BH27	LPDDR3_CH1_DQ0A4	LPDDR3_CH1_DQ0A4	15
		BB16	DDR3L_CH1_DQ5 / LPDDR3_CH1_DQ0A5	BG27	LPDDR3_CH1_DQ0A5	LPDDR3_CH1_DQ0A5	15
		BG11	DDR3L_CH1_DQ6 / LPDDR3_CH1_DQ0A6	BG26	LPDDR3_CH1_DQ0A6	LPDDR3_CH1_DQ0A6	15
15,18 LPDDR3_CH1_CAA2	LPDDR3_CH1_CAA2	BJ12	DDR3L_CH1_DQ7 / LPDDR3_CH1_DQ0A7	BJ30	LPDDR3_CH1_DQ0A7	LPDDR3_CH1_DQ0A7	15
15,18 LPDDR3_CH1_CAA0	LPDDR3_CH1_CAA0	BG14	DDR3L_CH1_DQ8 / LPDDR3_CH1_DQ0A8	BA30	LPDDR3_CH1_DQ0A8	LPDDR3_CH1_DQ0A8	15
15,18 LPDDR3_CH1_CAA3	LPDDR3_CH1_CAA3	BG12	DDR3L_CH1_DQ9 / LPDDR3_CH1_DQ0A9	BB30	LPDDR3_CH1_DQ0A9	LPDDR3_CH1_DQ0A9	15
15,18 LPDDR3_CH1_CAA1	LPDDR3_CH1_CAA1	BH11	DDR3L_CH1_DQ10 / LPDDR3_CH1_DQ0A10	BE30	LPDDR3_CH1_DQ0A10	LPDDR3_CH1_DQ0A10	15
15,18 LPDDR3_CH1_CAA4	LPDDR3_CH1_CAA4	BG7	DDR3L_CH1_DQ11 / LPDDR3_CH1_DQ0A11	BD30	LPDDR3_CH1_DQ0A11	LPDDR3_CH1_DQ0A11	15
15,18 LPDDR3_CH1_CAA6	LPDDR3_CH1_CAA6	BH13	DDR3L_CH1_DQ12 / LPDDR3_CH1_DQ0A12	BE25	LPDDR3_CH1_DQ0A12	LPDDR3_CH1_DQ0A12	15
15,18 LPDDR3_CH1_CAA5	LPDDR3_CH1_CAA5	BG13	DDR3L_CH1_DQ13 / LPDDR3_CH1_DQ0A13	BB27	LPDDR3_CH1_DQ0A13	LPDDR3_CH1_DQ0A13	15
16,18 LPDDR3_CH1_CAB0	LPDDR3_CH1_CAB0	BH3	DDR3L_CH1_DQ14 / LPDDR3_CH1_DQ0A14	BG25	LPDDR3_CH1_DQ0A14	LPDDR3_CH1_DQ0A14	15
15,18 LPDDR3_CH1_CAA8	LPDDR3_CH1_CAA8	BG15	DDR3L_CH1_DQ15 / LPDDR3_CH1_DQ0A15	BD27	LPDDR3_CH1_DQ0A15	LPDDR3_CH1_DQ0A15	15
15,18 LPDDR3_CH1_CAA9	LPDDR3_CH1_CAA9	BG16	DDR3L_CH1_DQ16 / LPDDR3_CH1_DQ0A16	BG24	LPDDR3_CH1_DQ0A16	LPDDR3_CH1_DQ0A16	15
		BH6	DDR3L_CH1_DQ17 / LPDDR3_CH1_DQ0A17	BJ20	LPDDR3_CH1_DQ0A17	LPDDR3_CH1_DQ0A17	15
16,18 LPDDR3_CH1_CAB2	LPDDR3_CH1_CAB2	BG8	DDR3L_CH1_DQ18 / LPDDR3_CH1_DQ0A18	BH23	LPDDR3_CH1_DQ0A18	LPDDR3_CH1_DQ0A18	15
16,18 LPDDR3_CH1_CAB8	LPDDR3_CH1_CAB8	BH15	DDR3L_CH1_DQ19 / LPDDR3_CH1_DQ0A19	BJ24	LPDDR3_CH1_DQ0A19	LPDDR3_CH1_DQ0A19	15
15,18 LPDDR3_CH1_CAA7	LPDDR3_CH1_CAA7	BH15	DDR3L_CH1_DQ20 / LPDDR3_CH1_DQ0A20	BG20	LPDDR3_CH1_DQ0A20	LPDDR3_CH1_DQ0A20	15
		BJ6	DDR3L_CH1_DQ21 / LPDDR3_CH1_DQ0A21	BG21	LPDDR3_CH1_DQ0A21	LPDDR3_CH1_DQ0A21	15
16,18 LPDDR3_CH1_CAB3	LPDDR3_CH1_CAB3	BJ6	DDR3L_CH1_DQ22 / LPDDR3_CH1_DQ0A22	BH19	LPDDR3_CH1_DQ0A22	LPDDR3_CH1_DQ0A22	15
16,18 LPDDR3_CH1_CAB1	LPDDR3_CH1_CAB1	BH4	DDR3L_CH1_DQ23 / LPDDR3_CH1_DQ0A23	BG25	LPDDR3_CH1_DQ0A23	LPDDR3_CH1_DQ0A23	15
16,18 LPDDR3_CH1_CAB4	LPDDR3_CH1_CAB4	BH7	DDR3L_CH1_DQ24 / LPDDR3_CH1_DQ0A24	AT27	LPDDR3_CH1_DQ0A24	LPDDR3_CH1_DQ0A24	15
		AW17	DDR3L_CH1_DQ25 / LPDDR3_CH1_DQ0A25	AW29	LPDDR3_CH1_DQ0A25	LPDDR3_CH1_DQ0A25	15
16 LPDDR3_CH1_CSB1_N	LPDDR3_CH1_CSB1_N	AW17	DDR3L_CH1_DQ26 / LPDDR3_CH1_DQ0A26	AR27	LPDDR3_CH1_DQ0A26	LPDDR3_CH1_DQ0A26	15
16 LPDDR3_CH1_CSB0_N	LPDDR3_CH1_CSB0_N	AV17	DDR3L_CH1_DQ27 / LPDDR3_CH1_DQ0A27	AT23	LPDDR3_CH1_DQ0A27	LPDDR3_CH1_DQ0A27	15
15 LPDDR3_CH1_CSA1_N	LPDDR3_CH1_CSA1_N	BD17	DDR3L_CH1_DQ28 / LPDDR3_CH1_DQ0A28	AV27	LPDDR3_CH1_DQ0A28	LPDDR3_CH1_DQ0A28	15
15 LPDDR3_CH1_CSA0_N	LPDDR3_CH1_CSA0_N	BD17	DDR3L_CH1_DQ29 / LPDDR3_CH1_DQ0A29	AR25	LPDDR3_CH1_DQ0A29	LPDDR3_CH1_DQ0A29	15
		BG18	DDR3L_CH1_DQ30 / LPDDR3_CH1_DQ0A30	AR23	LPDDR3_CH1_DQ0A30	LPDDR3_CH1_DQ0A30	15
15 LPDDR3_CH1_CKEA0_N	LPDDR3_CH1_CKEA0_N	BG17	DDR3L_CH1_DQ31 / LPDDR3_CH1_DQ0A31	AW27	LPDDR3_CH1_DQ0A31	LPDDR3_CH1_DQ0A31	15
15 LPDDR3_CH1_CKEA1_N	LPDDR3_CH1_CKEA1_N	BH17	DDR3L_CH1_DQ32 / LPDDR3_CH1_DQ0A32	BF6	LPDDR3_CH1_DQ0A32	LPDDR3_CH1_DQ0A32	15
15 LPDDR3_CH1_CKEB0_N	LPDDR3_CH1_CKEB0_N	BJ16	DDR3L_CH1_DQ33 / LPDDR3_CH1_DQ0A33	BD10	LPDDR3_CH1_DQ0A33	LPDDR3_CH1_DQ0A33	15
16 LPDDR3_CH1_CKEB1_N	LPDDR3_CH1_CKEB1_N	BJ16	DDR3L_CH1_DQ34 / LPDDR3_CH1_DQ0A34	BE14	LPDDR3_CH1_DQ0A34	LPDDR3_CH1_DQ0A34	15
		AW16	DDR3L_CH1_DQ35 / LPDDR3_CH1_DQ0A35	BB10	LPDDR3_CH1_DQ0A35	LPDDR3_CH1_DQ0A35	15
15 LPDDR3_CH1_ODT_A	LPDDR3_CH1_ODT_A	AW16	DDR3L_CH1_DQ36 / LPDDR3_CH1_DQ0A36	BA14	LPDDR3_CH1_DQ0A36	LPDDR3_CH1_DQ0A36	15
16 LPDDR3_CH1_ODT_B	LPDDR3_CH1_ODT_B	AV16	DDR3L_CH1_DQ37 / LPDDR3_CH1_DQ0A37	BB14	LPDDR3_CH1_DQ0A37	LPDDR3_CH1_DQ0A37	15
		AT30	DDR3L_CH1_DQ38 / LPDDR3_CH1_DQ0A38	BD14	LPDDR3_CH1_DQ0A38	LPDDR3_CH1_DQ0A38	15
15 +VREFDQ1_SOC	+VREFDQ1_SOC	AT30	DDR3L_CH1_DQ39 / LPDDR3_CH1_DQ0A39	BE3	LPDDR3_CH1_DQ0A39	LPDDR3_CH1_DQ0A39	15
15 +VREFCA1_SOC	+VREFCA1_SOC	AR29	DDR3L_CH1_DQ40 / LPDDR3_CH1_DQ0A40	AV12	LPDDR3_CH1_DQ0A40	LPDDR3_CH1_DQ0A40	15
		AR29	DDR3L_CH1_DQ41 / LPDDR3_CH1_DQ0A41	BD6	LPDDR3_CH1_DQ0A41	LPDDR3_CH1_DQ0A41	15
		BE19	DDR3L_CH1_DQ42 / LPDDR3_CH1_DQ0A42	BD5	LPDDR3_CH1_DQ0A42	LPDDR3_CH1_DQ0A42	15
16,18 LPDDR3_CH1_CLKB_N	LPDDR3_CH1_CLKB_N	BE19	DDR3L_CH1_DQ43 / LPDDR3_CH1_DQ0A43	BB7	LPDDR3_CH1_DQ0A43	LPDDR3_CH1_DQ0A43	15
16,18 LPDDR3_CH1_CLKB_P	LPDDR3_CH1_CLKB_P	BD19	DDR3L_CH1_DQ44 / LPDDR3_CH1_DQ0A44	AV10	LPDDR3_CH1_DQ0A44	LPDDR3_CH1_DQ0A44	15
		BD21	DDR3L_CH1_DQ45 / LPDDR3_CH1_DQ0A45	AY9	LPDDR3_CH1_DQ0A45	LPDDR3_CH1_DQ0A45	15
15,18 LPDDR3_CH1_CLKA_N	LPDDR3_CH1_CLKA_N	BD21	DDR3L_CH1_DQ46 / LPDDR3_CH1_DQ0A46	AY7	LPDDR3_CH1_DQ0A46	LPDDR3_CH1_DQ0A46	15
15,18 LPDDR3_CH1_CLKA_P	LPDDR3_CH1_CLKA_P	BB21	DDR3L_CH1_DQ47 / LPDDR3_CH1_DQ0A47	BF5	LPDDR3_CH1_DQ0A47	LPDDR3_CH1_DQ0A47	15
		BD21	DDR3L_CH1_DQ48 / LPDDR3_CH1_DQ0A48	AU2	LPDDR3_CH1_DQ0A48	LPDDR3_CH1_DQ0A48	15
		BB21	DDR3L_CH1_DQ49 / LPDDR3_CH1_DQ0A49	AT10	LPDDR3_CH1_DQ0A49	LPDDR3_CH1_DQ0A49	15
		AT9	DDR3L_CH1_DQ50 / LPDDR3_CH1_DQ0A50	AT9	LPDDR3_CH1_DQ0A50	LPDDR3_CH1_DQ0A50	15
		AU1	DDR3L_CH1_DQ51 / LPDDR3_CH1_DQ0A51	AY5	LPDDR3_CH1_DQ0A51	LPDDR3_CH1_DQ0A51	15
		AY5	DDR3L_CH1_DQ52 / LPDDR3_CH1_DQ0A52	AY5	LPDDR3_CH1_DQ0A52	LPDDR3_CH1_DQ0A52	15
		AV5	DDR3L_CH1_DQ53 / LPDDR3_CH1_DQ0A53	AV5	LPDDR3_CH1_DQ0A53	LPDDR3_CH1_DQ0A53	15
		AV6	DDR3L_CH1_DQ54 / LPDDR3_CH1_DQ0A54	AV6	LPDDR3_CH1_DQ0A54	LPDDR3_CH1_DQ0A54	15
		AV7	DDR3L_CH1_DQ55 / LPDDR3_CH1_DQ0A55	AV7	LPDDR3_CH1_DQ0A55	LPDDR3_CH1_DQ0A55	15
		AY2	DDR3L_CH1_DQ56 / LPDDR3_CH1_DQ0A56	AY2	LPDDR3_CH1_DQ0A56	LPDDR3_CH1_DQ0A56	15
		BD2	DDR3L_CH1_DQ57 / LPDDR3_CH1_DQ0A57	BD2	LPDDR3_CH1_DQ0A57	LPDDR3_CH1_DQ0A57	15
		BD1	DDR3L_CH1_DQ58 / LPDDR3_CH1_DQ0A58	BD1	LPDDR3_CH1_DQ0A58	LPDDR3_CH1_DQ0A58	15
		BE2	DDR3L_CH1_DQ59 / LPDDR3_CH1_DQ0A59	BE2	LPDDR3_CH1_DQ0A59	LPDDR3_CH1_DQ0A59	15
		AW1	DDR3L_CH1_DQ60 / LPDDR3_CH1_DQ0A60	AW1	LPDDR3_CH1_DQ0A60	LPDDR3_CH1_DQ0A60	15
		AW2	DDR3L_CH1_DQ61 / LPDDR3_CH1_DQ0A61	AW2	LPDDR3_CH1_DQ0A61	LPDDR3_CH1_DQ0A61	15
		AY3	DDR3L_CH1_DQ62 / LPDDR3_CH1_DQ0A62	AY3	LPDDR3_CH1_DQ0A62	LPDDR3_CH1_DQ0A62	15
		BG2	DDR3L_CH1_DQ63 / LPDDR3_CH1_DQ0A63	BG2	LPDDR3_CH1_DQ0A63	LPDDR3_CH1_DQ0A63	15
		BG28	DDR3L_CH1_DQSPA0 / LPDDR3_CH1_DQSPA0	BG28	LPDDR3_CH1_DQSPA0	LPDDR3_CH1_DQSPA0	15
		BH29	DDR3L_CH1_DQSN0 / LPDDR3_CH1_DQSN0	BH29	LPDDR3_CH1_DQSN0	LPDDR3_CH1_DQSN0	15
		BD29	DDR3L_CH1_DQSP1 / LPDDR3_CH1_DQSP1	BD29	LPDDR3_CH1_DQSP1	LPDDR3_CH1_DQSP1	15
		BB29	DDR3L_CH1_DQSN1 / LPDDR3_CH1_DQSN1	BB29	LPDDR3_CH1_DQSN1	LPDDR3_CH1_DQSN1	15
		BJ22	DDR3L_CH1_DQSP2 / LPDDR3_CH1_DQSP2	BJ22	LPDDR3_CH1_DQSP2	LPDDR3_CH1_DQSP2	15
		BG22	DDR3L_CH1_DQSN2 / LPDDR3_CH1_DQSN2	BG22	LPDDR3_CH1_DQSN2	LPDDR3_CH1_DQSN2	15
		AV25	DDR3L_CH1_DQSP3 / LPDDR3_CH1_DQSP3	AV25	LPDDR3_CH1_DQSP3	LPDDR3_CH1_DQSP3	15
		AW25	DDR3L_CH1_DQSN3 / LPDDR3_CH1_DQSN3	AW25	LPDDR3_CH1_DQSN3	LPDDR3_CH1_DQSN3	15
		BB12	DDR3L_CH1_DQSP4 / LPDDR3_CH1_DQSP4	BB12	LPDDR3_CH1_DQSP4	LPDDR3_CH1_DQSP4	15
		BD12	DDR3L_CH1_DQSN4 / LPDDR3_CH1_DQSN4	BD12	LPDDR3_CH1_DQSN4	LPDDR3_CH1_DQSN4	15
		BB5	DDR3L_CH1_DQSP5 / LPDDR3_CH1_DQSP5	BB5	LPDDR3_CH1_DQSP5	LPDDR3_CH1_DQSP5	15
		BG6	DDR3L_CH1_DQSN5 / LPDDR3_CH1_DQSN5	BG6	LPDDR3_CH1_DQSN5	LPDDR3_CH1_DQSN5	15
		AT5	DDR3L_CH1_DQSP6 / LPDDR3_CH1_DQSP6	AT5	LPDDR3_CH1_DQSP6	LPDDR3_CH1_DQSP6	15
		AT6	DDR3L_CH1_DQSN6 / LPDDR3_CH1_DQSN6	AT6	LPDDR3_CH1_DQSN6	LPDDR3_CH1_DQSN6	15
		BC2	DDR3L_CH1_DQSP7 / LPDDR3_CH1_DQSP7	BC2	LPDDR3_CH1_DQSP7	LPDDR3_CH1_DQSP7	15
		BB1	DDR3L_CH1_DQSN7 / LPDDR3_CH1_DQSN7	BB1	LPDDR3_CH1_DQSN7	LPDDR3_CH1_DQSN7	15

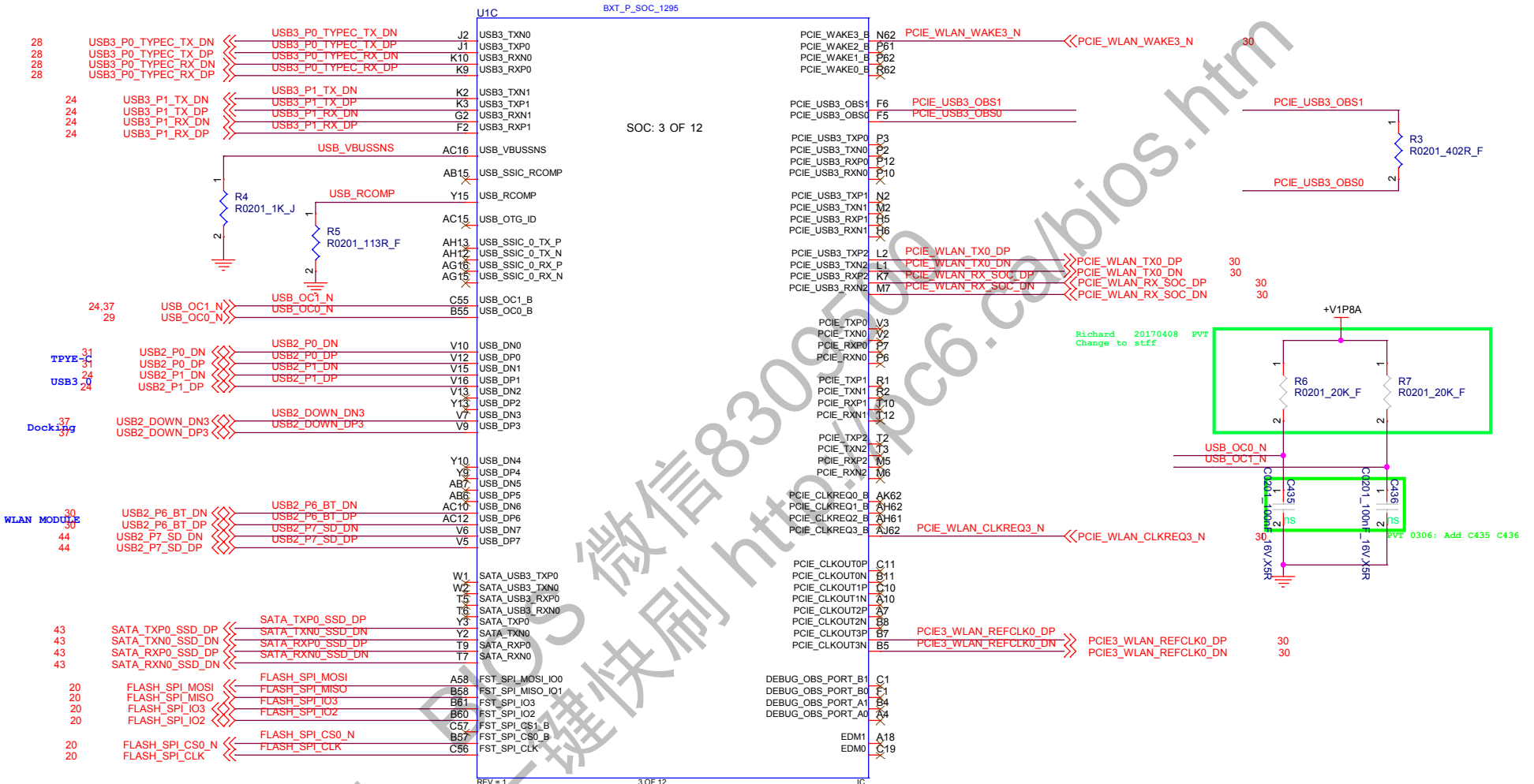
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2 OF 12

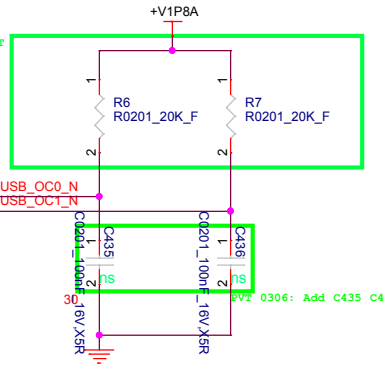
IC

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Page name:	Title
Size: A4	Project Name: <b>XM AIR 12.5 INDIA</b>
Date: Friday, April 14, 2017	REV: V1.0
Sheet: 4	of 44

+V1P8A 7,8,11,12,20,22,27,29,30,32,34,37,38,39,43



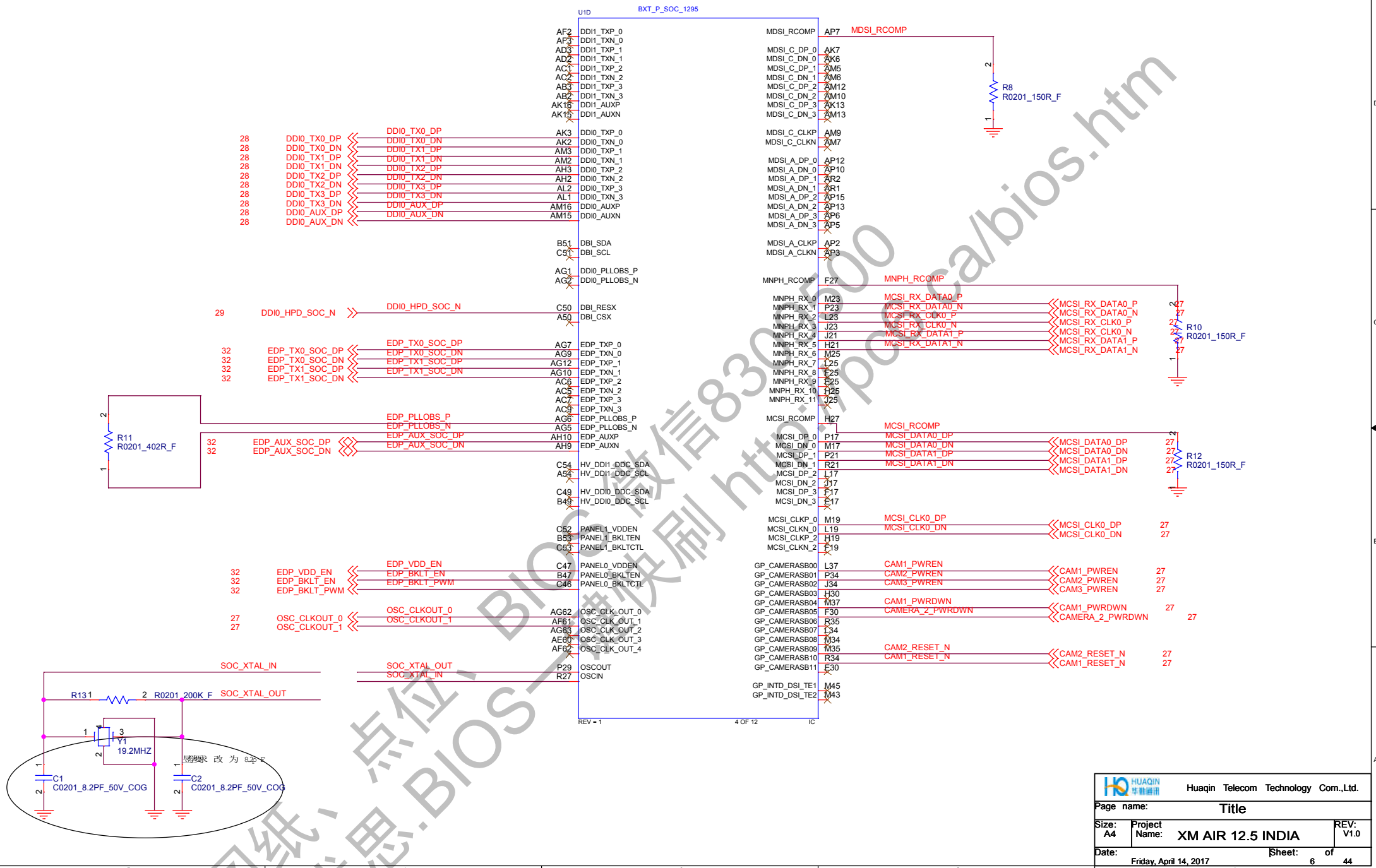
Richard 20170408 PVT Change to stiff



PVT 0306: Add C435 C436

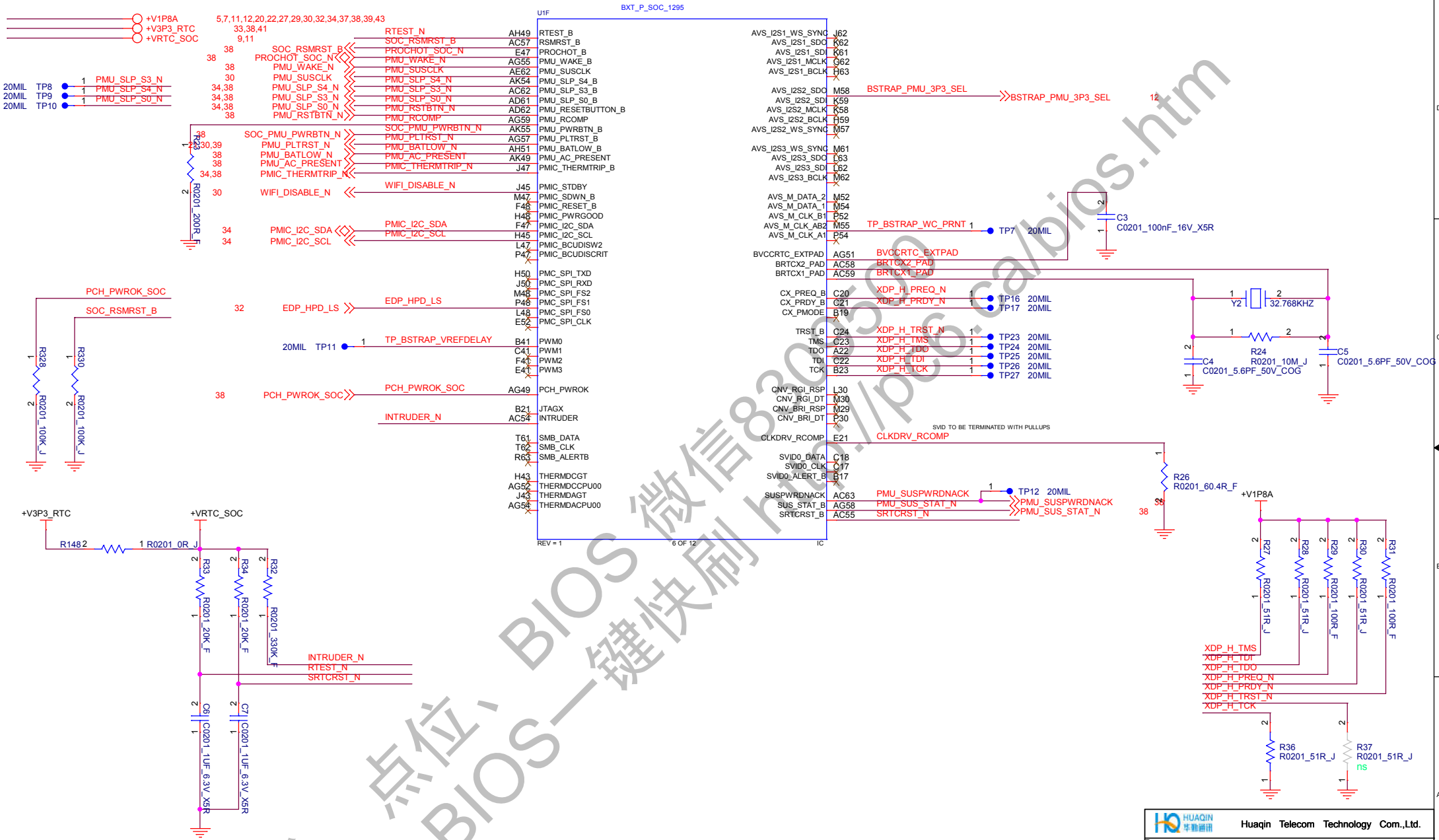
图纸、点位、百优思.BIOS一键快刷

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Date:	Friday, April 14, 2017	Sheet:	5 of 44



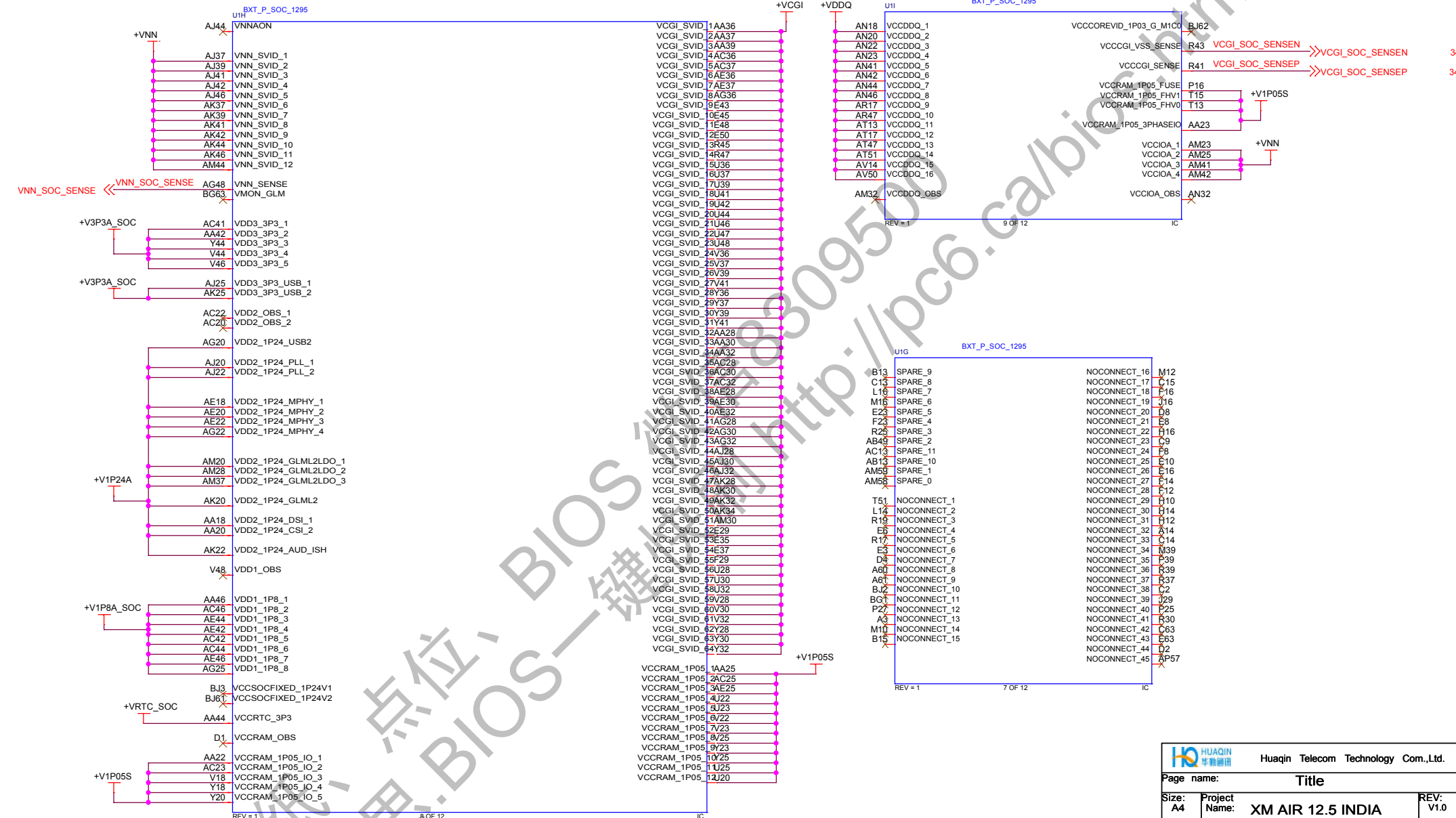
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Date:	Friday, April 14, 2017	Sheet:	6 of 44






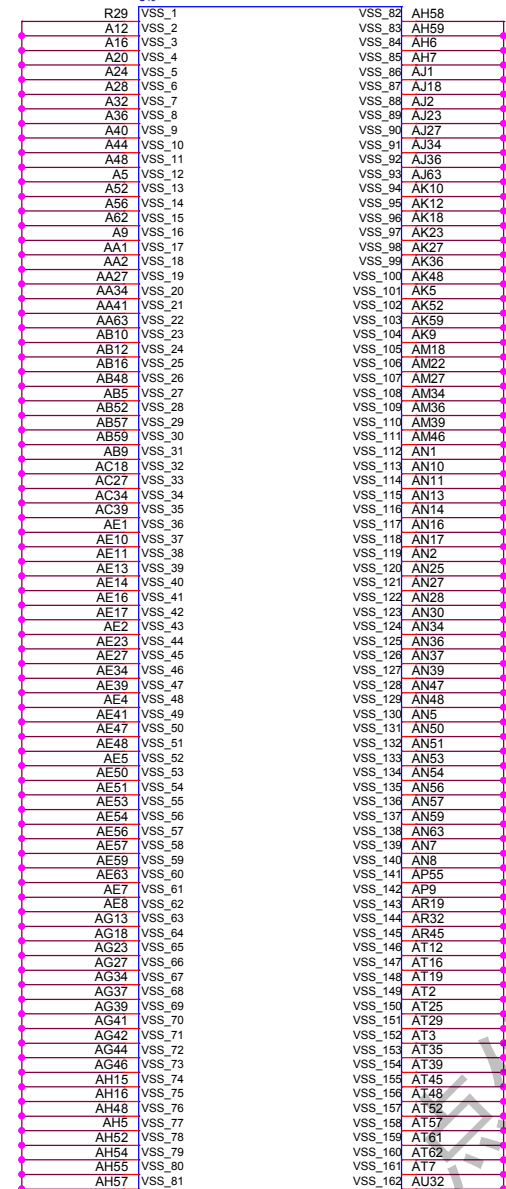
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Size: A4	Project Name:	XM AIR 12.5 INDIA	REV: V1.0
Date: Friday, April 14, 2017	Sheet: 8	of 44	

- +VNN 11,34,35
- +VCGI 11,35
- +VDDQ 11,13,14,15,16,19,27,34,36
- +V3P3A\_SOC 11
- +V1P24A 11,34
- +V1P8A\_SOC 11
- +VRTC\_SOC 8,11
- +V1P05S 11,34



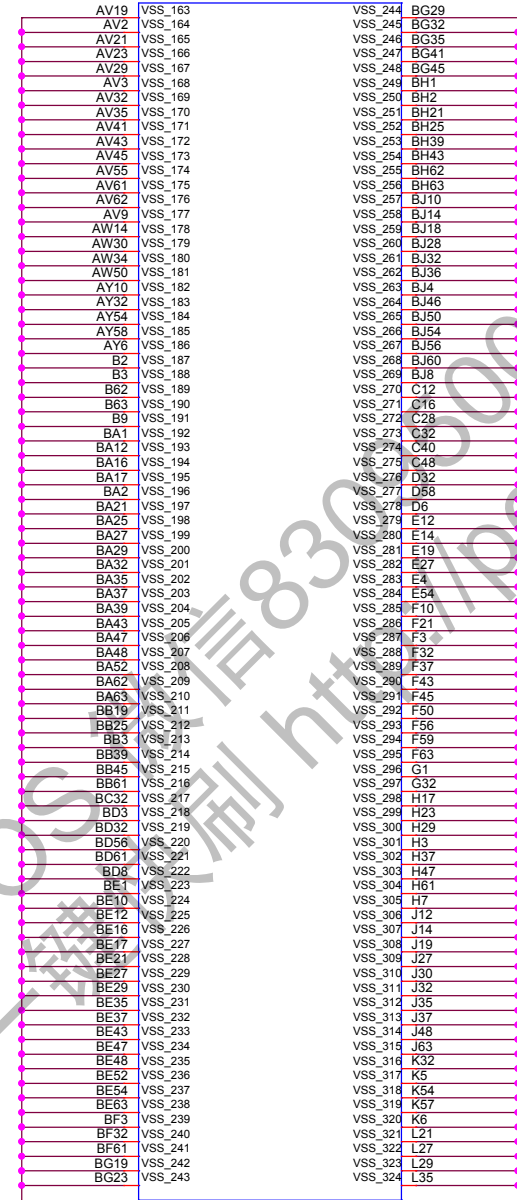
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Size: A4	Project Name: XM AIR 12.5 INDIA
Date: Friday, April 14, 2017	REV: V1.0
Sheet: 9	of 44

BXT\_P\_SOC\_1295



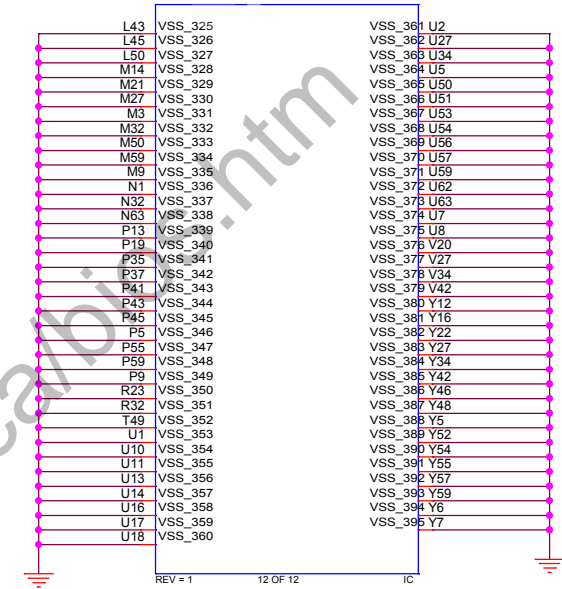
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BXT\_P\_SOC\_1295



REV = 1 11 OF 12 IC

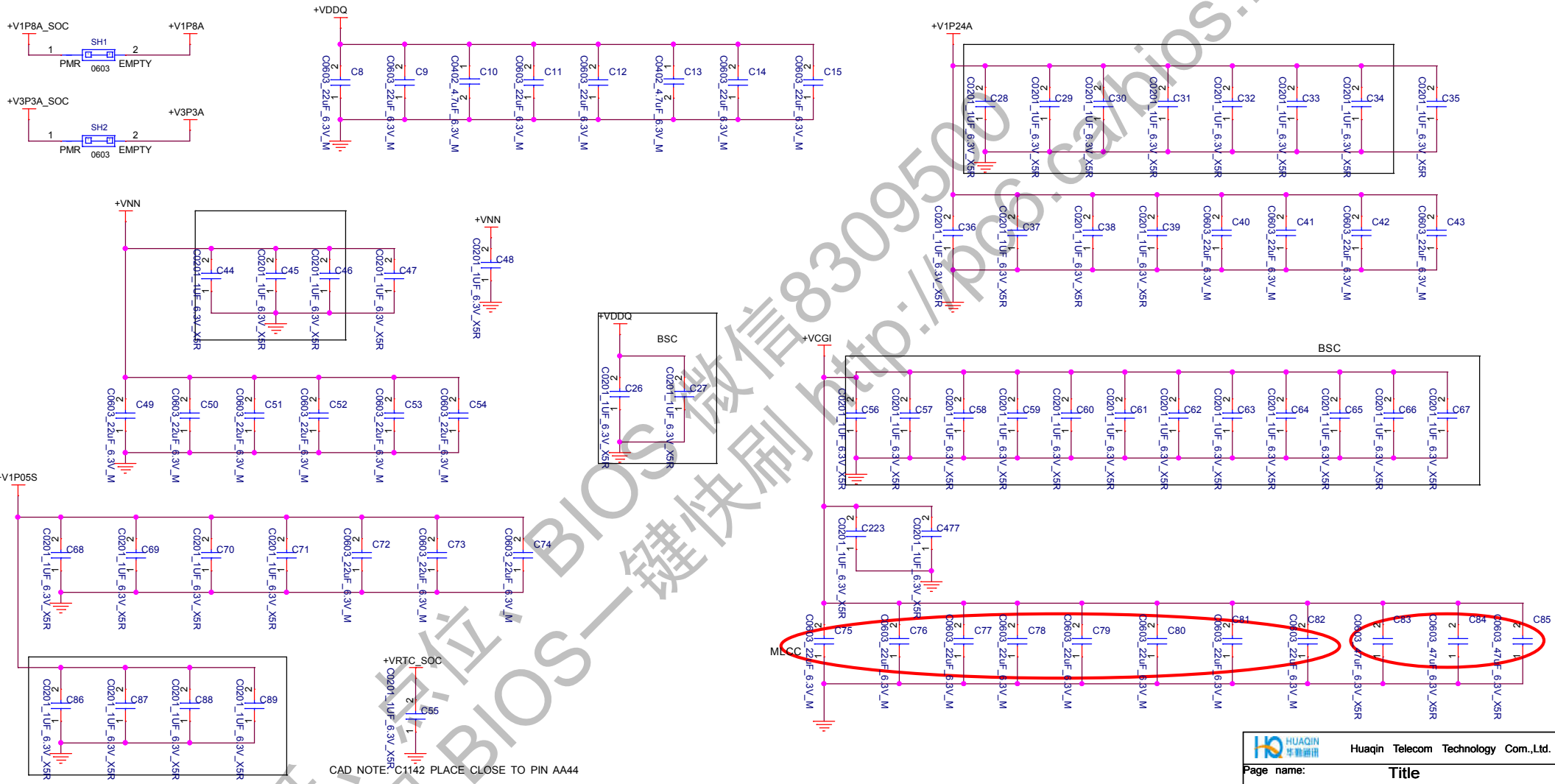
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REV = 1 12 OF 12 IC

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<b>Size:</b> A4	<b>Project Name:</b> XM AIR 12.5 INDIA	<b>Sheet:</b> 10 of 44	
<b>Date:</b> Friday, April 14, 2017		<b>Sheet:</b> 10 of 44	

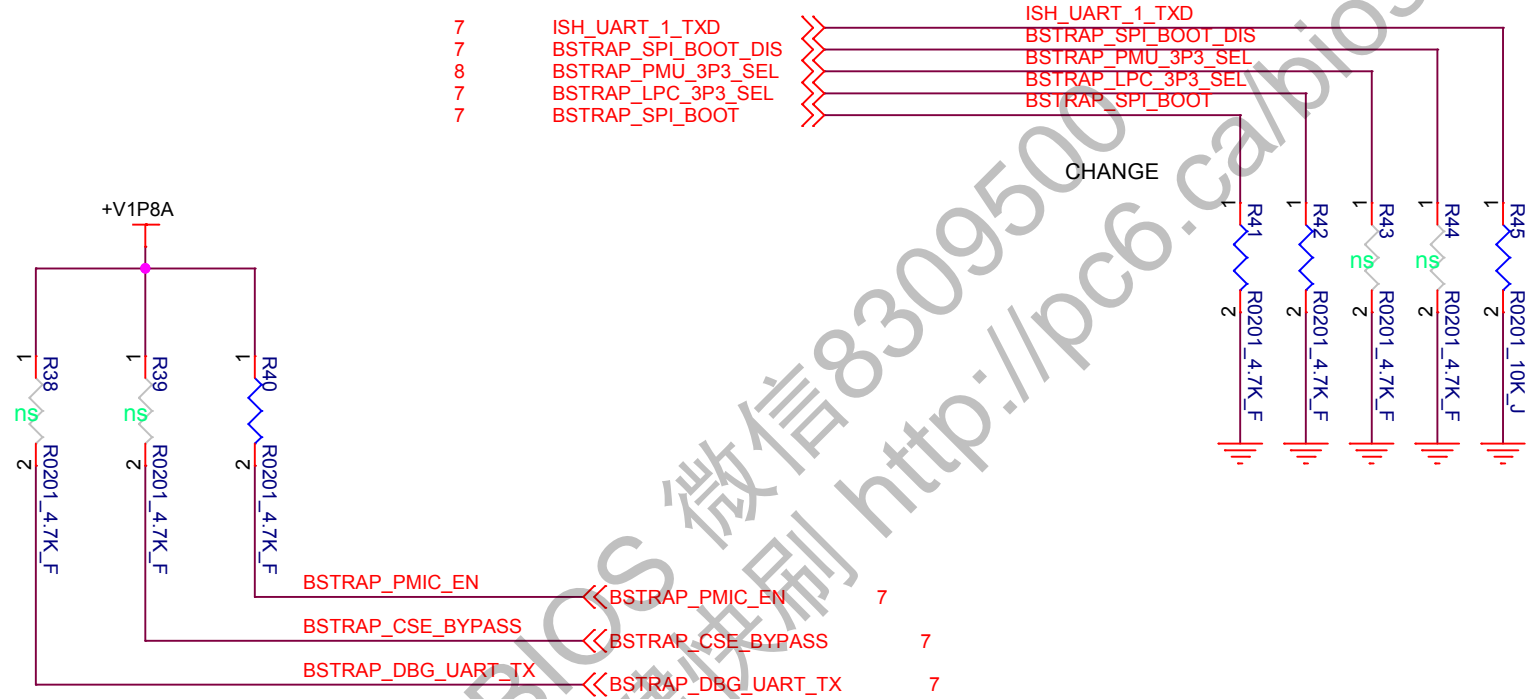
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+VCGI	9,35
+VDDQ	9,13,14,15,16,19,27,34,36
+V3P3A_SOC	9
+V1P24A	9,34
+V1P8A_SOC	9
+VRTC_SOC	8,9
+V1P05S	9,34
+V1P8A	5,7,8,12,20,22,27,29,30,32,34,37,38,39,43
+V3P3A	20,22,27,28,29,30,32,33,34,39



CAD NOTE: C1142 PLACE CLOSE TO PIN AA44

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<b>Page name:</b>		<b>Title</b>	
<b>Size:</b> A4	<b>Project Name:</b> <b>XM AIR 12.5 INDIA</b>	<b>REV:</b> V1.0	
<b>Date:</b> Friday, April 14, 2017	<b>Sheet:</b> 11	<b>of</b> 44	

+V1P8A 5,7,8,11,20,22,27,29,30,32,34,37,38,39,43




\$CDS\_IMAGE|BOOTSTARP\_2.bmp|2165|4414

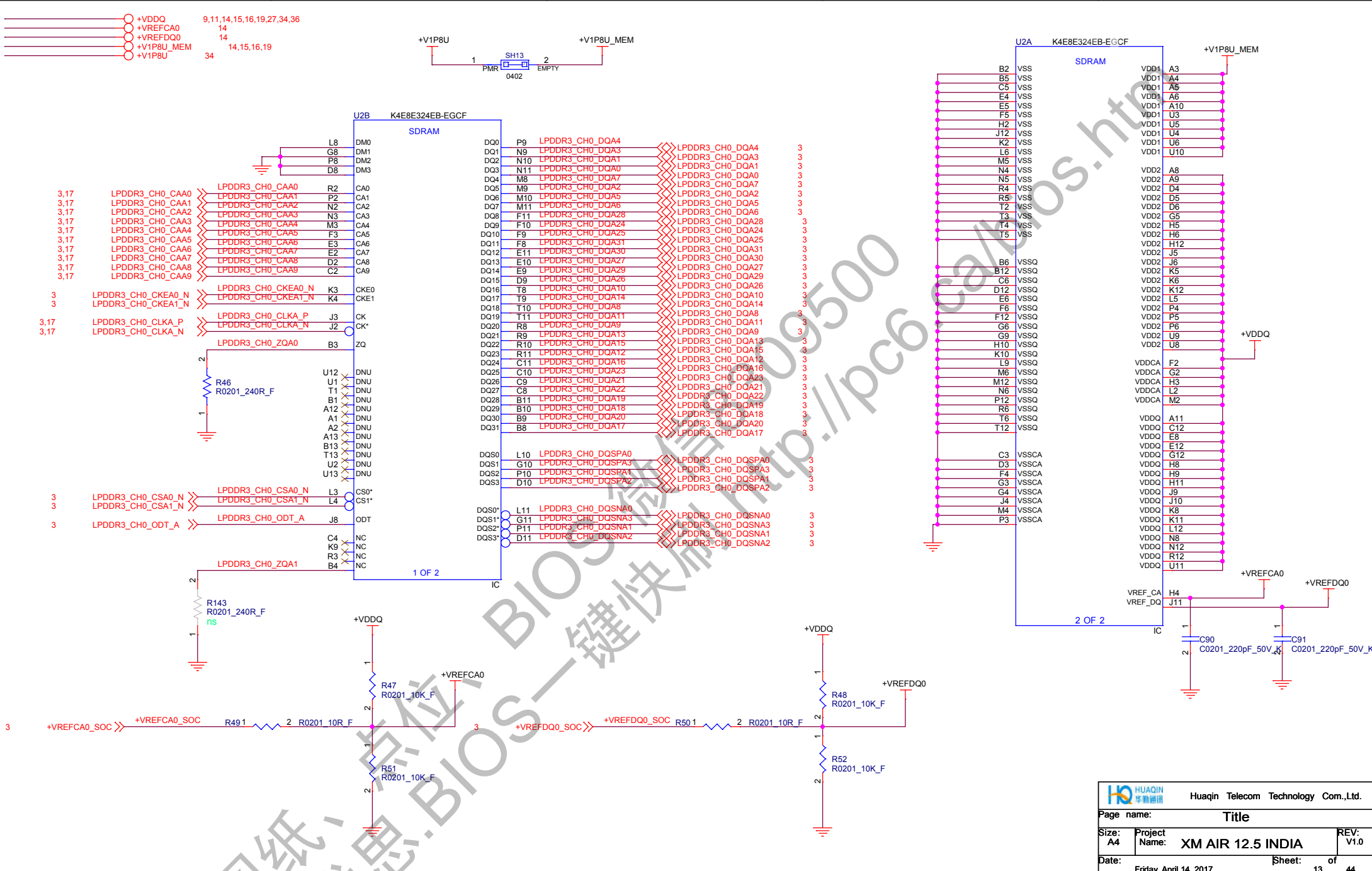
- 7 ISH\_UART\_1\_TXD
- 7 BSTRAP\_SPI\_BOOT\_DIS
- 8 BSTRAP\_PMU\_3P3\_SEL
- 7 BSTRAP\_LPC\_3P3\_SEL
- 7 BSTRAP\_SPI\_BOOT


- ISH\_UART\_1\_TXD
- BSTRAP\_SPI\_BOOT\_DIS
- BSTRAP\_PMU\_3P3\_SEL
- BSTRAP\_LPC\_3P3\_SEL
- BSTRAP\_SPI\_BOOT

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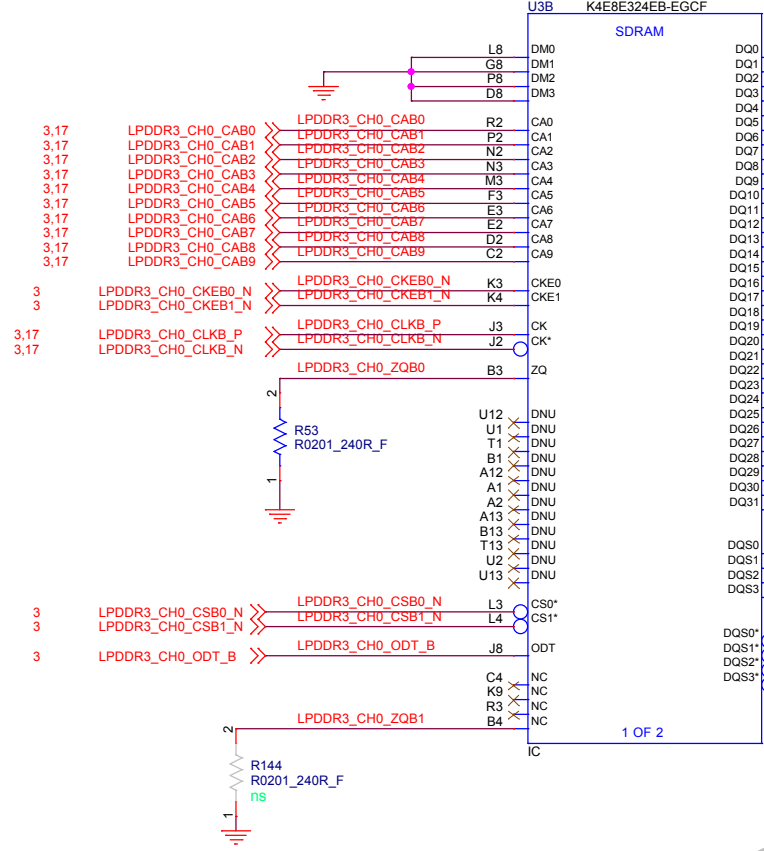
图纸、点位、BIOS一键复制  
百优思.BIOS

 <b>HUAQIN</b> 华勤通讯		Huaqin Telecom Technology Com.,Ltd.	
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Size: A4	Project Name: XM AIR 12.5 INDIA	REV: V1.0	
Date: Friday, April 14, 2017		Sheet:	of 12 of 44

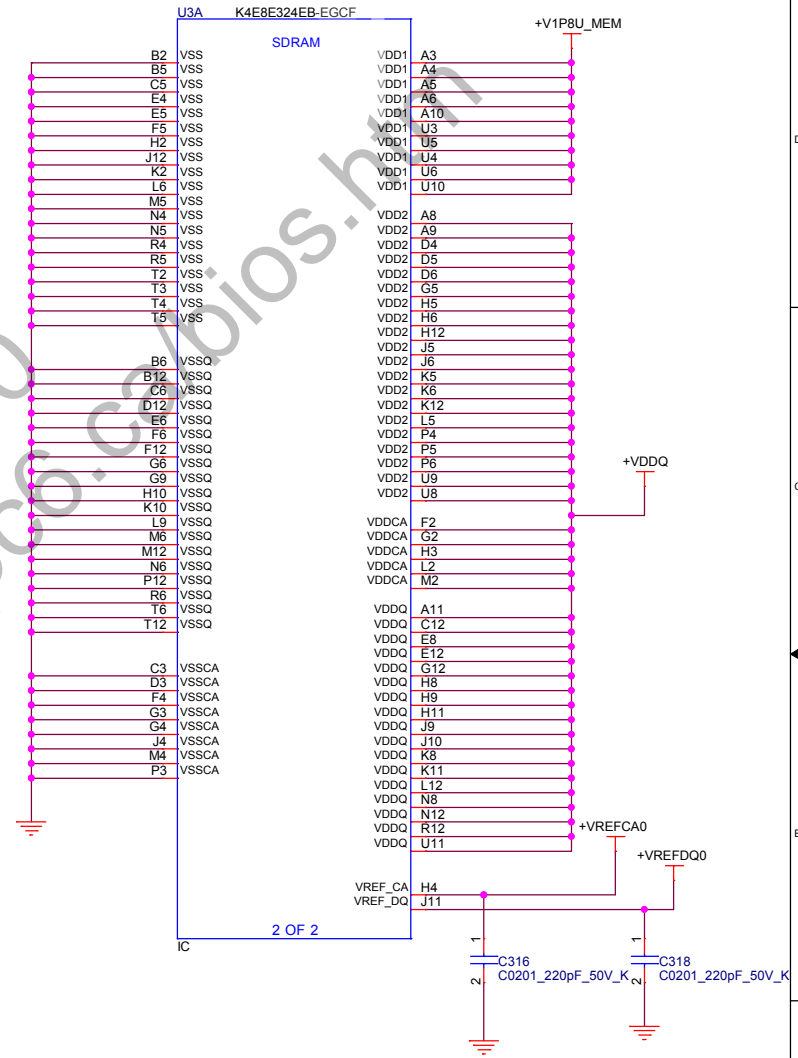
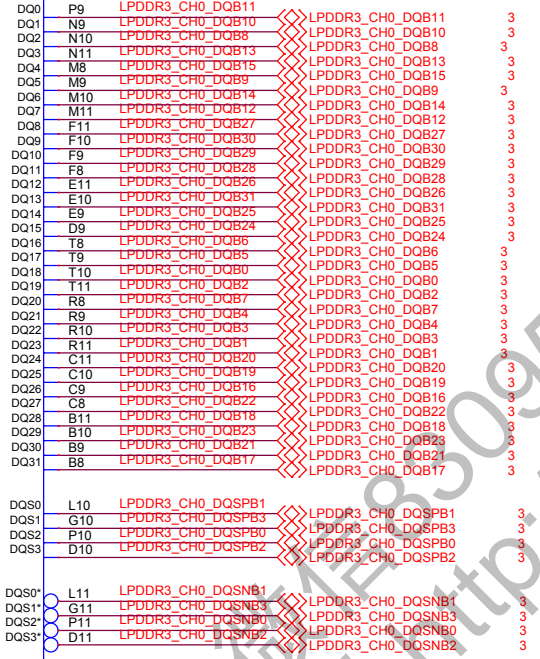


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Page name:	Title
Size: A4	Project Name: <b>XM AIR 12.5 INDIA</b>
Date:	REV: V1.0
Friday, April 14, 2017	Sheet: 13 of 44

+VDDQ 9,11,13,15,16,19,27,34,36  
 +VREFCA0 13  
 +VREFDQ0 13  
 +V1P8U\_MEM 13,15,16,19



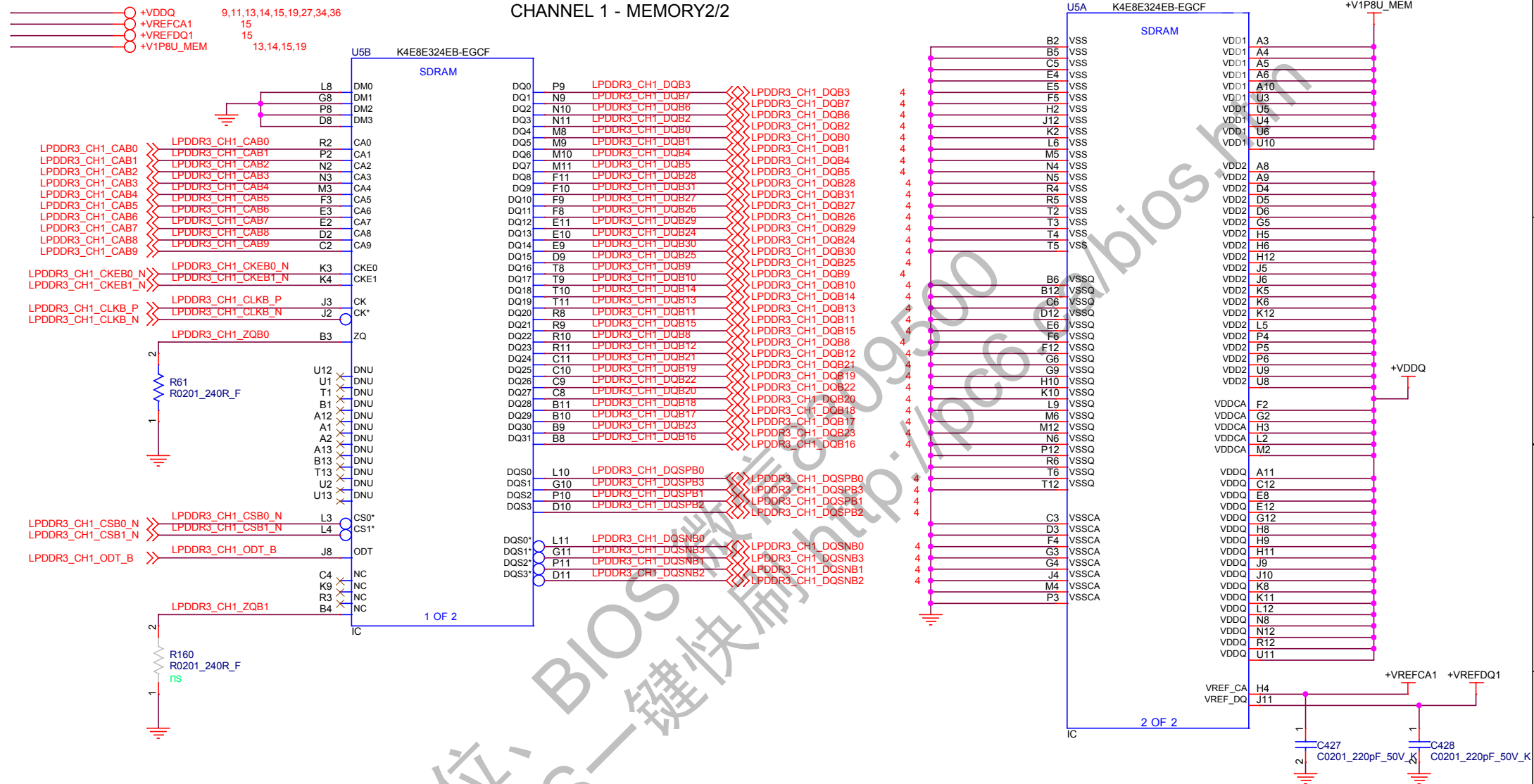
CHANNEL 0 - MEMORY2/2



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Size: A4	Project Name: XM AIR 12.5 INDIA	REV: V1.0	
Date: Friday, April 14, 2017	Sheet: 14	of 44	

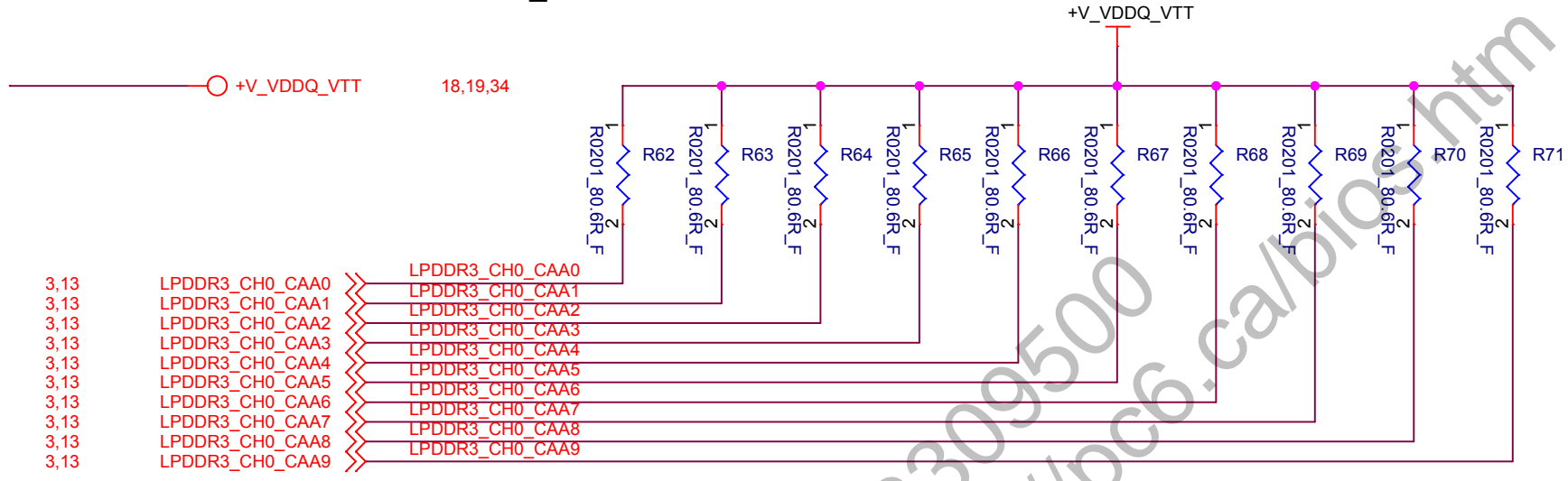


CHANNEL 1 - MEMORY2/2



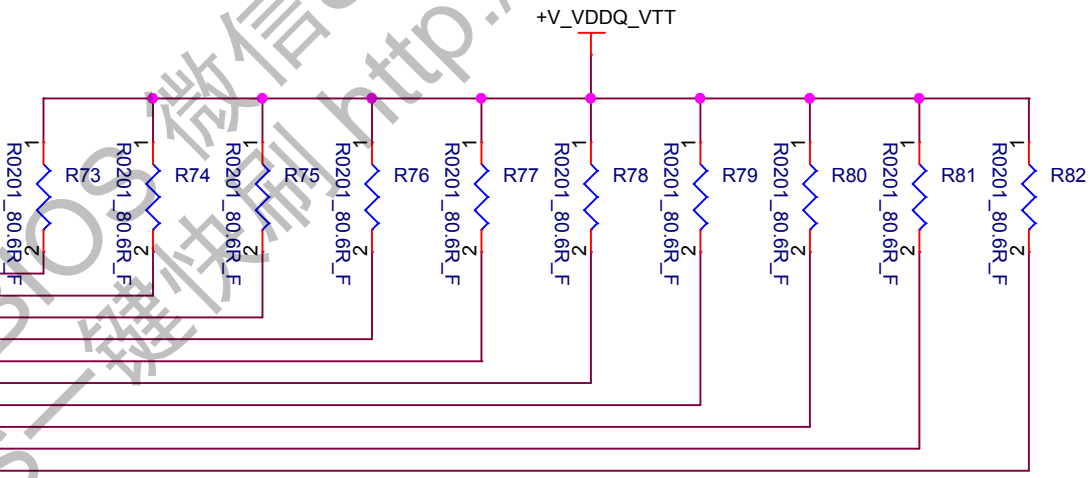
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Page name:		Title	
Size: A4	Project Name: XM AIR 12.5 INDIA	REV: V1.0	
Date: Friday, April 14, 2017	Sheet: 16	of 44	

# LPDDR3L-RS TERMINATION RESISTORS\_CH0



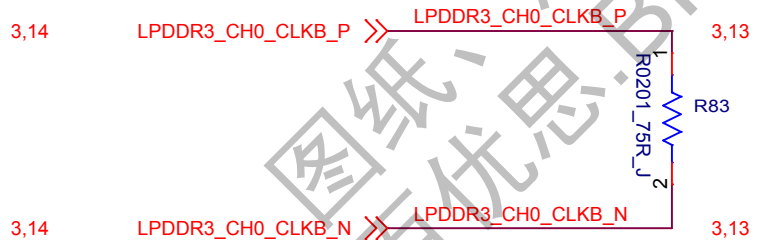
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- 3,13 LPDDR3\_CH0\_CAA1
- 3,13 LPDDR3\_CH0\_CAA2
- 3,13 LPDDR3\_CH0\_CAA3
- 3,13 LPDDR3\_CH0\_CAA4
- 3,13 LPDDR3\_CH0\_CAA5
- 3,13 LPDDR3\_CH0\_CAA6
- 3,13 LPDDR3\_CH0\_CAA7
- 3,13 LPDDR3\_CH0\_CAA8
- 3,13 LPDDR3\_CH0\_CAA9

- LPDDR3\_CH0\_CAA0
- LPDDR3\_CH0\_CAA1
- LPDDR3\_CH0\_CAA2
- LPDDR3\_CH0\_CAA3
- LPDDR3\_CH0\_CAA4
- LPDDR3\_CH0\_CAA5
- LPDDR3\_CH0\_CAA6
- LPDDR3\_CH0\_CAA7
- LPDDR3\_CH0\_CAA8
- LPDDR3\_CH0\_CAA9



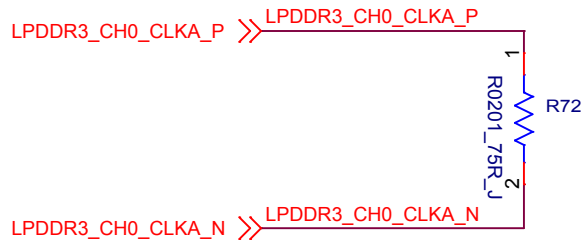
- 3,14 LPDDR3\_CH0\_CAB0
- 3,14 LPDDR3\_CH0\_CAB1
- 3,14 LPDDR3\_CH0\_CAB2
- 3,14 LPDDR3\_CH0\_CAB3
- 3,14 LPDDR3\_CH0\_CAB4
- 3,14 LPDDR3\_CH0\_CAB5
- 3,14 LPDDR3\_CH0\_CAB6
- 3,14 LPDDR3\_CH0\_CAB7
- 3,14 LPDDR3\_CH0\_CAB8
- 3,14 LPDDR3\_CH0\_CAB9

- LPDDR3\_CH0\_CAB0
- LPDDR3\_CH0\_CAB1
- LPDDR3\_CH0\_CAB2
- LPDDR3\_CH0\_CAB3
- LPDDR3\_CH0\_CAB4
- LPDDR3\_CH0\_CAB5
- LPDDR3\_CH0\_CAB6
- LPDDR3\_CH0\_CAB7
- LPDDR3\_CH0\_CAB8
- LPDDR3\_CH0\_CAB9



- 3,14 LPDDR3\_CH0\_CLKB\_P
- 3,14 LPDDR3\_CH0\_CLKB\_N

- LPDDR3\_CH0\_CLKB\_P
- LPDDR3\_CH0\_CLKB\_N



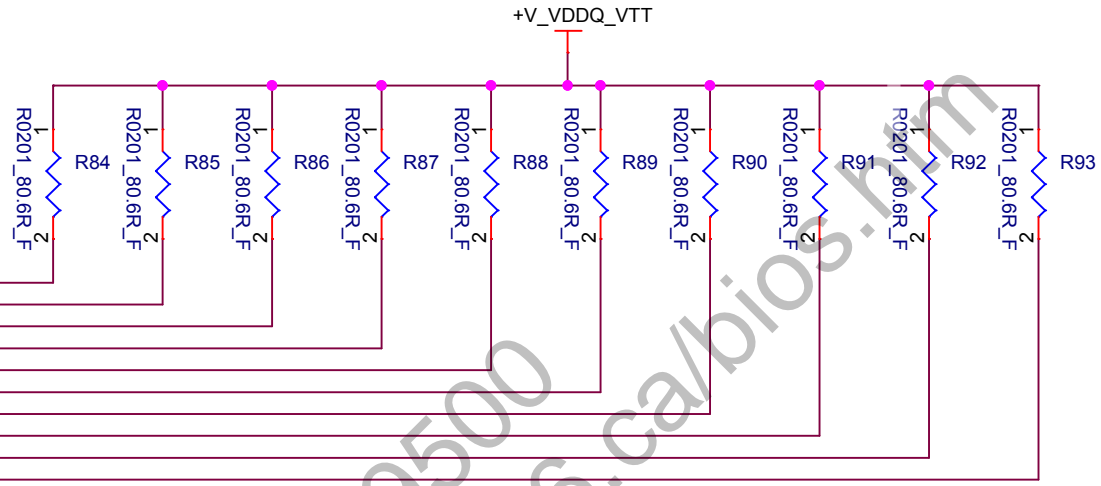
- LPDDR3\_CH0\_CLKA\_P
- LPDDR3\_CH0\_CLKA\_N

		Huaqin Telecom Technology Com.,Ltd.	
Page name:		Title	
Size: A4	Project Name:	XM AIR 12.5 INDIA	REV: V1.0
Date:	Friday, April 14, 2017	Sheet:	17 of 44

+V\_VDDQ\_VTT 17,19,34

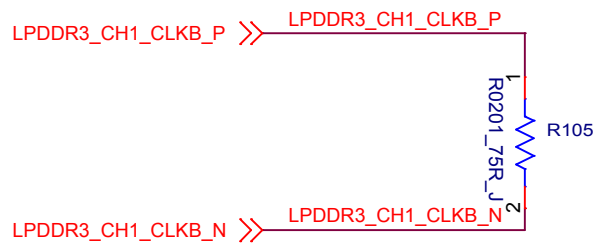
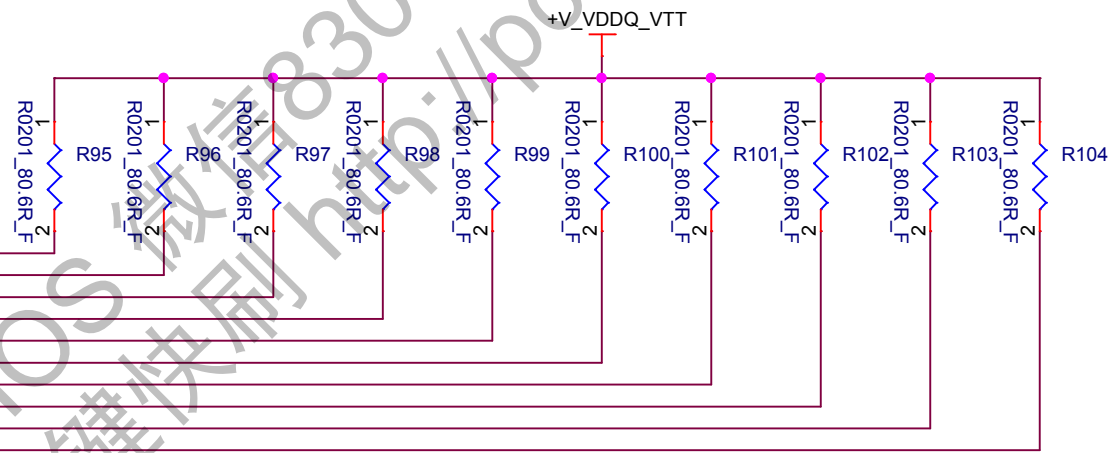
4,15 LPDDR3\_CH1\_CAA0  
4,15 LPDDR3\_CH1\_CAA1  
4,15 LPDDR3\_CH1\_CAA2  
4,15 LPDDR3\_CH1\_CAA3  
4,15 LPDDR3\_CH1\_CAA4  
4,15 LPDDR3\_CH1\_CAA5  
4,15 LPDDR3\_CH1\_CAA6  
4,15 LPDDR3\_CH1\_CAA7  
4,15 LPDDR3\_CH1\_CAA8  
4,15 LPDDR3\_CH1\_CAA9

LPDDR3\_CH1\_CAA0  
LPDDR3\_CH1\_CAA1  
LPDDR3\_CH1\_CAA2  
LPDDR3\_CH1\_CAA3  
LPDDR3\_CH1\_CAA4  
LPDDR3\_CH1\_CAA5  
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LPDDR3\_CH1\_CAA8  
LPDDR3\_CH1\_CAA9



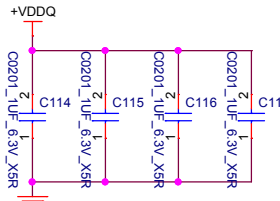
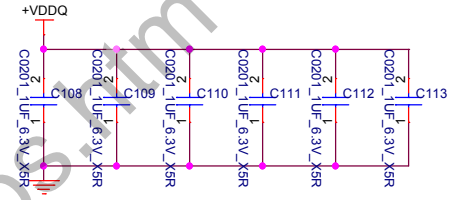
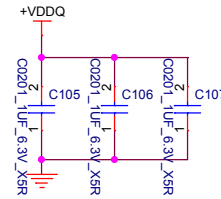
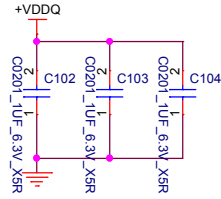
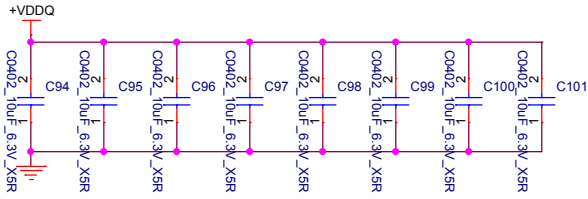
4,16 LPDDR3\_CH1\_CAB0  
4,16 LPDDR3\_CH1\_CAB1  
4,16 LPDDR3\_CH1\_CAB2  
4,16 LPDDR3\_CH1\_CAB3  
4,16 LPDDR3\_CH1\_CAB4  
4,16 LPDDR3\_CH1\_CAB5  
4,16 LPDDR3\_CH1\_CAB6  
4,16 LPDDR3\_CH1\_CAB7  
4,16 LPDDR3\_CH1\_CAB8  
4,16 LPDDR3\_CH1\_CAB9

LPDDR3\_CH1\_CAB0  
LPDDR3\_CH1\_CAB1  
LPDDR3\_CH1\_CAB2  
LPDDR3\_CH1\_CAB3  
LPDDR3\_CH1\_CAB4  
LPDDR3\_CH1\_CAB5  
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LPDDR3\_CH1\_CAB8  
LPDDR3\_CH1\_CAB9

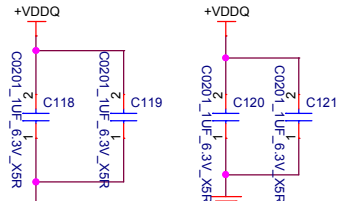


		Huaqin Telecom Technology Com.,Ltd.	
Page name:		Title	
Size: A4	Project Name: XM AIR 12.5 INDIA	REV: V1.0	
Date: Friday, April 14, 2017	Sheet: 18	of 44	

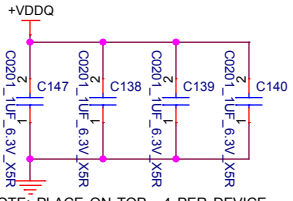
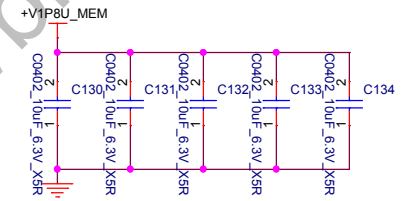
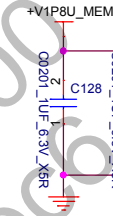
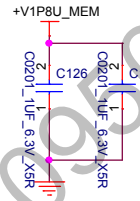
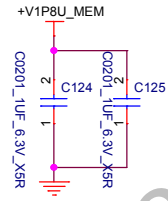
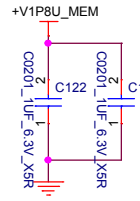
+VDDQ 9,11,13,14,15,16,27,34,36  
 +V1P8U\_MEM 13,14,15,16  
 +V\_VDDQ\_VTT 17,18,34



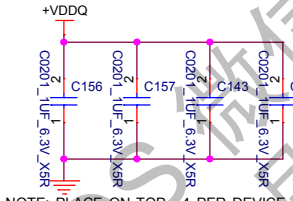
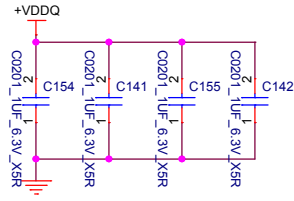
FOUR 0402 IN MID CHANNEL



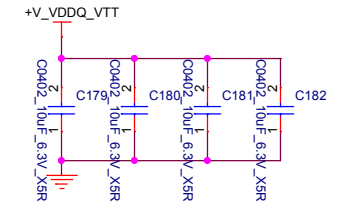
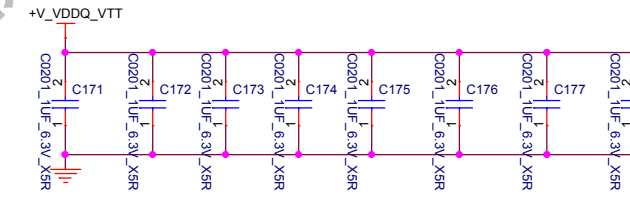
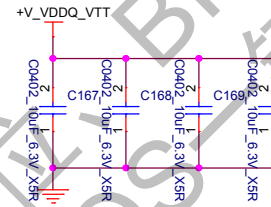
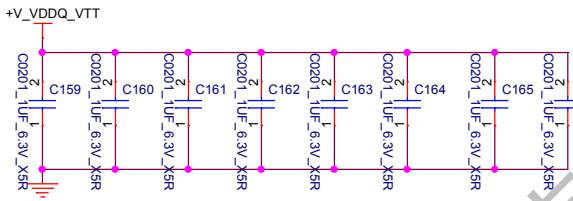
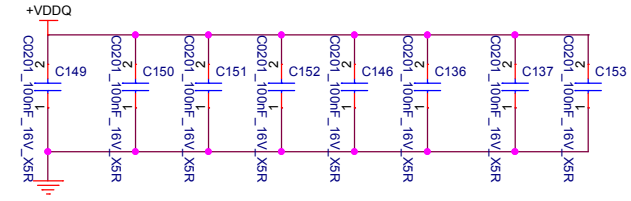
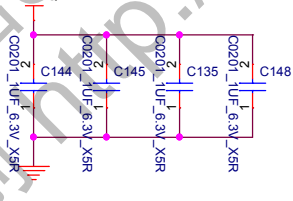
TWO 0402 ON EACH END CHANNEL



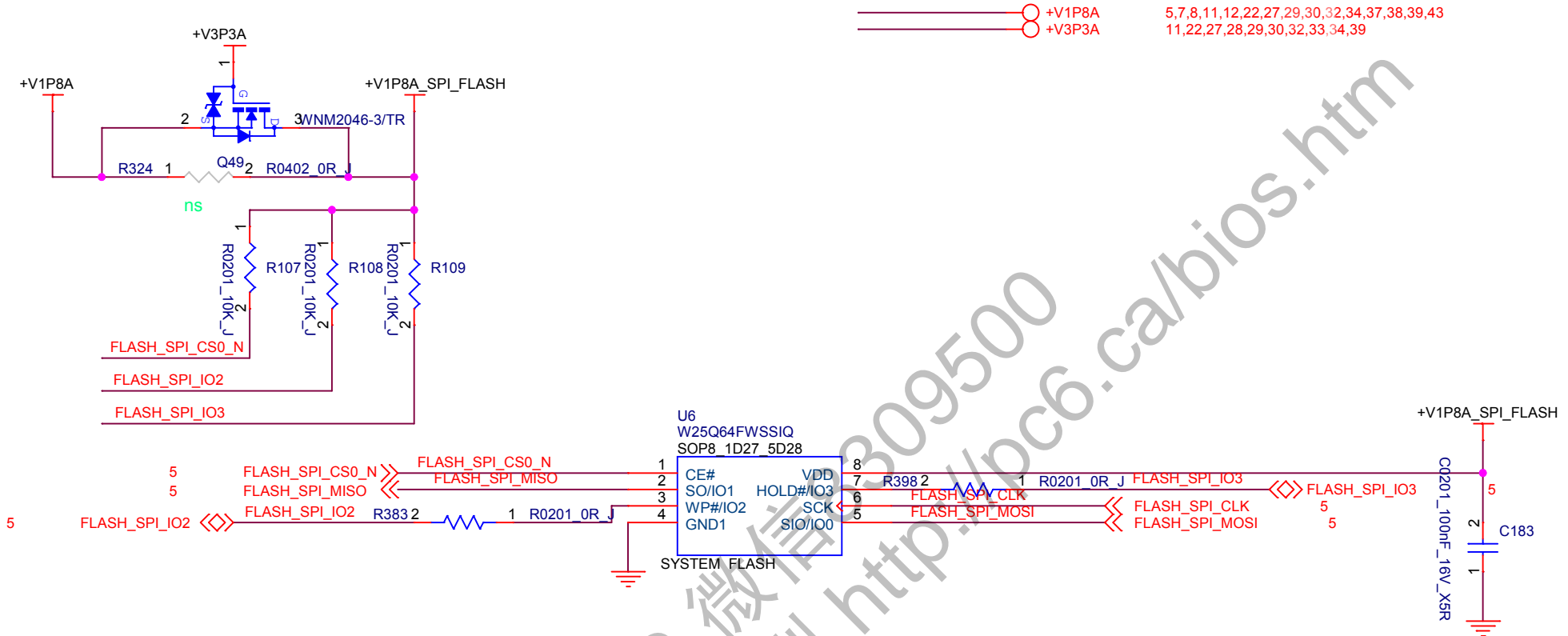
NOTE: PLACE ON TOP - 4 PER DEVICE



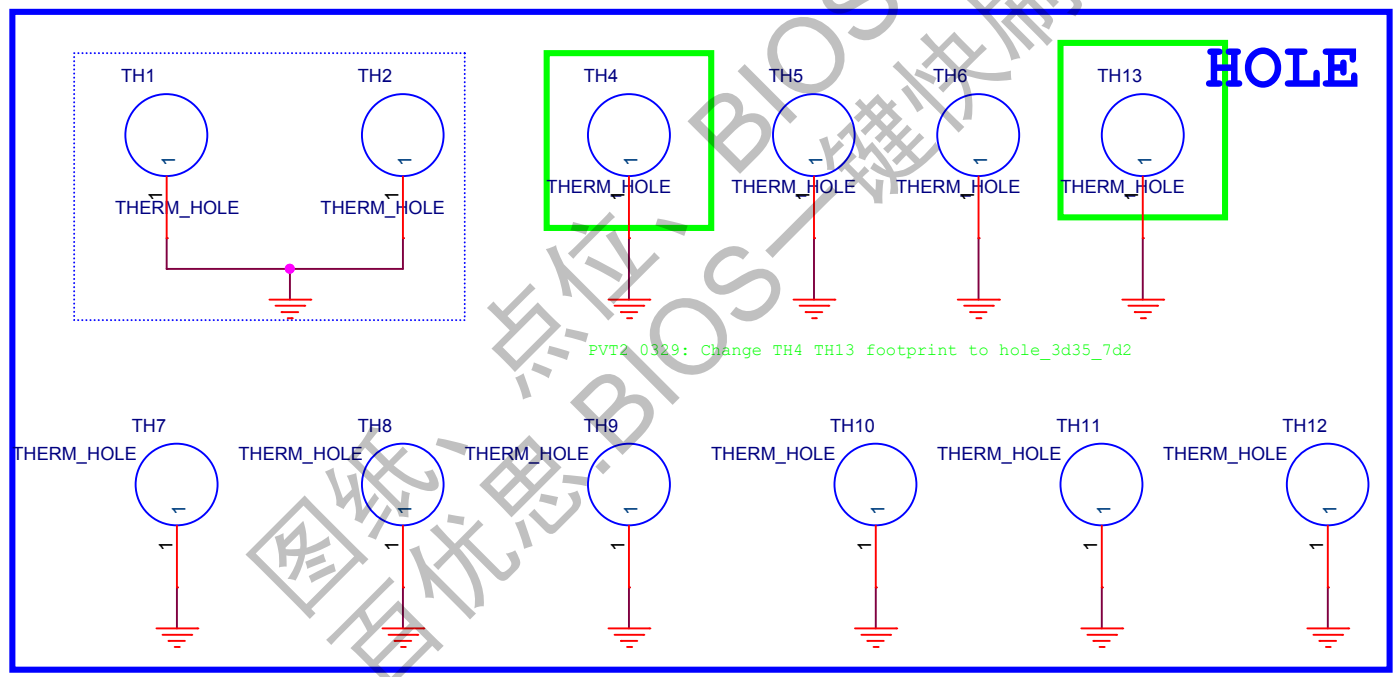
NOTE: PLACE ON TOP - 4 PER DEVICE



 Huaqin Telecom Technology Com.,Ltd.	
Page name:	Title
Size: A4	Project Name: XM AIR 12.5 INDIA
Date:	REV: V1.0
Friday, April 14, 2017	Sheet: 19 of 44

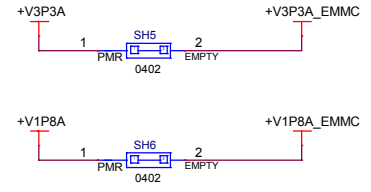


+V1P8A 5,7,8,11,12,22,27,29,30,32,34,37,38,39,43  
 +V3P3A 11,22,27,28,29,30,32,33,34,39

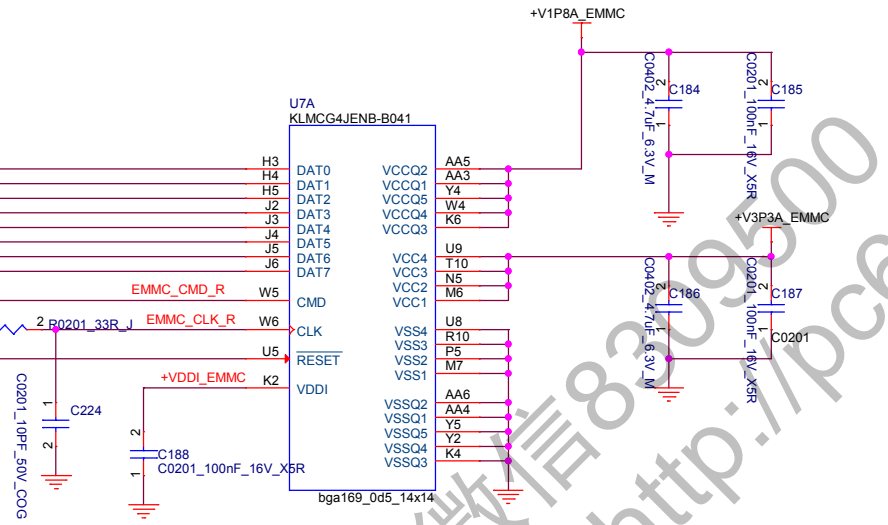


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Page name: Title	
Size: A4	Project Name: XM AIR 12.5 INDIA
Date: Friday, April 14, 2017	REV: V1.0
Sheet: 20 of 44	

+V3P3A 11,20,27,28,29,30,32,33,34,39  
 +V1P8A 5,7,8,11,12,20,27,29,30,32,34,37,38,39,43



- 7 EMMC\_DATA\_0
- 7 EMMC\_DATA\_1
- 7 EMMC\_DATA\_2
- 7 EMMC\_DATA\_3
- 7 EMMC\_DATA\_4
- 7 EMMC\_DATA\_5
- 7 EMMC\_DATA\_6
- 7 EMMC\_DATA\_7
- 7 EMMC\_CMD
- 7 EMMC\_CLK
- 8,30,39 PMU\_PLTRST\_N



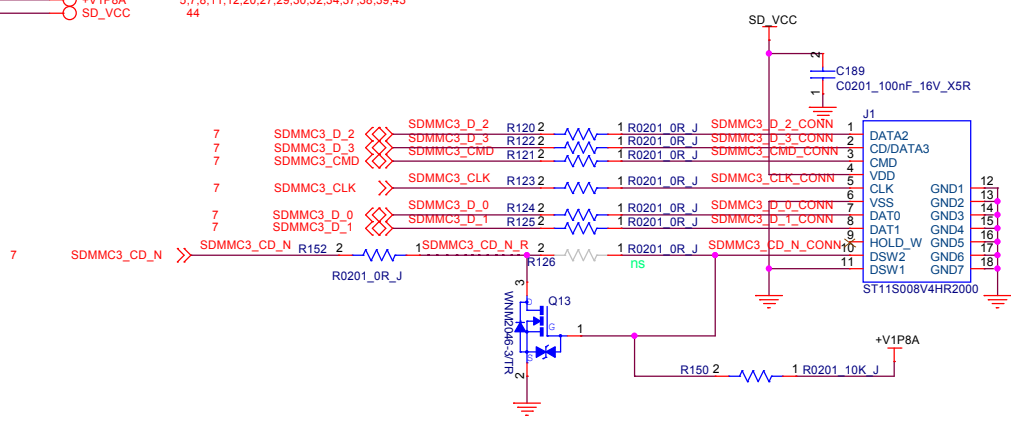
U7B  
KLMCG4JENB-B041

A4	NC_A4	NC_R1	R2
A6	NC_A6	NC_R2	R3
A9	NC_A9	NC_R3	R5
A11	NC_A11	NC_R5	R12
B2	NC_B2	NC_R12	R13
B13	NC_B13	NC_R13	R14
D1	NC_D1	NC_R14	T1
D14	NC_D14	NC_T1	T2
H1	NC_H1	NC_T2	T3
H2	NC_H2	NC_T3	T5
		NC_T5	T12
H7	NC_H7	NC_T12	T13
H8	NC_H8	NC_T13	T14
H9	NC_H9	NC_T14	U1
H10	NC_H10	NC_U1	U2
H11	NC_H11	NC_U2	U3
H12	NC_H12	NC_U3	U6
H13	NC_H13	NC_U6	U7
H14	NC_H14	NC_U7	U10
J1	NC_J1	NC_U10	U12
J7	NC_J7	NC_U12	U13
J8	NC_J8	NC_U13	U14
J9	NC_J9	NC_U14	V1
J10	NC_J10	NC_V1	V2
J11	NC_J11	NC_V2	V3
J12	NC_J12	NC_V3	V12
J13	NC_J13	NC_V12	V13
J14	NC_J14	NC_V13	V14
K1	NC_K1	NC_V14	W1
K3	NC_K3	NC_W1	W2
K5	NC_K5	NC_W2	W3
K7	NC_K7	NC_W3	W7
K8	NC_K8	NC_W7	W8
K9	NC_K9	NC_W8	W10
K10	NC_K10	NC_W10	W11
K11	NC_K11	NC_W11	W12
K12	NC_K12	NC_W12	W13
K13	NC_K13	NC_W13	W14
K14	NC_K14	NC_W14	Y1
L1	NC_L1	NC_Y1	Y3
L2	NC_L2	NC_Y3	Y6
L3	NC_L3	NC_Y6	Y7
L4	NC_L4	NC_Y7	Y8
L12	NC_L12	NC_Y8	Y9
L13	NC_L13	NC_Y9	Y10
L14	NC_L14	NC_Y10	Y11
M1	NC_M1	NC_Y11	Y12
M2	NC_M2	NC_Y12	Y13
M3	NC_M3	NC_Y13	Y14
M5	NC_M5	NC_Y14	AA1
M8	NC_M8	NC_AA1	AA2
M9	NC_M9	NC_AA2	AA7
M10	NC_M10	NC_AA7	AA8
M12	NC_M12	NC_AA8	AA9
M13	NC_M13	NC_AA9	AA10
M14	NC_M14	NC_AA10	AA11
N1	NC_N1	NC_AA11	AA12
N2	NC_N2	NC_AA12	AA13
N3	NC_N3	NC_AA13	AA14
N10	NC_N10	NC_AA14	AE1
N12	NC_N12	NC_AE1	AE14
N13	NC_N13	NC_AE14	AG2
N14	NC_N14	NC_AG2	AG13
P1	NC_P1	NC_AG13	AH4
P2	NC_P2	NC_AH4	AH6
P3	NC_P3	NC_AH6	AH9
P10	NC_P10	NC_AH9	AH11
P12	NC_P12	NC_AH11	
P13	NC_P13		
P14	NC_P14		

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 百优思.BIOS 一键快刷 http://pc6

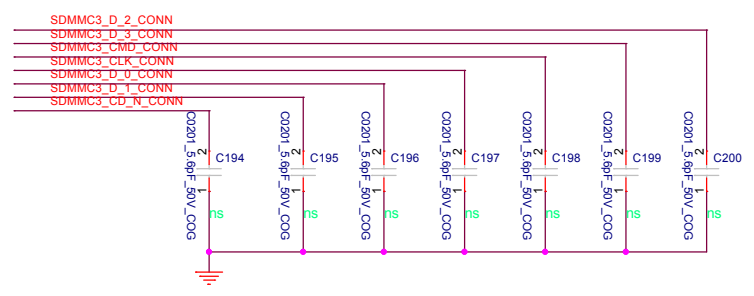
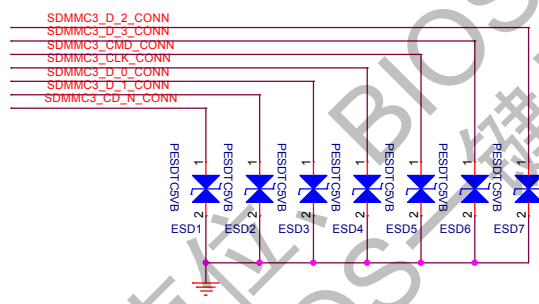
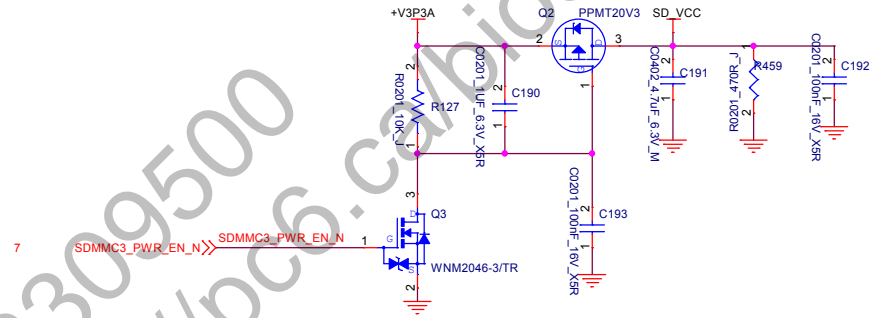
		Huaqin Telecom Technology Com.,Ltd.	
Page name:		Title	
Size: A4	Project Name:	XM AIR 12.5 INDIA	REV: V1.0
Date:	Friday, April 14, 2017	Sheet: 21	of 44

+V3P3A 11,20,27,28,29,30,32,33,34,39  
 +V1P8A 5,7,8,11,12,20,27,29,30,32,34,37,38,39,43  
 SD\_VCC 44



PVT 0303: Add for co-lay USB SD Card

44	USBSD_CD#	<<	R128 2	ns	1 R0201_0R J	SDMMC3_CD_N_R
44	USBSD_DATA0	>>	R497 2	ns	1 R0201_0R J	SDMMC3_D_0_CONN
44	USBSD_DATA1	>>	R498 2	ns	1 R0201_0R J	SDMMC3_D_1_CONN
44	USBSD_CLK_R	>>	R499 2	ns	1 R0201_0R J	SDMMC3_CLK_CONN
44	USBSD_CMD	>>	R500 2	ns	1 R0201_0R J	SDMMC3_CMD_CONN
44	USBSD_DATA3	>>	R501 2	ns	1 R0201_0R J	SDMMC3_D_3_CONN
44	USBSD_DATA2	>>	R502 2	ns	1 R0201_0R J	SDMMC3_D_2_CONN

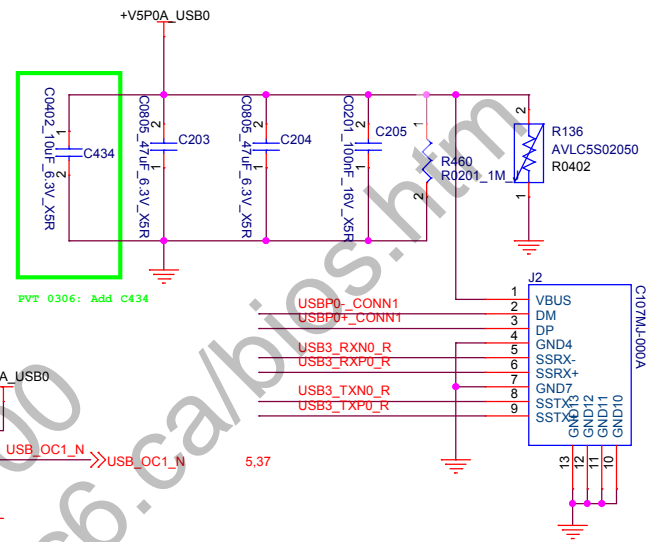
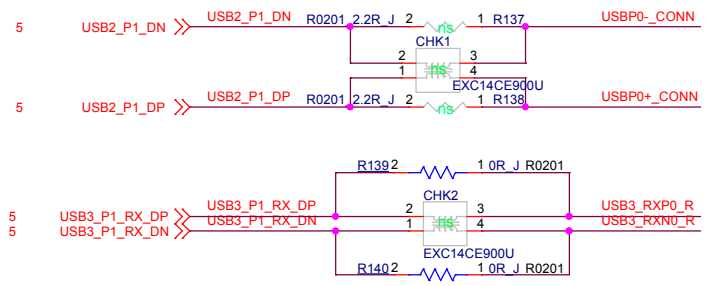


图纸、点位、BIOS 微信8309500 键快刷 http://pc6.com/bios.htm

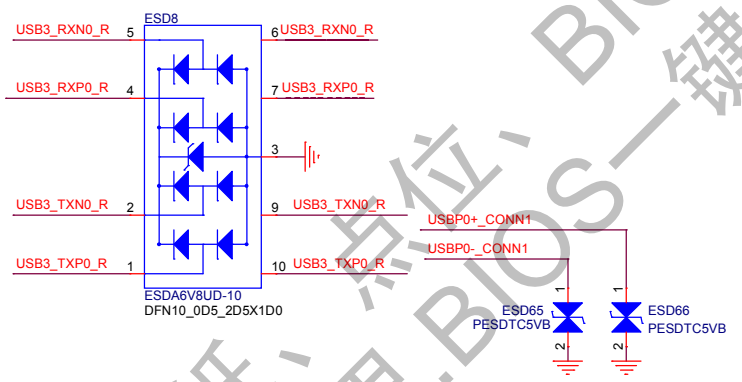
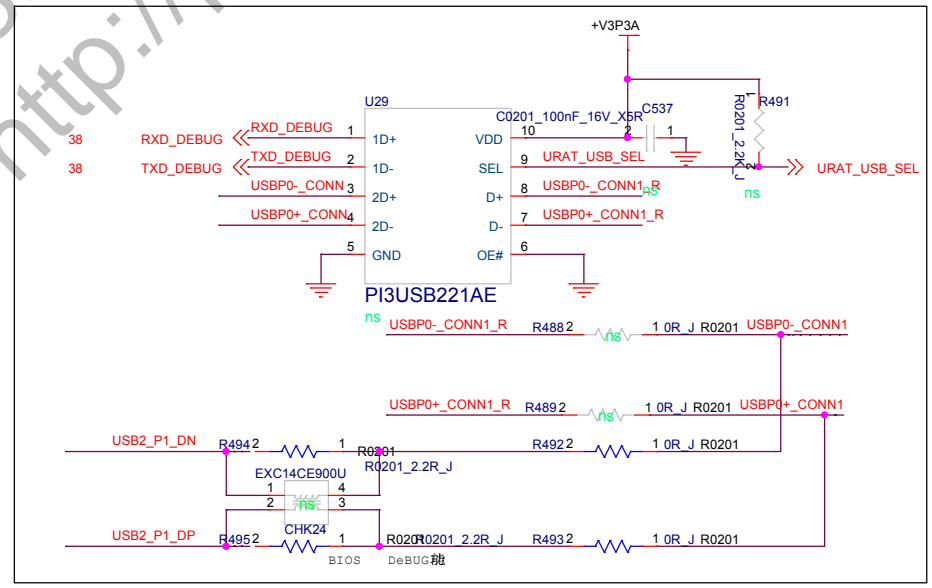
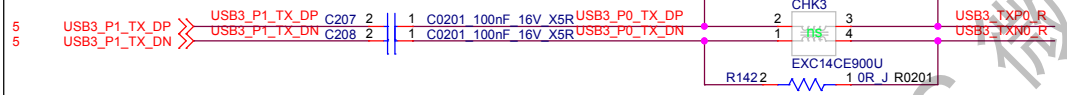


+V5P0A 25,29,33,34,35,36,37  
 +V3P3A 25,29,33,34,35,36,37

PVT 0304: Add +V3P3A Offpage, and Delete +V5P0A\_USB0

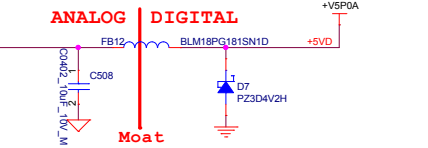
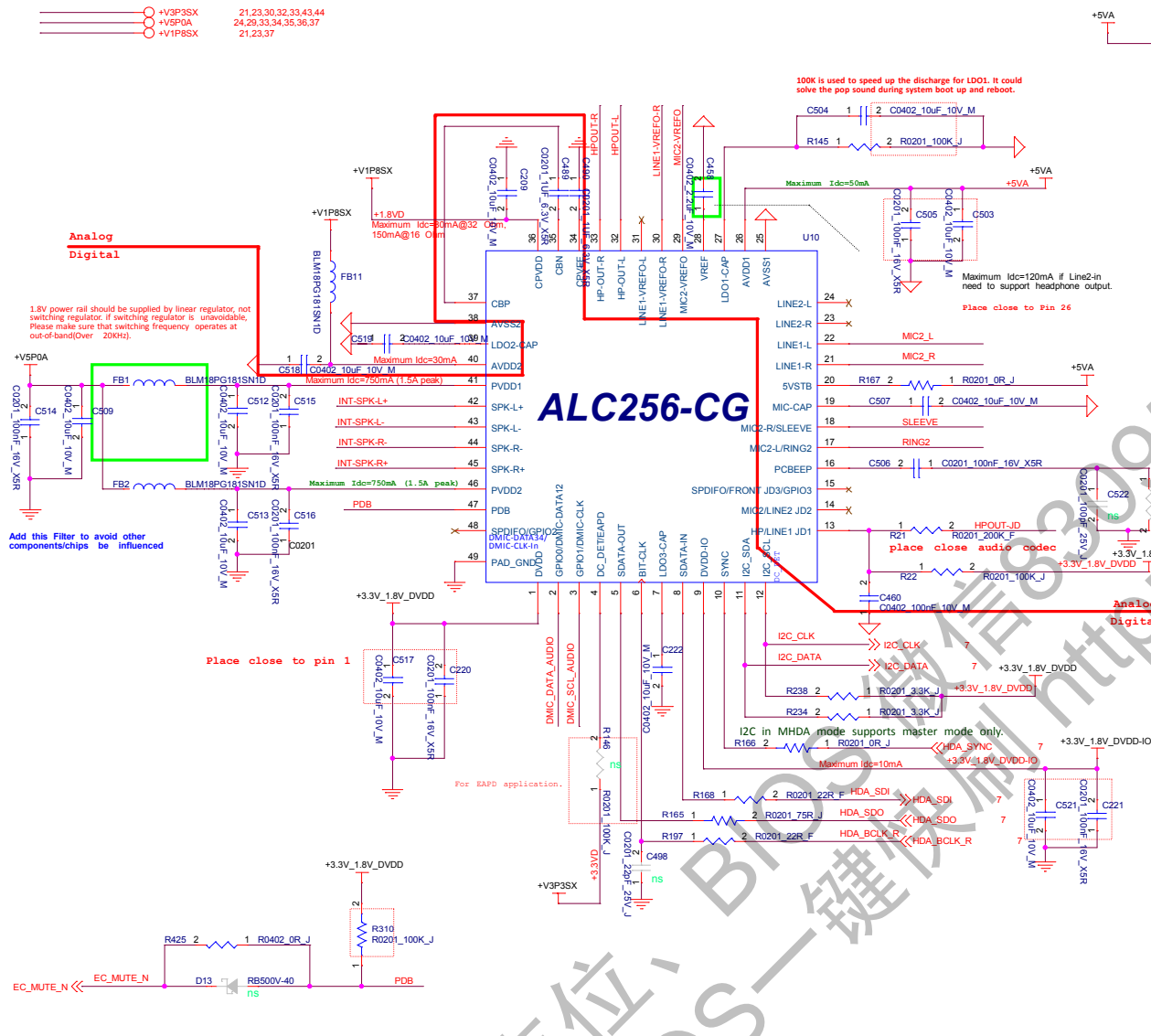


PVT 0306: Add C434

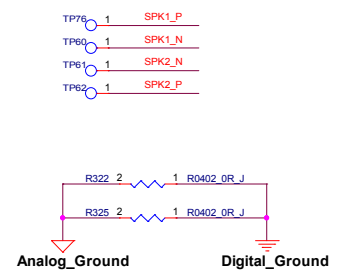
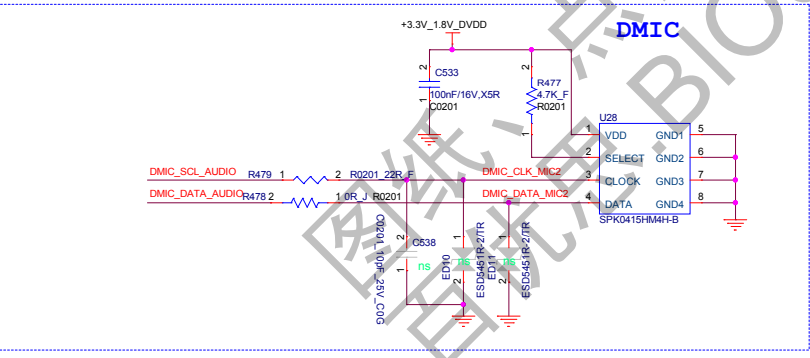
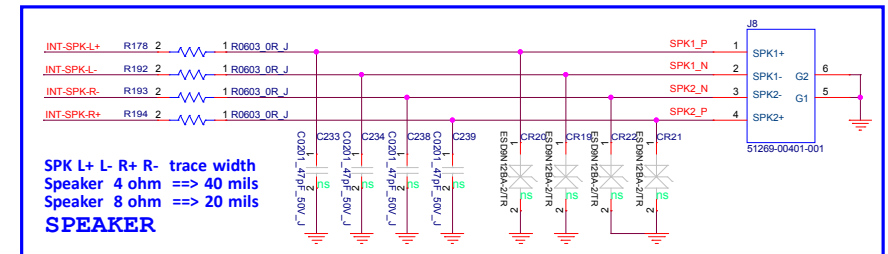
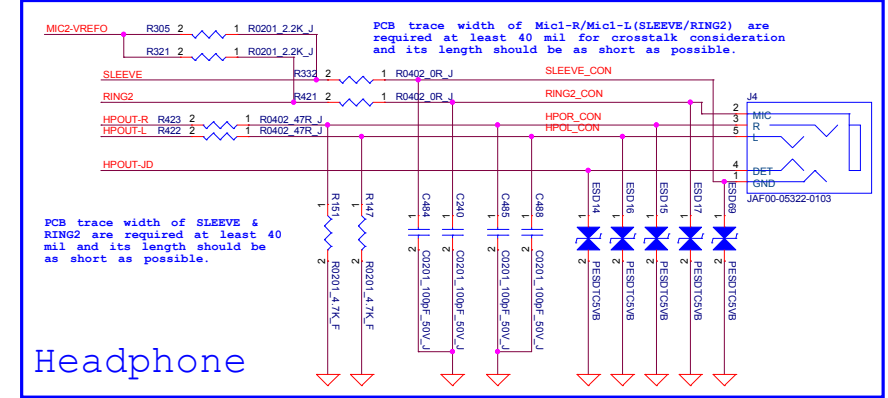
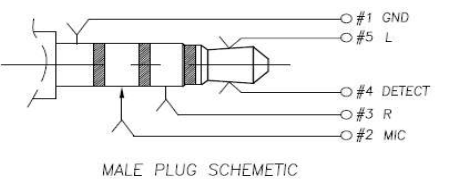
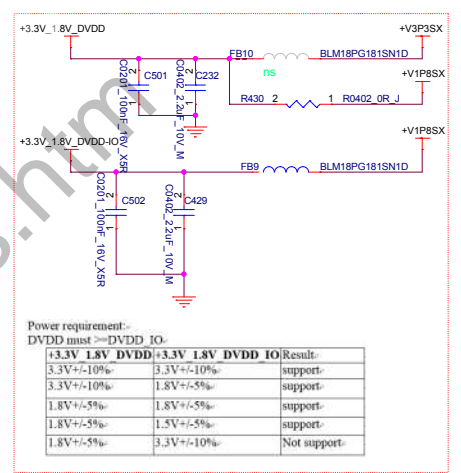


		Huaqin Telecom Technology Com.,Ltd.	
Page name:		Title	
Size: A4	Project Name:	XM AIR 12.5 INDIA	REV: V1.0
Date:	Friday, April 14, 2017	Sheet: 24	of 44

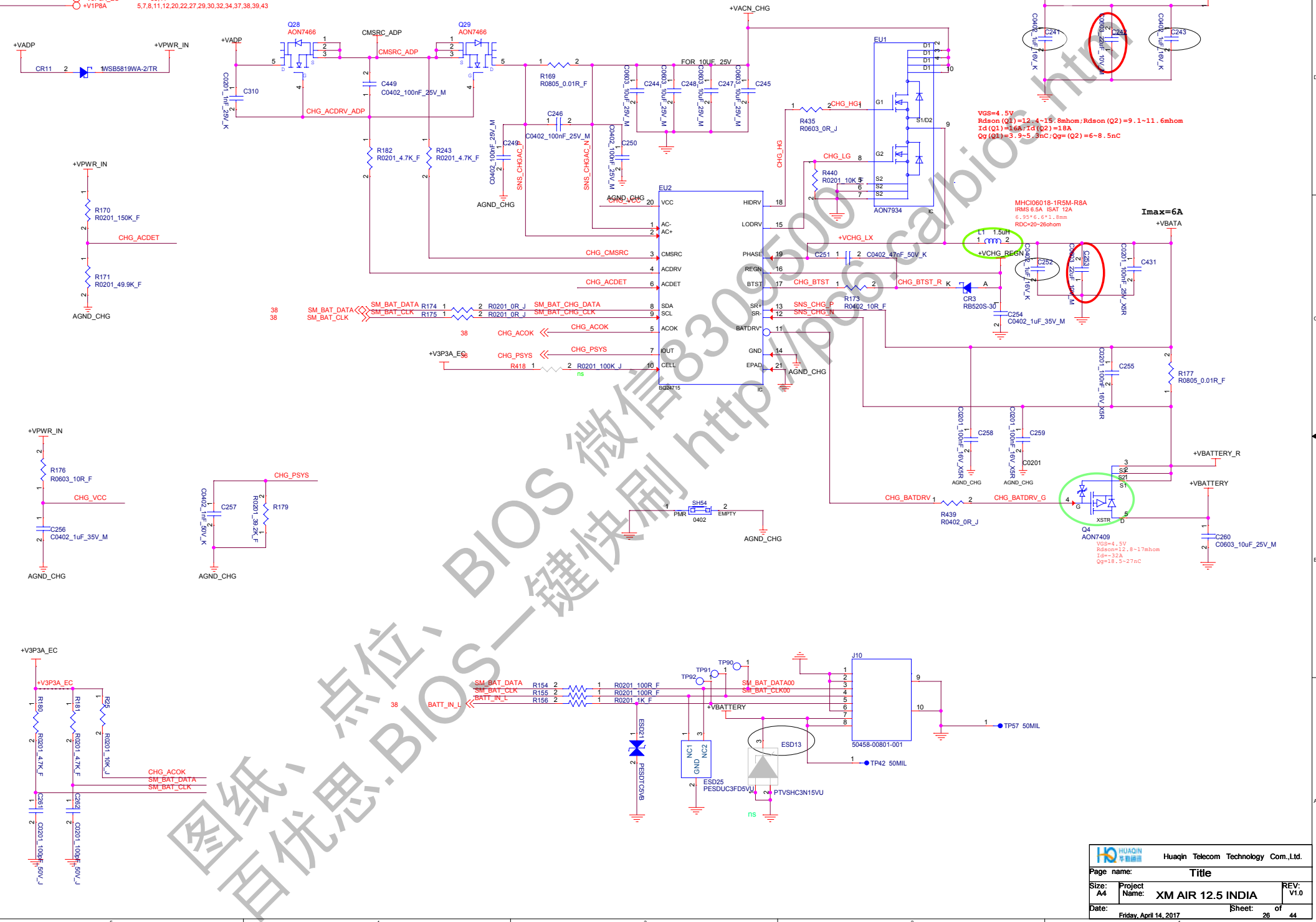
+V3P3SX 21,23,30,32,33,43,44  
 +V5P0A 24,29,33,34,35,36,37  
 +V1P8SX 21,23,37



In order to prevent the built-in LDO damaged from over-voltage on +5VSYS or Standby power line, we suggested using this voltage suppressing device.



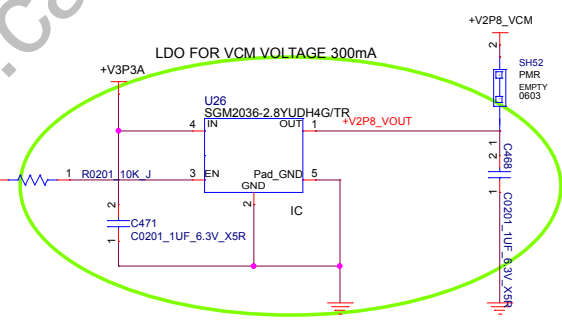
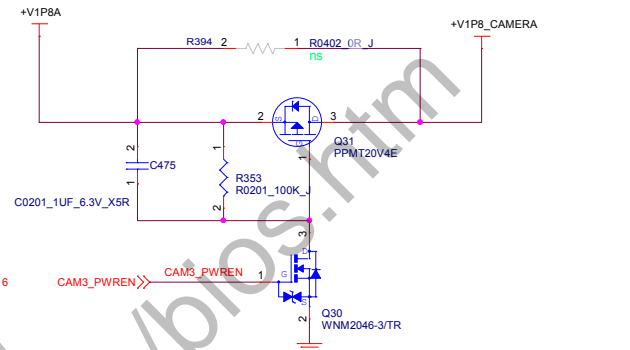
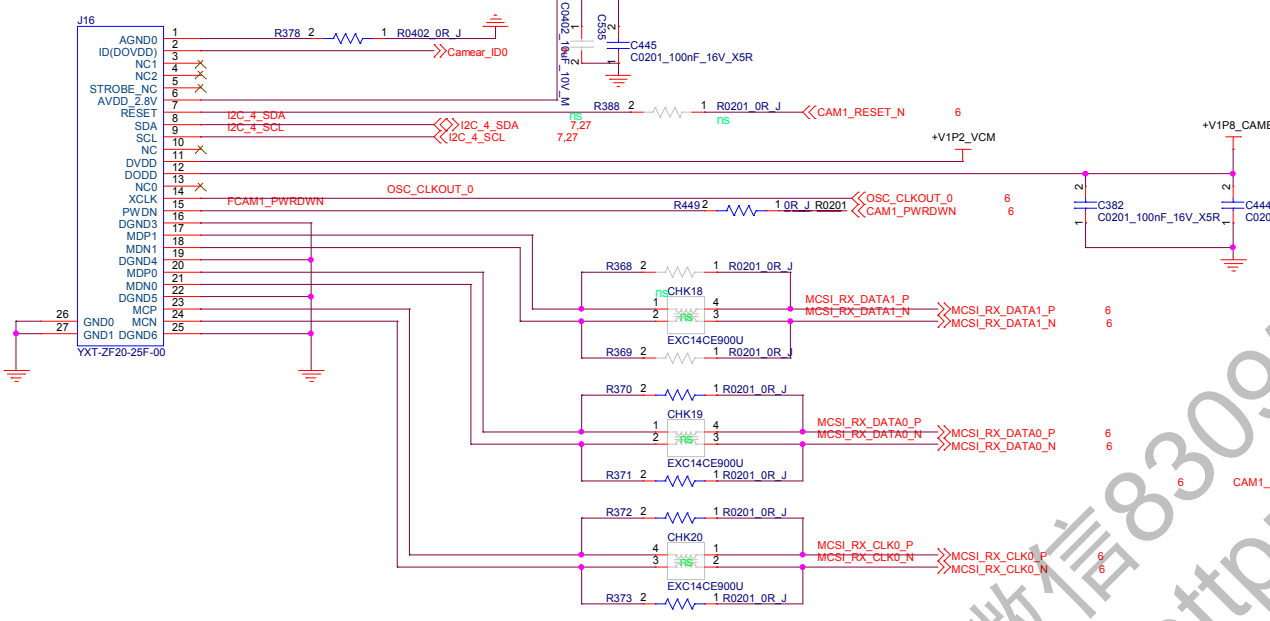
+VADP 40  
 +VPWR\_IN 41  
 PVT 0304: Delete +VACN\_CHG 32,33,34,35,36,39  
 +VBATA 38,41  
 +V3P3A\_EC 38,41  
 +VP3A 5,7,8,11,12,20,22,27,29,30,32,34,37,38,39,43



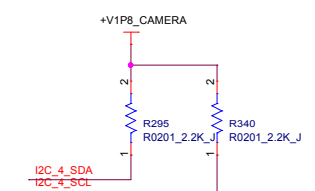
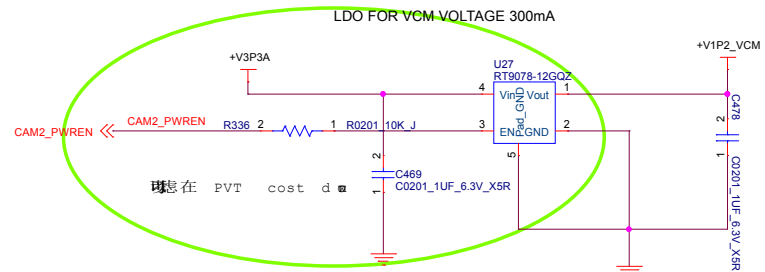
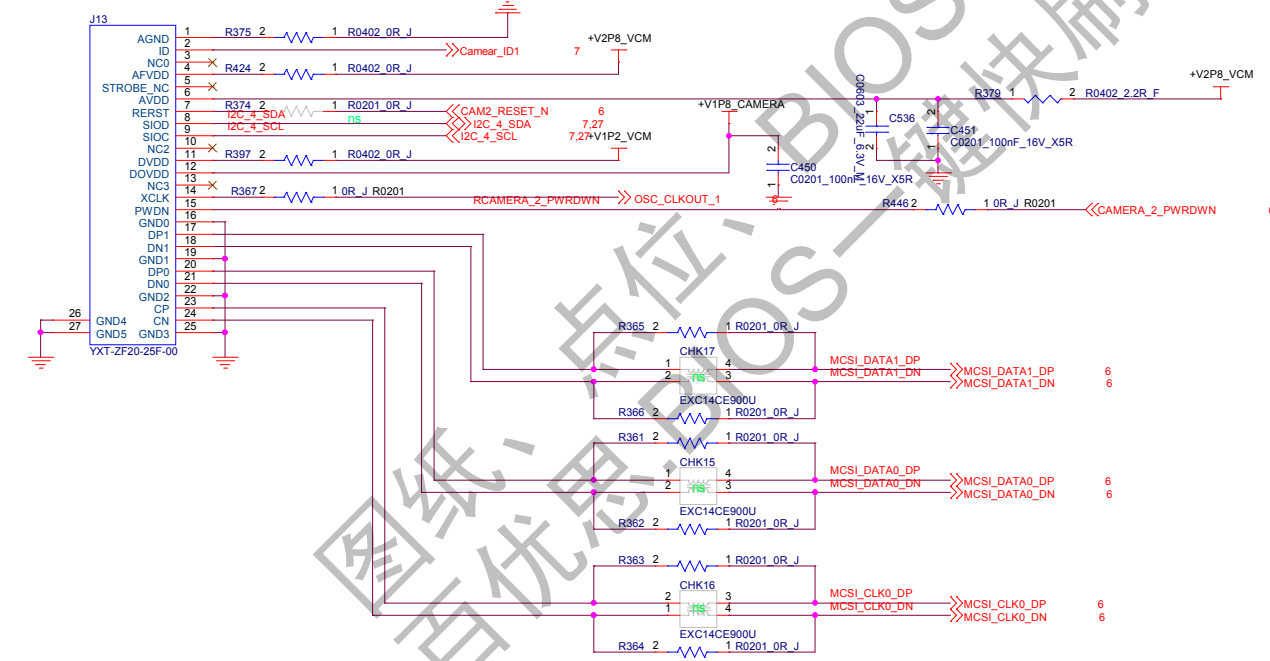
图纸、点位、BIOS 微信 830018888 一键快刷 http://www.cabio.com

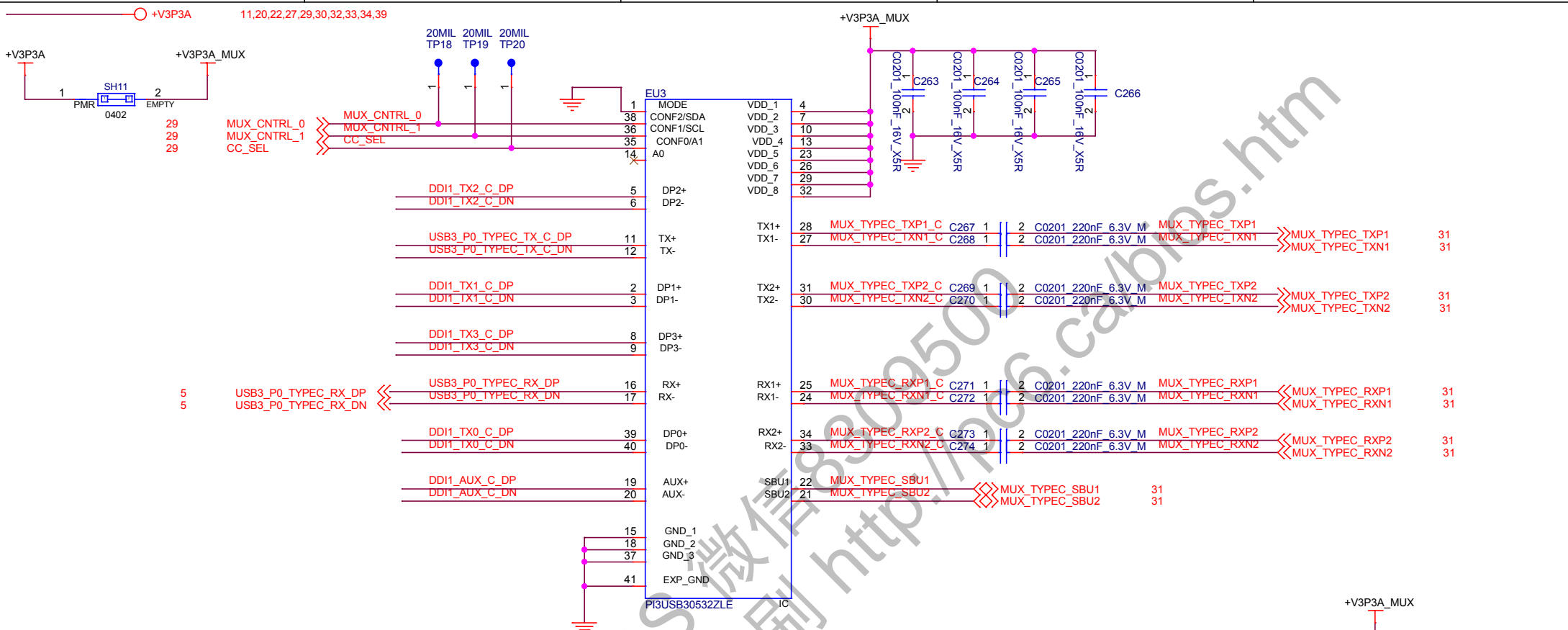
+V1P8A 5,7,8,11,12,20,22,29,30,32,34,37,38,39,43  
 PVT 0304: Delete +V1P2\_VCM, +V2P8\_VCM and +V1P8\_CAMERA  
 +V3P3A 11,20,22,28,29,30,32,33,34,39  
 +VDDQ 9,11,13,14,15,16,19,34,36

### Front Camera

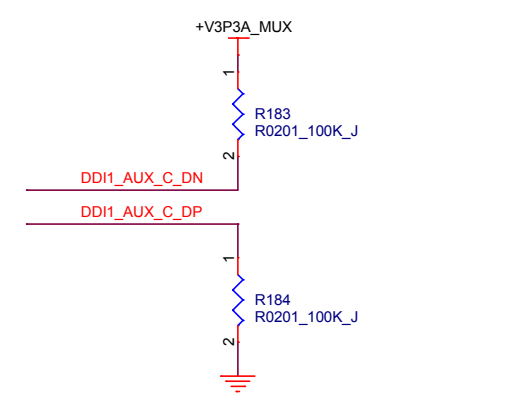


### Rear Camera





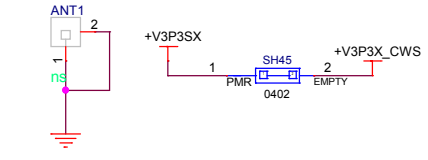
6	DDI0_TX0_DN	<<	DDI0_TX0_DN	C275	2	1	C0201_100nF_16V_X5R	DDI1_TX0_C_DN
6	DDI0_TX0_DP	<<	DDI0_TX0_DP	C276	2	1	C0201_100nF_16V_X5R	DDI1_TX0_C_DP
6	DDI0_TX1_DN	<<	DDI0_TX1_DN	C277	2	1	C0201_100nF_16V_X5R	DDI1_TX1_C_DN
6	DDI0_TX1_DP	<<	DDI0_TX1_DP	C278	2	1	C0201_100nF_16V_X5R	DDI1_TX1_C_DP
6	DDI0_TX2_DN	<<	DDI0_TX2_DN	C279	2	1	C0201_100nF_16V_X5R	DDI1_TX2_C_DN
6	DDI0_TX2_DP	<<	DDI0_TX2_DP	C280	2	1	C0201_100nF_16V_X5R	DDI1_TX2_C_DP
6	DDI0_TX3_DN	<<	DDI0_TX3_DN	C281	2	1	C0201_100nF_16V_X5R	DDI1_TX3_C_DN
6	DDI0_TX3_DP	<<	DDI0_TX3_DP	C282	2	1	C0201_100nF_16V_X5R	DDI1_TX3_C_DP
5	USB3_P0_TYPEC_TX_DN	>>	USB3_P0_TYPEC_TX_DN	C283	2	1	C0201_100nF_16V_X5R	USB3_P0_TYPEC_TX_C_DN
5	USB3_P0_TYPEC_TX_DP	>>	USB3_P0_TYPEC_TX_DP	C284	2	1	C0201_100nF_16V_X5R	USB3_P0_TYPEC_TX_C_DP
6	DDI0_AUX_DN	<<	DDI0_AUX_DN	C285	2	1	C0201_100nF_16V_X5R	DDI1_AUX_C_DN
6	DDI0_AUX_DP	<<	DDI0_AUX_DP	C286	2	1	C0201_100nF_16V_X5R	DDI1_AUX_C_DP



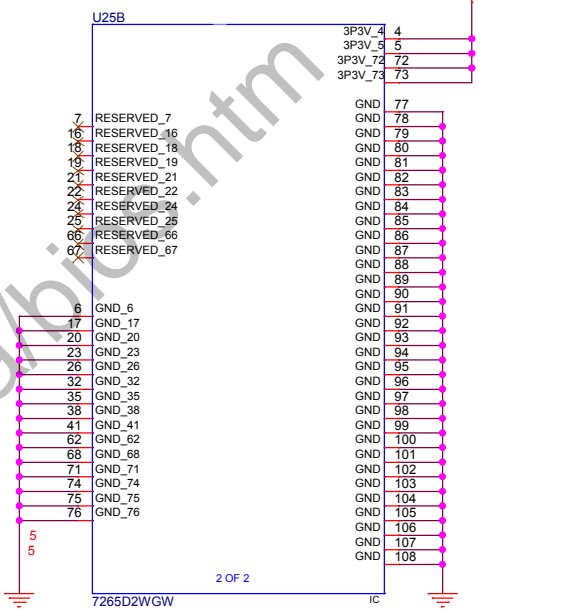
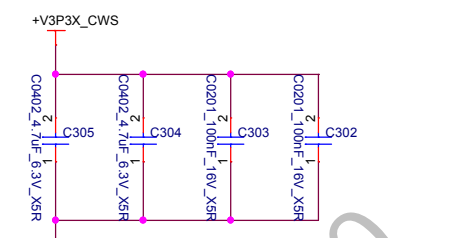
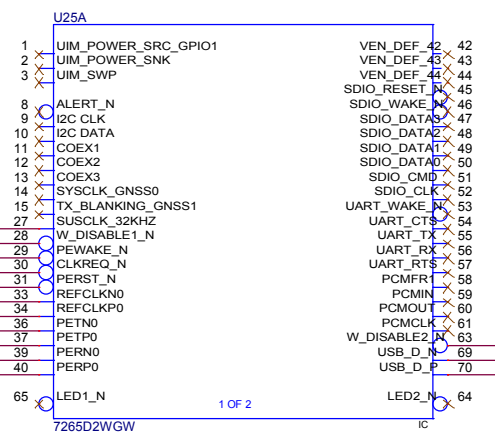
		Huaqin Telecom Technology Com.,Ltd.	
Page name:		Title	
Size: A4	Project Name: XM AIR 12.5 INDIA	REV: V1.0	
Date: Friday, April 14, 2017	Sheet: 28		of 44



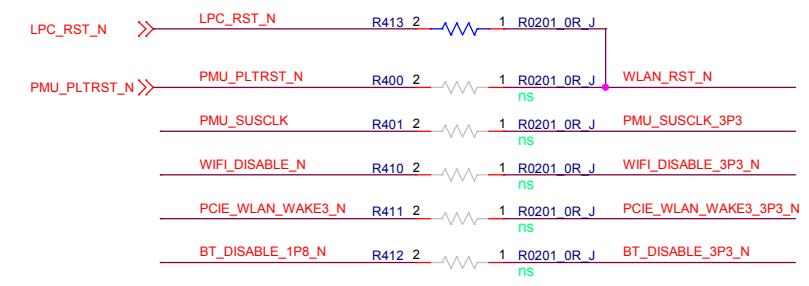
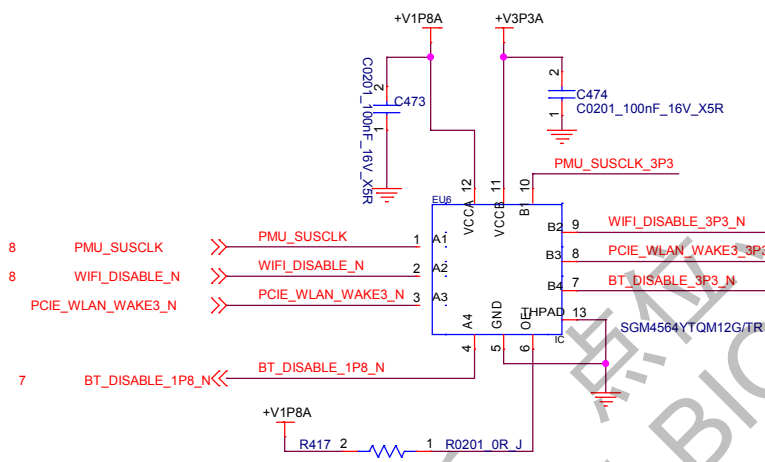
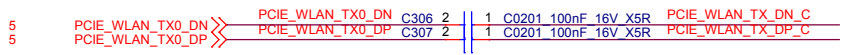
+V3P3SX 21,23,25,32,33,43,44  
 PVT 0304: Delete +V3P3X\_CWS  
 +V1P8A 5,7,8,11,12,20,22,27,29,32,34,37,38,39,43  
 +V3P3A 11,20,22,27,28,29,32,33,34,39



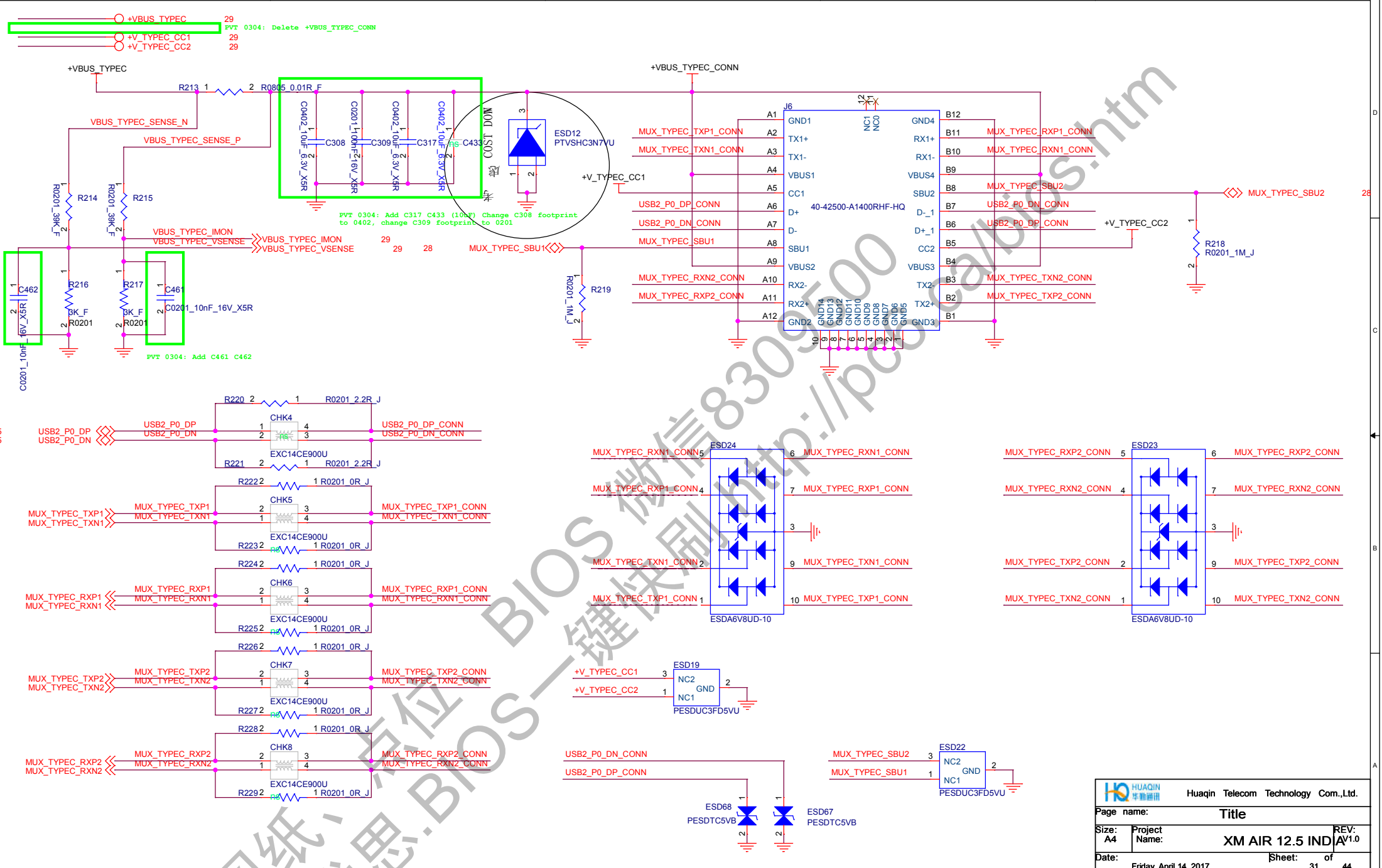
THIS DESIGN SUPPORTS STONE PEAK ONLY




是不是需要加Level shift变



		Huaqin Telecom Technology Com.,Ltd.	
Page name:		Title	
Size: A4	Project Name:	XM AIR 12.5 INDIA	REV: V1.0
Date:	Friday, April 14, 2017	Sheet:	30 of 44



 <b>Huaqin Telecom Technology Com.,Ltd.</b>	
Page name:	Title
Size: A4	Project Name: XM AIR 12.5 IND
Date: Friday, April 14, 2017	REV: AV1.0
Sheet: 31	of 44

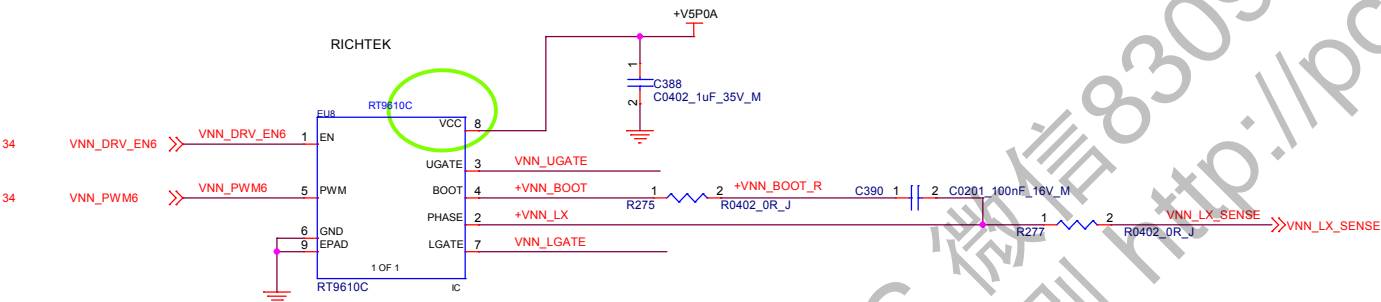
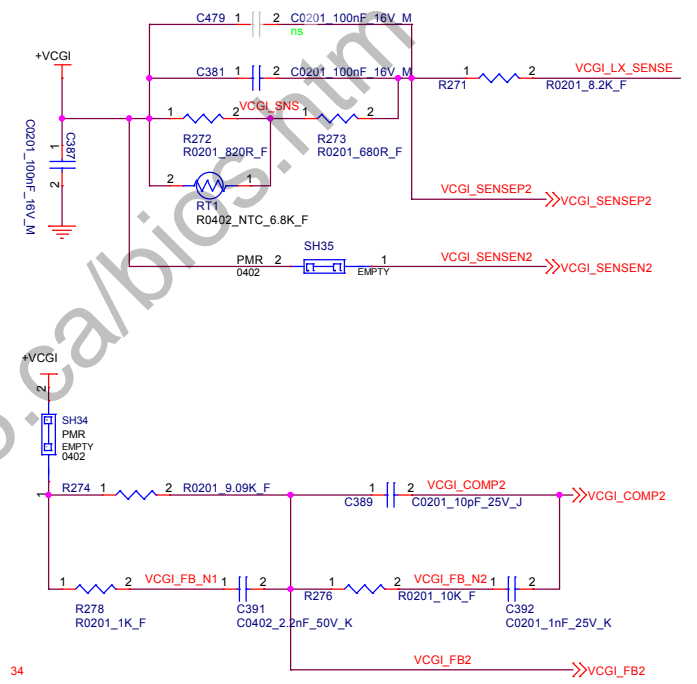
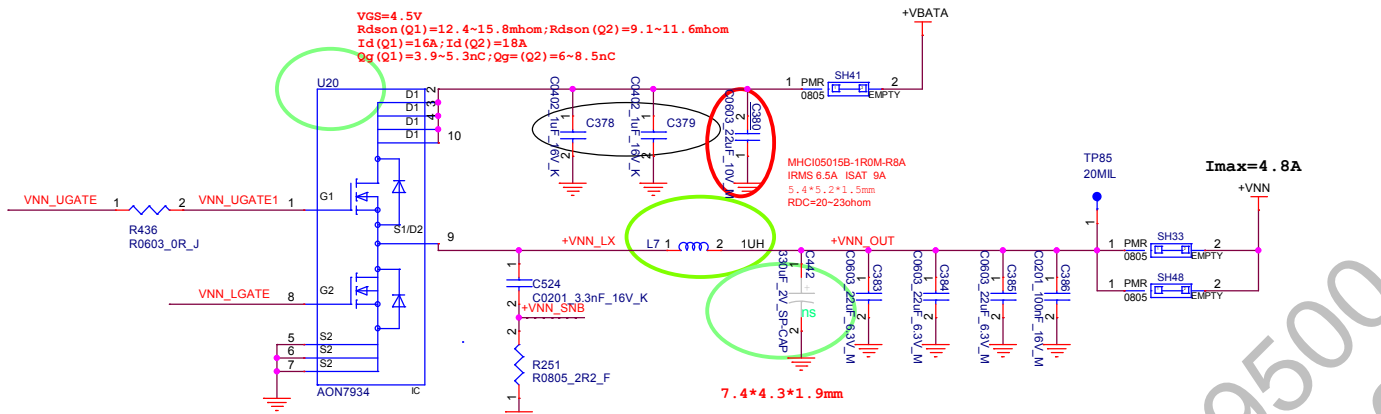




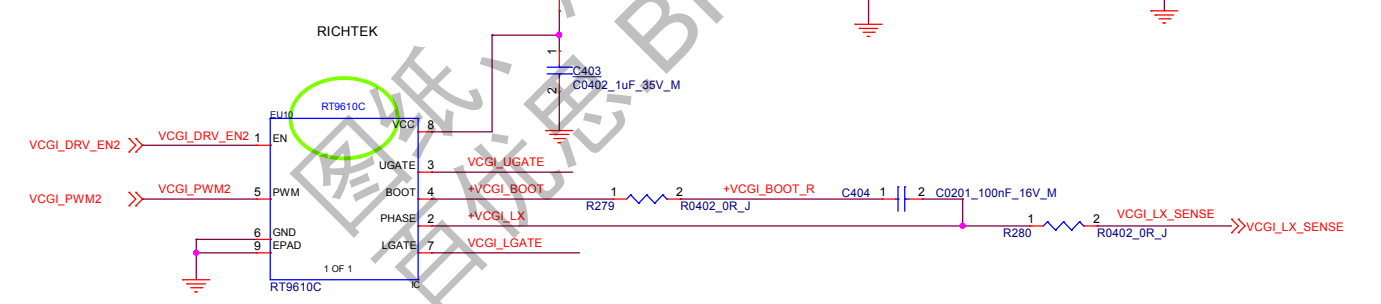
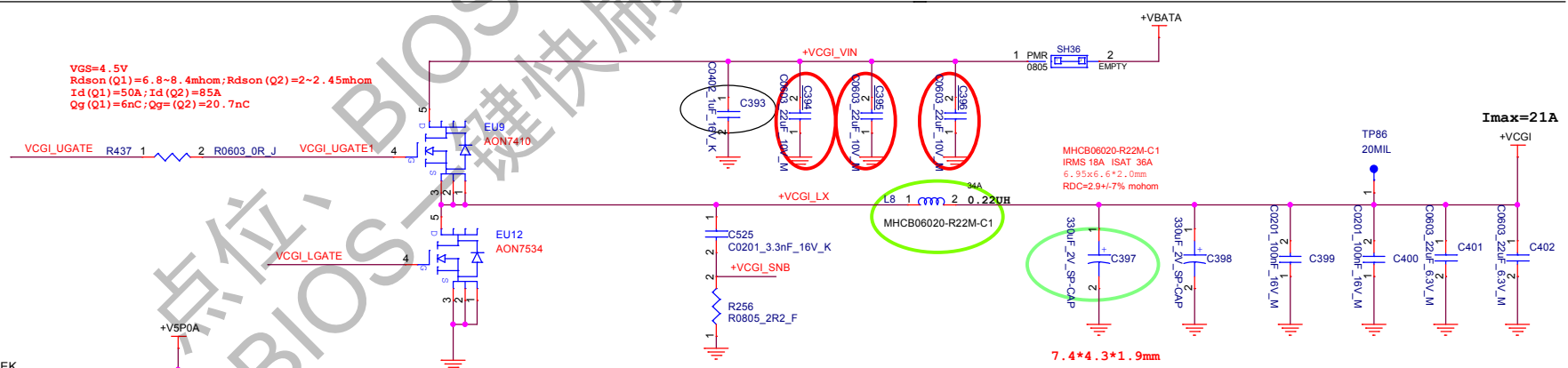


+VBATA 26,32,33,34,36,39  
 +VNN 9,11,34  
 +VSP0A 24,25,29,33,34,36,37  
 +VCGI 9,11

**VGS=4.5V**  
**Rdson (Q1)=12.4~15.8mohm; Rdson (Q2)=9.1~11.6mohm**  
**Id (Q1)=16A; Id (Q2)=18A**  
**Qg (Q1)=3.9~5.3nC; Qg (Q2)=6~8.5nC**



**VGS=4.5V**  
**Rdson (Q1)=6.8~8.4mohm; Rdson (Q2)=2~2.45mohm**  
**Id (Q1)=50A; Id (Q2)=85A**  
**Qg (Q1)=6nC; Qg (Q2)=20.7nC**

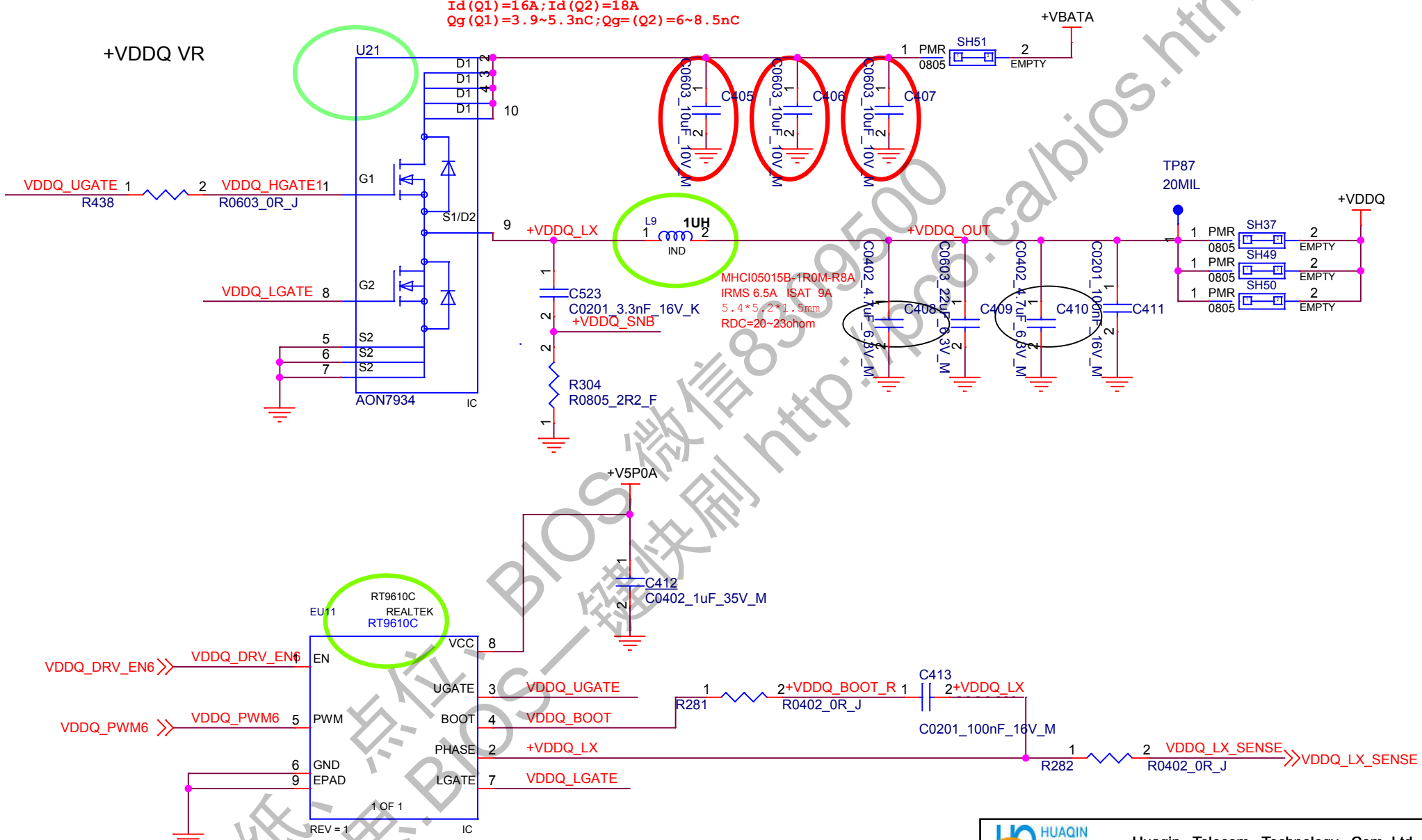


○ +VBATA  
 ○ +VDDQ  
 ○ +V5P0A

26,32,33,34,35,39  
 9,11,13,14,15,16,19,27,34  
 V<sub>GS</sub>=4.5V  
 24,25,29,33,34,35,37

$R_{dson}(Q1)=12.4\sim 15.8m\Omega$ ;  $R_{dson}(Q2)=9.1\sim 11.6m\Omega$   
 $I_d(Q1)=16A$ ;  $I_d(Q2)=18A$   
 $Q_g(Q1)=3.9\sim 5.3nC$ ;  $Q_g(Q2)=6\sim 8.5nC$

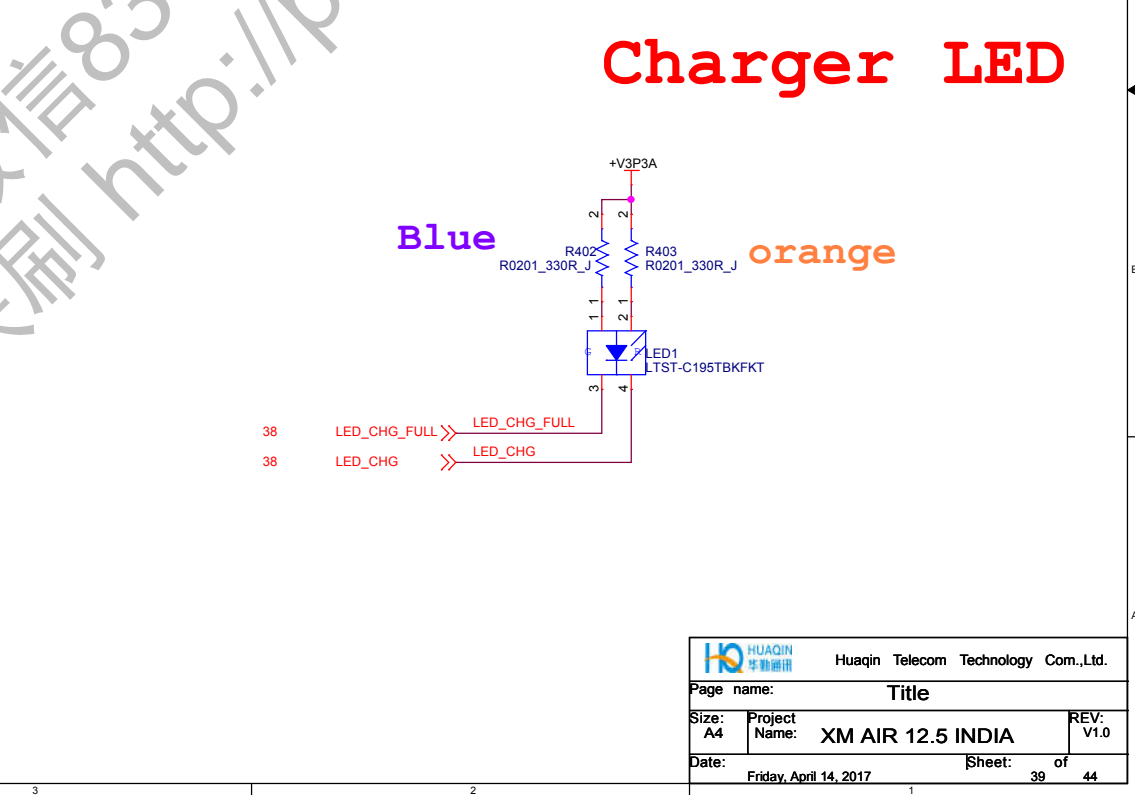
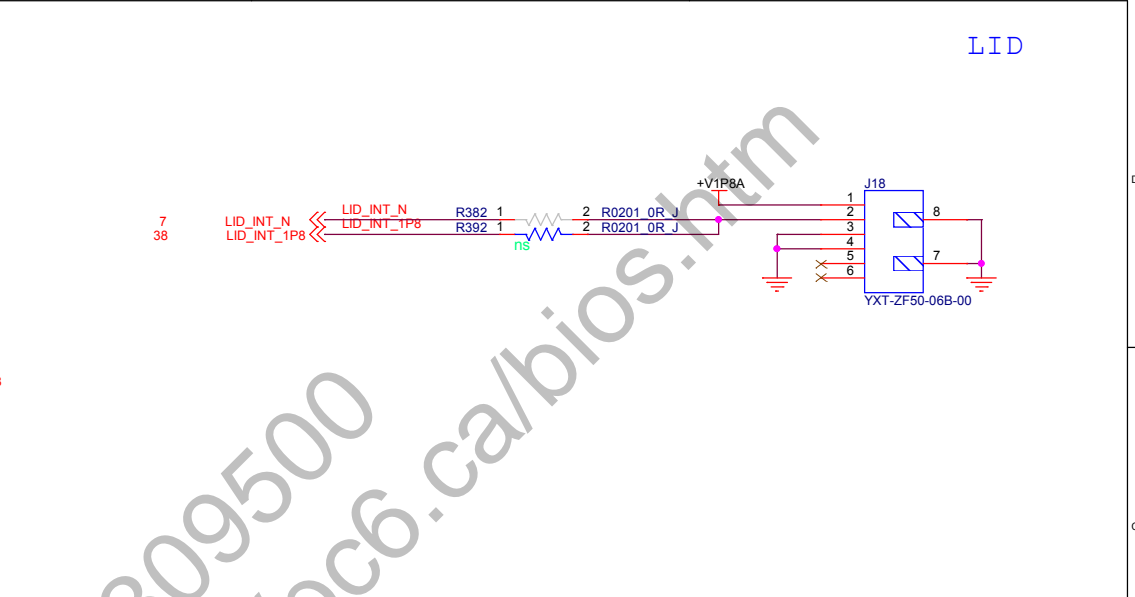
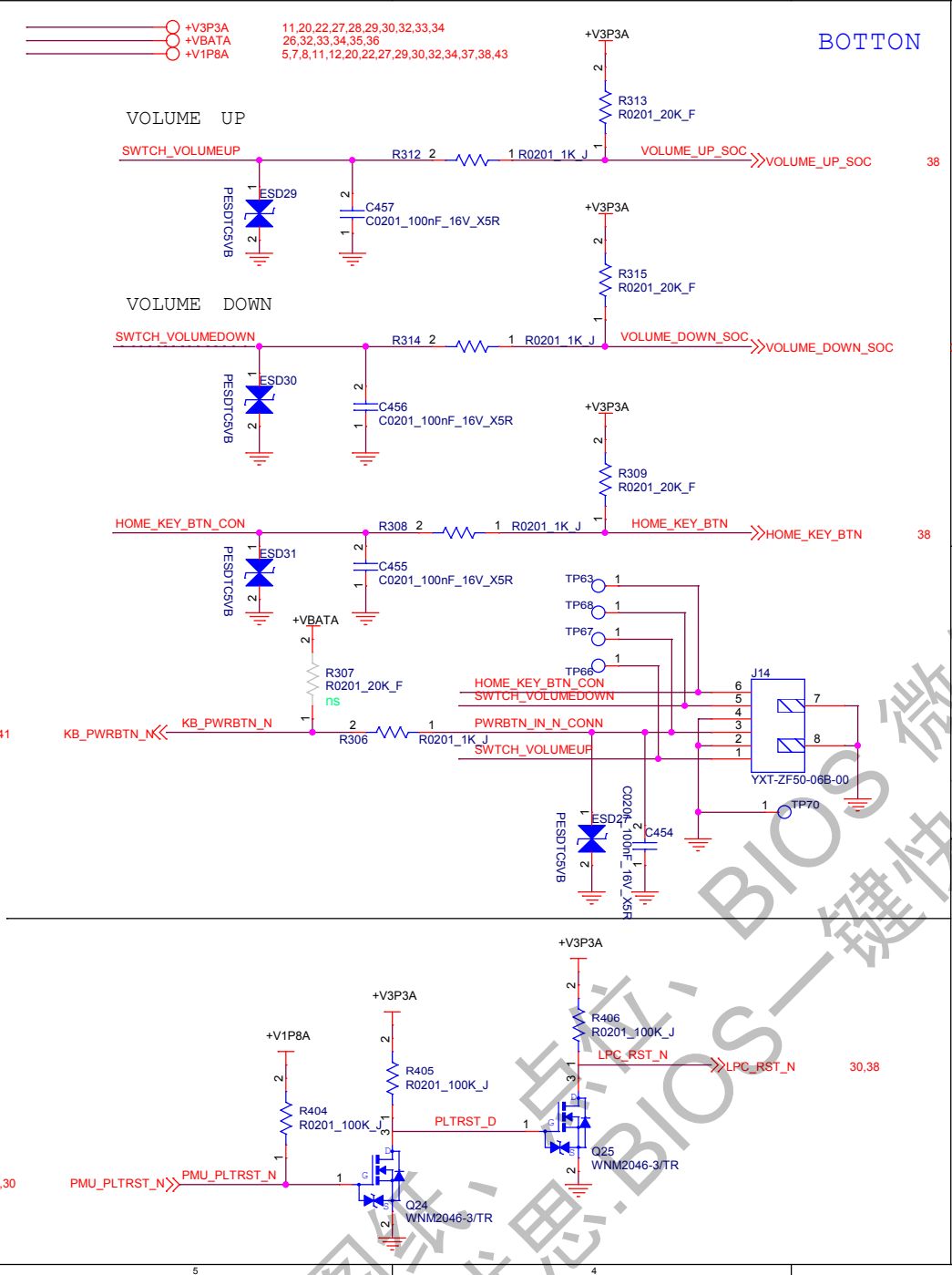
+VDDQ VR



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Page name:		Title	
Size: A4	Project Name:	XM AIR 12.5 INDIA	REV: V1.0
Date:	Friday, April 14, 2017	Sheet:	36 of 44

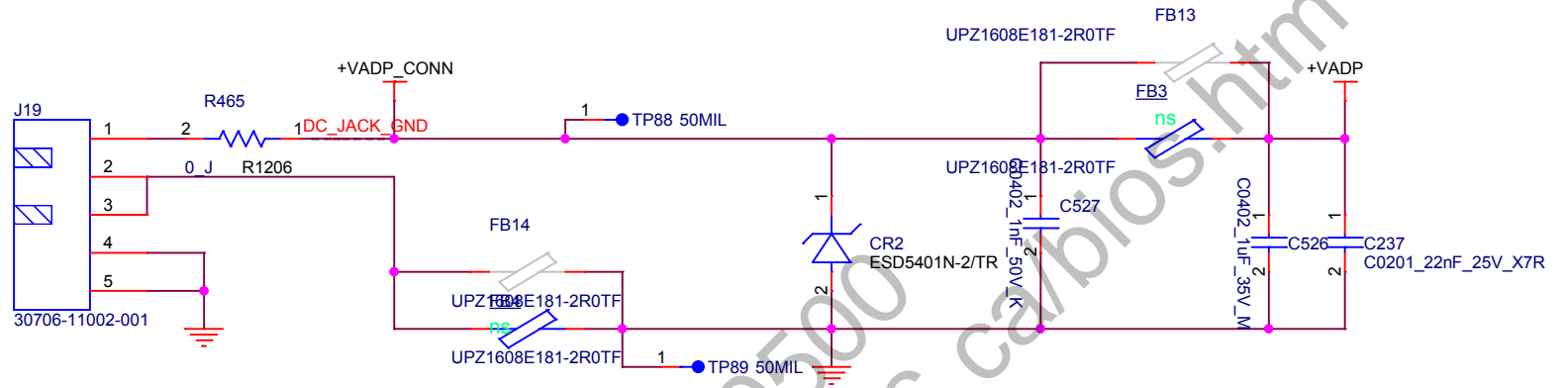




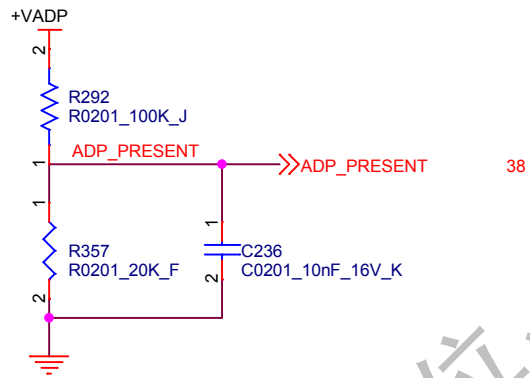



		Huaqin Telecom Technology Com.,Ltd.	
Page name:		Title	
Size: A4	Project Name:	XM AIR 12.5 INDIA	REV: V1.0
Date:	Friday, April 14, 2017	Sheet: 39	of 44

PVT 0304: Delete +VADP\_CONN  
+VADP 26

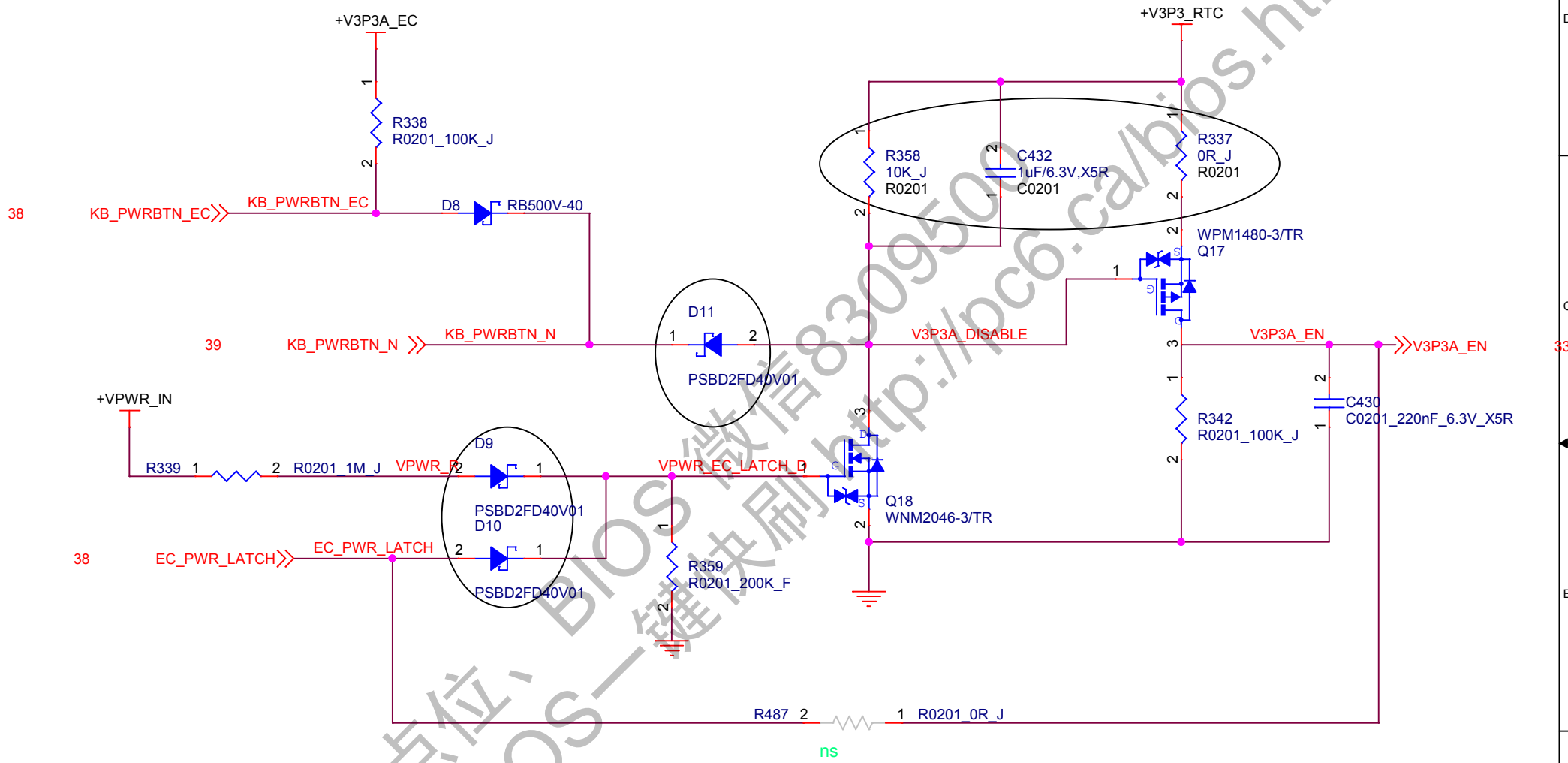



CAD NOTE: THICK COPPER POUR BETWEEN TP2501 AND FB2500




 HUAQIN 华勤通讯		Huaqin Telecom Technology Com.,Ltd.	
Page name:		Title	
Size: A4	Project Name: XM AIR 12.5 INDIA	REV: V1.0	
Date: Friday, April 14, 2017	Sheet: 40		of 44

- +V3P3\_RTC 8,33,38
- +V3P3A 11,20,22,27,28,29,30,32,33,34,39
- +VPWR\_IN 26
- +V3P3A\_EC 26,38

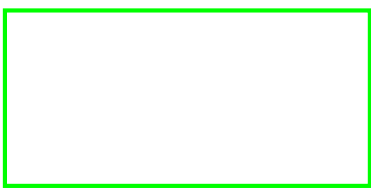


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Page name:		Title	
Size: A4	Project Name: XM AIR 12.5 INDIA	REV: V1.0	
Date: Friday, April 14, 2017	Sheet: 41		of 44

PVT 0304: Delete Reserve VCONN

 HUAQIN 华勤通讯		Huaqin Telecom Technology Com.,Ltd.	
Page name:		Title	
Size: A4	Project Name:	XM AIR 12.5 INDIA	REV: V1.0
Date:	Friday, April 14, 2017		Sheet: 42 of 44

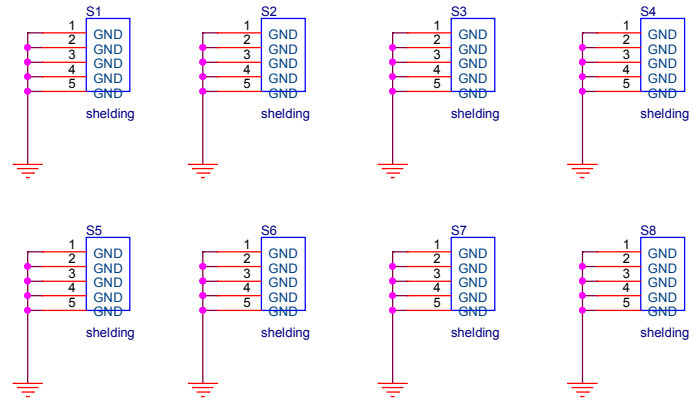
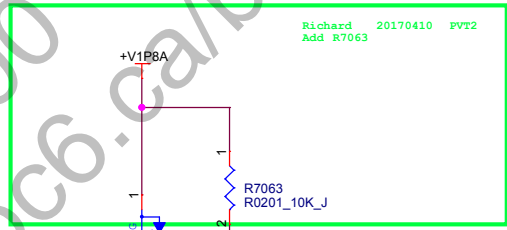
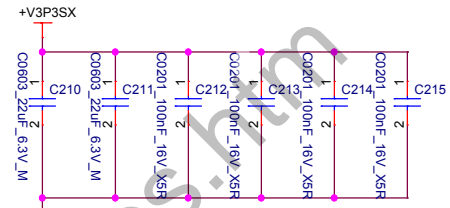
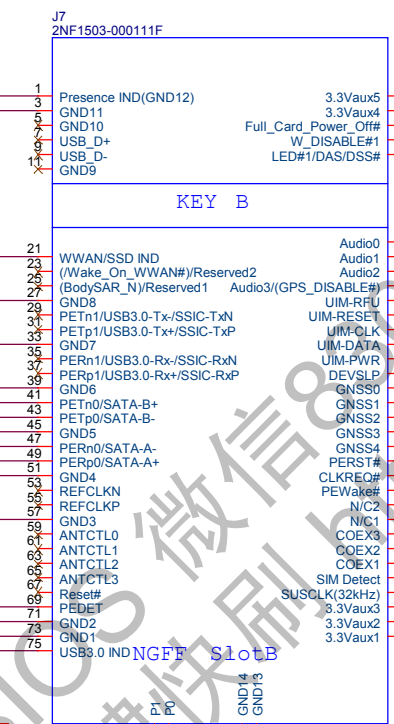
+V3P3SX 21,23,25,30,32,33,44  
 +V1P8A 5,7,8,11,12,20,22,27,29,30,32,34,37,38,39



PVT2 0327: Delete ESD9

PLACE THESE AC CAPS CLOSE TO M.2 CONN

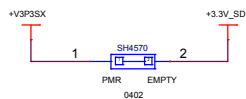
5 SATA\_RXP0\_SSD\_DP << SATA\_RXP0\_SSD\_DP C216 1  
 5 SATA\_RXN0\_SSD\_DN << SATA\_RXN0\_SSD\_DN C217 1  
 5 SATA\_TXN0\_SSD\_DN << SATA\_TXN0\_SSD\_DN C218 1  
 5 SATA\_TXP0\_SSD\_DP << SATA\_TXP0\_SSD\_DP C219 1



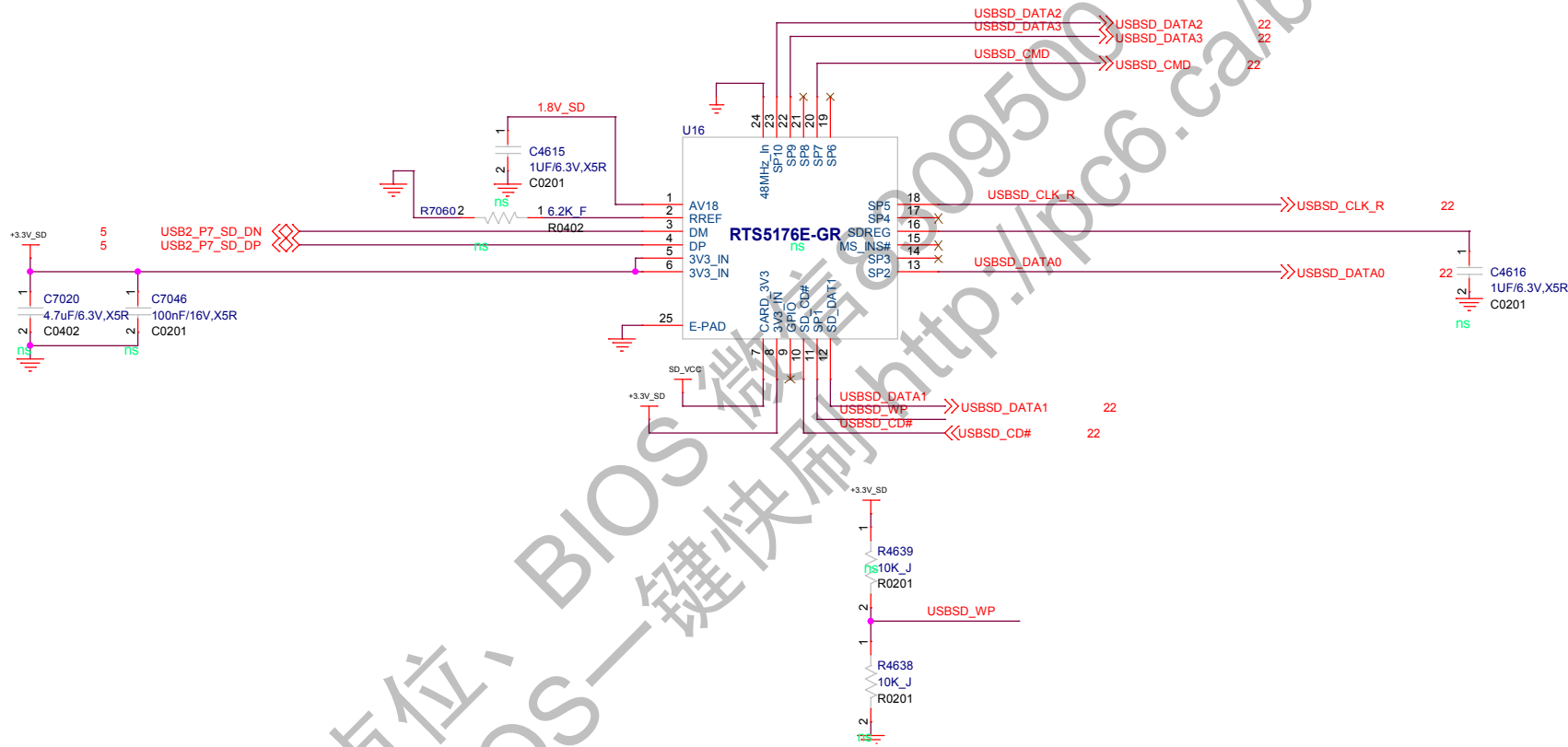
图纸、点位、百优思.BIOS一鍵快刷

		Huaqin Telecom Technology Com.,Ltd.	
Page name:		Title	
Size: A4	Project Name:	XM AIR 12.5 INDIA	REV: V1.0
Date:	Friday, April 14, 2017	Sheet: 43	of 44

○ +V3P3SX 21 23,25,30,32,33,43  
 ○ SD\_VCC 22



FVT 0303: Add USB Card



 Huaqin Telecom Technology Com.,Ltd.	
Page name:	Title
Size: A4	Project Name: XM AIR 12.5 INDIA
Date: Friday, April 14, 2017	REV: V1.0
Sheet: 44	of 44