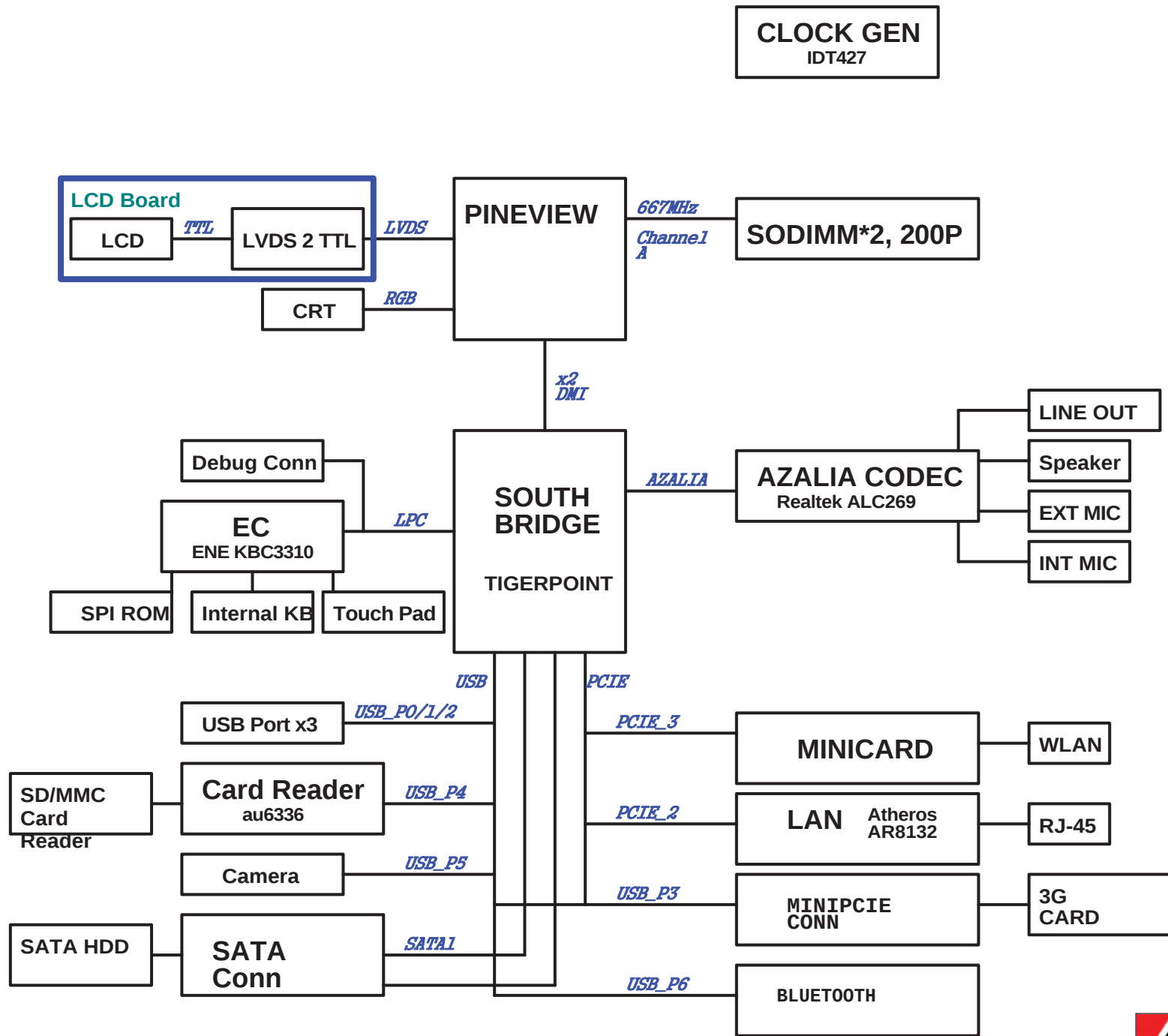
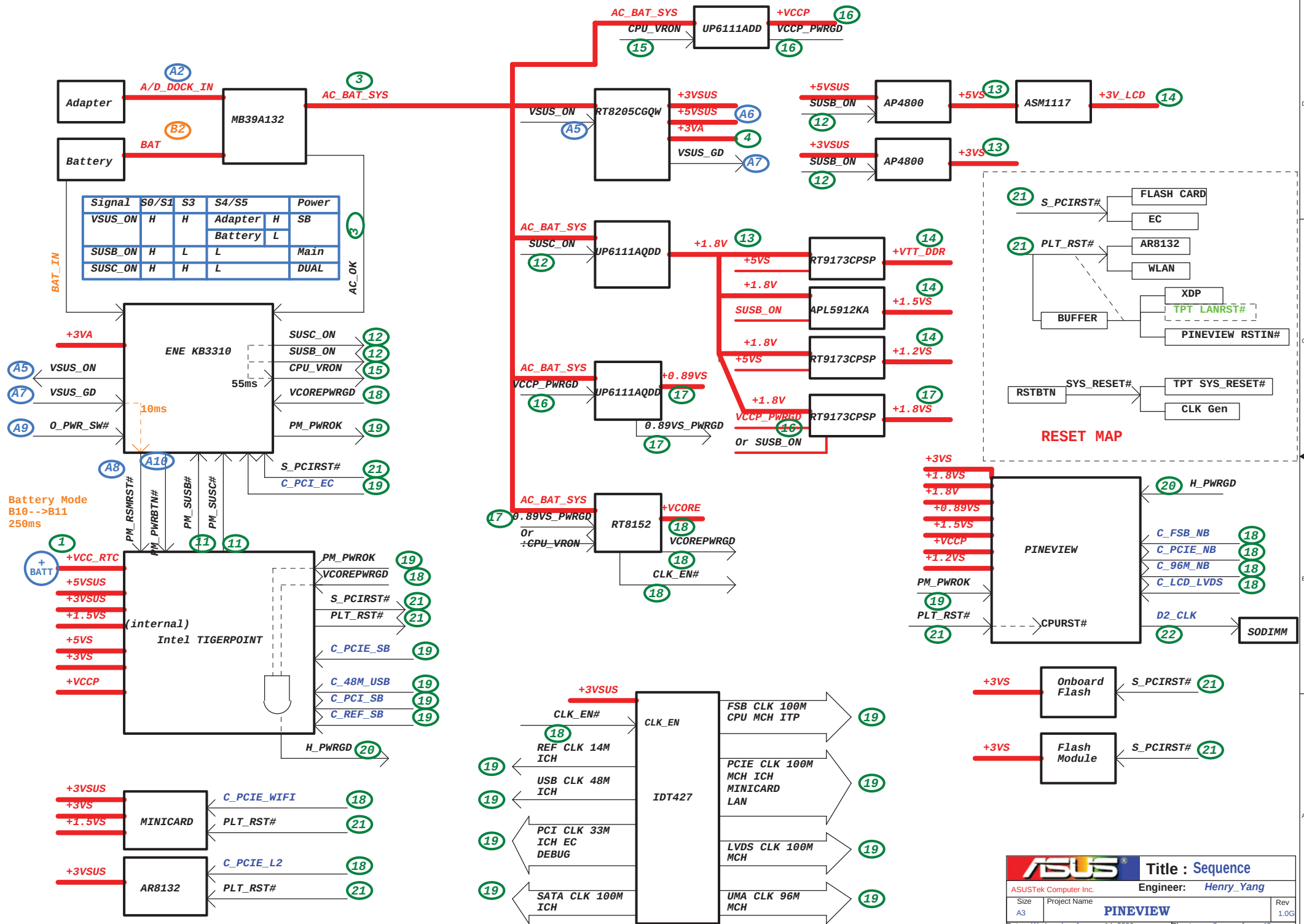


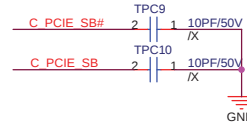


ICH7M GPIO DEFAULT&NOW
SETTING

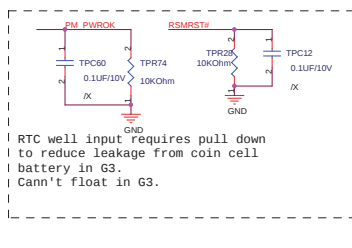
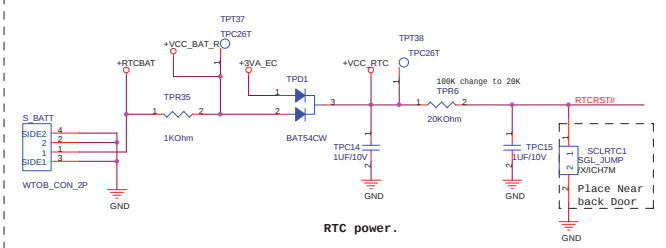
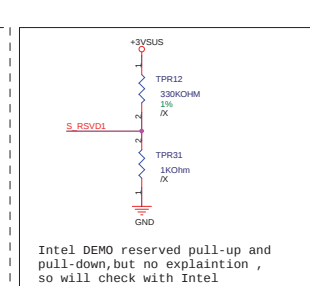
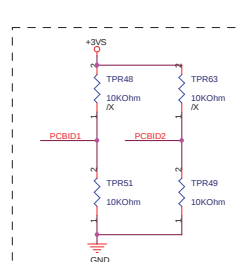
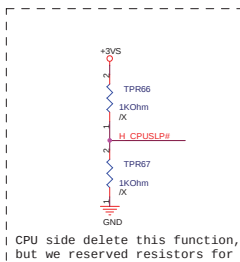
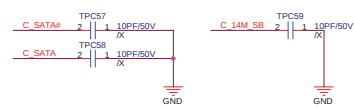
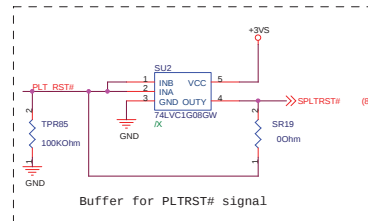
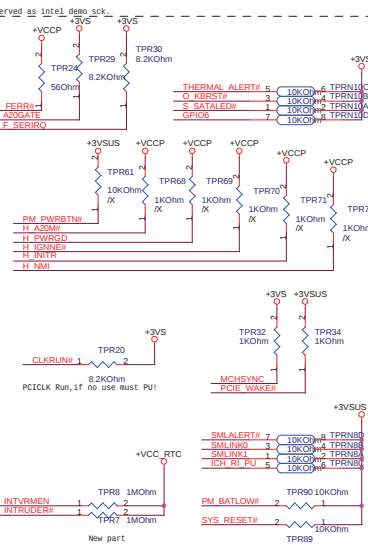
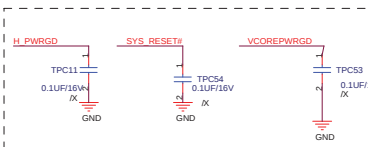
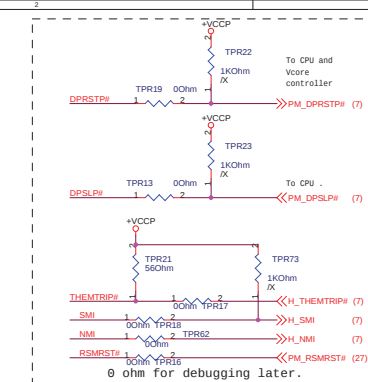
Pin	Pin Name	Type	Tolerance	Powe Well	Default	Now Setting
AB18	GPIO0/BM_BUSY#	I/O	3.3V	CORE	GPI	BM_BUSY#
C8	GPIO1/REQ5#	I/O	5V	CORE	GPI	REQ5#
G8	GPIO2/PIRQE#	I/OD	5V	CORE	GPI	PIRQE#
F7	GPIO3/PIRQF#	I/OD	5V	CORE	GPI	PIRQF#
F8	GPIO4/PIRQG#	I/OD	5V	CORE	GPI	PIRQG#
G7	GPIO5/PIRQH#	I/OD	5V	CORE	GPI	PIRQH#
AC21	GPIO6	I/O	3.3V	CORE	GPI	GPI,No Function,10K Pull +3VS
AC18	GPIO7	I/O	3.3V	CORE	GPI	GPO,WLAN_LED
E21	GPIO8	I/O	3.3V	Resume	GPI	GPI,EXTSMI#
E20	GPIO9	I/O	3.3V	Resume	GPI	GPI,No Function,10K Pull +3VS
A20	GPIO10	I/O	3.3V	Resume	GPI	GPO,WLAN_ON#
B23	GPIO11/SMBALERT#	I/O	3.3V	Resume	Native	SMBALERT#
F19	GPIO12	I/O	3.3V	Resume	GPI	GPI,KBC_SCI#
E19	GPIO13	I/O	3.3V	Resume	GPI	GPO,VCCP_DOWN
R4	GPIO14	I/O	3.3V	Resume	GPI	GPO,1.5VS_DOWN
E22	GPIO15	I/O	3.3V	Resume	GPI	GPI,No Function,10K Pull +3VSUS
AC22	GPIO16/DPRSLVR	I/O	3.3V	CORE	Native	DPRSLVR
D8	GPIO17/GNT5#	I/O	3.3V	CORE	GPO	BIOS_SEL1
AC20	GPIO18/STPPCI#	I/O	3.3V	CORE	GPO	STP_PCI#
AH18	GPIO19/SATA1GP	I/O	3.3V	CORE	GPI	GPI,No Function,10K Pull +3VS
AF21	GPIO20/STPCPU#	I/O	3.3V	CORE	GPO	STP_CPU#
AF19	GPIO21/SATA0GP	I/O	3.3V	CORE	GPI	GPI,No Function,10K Pull +3VS
A13	GPIO22/REQ4#	I/O	3.3V	CORE	Native	REQ4#
AA5	GPIO23/LDRQ1#	I/O	3.3V	CORE	Native	LDRQ1#
R3	GPIO24	I/O	3.3V	Resume	GPO	GPO,MINICARD1_EN#
D20	GPIO25	I/O	3.3V	Resume	GPO	GPO,DUAL_DOWN

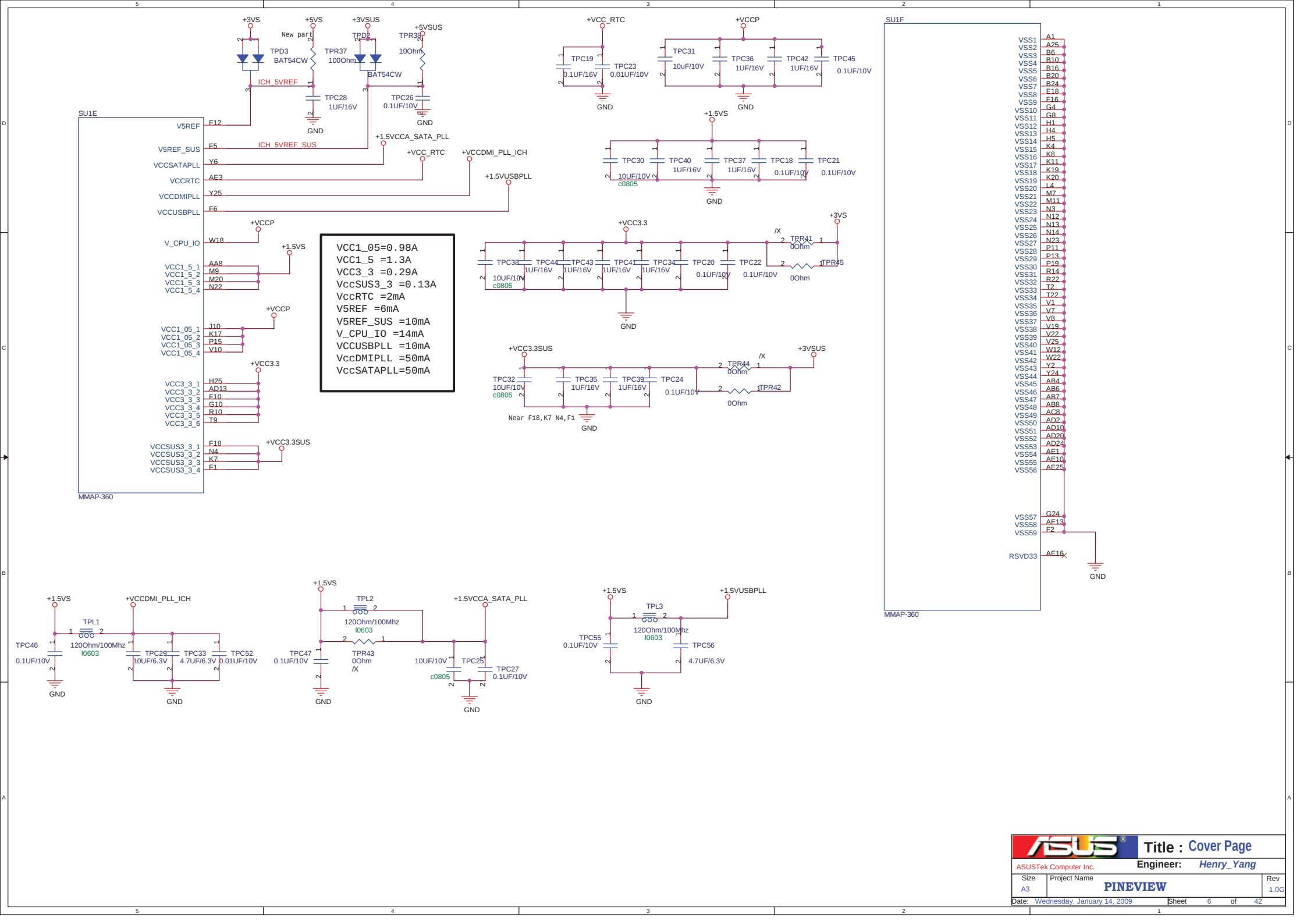
Pin	Pin Name	Type	Tolerance	Powe Well	Default	Now Setting
A21	GPIO26	I/O	3.3V	Resume	GPO	GPO,VCORE_DOWN
B21	GPIO27	I/O	3.3V	Resume	GPO	GPO,CARD_READER_EN#
E23	GPIO28	I/O	3.3V	Resume	GPO	GPO,MODEM_EN#
C3	GPIO29/OC5#	I/O	3.3V	Resume	Native	OC5#
A2	GPIO30/OC6#	I/O	3.3V	Resume	Native	OC6#
B3	GPIO31/OC7#	I/O	3.3V	Resume	Native	OC7#
AG18	GPIO32/CLKRUN#	I/O	3.3V	CORE	GPO	CLKRUN#
AC19	GPIO33/AZ_DOCK_EN#	I/O	3.3V	CORE	GPO	GPO,No Function,NC
U2	GPIO34/AZ_DOCK_RST#	I/O	3.3V	CORE	GPO	GPO,No Function,NC
AD21	GPIO35	I/O	3.3V	CORE	GPO	GPO,CAMERA_EN
AH19	GPIO36/SATA2GP	I/O	3.3V	CORE	GPI	GPI,No Function,10K Pull +3VS
AE19	GPIO37/SATA3GP	I/O	3.3V	CORE	GPI	GPI,PCB_ID0
AE20	GPIO38	I/O	3.3V	CORE	GPI	GPI,PCB_ID1
AD20	GPIO39	I/O	3.3V	CORE	GPI	GPI,PCB_ID2
NA	GPIO40	NA	NA	NA	NA	NA
NA	GPIO41	NA	NA	NA	NA	NA
NA	GPIO42	NA	NA	NA	NA	NA
NA	GPIO43	NA	NA	NA	NA	NA
NA	GPIO44	NA	NA	NA	NA	NA
NA	GPIO45	NA	NA	NA	NA	NA
NA	GPIO46	NA	NA	NA	NA	NA
NA	GPIO47	NA	NA	NA	NA	NA
A14	GPIO48/GNT4#	I/O	3.3V	CORE	Native	BIOS_SEL0
AG24	GPIO49/CPUPWRGD	I/O	V_CPU_IO	V_CPU_IO	Native	CPUPWRGD

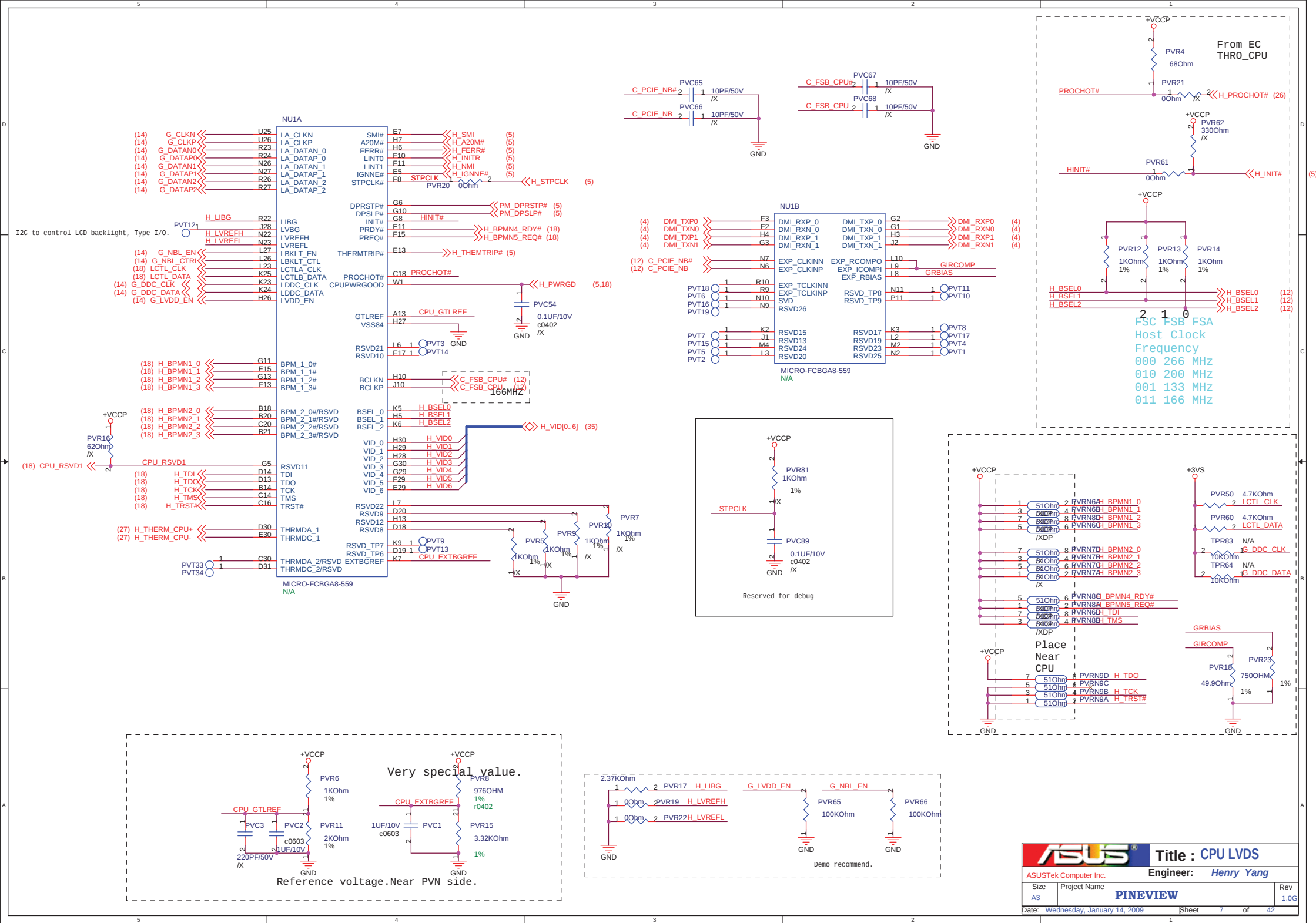


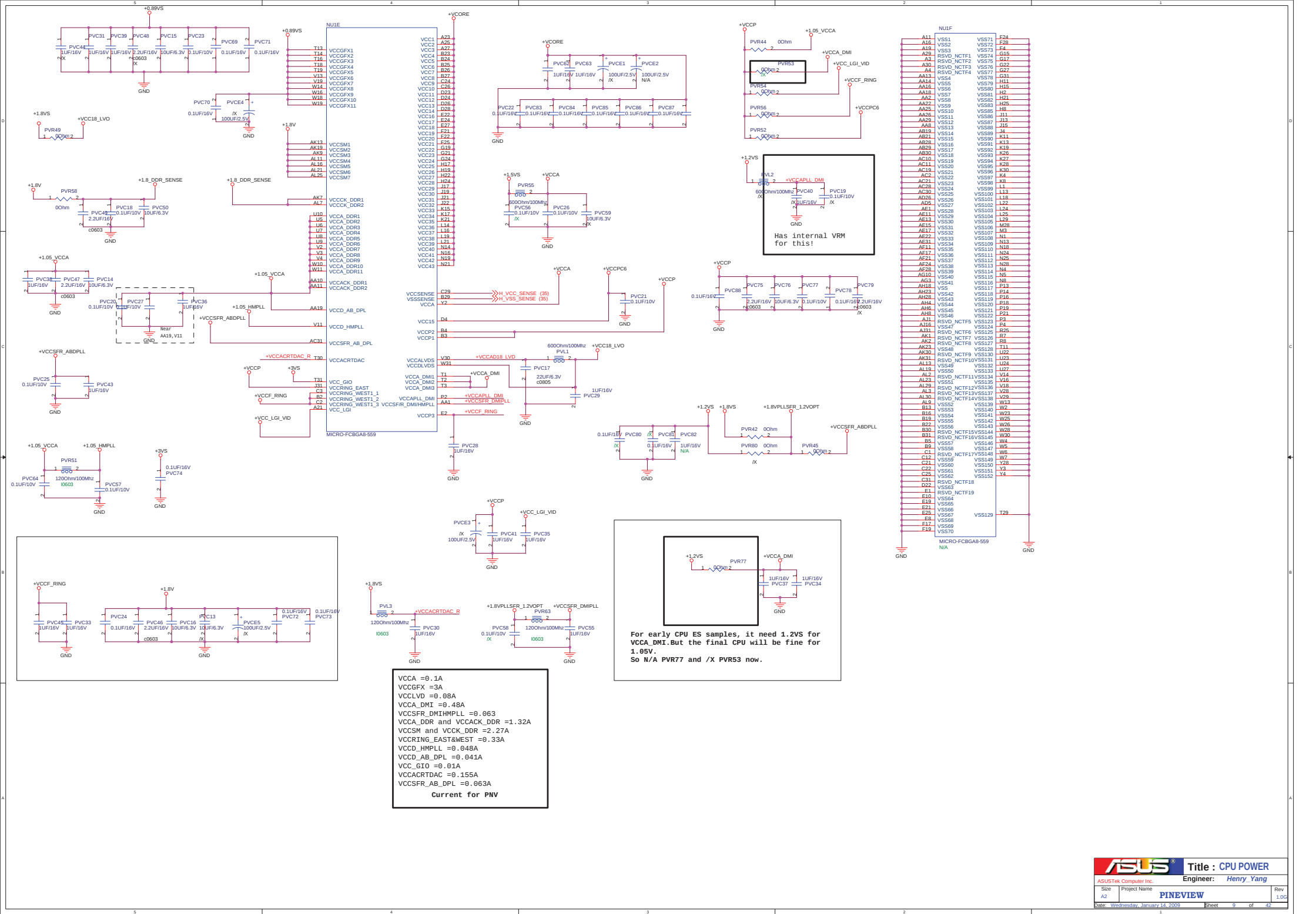


SATA RX,TX all need AC couple and place near Connector side for signal quality.And Traces to them should match length. ICH7M need pull down RX, if no use.



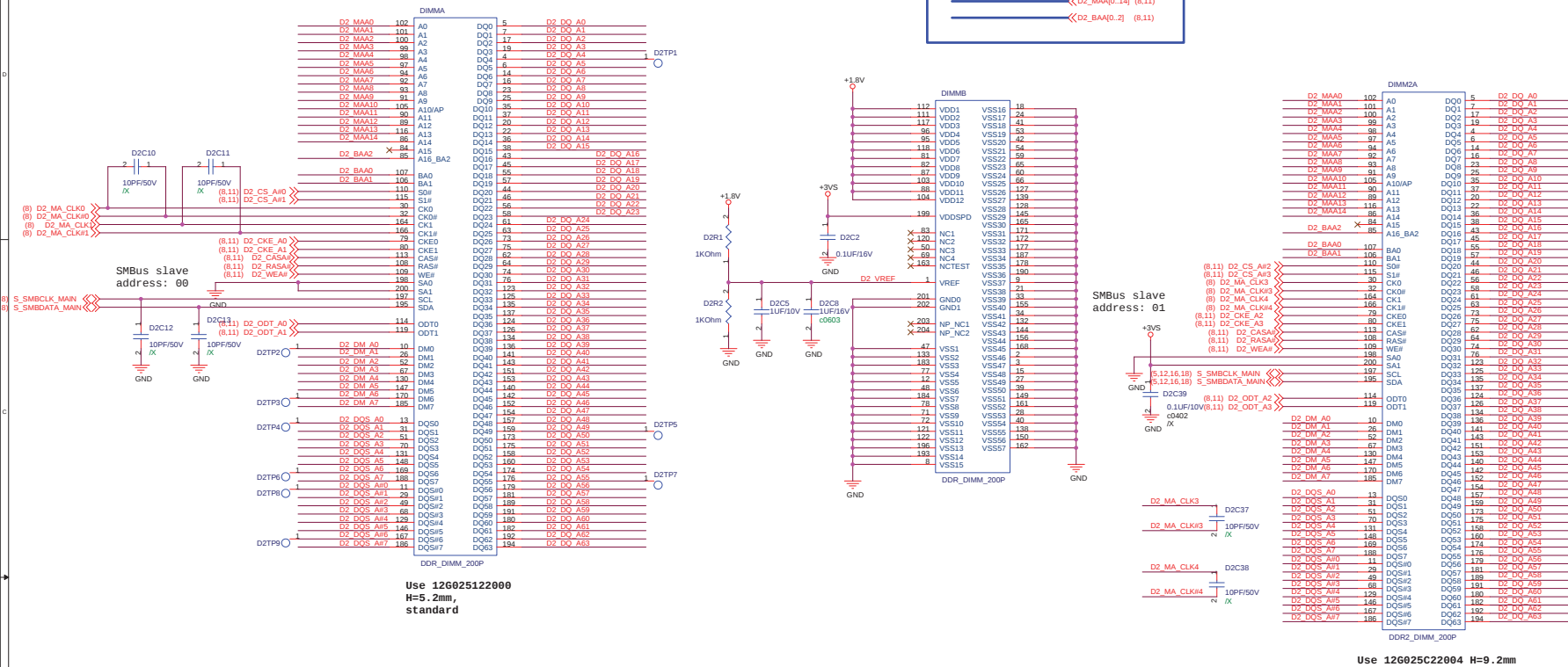


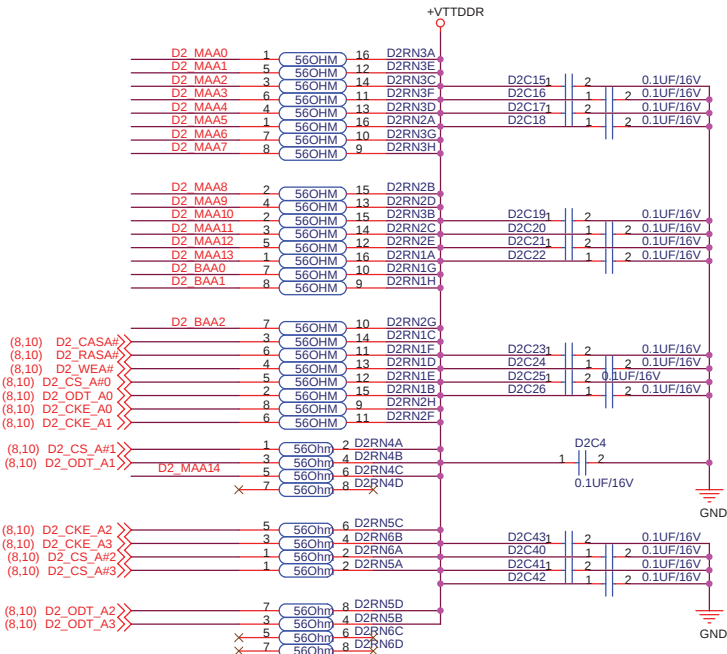
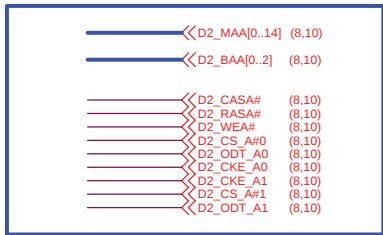


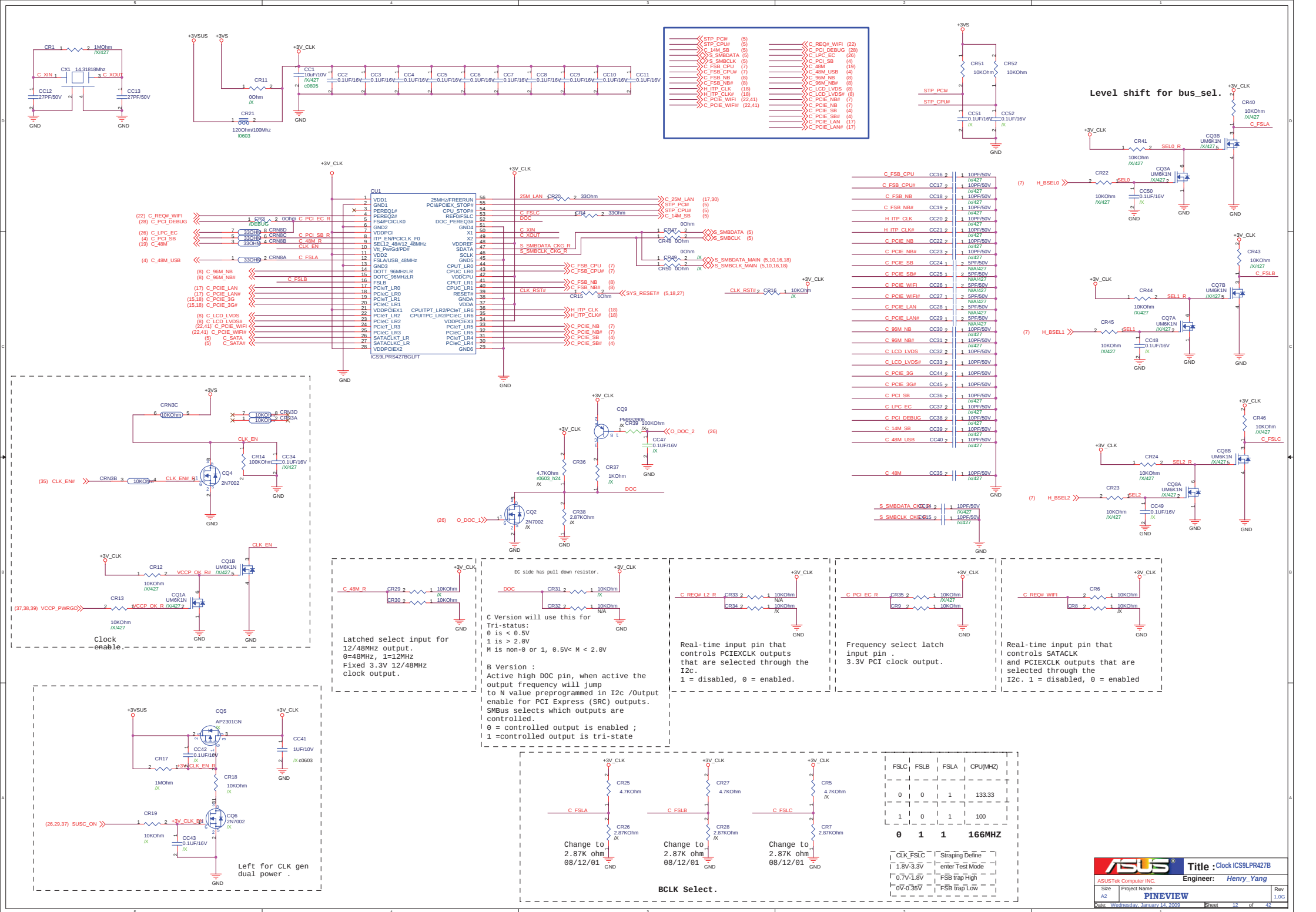


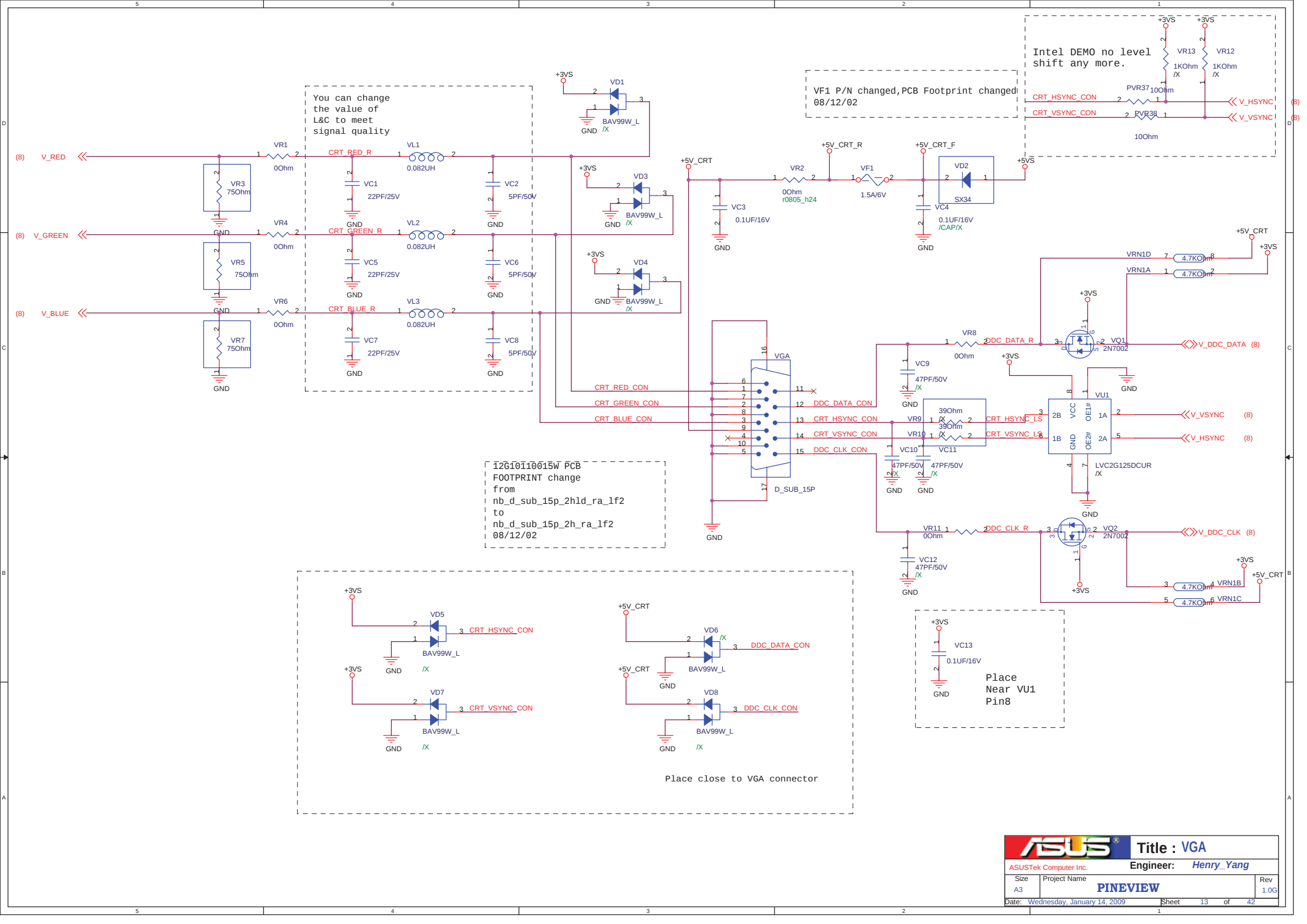
0.1B Beta

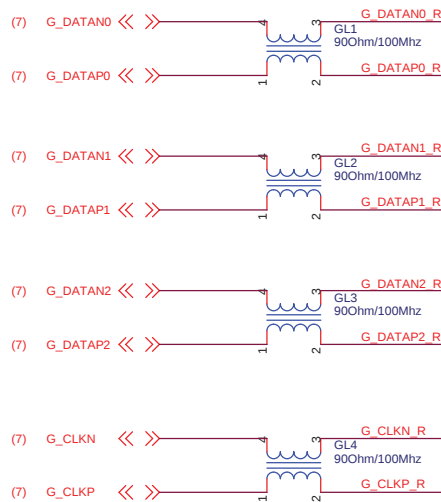
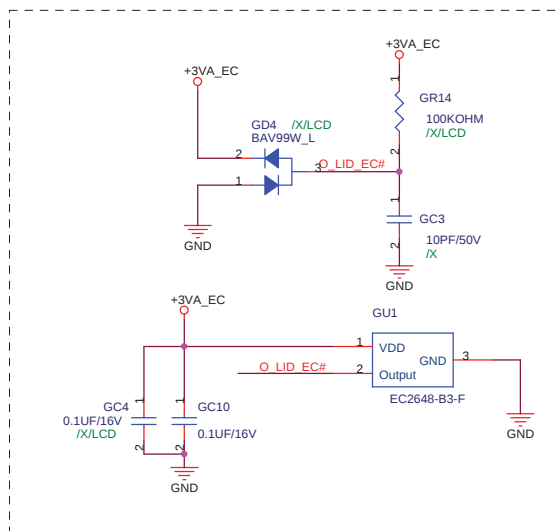
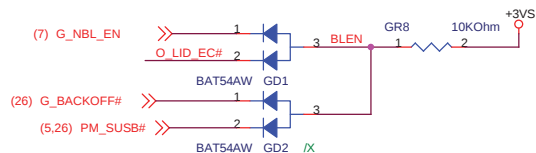
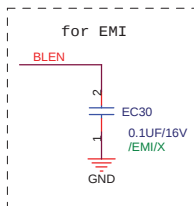
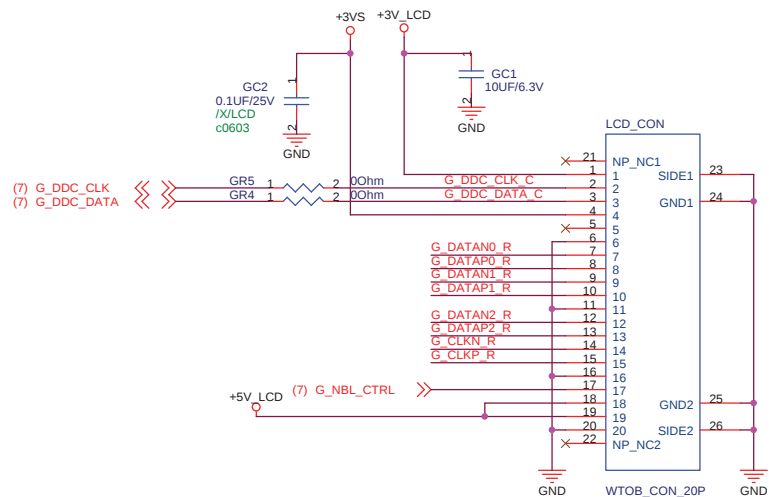
- ⌘ D2_DQ_A[0..63] (R)
- ⌘ D2_DQS_A[0..7] (R)
- ⌘ D2_DQS_A[0..7] (R)
- ⌘ D2_DM_A[0..7] (R)
- ⌘ D2_MAA[0..14] (R.11)
- ⌘ D2_BAA[0..2] (R.11)



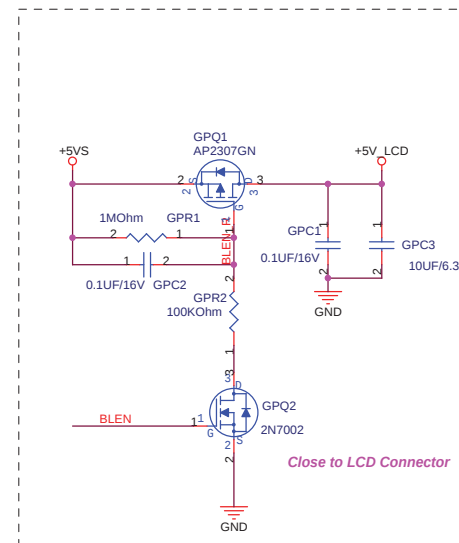






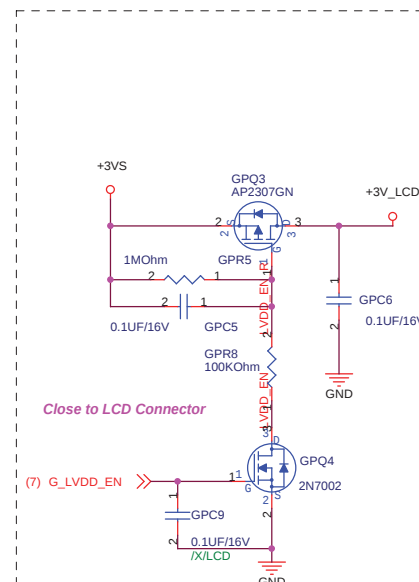


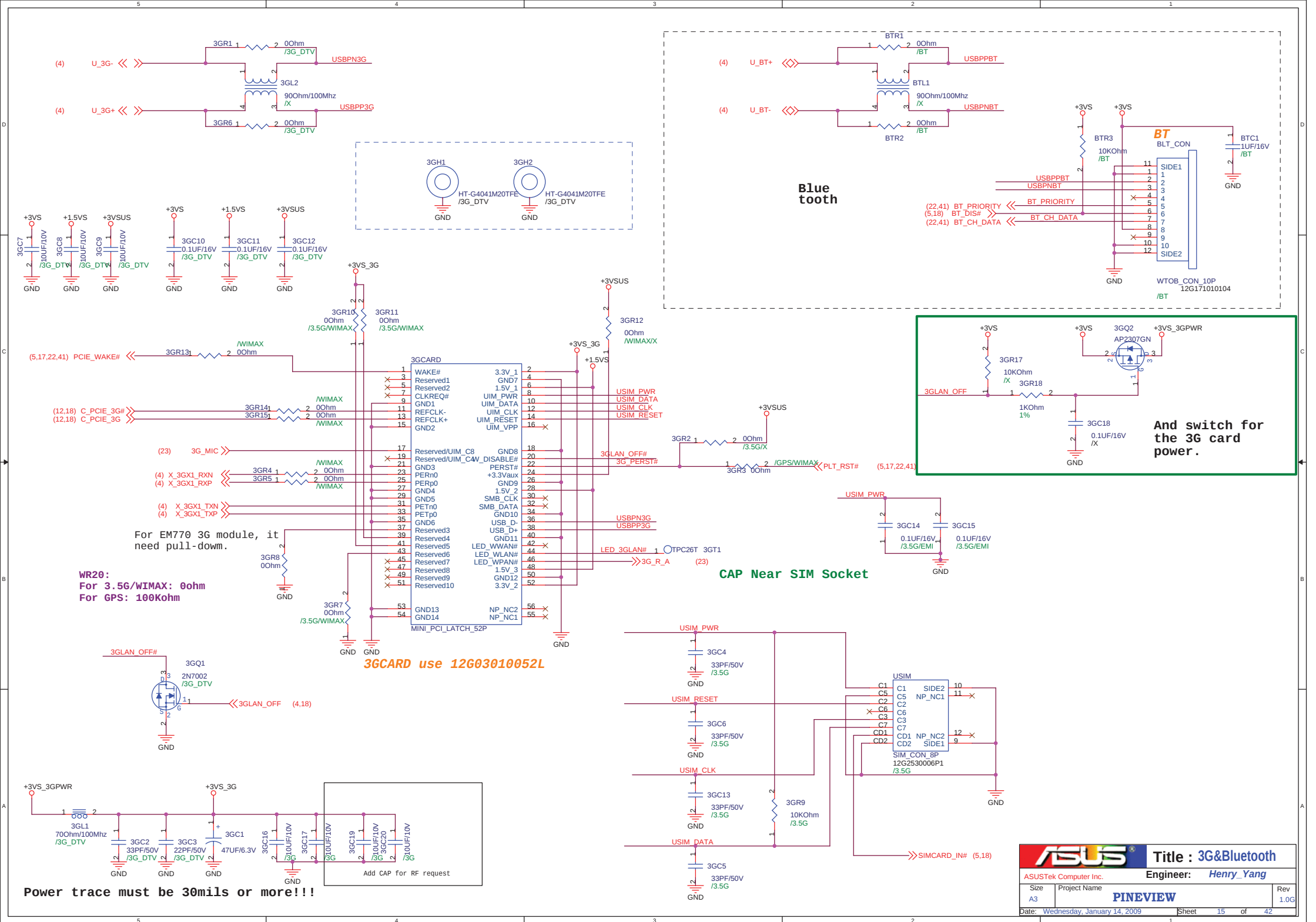
Add for RF request.



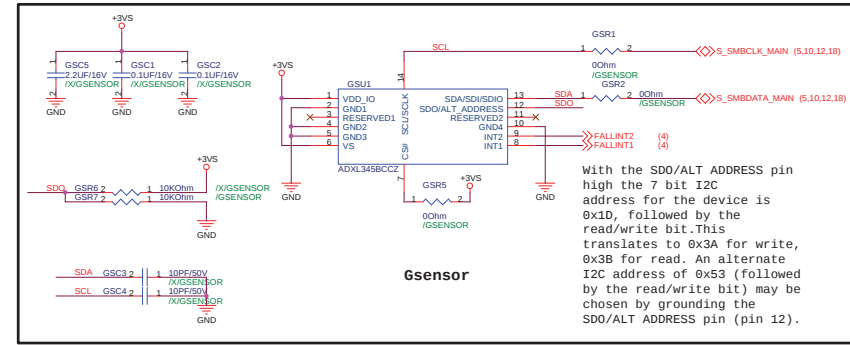
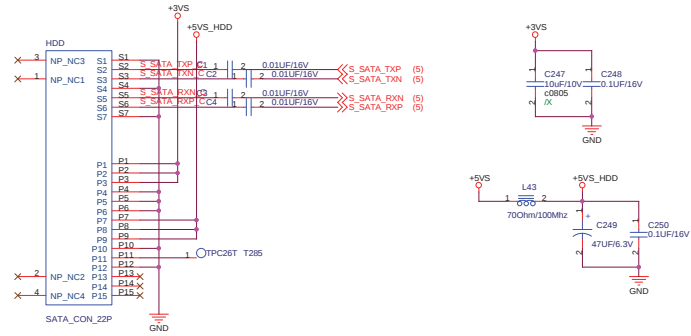
0.1 B Beta

- (7) G_NBL_CTRL
- (7) G_CLKP
- (7) G_CLKN
- (7) G_DATAP2
- (7) G_DATAN2
- (7) G_DATA0
- (7) G_DATA1
- (7) G_DATA2
- (7) G_CLKP_R
- (7) G_CLKN_R
- (7) G_DDC_DATA
- (7) G_DDC_CLK
- (7) G_NBL_EN
- (26) G_BACKOFF#
- (26,27) O_LID_EC#

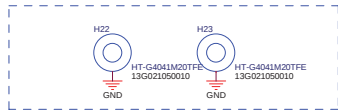




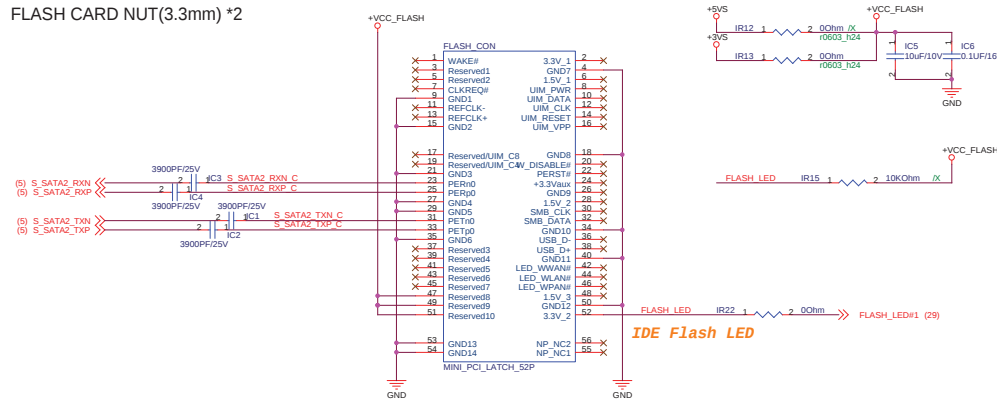
SATA HDD Connector



With the SDO/ALT ADDRESS pin high the 7 bit I2C address for the device is 0x10, followed by the read/write bit. This translates to 0x3A for write, 0x3B for read. An alternate I2C address of 0x53 (followed by the read/write bit) may be chosen by grounding the SDO/ALT ADDRESS pin (pin 12).

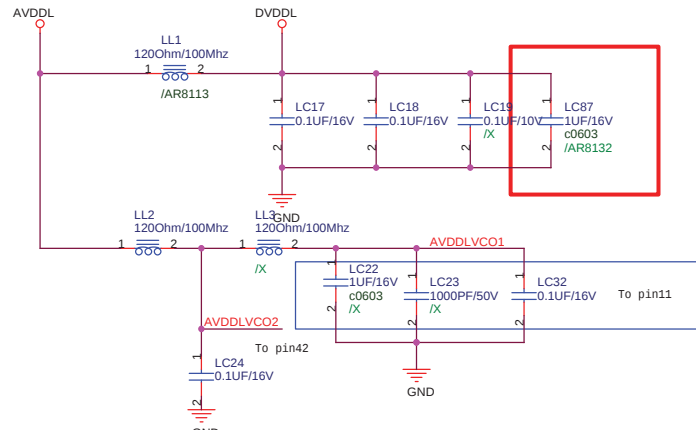
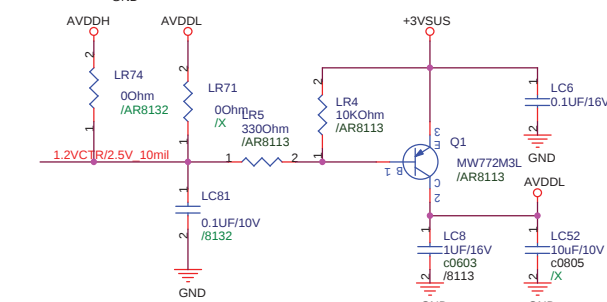
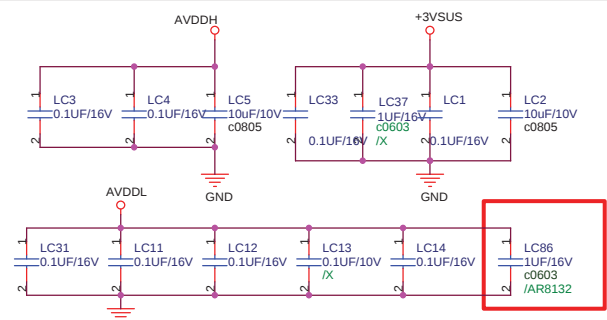


FLASH CARD NUT(3.3mm) *2

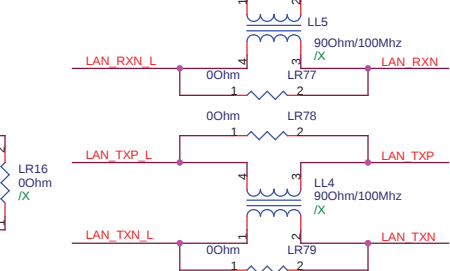
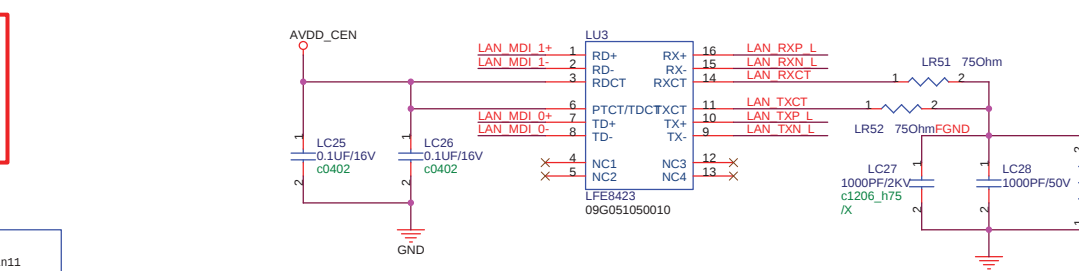
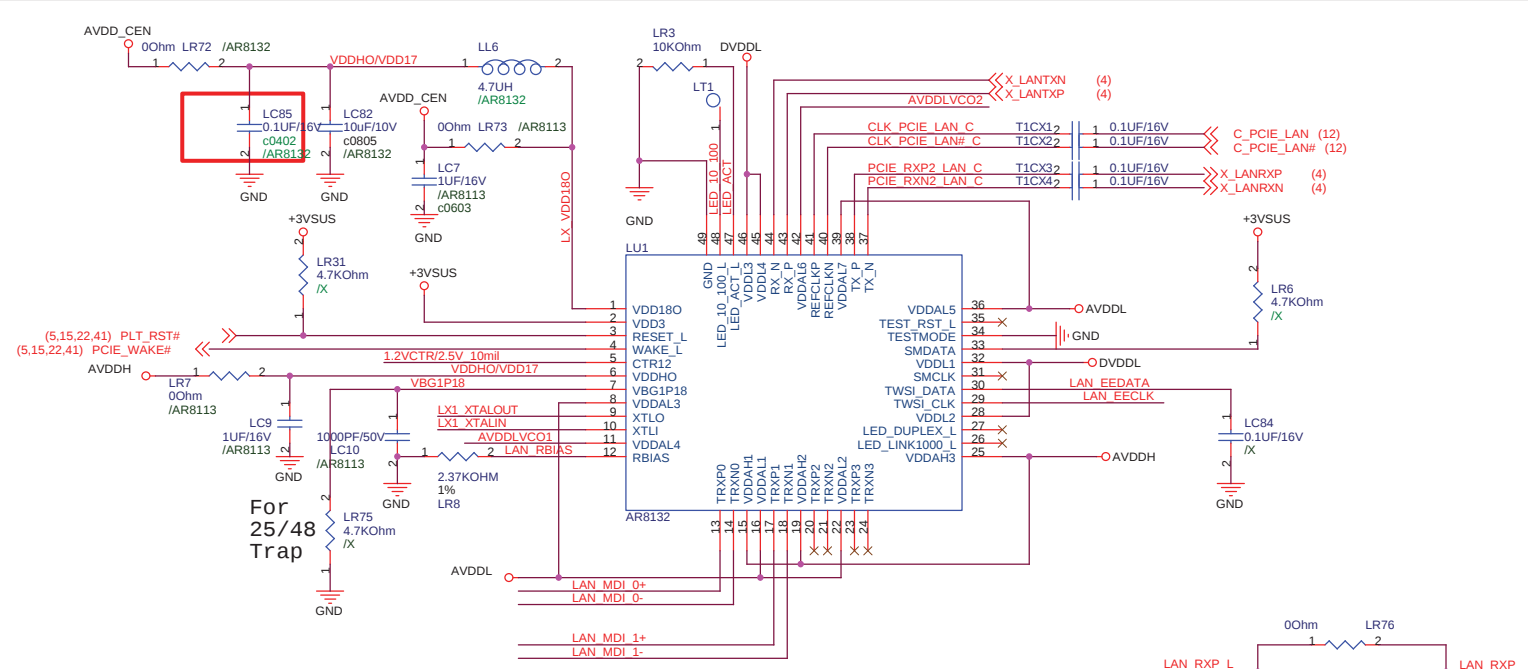
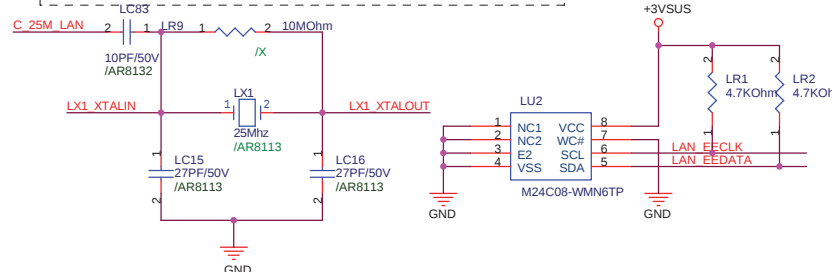


<Variant Name>

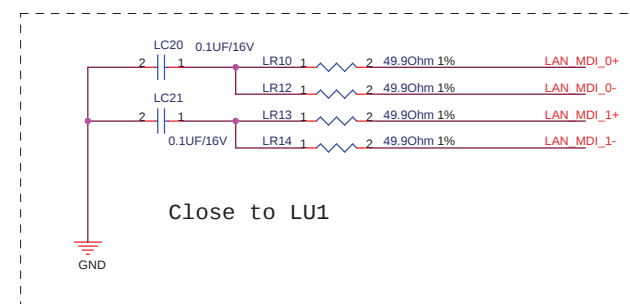
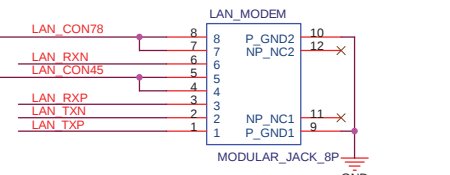
ASUS		Title : HDD&Flash card	
ASUSTek Computer INC.		Engineer: Henry_Yang	
Size	Project Name	Rev	1.0G
Date: Wednesday, January 14, 2009		Sheet 18 of 42	

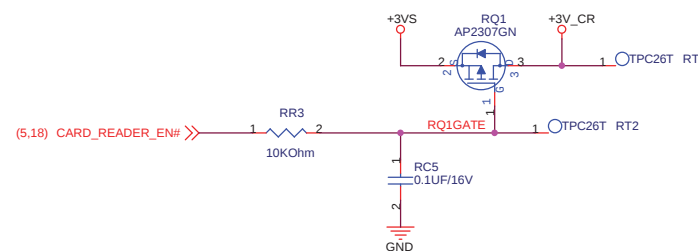
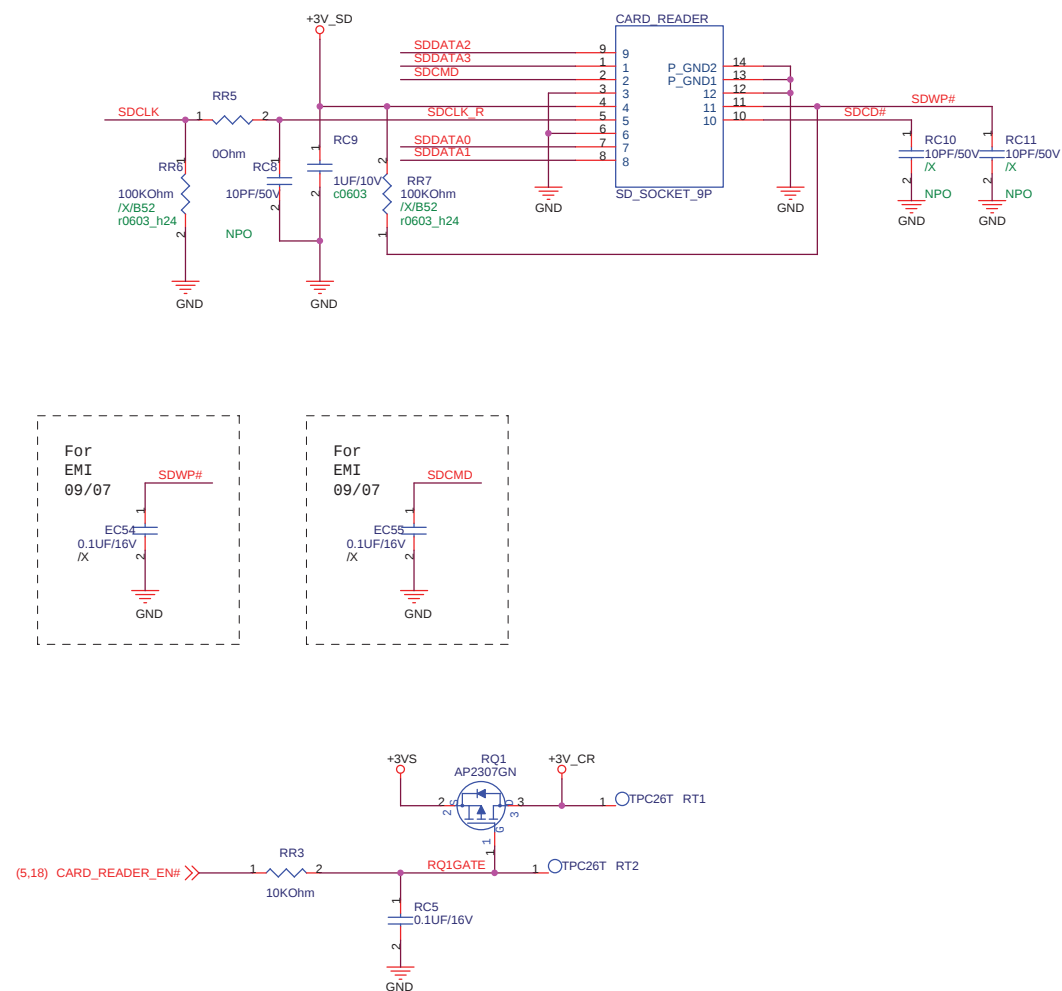


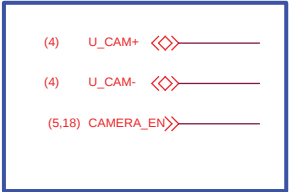
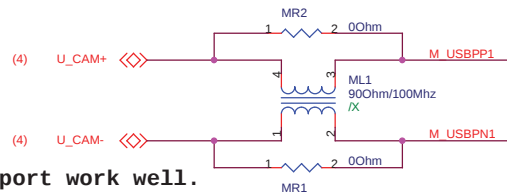
if overlocking LL3 Kept and LL2 removed
if not overlocking LL3 removed and LL2 Kept



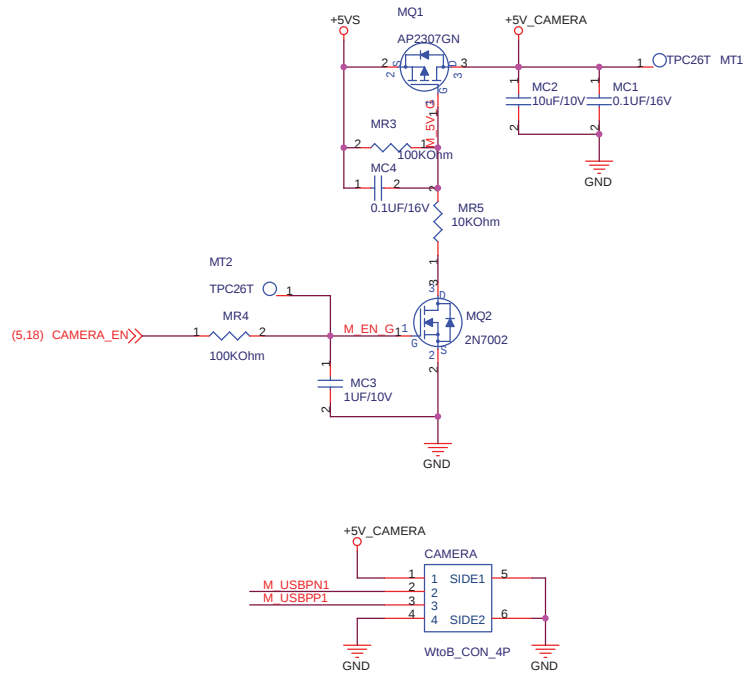
LAN connector: 12G148301086

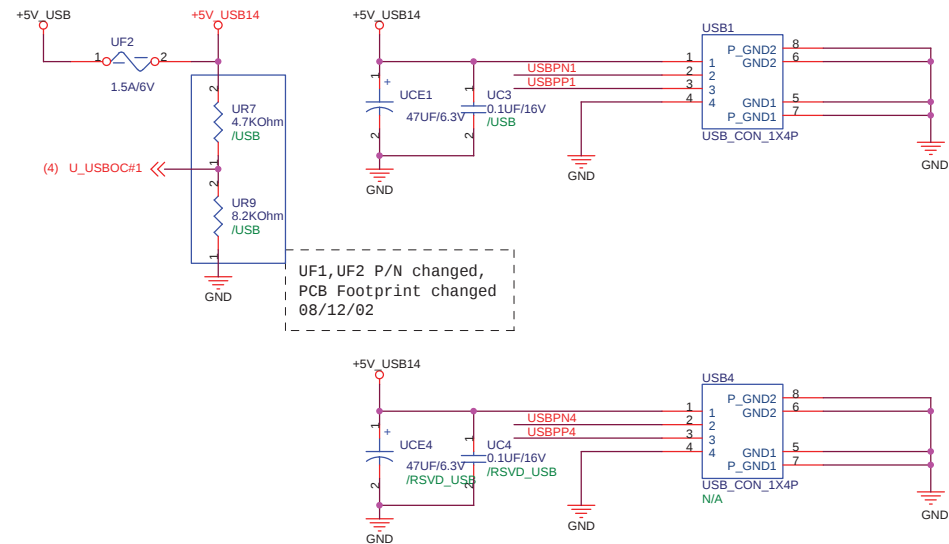
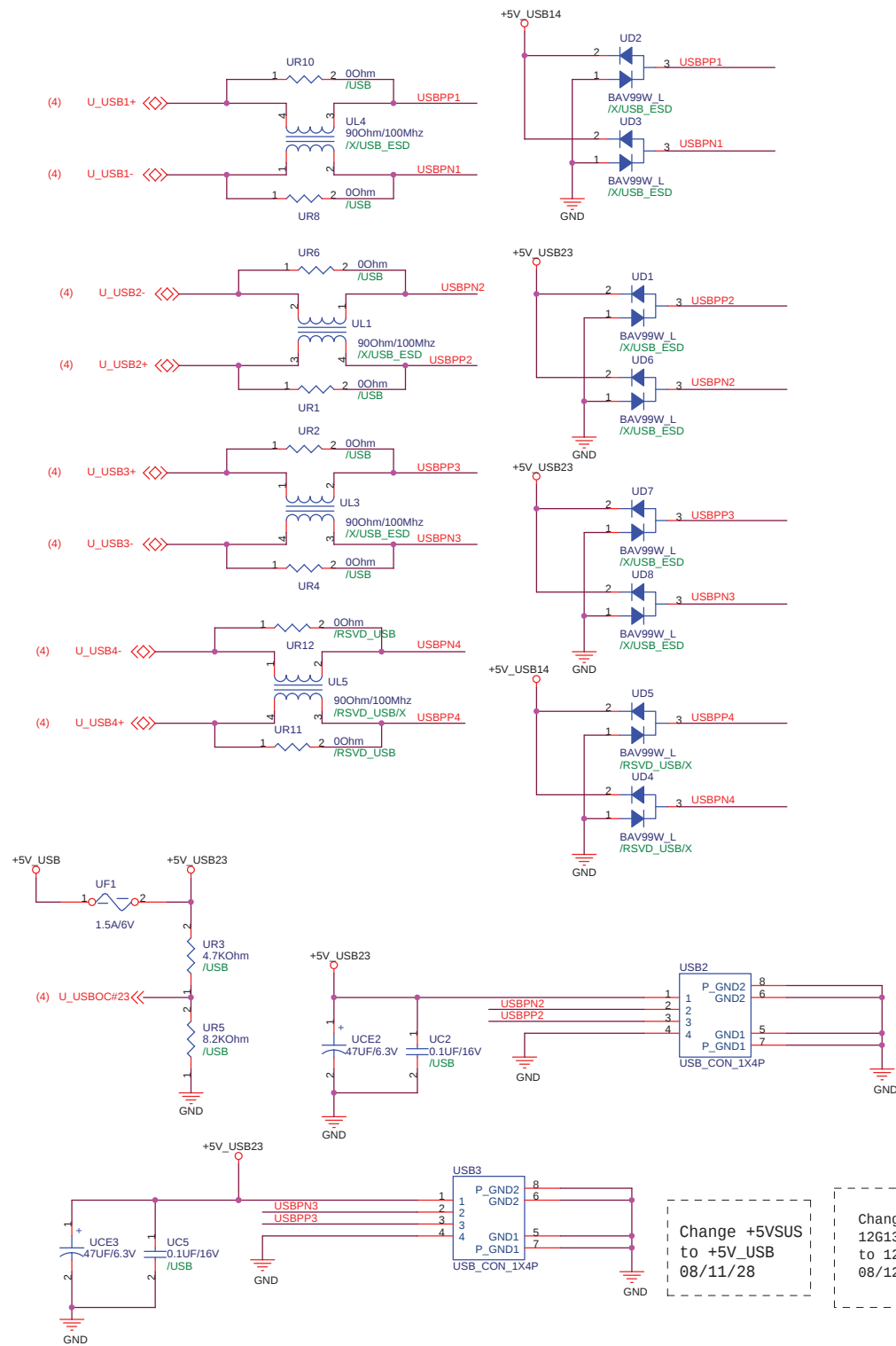


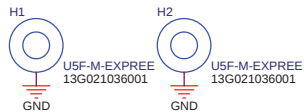




For check if the USB port work well.

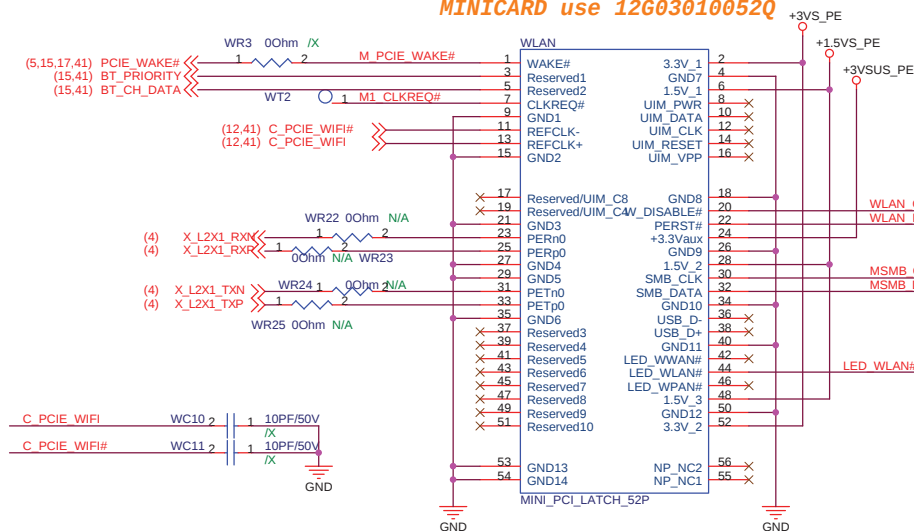




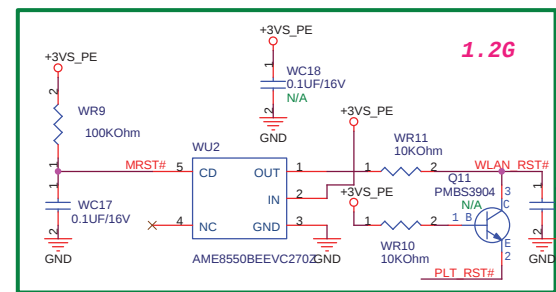
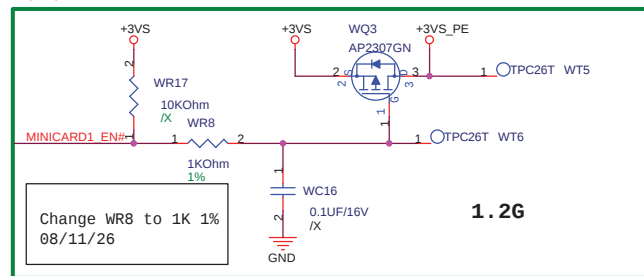
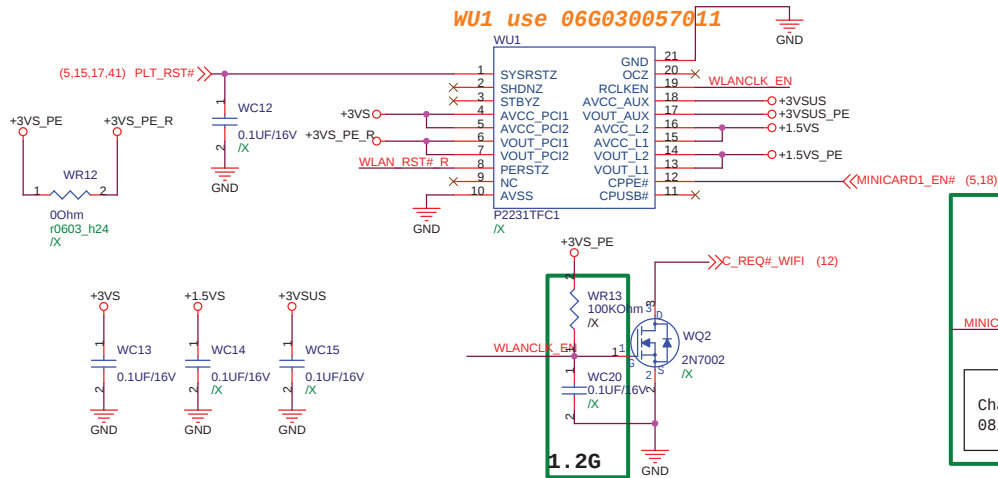


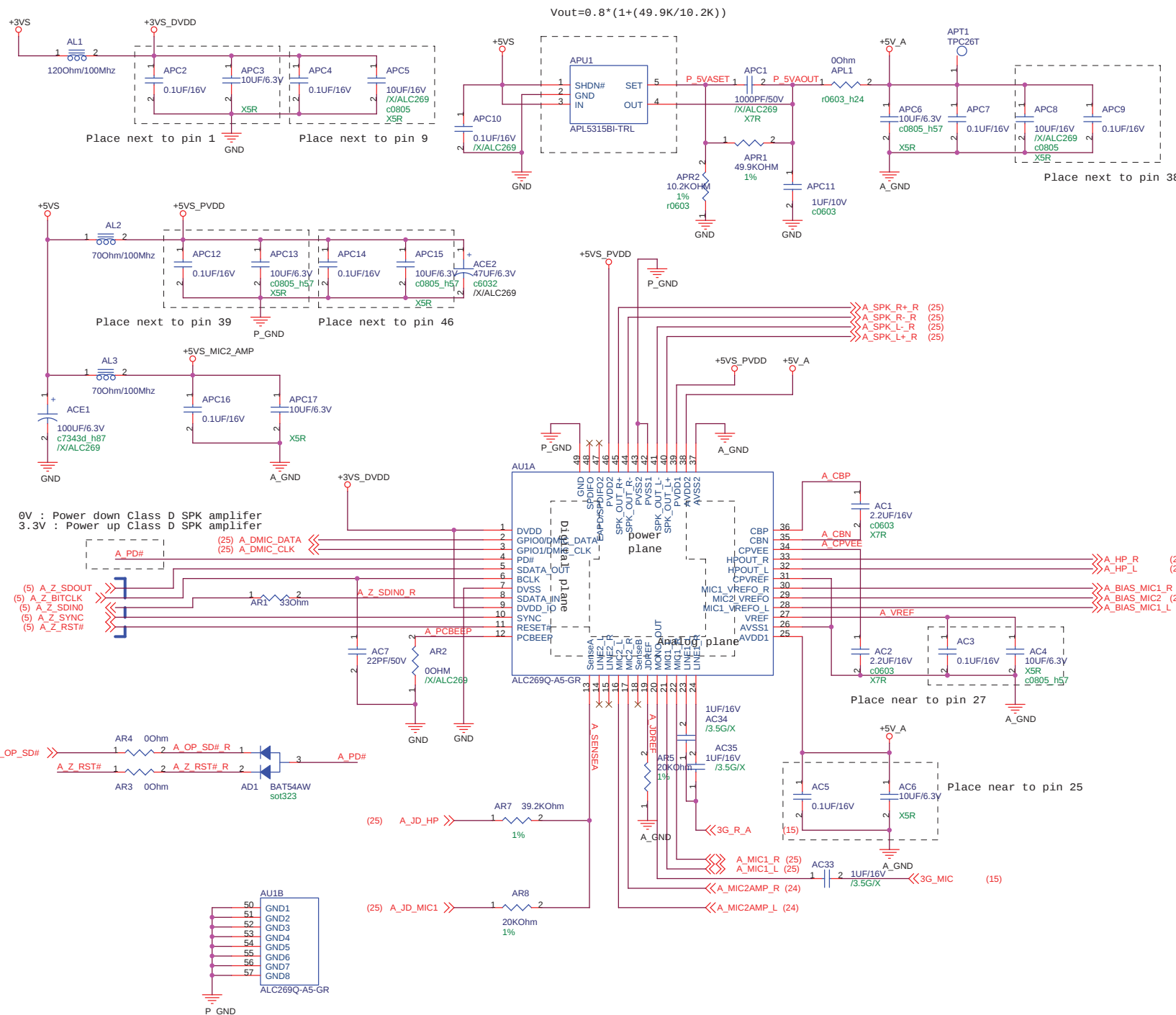
MINI CARD NUT(1.6mm) *2

MINICARD use 12G03010052Q

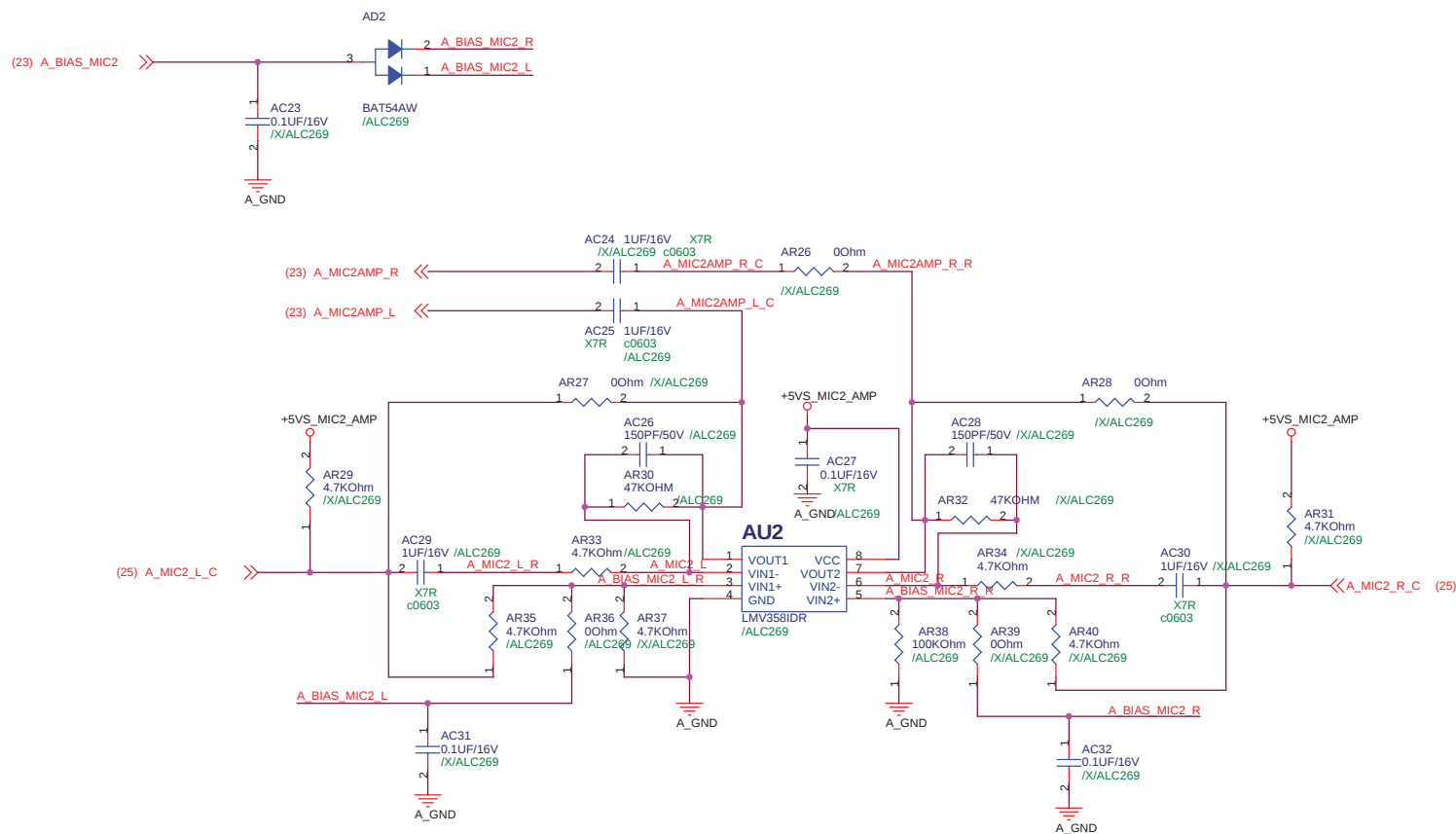


WU1 use 06G030057011



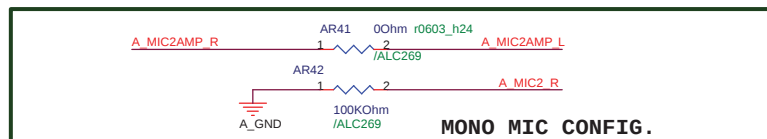


0.1ABeta

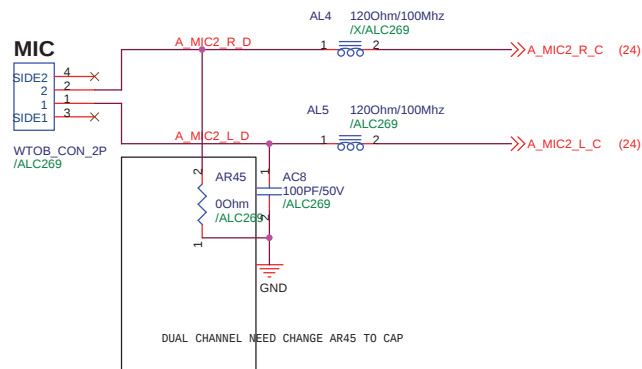


Internal MIC Amp.

FL = 33.86kHz, FH = 22.5kHz



Internal MIC

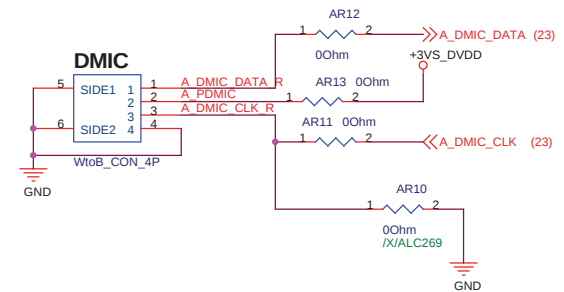
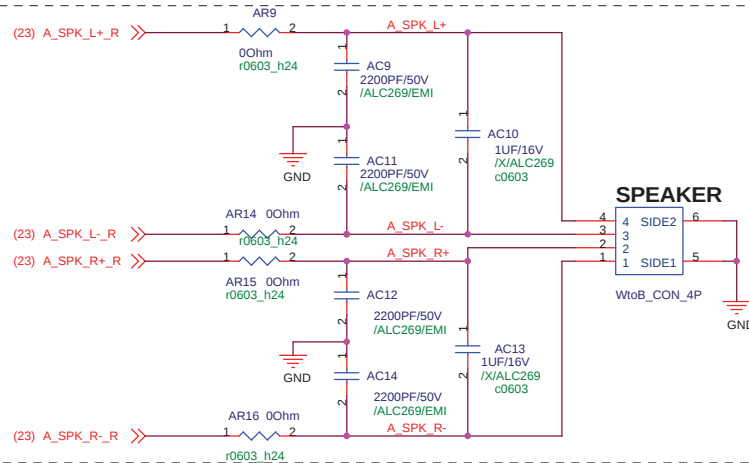


Change AR21,AR22 to 1K 1%
08/11/26

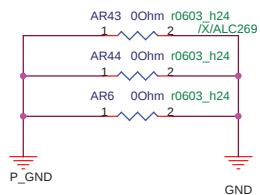
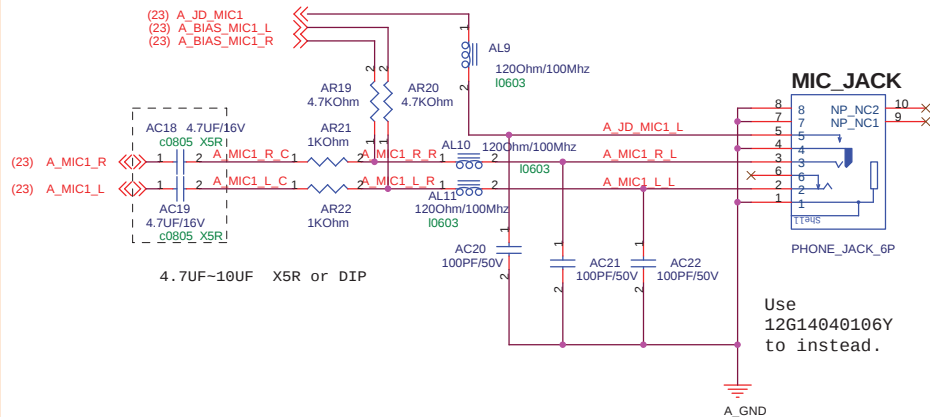
SPEAKER

Demodulation Filter
Placement near
Audio Codec

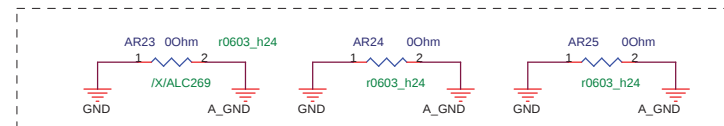
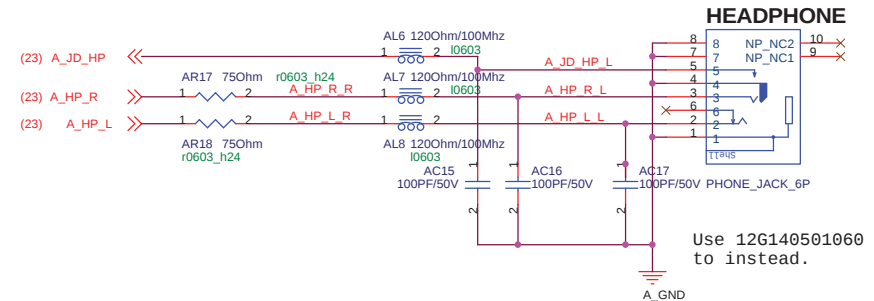
<<Attention>>
you can use LC filter(AR9,AR14,AR15,AR16
mount 8.2uH L ;and mount AC10,AC13) to
eliminate the EMI(please don't use
general beads,because they may influence
the THD+N quality) , AC9/AC11/AC12/AC14
are reserved for EMI fine-tune ; For EMI
issue, All L and C should near to codec



MIC JACK



HEADPHONE

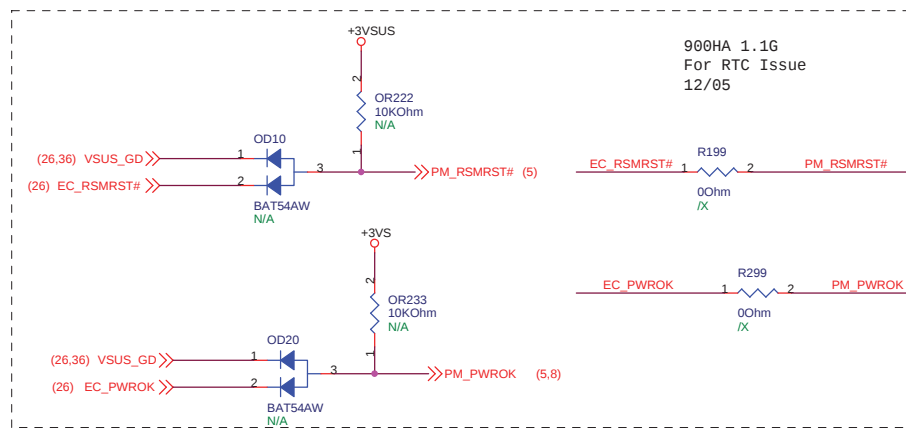
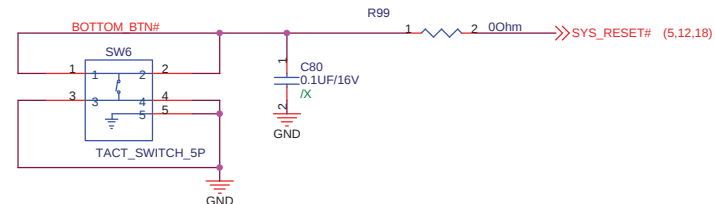
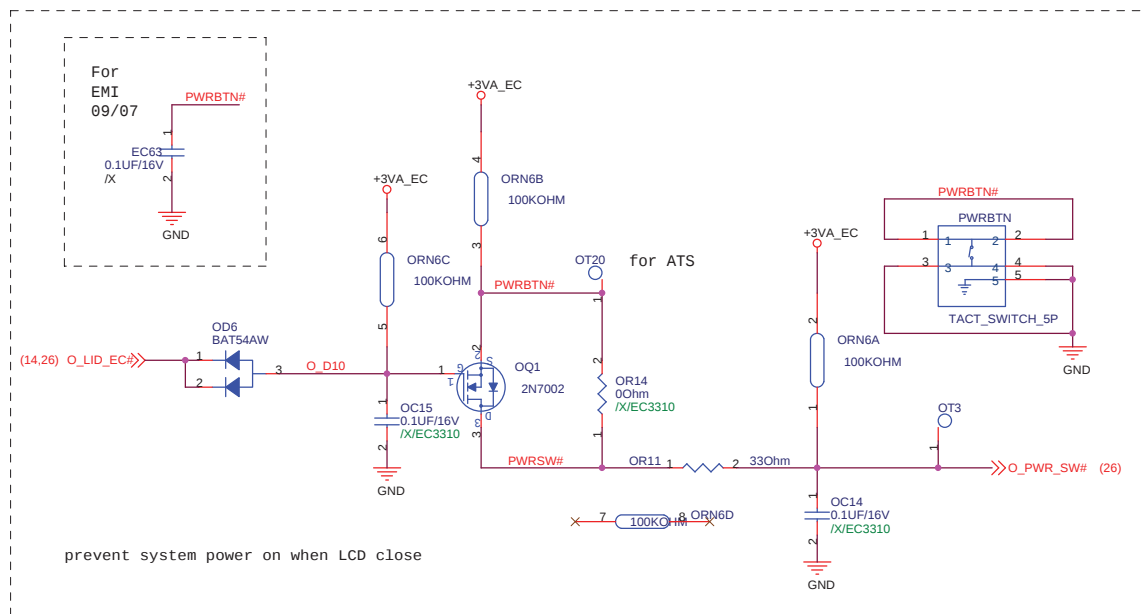
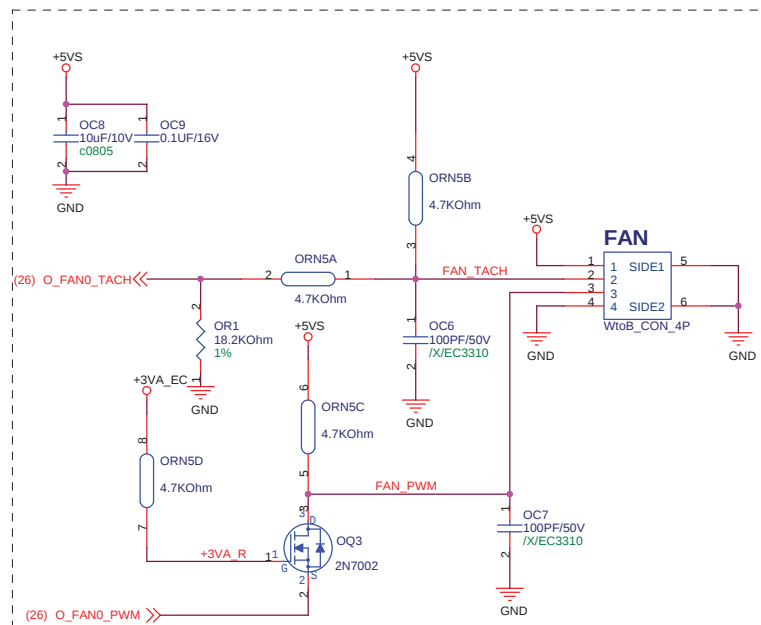
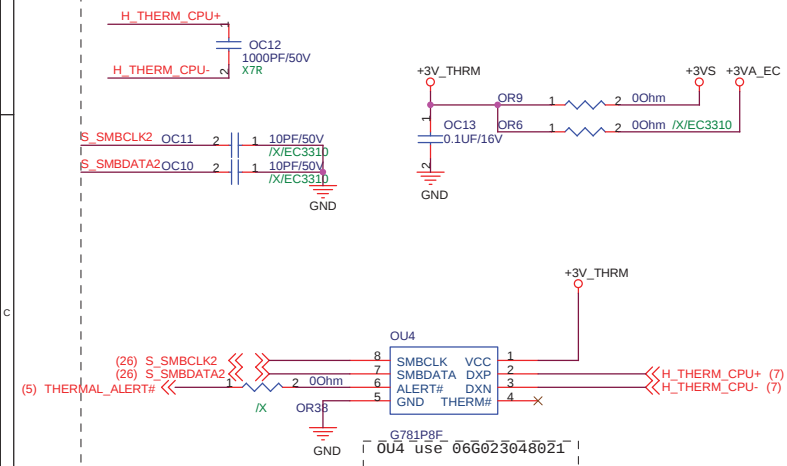


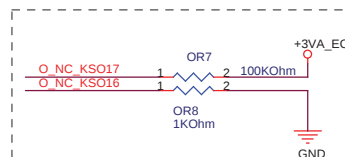
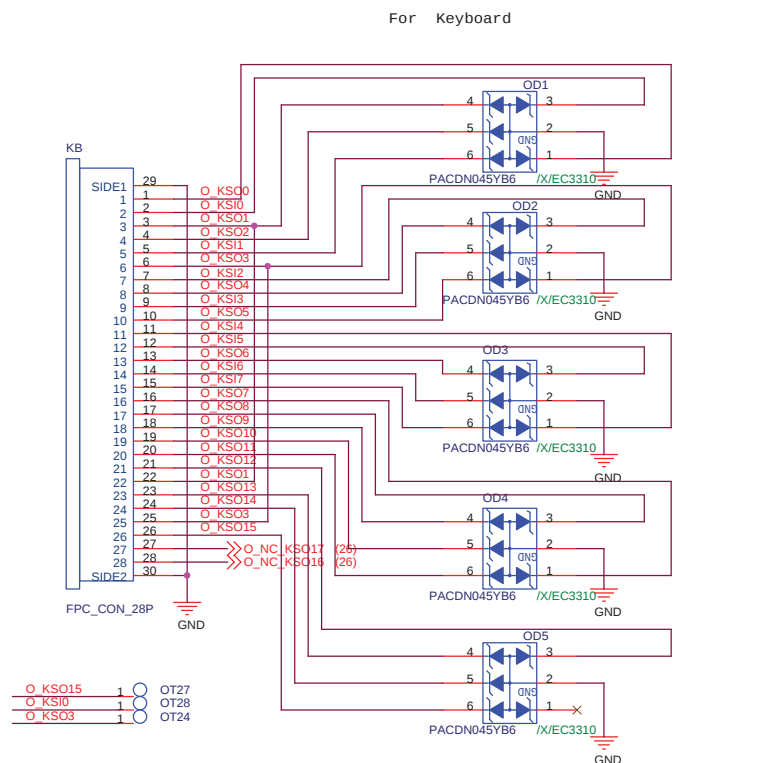
AR24, AR25 can use
0.1UF 11G233310432320
for EMI



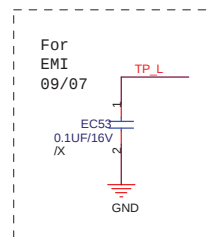
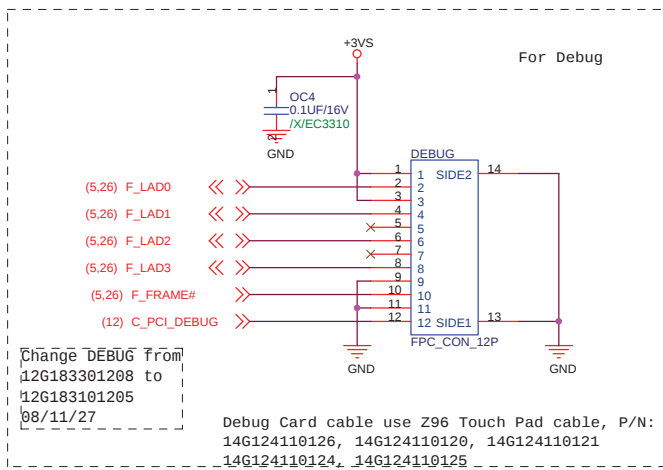
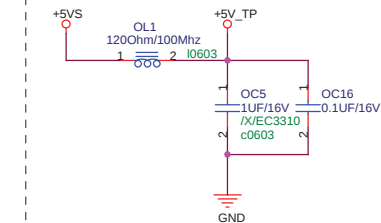
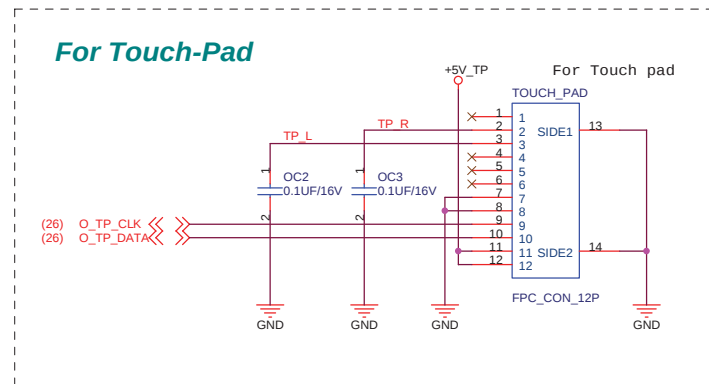
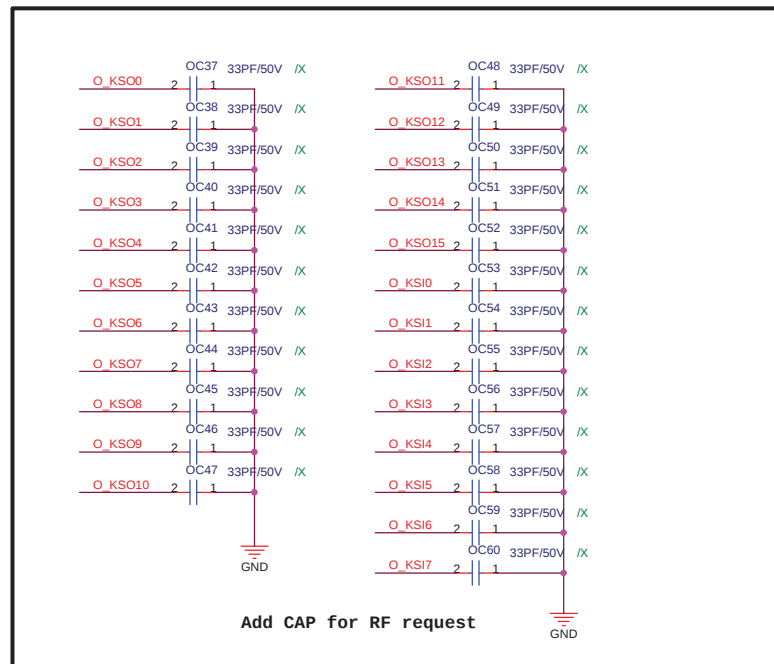
0.1 A Beta

Remove Force_off
function
08/11/28

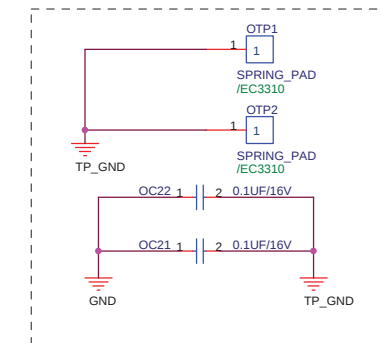
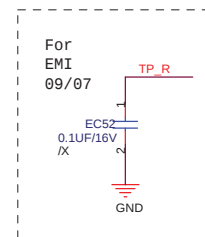
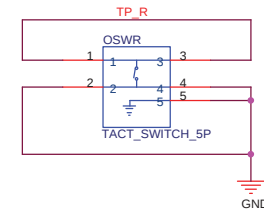
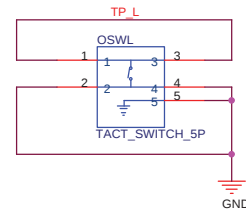




Change OR8 to 1K 1%
08/11/26



```
OSW1, OSW2 use
12G09103305N
```



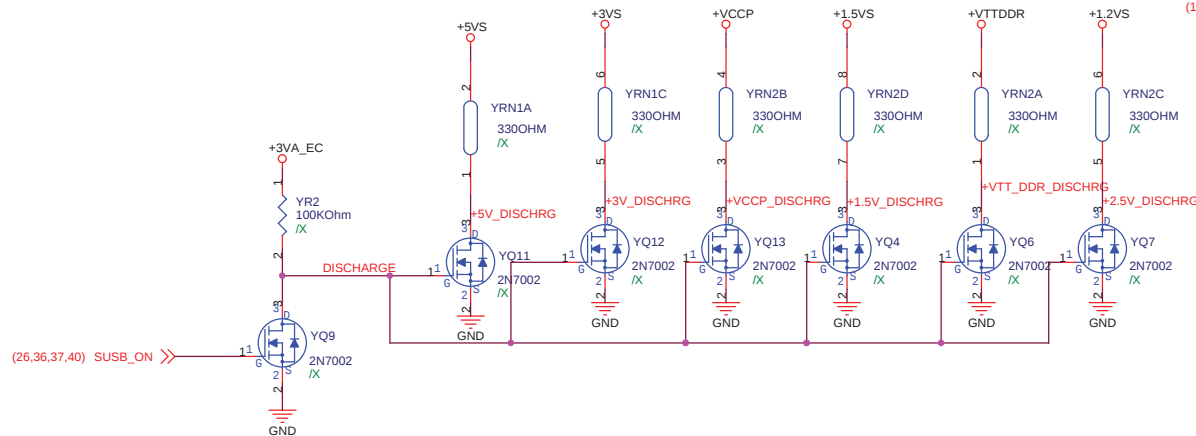
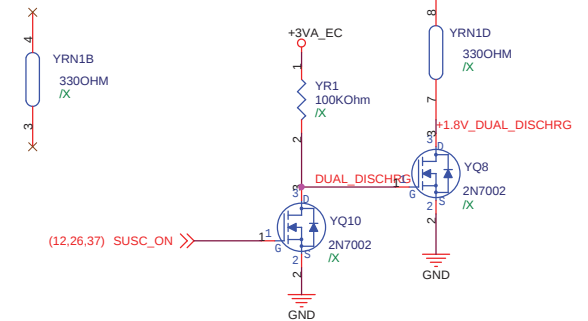
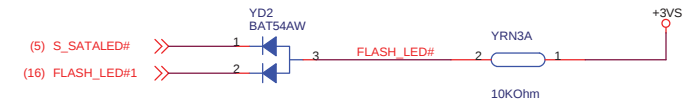
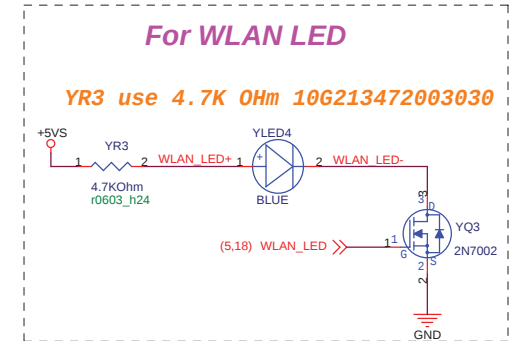
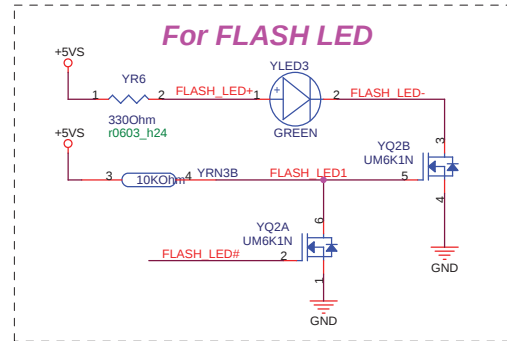
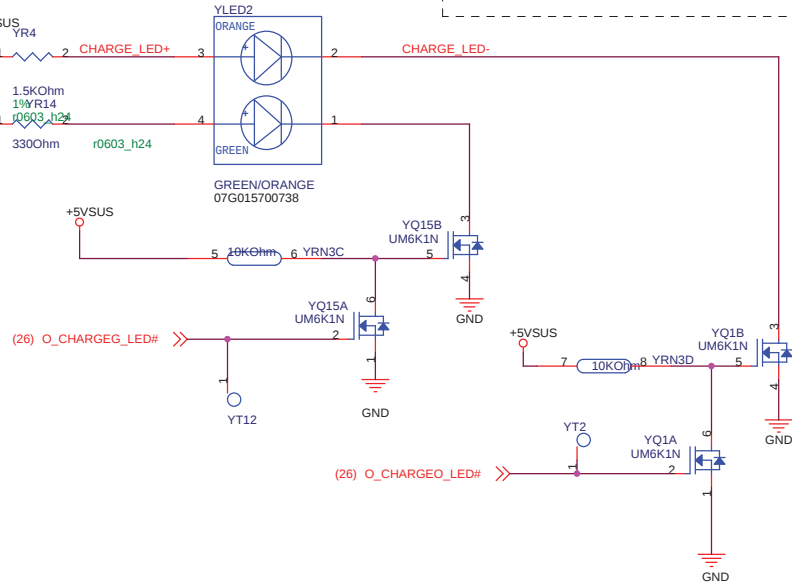
For CHARGE LED

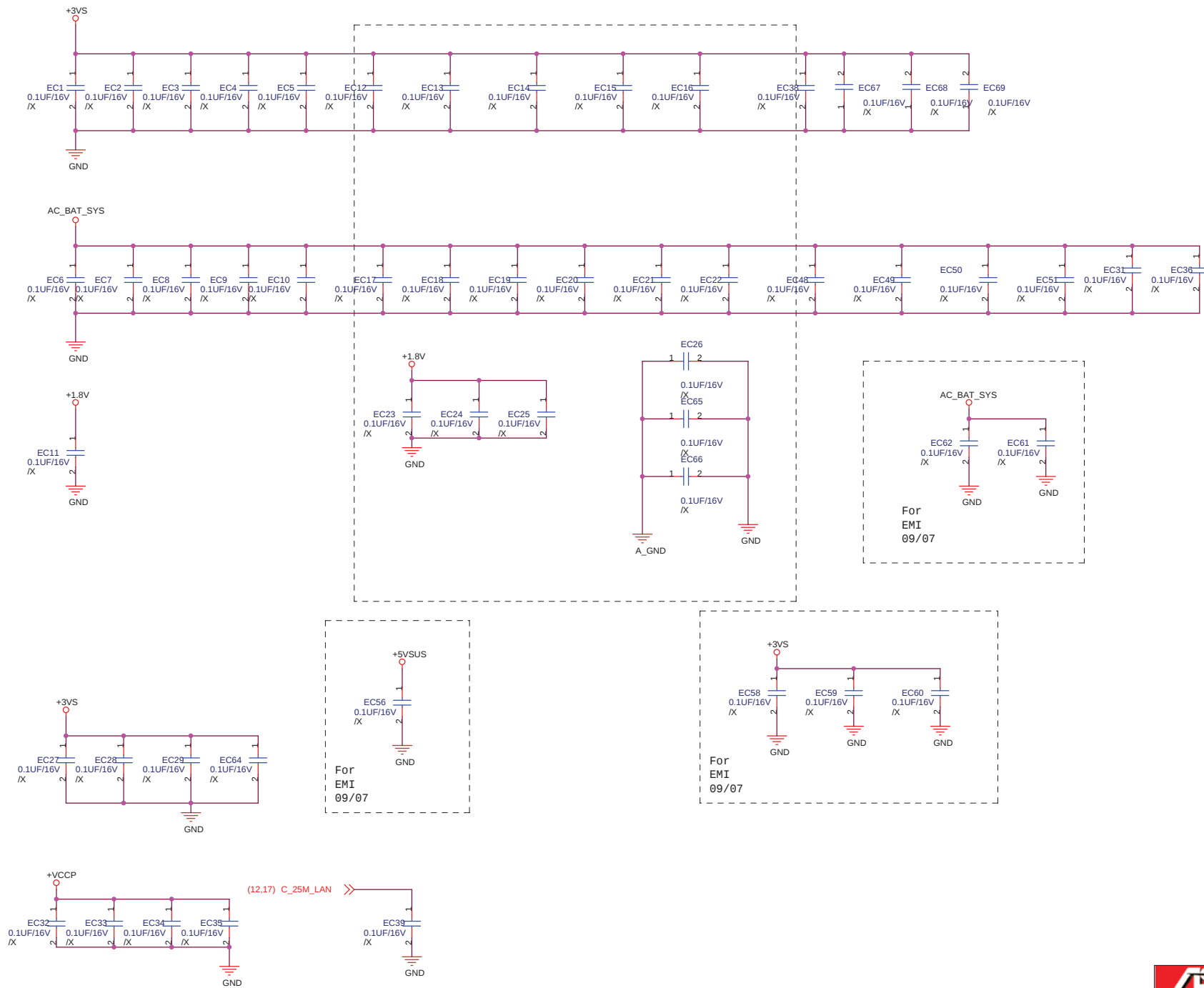
For POWER LED

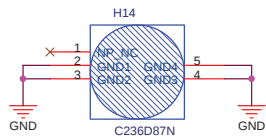
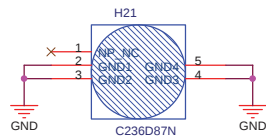
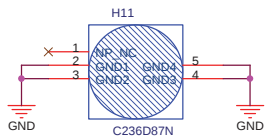
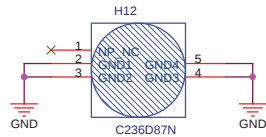
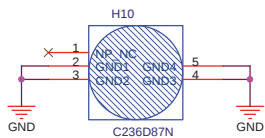
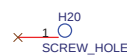
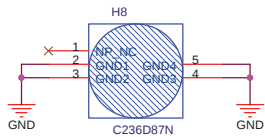
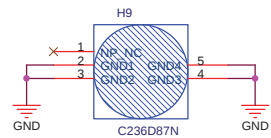
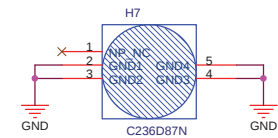
For FLASH LED

For WLAN LED

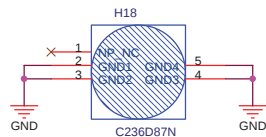
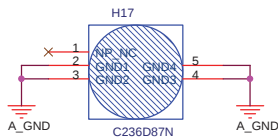
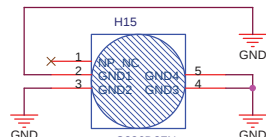
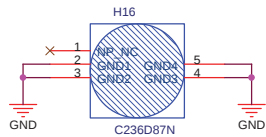
YR3 use 4.7K 0Hm 10G213472003030



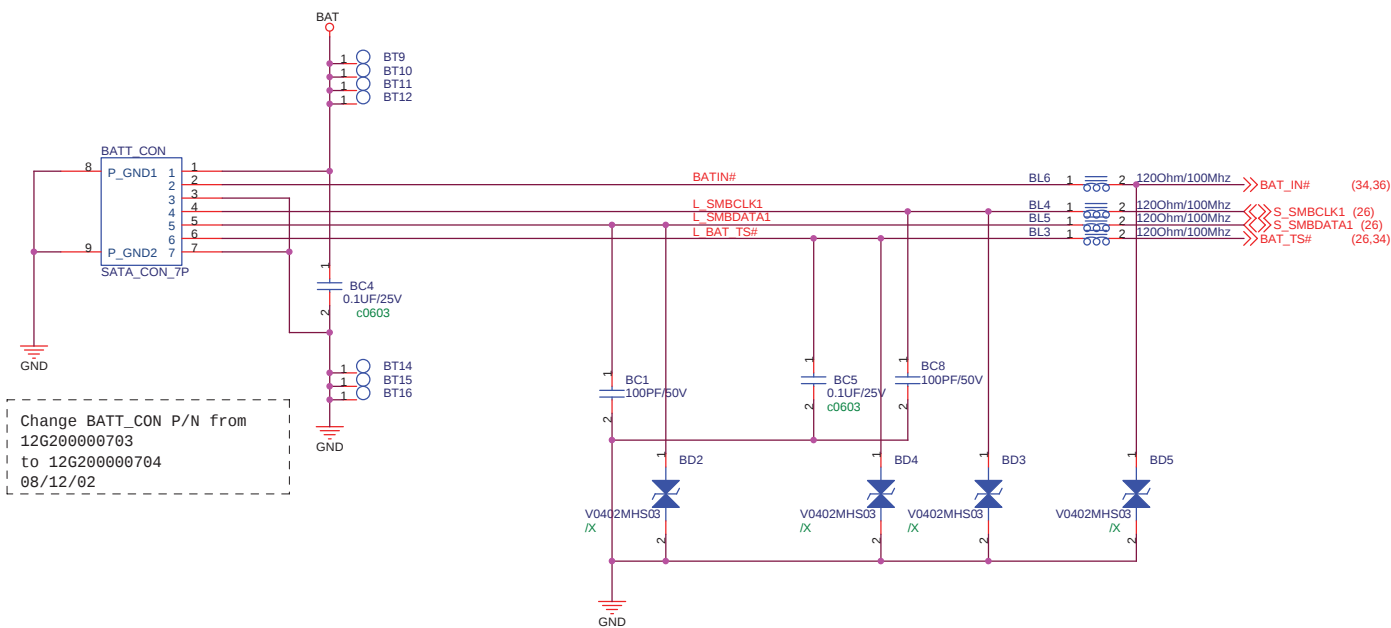
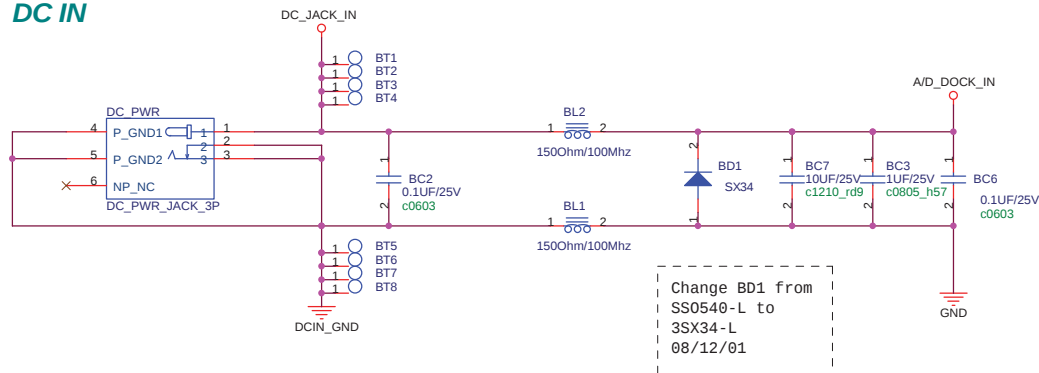


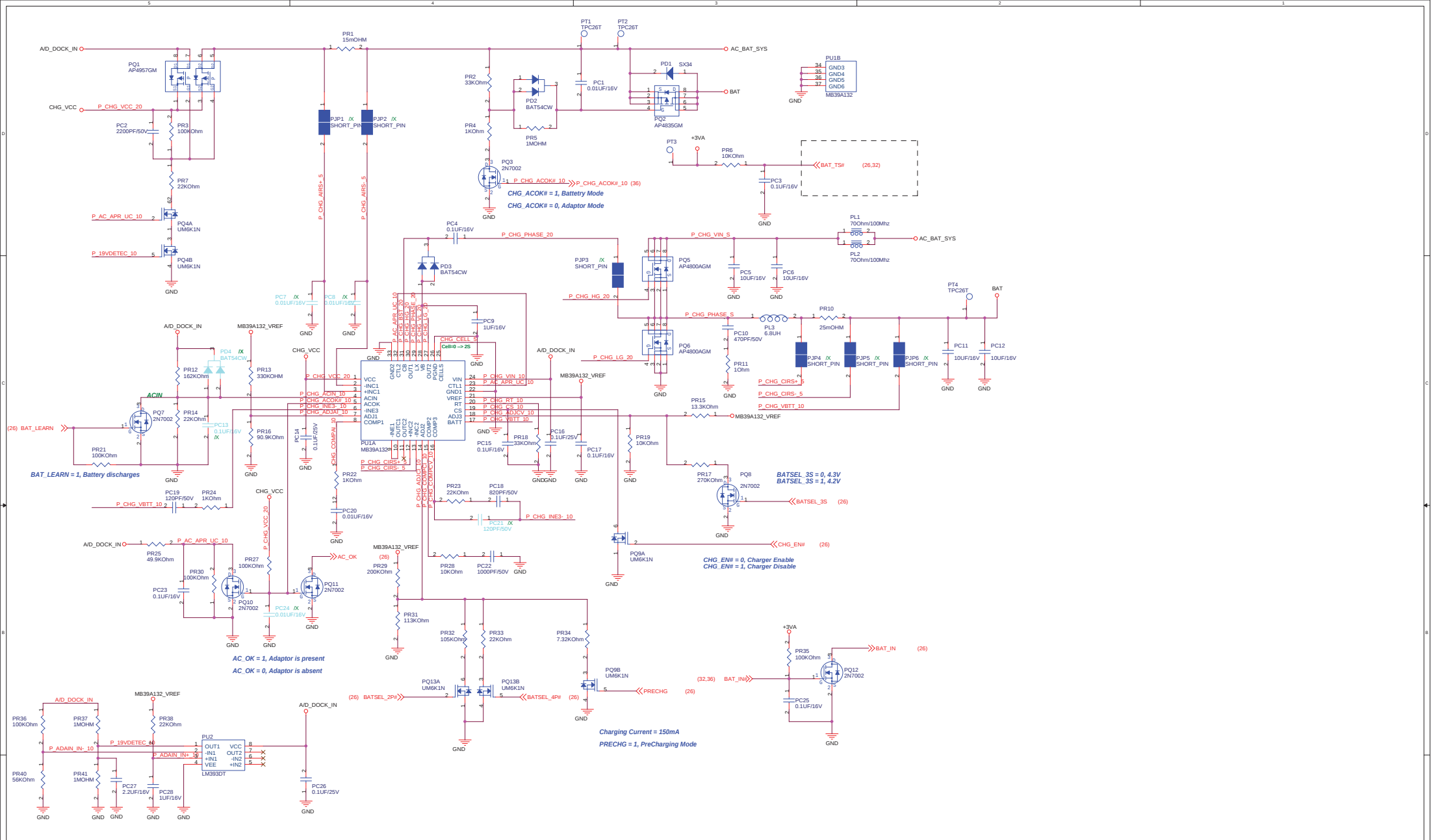


Due to 900PV cut a piece of PCB for bluetooth, so we has removed the H13.



DC IN



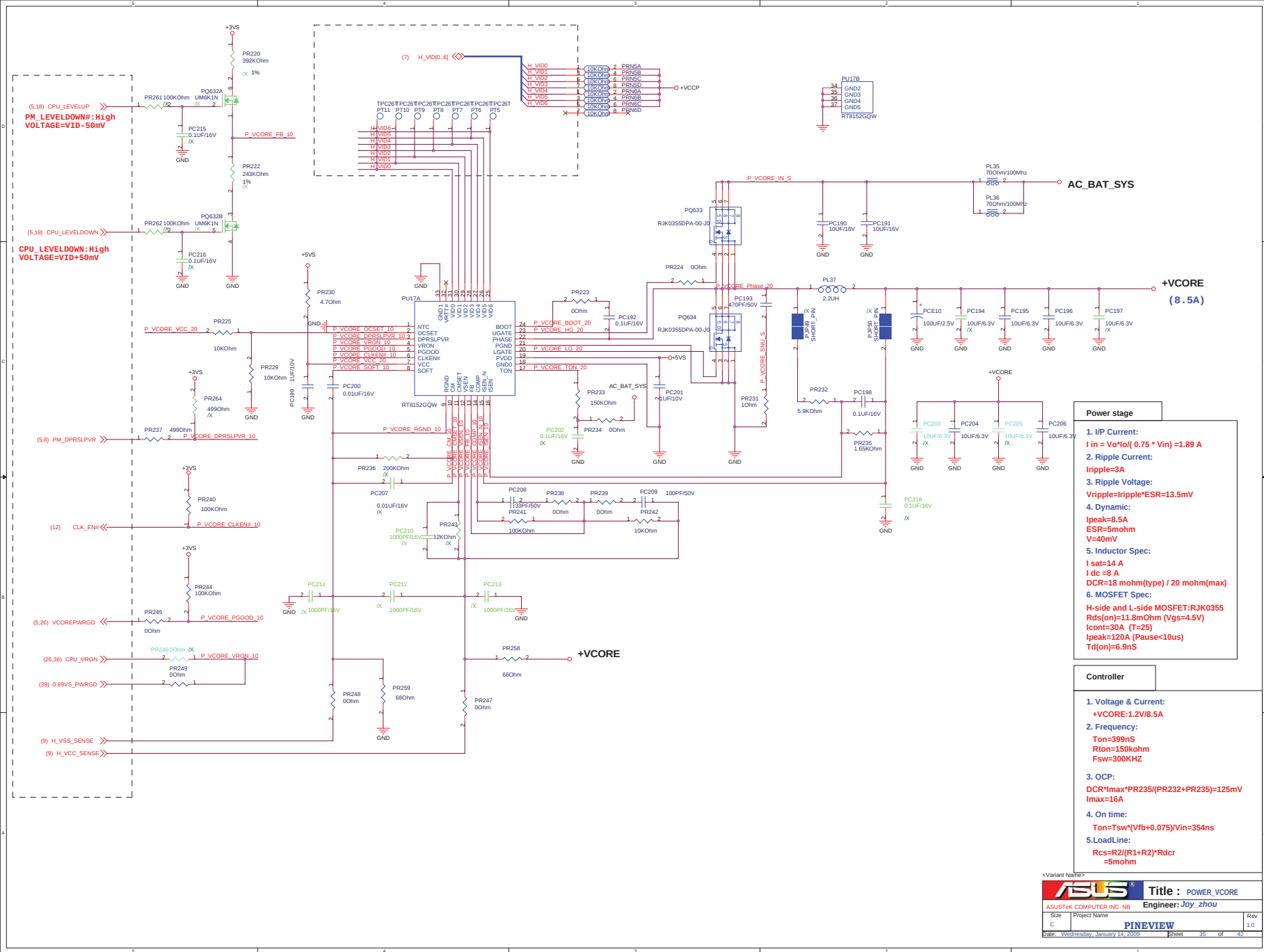


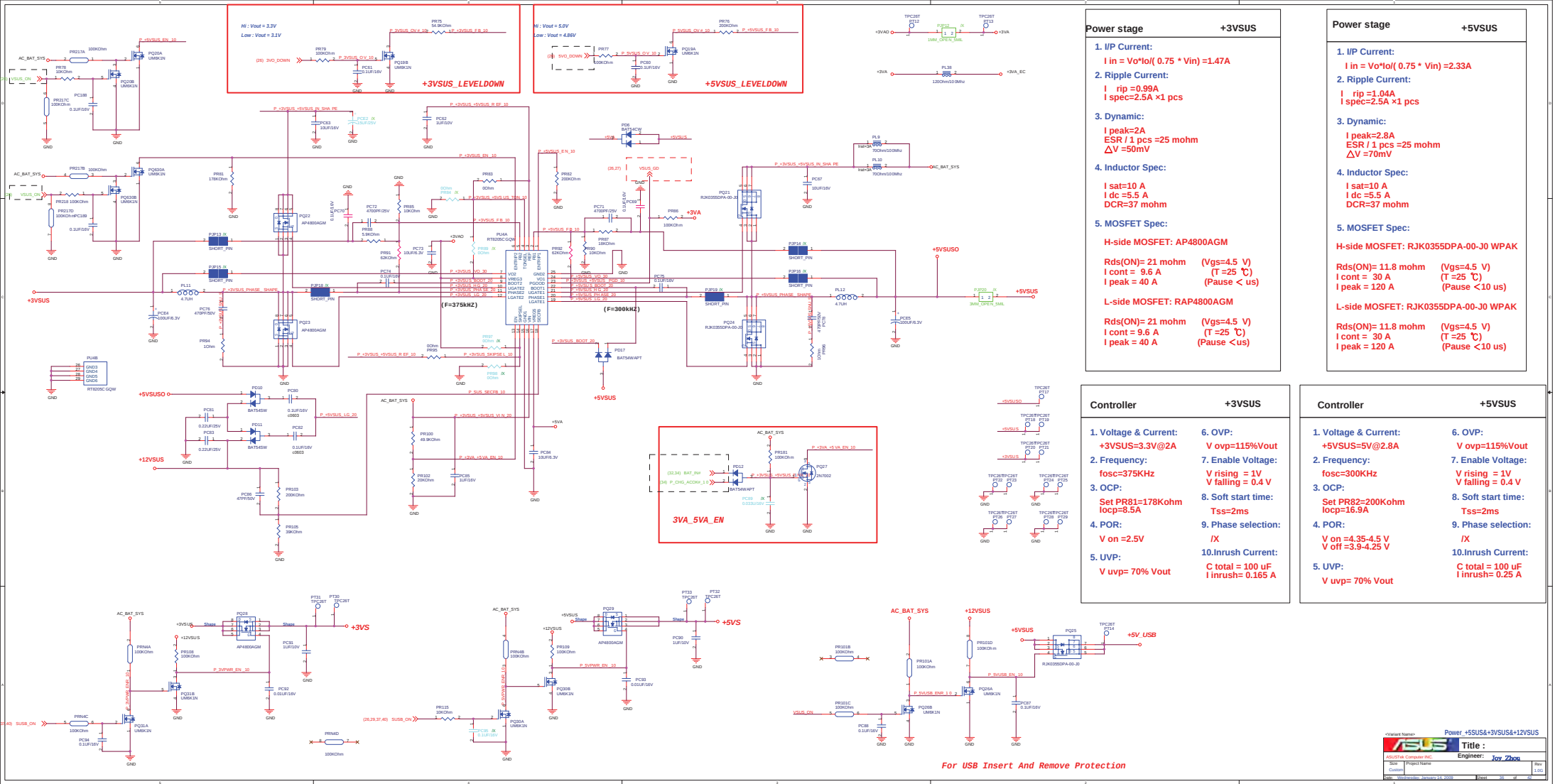
Battery Charging Voltage : Vadj3 > 4.1V ==> Vbat = 4.2V /cell 2.2V>Vadj3>1.1V ==> Vbat = 2*Vadj3 /cell Battery Charging Current : 4.4V > Vadj2 >= 0V ==> Ichg = (Vadj2-0.075)/(25*Rs) Input Adaptor Max. Current Limit : Ilimit_current = (Vadj1-0.075) / (25*Rs)	Pre-Charging Mode : Precharging current = 146mA Vadj2 = 166mV Adaptor Max. Current : PR13=330KOHM; PR16=90.9KOHM=> Ilimit = 2.679A ACIN Threshold = 1.25V Adaptor >10.45V, System Powered by Adaptor Adaptor <10.45V, System Powered by Battery	Prevent Input from 19V : Adaptor > 13.92V, PQ4B Turn-off Adaptor < 13.92V, PQ4B Turn-on Battery Cell Selection : CELLS:VREF,4-Cell; CELLS:OPEN,3-Cell; CELLS:GND,2-Cell;	VREF = 5.0V fosc(KHz) = 17000 / RT (KOhm) =515KHz Soft start: ts(s) = 0.23 * CS (uF) =23mS	Charging Current : <table><tr><th>4P#</th><th>2P#</th><th>Icharge</th></tr><tr><td>1</td><td>0</td><td>0.56A</td></tr><tr><td>0</td><td>1</td><td>1.6A</td></tr><tr><td>0</td><td>0</td><td>2.8A</td></tr></table>	4P#	2P#	Icharge	1	0	0.56A	0	1	1.6A	0	0	2.8A
4P#	2P#	Icharge														
1	0	0.56A														
0	1	1.6A														
0	0	2.8A														

**ASUSTek Computer Inc.**

Title : CHARGER
Engineer: Joy ZHOU

Size	Project Name	Rev
A2	PINEVIEW	1.0G
Date: Wednesday, January 14, 2009		Sheet 34 of 42



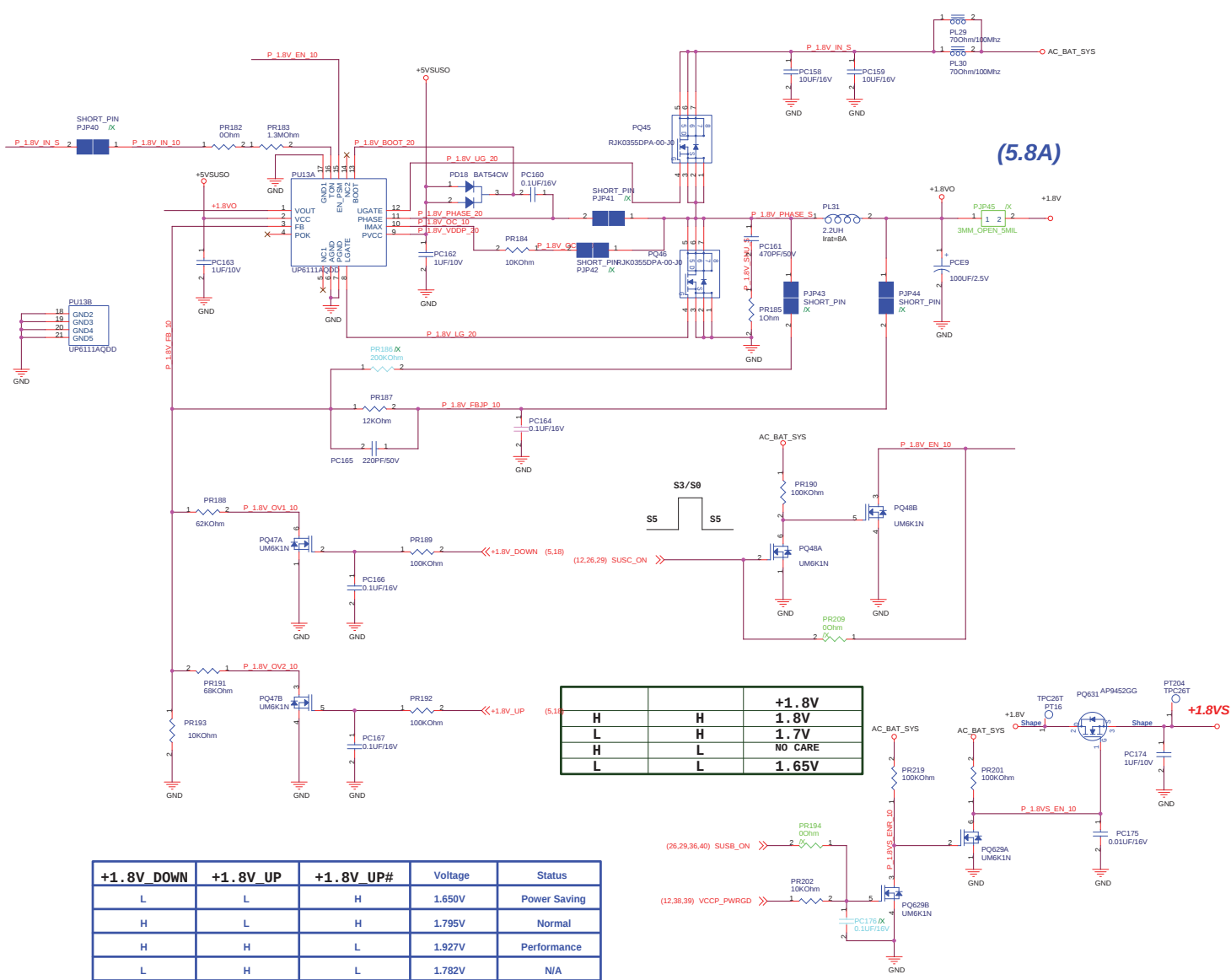


Power stage	+3VSUS
1. I/P Current:	$I_{in} = V_o \cdot I_o / (0.75 \cdot V_{in}) = 1.47A$
2. Ripple Current:	$I_{rip} = 0.99A$ $I_{spec} = 2.5A \times 1 \text{ pcs}$
3. Dynamic:	$I_{peak} = 2A$ $ESR / 1 \text{ pcs} \approx 25 \text{ mohm}$ $\Delta V = 50mV$
4. Inductor Spec:	$I_{sat} = 10 A$ $I_{dc} = 5.5 A$ $DCR = 37 \text{ mohm}$
5. MOSFET Spec:	H-side MOSFET: AP4800AGM $R_{ds(ON)} = 21 \text{ mohm}$ ($V_{gs} = 4.5 V$) $I_{cont} = 9.6 A$ ($T = 25^\circ C$) $I_{peak} = 40 A$ (Pause < us) L-side MOSFET: RAP4800AGM $R_{ds(ON)} = 21 \text{ mohm}$ ($V_{gs} = 4.5 V$) $I_{cont} = 9.6 A$ ($T = 25^\circ C$) $I_{peak} = 40 A$ (Pause < us)

Power stage	+5VSUS
1. I/P Current:	$I_{in} = V_o \cdot I_o / (0.75 \cdot V_{in}) = 2.33A$
2. Ripple Current:	$I_{rip} = 1.04A$ $I_{spec} = 2.5A \times 1 \text{ pcs}$
3. Dynamic:	$I_{peak} = 2.8A$ $ESR / 1 \text{ pcs} \approx 25 \text{ mohm}$ $\Delta V = 70mV$
4. Inductor Spec:	$I_{sat} = 10 A$ $I_{dc} = 5.5 A$ $DCR = 37 \text{ mohm}$
5. MOSFET Spec:	H-side MOSFET: RJK0355DPA-00-J0 WPAK $R_{ds(ON)} = 11.8 \text{ mohm}$ ($V_{gs} = 4.5 V$) $I_{cont} = 30 A$ ($T = 25^\circ C$) $I_{peak} = 120 A$ (Pause < 10 us) L-side MOSFET: RJK0355DPA-00-J0 WPAK $R_{ds(ON)} = 11.8 \text{ mohm}$ ($V_{gs} = 4.5 V$) $I_{cont} = 30 A$ ($T = 25^\circ C$) $I_{peak} = 120 A$ (Pause < 10 us)

Controller	+3VSUS
1. Voltage & Current:	+3VSUS=3.3V@2A
2. Frequency:	fosc=375KHz
3. OCP:	Set PR81=178Kohm locp=8.5A
4. POR:	V on =2.5V
5. UVP:	V uvp= 70% Vout
6. OVP:	V ovp=115%Vout
7. Enable Voltage:	V rising = 1V V falling = 0.4 V
8. Soft start time:	Tss=2ms
9. Phase selection:	/X
10. Inrush Current:	C total = 100 uF I inrush= 0.165 A

Controller	+5VSUS
1. Voltage & Current:	+5VSUS=5V@2.8A
2. Frequency:	fosc=300KHz
3. OCP:	Set PR82=200Kohm locp=16.9A
4. POR:	V on =4.35-4.5 V V off =3.9-4.25 V
5. UVP:	V uvp= 70% Vout
6. OVP:	V ovp=115%Vout
7. Enable Voltage:	V rising = 1V V falling = 0.4 V
8. Soft start time:	Tss=2ms
9. Phase selection:	/X
10. Inrush Current:	C total = 100 uF I inrush= 0.25 A

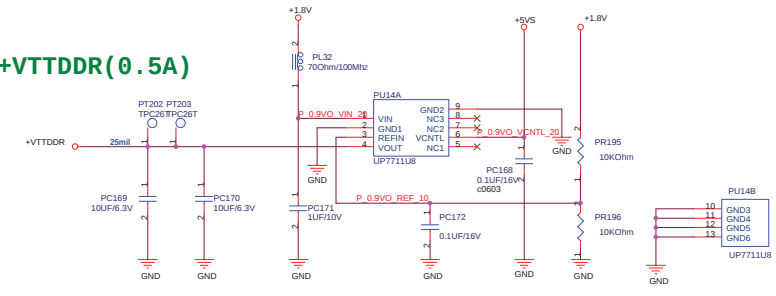


Power stage

- I/P Current:**
 $I_{in} = V_o * I_o / (0.8 * V_{in}) = 2.175A$
- Ripple Current:**
 $I_{rip} = 2.65A$
 $I_{spec} = 2.5A \times 2 \text{ pcs}$
- Dynamic:**
 $I_{peak} = 5.8A$
 $ESR / 1 \text{ pcs} = 18 \text{ mohm}$
 $\Delta V = 104.4mV$
- Inductor Spec:**
 $I_{sat} = 14A$
 $I_{dc} = 8A$
 $DCR = 18 \text{ mohm}$
- MOSFET Spec:**
H-side MOSFET: RJK0355DPA-00-J0 WPAK
 $R_{ds(ON)} = 11.8 \text{ mohm}$ ($V_{gs} = 4.5V$)
 $I_{cont} = 30A$ ($T = 25^\circ C$)
 $I_{peak} = 120A$ (Pause < 10 us)
L-side MOSFET: RJK0355DPA-00-J0 WPAK
 $R_{ds(ON)} = 11.8 \text{ mohm}$ ($V_{gs} = 4.5V$)
 $I_{cont} = 30A$ ($T = 25^\circ C$)
 $I_{peak} = 120A$ (Pause < 10 us)

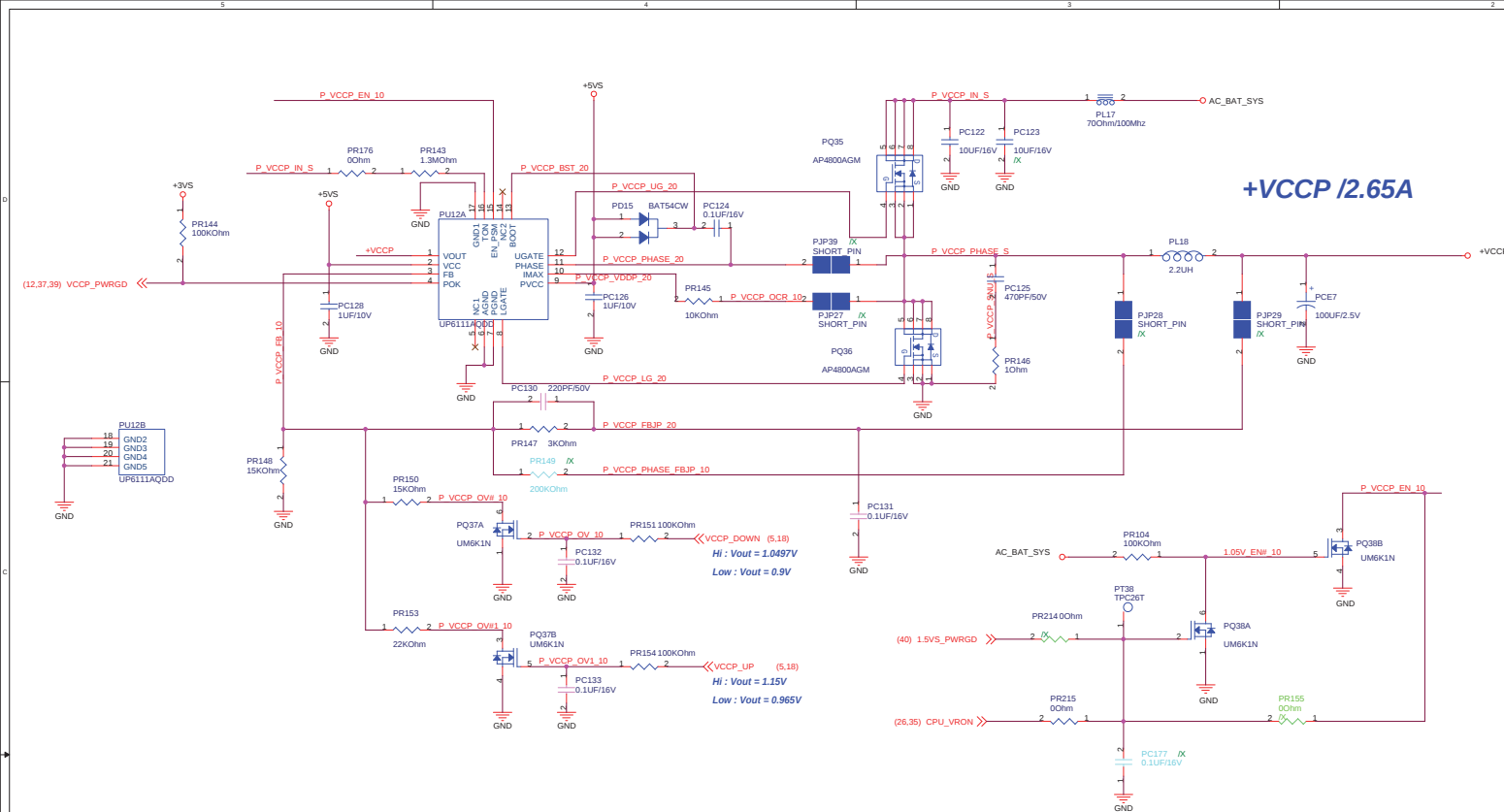
Controller

- Voltage & Current:**
+1.8V@5.8A
- Frequency:**
 $PR183 = 1.3M \text{ ohm}$
 $F_{osc} = 191KHz$
- OCP:**
 $PR184 = 10K \text{ ohm} \rightarrow 16.7A$
- POR:**
 $V_{ccrth} = 3.7\sim 4.1V$
 $V_{ccchys} = 0.2V$
- UVP:**
 $V_{out} * 70\%$
- OVP:**
 $V_{out} * 115\%$
- Enable Voltage:**
 $V = 2.9V$
- Soft start time:**
 $T_{ss} = 1.2 \text{ ms}$
- Phase selection:**
 $/X$
- Inrush Current:**
 $C_{total} = 100 \text{ uF}$
 $I_{inrush} = 0.15A$



1.5VS @1.1 A

- Dropout Voltage:**
 $\Delta V = 0.3V$ ($I_o = 2A$)
- Current Limit:**
 $I_{limit} = 4A$
- Continue Current:**
 $I_{cont} = 3A$
- Power Dissipation:**
 $R_{thjc} = 52^\circ C/W$
 $P_d = 1.9W$
- EN Voltage:**
 $V_{en} = 1.4V$
 $V_{sd} = 0.8V$
- Supply Voltage:**
 $V_{cc} = 5V$
- Inrush current:**
 $T_{ss} = 5 \text{ ms}$
 $C_{total} = 10 \text{ uF}$
 $I_{inrush} = 3 \text{ mA}$



VCCP_DOWN	VCCP_UP	VCCP_UP#	Voltage	Status
L	L	H	0.9V	Power Saving
H	L	H	1.050V	Normal
H	H	L	1.152V	Performance
L	H	L	1.002V	N/A

Power stage

1. I/P Current:

$$I_{in} = V_o * I_o / (0.8 * V_{in}) = 0.58A$$

2. Ripple Current:

$$I_{rip} = 1A$$

$$I_{spec} = 2.5A \times 1 \text{ pcs}$$

3. Dynamic:

$$I_{peak} = 2.65 A$$

$$ESR / 1 \text{ pcs} = 18 \text{ mohm}$$

$$\Delta V = 47.7mV$$

4. Inductor Spec:

$$I_{sat} = 14 A$$

$$I_{dc} = 8 A$$

$$DCR = 18 \text{ mohm}$$

5. MOSFET Spec:

H-side MOSFET: AP4800AGM

$$R_{ds(ON)} = 21 \text{ mohm} \quad (V_{gs} = 4.5 V)$$

$$I_{cont} = 9.6 A$$

$$I_{peak} = 40 A$$

$$(T = 25 ^\circ C)$$

$$(Pause < us)$$

L-side MOSFET: RAP4800AGM

$$R_{ds(ON)} = 21 \text{ mohm} \quad (V_{gs} = 4.5 V)$$

$$I_{cont} = 9.6 A$$

$$I_{peak} = 40 A$$

$$(T = 25 ^\circ C)$$

$$(Pause < us)$$

Controller

1. Voltage & Current:

$$+1.05V @ 2.65A$$

2. Frequency:

$$PR143 = 1.3M \text{ ohm}$$

$$F_{osc} = 191KHz$$

3. OCP:

$$PR145 = 10K \text{ ohm} \rightarrow 9.5A$$

4. POR:

$$V_{certh} = 3.7 - 4.1V$$

$$V_{chys} = 0.2V$$

5. UVP:

$$V_{out} * 70\%$$

6. OVP:

$$V_{out} * 115\%$$

7. Enable Voltage:

$$V = 2.9V$$

8. Soft start time:

$$T_{ss} = 1.2 \text{ ms}$$

9. Phase selection:

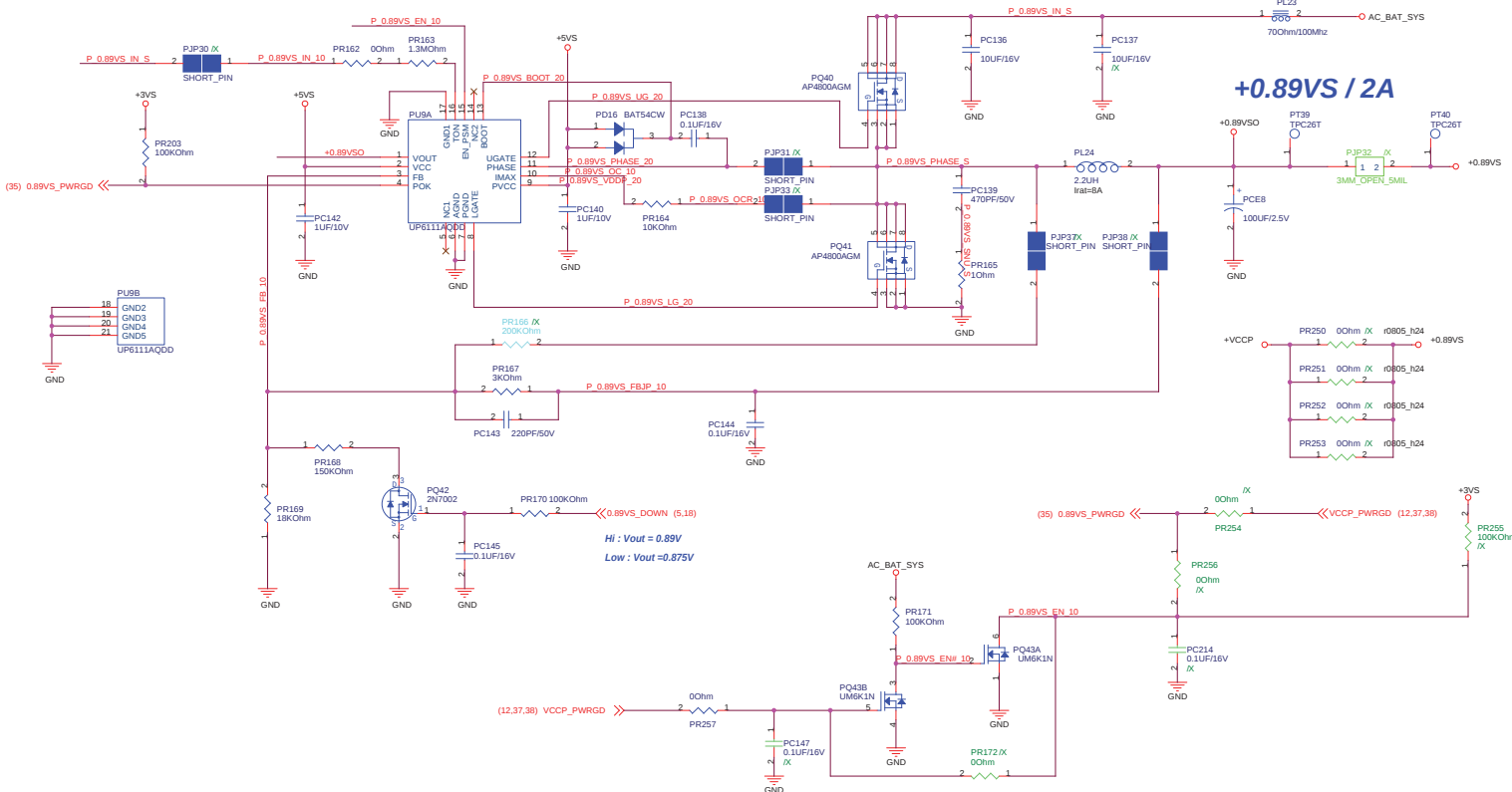
$$/X$$

10. Inrush Current:

$$C_{total} = 100 \text{ uF}$$

$$I_{inrush} = 0.0875 A$$

<Variant Name>



+0.89VS / 2A

Power stage

1. I/P Current:

$$I_{in} = V_o * I_o / (0.75 * V_{in}) = 0.4A$$

2. Ripple Current:

$$I_{rip} = 0.52A$$

$$I_{spec} = 2.5A \times 1 \text{ pcs}$$

3. Dynamic:

$$I_{peak} = 2A$$

$$ESR = 18 \text{ mohm}$$

$$\Delta V = 36mV$$

4. Inductor Spec:

$$I_{sat} = 14 A$$

$$I_{dc} = 8 A$$

$$DCR = 18 \text{ mohm}$$

5. MOSFET Spec:

H-side MOSFET: AP4800AGM

$$R_{ds(ON)} = 21 \text{ mohm} \quad (V_{gs} = 4.5 V)$$

$$I_{cont} = 9.6 A \quad (T = 25^\circ C)$$

$$I_{peak} = 40 A \quad (\text{Pause} < u s)$$

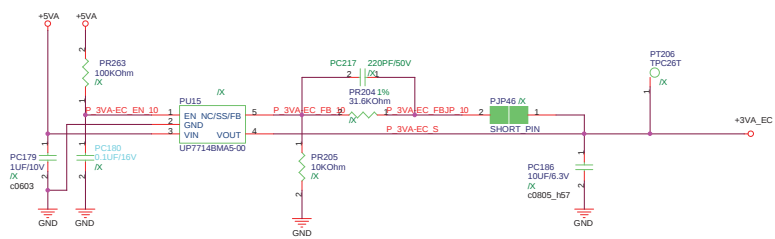
L-side MOSFET: RAP4800AGM

$$R_{ds(ON)} = 21 \text{ mohm} \quad (V_{gs} = 4.5 V)$$

$$I_{cont} = 9.6 A \quad (T = 25^\circ C)$$

$$I_{peak} = 40 A \quad (\text{Pause} < u s)$$

+3VA_AEC / 100mA



+3VA_AEC / 100mA

1. Dropout Voltage:

$$\Delta V = 0.21V \quad (I_o = 0.3A)$$

2.OCP:

$$I_{ocp} = 480mA$$

3. Short Circuit Current Limit:

$$I_{sc} = 320mA$$

4. Power Dissipation:

$$R_{thjc} = 250^\circ C/W$$

$$P_d = 0.4W$$

5. EN Voltage:

$$V_{en} = 2V$$

$$V_{sd} = 0.4V$$

6. Power OK Voltage:

$$V_{pokh} = 92\% * V_{out}$$

$$V_{pokhys} = 8\%$$

8. FB Voltage:

$$V_{FB} = 0.8 V$$

Controller

1. Voltage & Current:

$$+0.89VS @ 2A$$

2. Frequency:

$$f_{osc} = 251KHz$$

3. OCP:

$$\text{Set } PR164 = 10Kohm$$

$$I_{ocp} = 9.5A$$

4. POR:

$$POR \text{ Hysteresis} = 0.2V$$

$$V_{on} = 3.9V$$

5. UVP:

$$V_{uvp} = 70\% V_{out}$$

6. OVP:

$$V_{ovp} = 115\% V_{out}$$

7. Enable Voltage:

$$V = 2.9 V$$

8. Soft start time:

$$T_{ss} = 1.2ms$$

9. Phase selection:

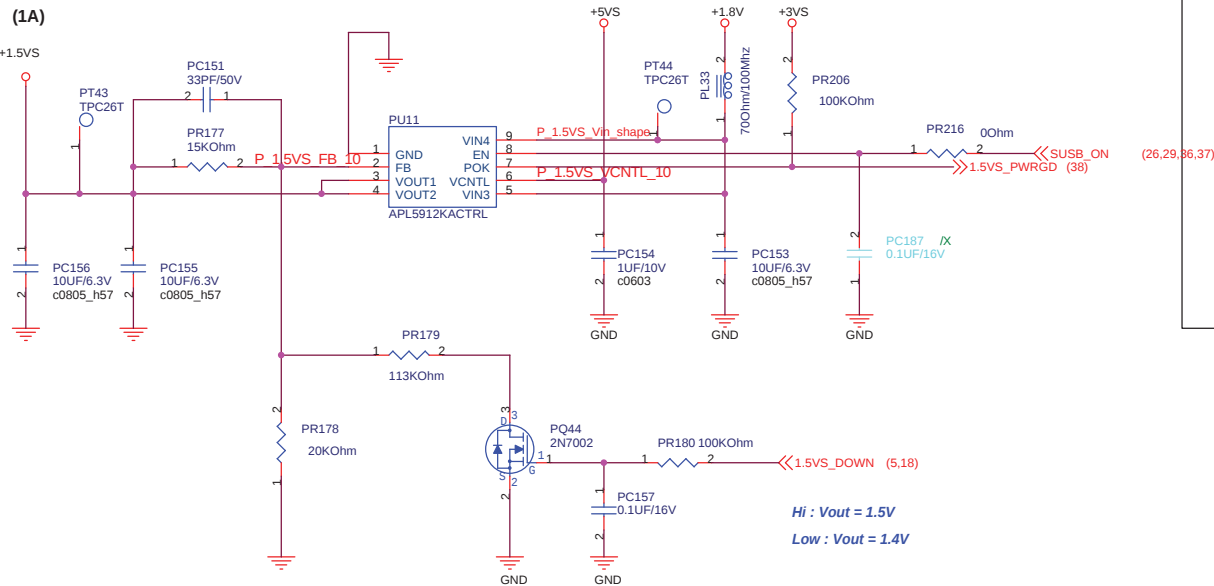
$$IX$$

10. Inrush Current:

$$C_{total} = 100 \mu F$$

$$I_{inrush} = 0.074 A$$

<Variant Name>



1.5VS @ 2 A

1. Dropout Voltage:

$$\Delta V = 0.2V \text{ (Io = 5A)}$$

2. Current Limit:

$$I_{\text{limit}} = 7A$$

3. Continue Current:

$$I_{\text{cont}} = 6A$$

4. Power Dissipation:

$$R_{\text{thjc}} = 40^{\circ}\text{C/W}$$

$$P_d = 3W$$

5. EN Voltage:

$$V_{\text{rising}} = 0.4V$$

$$\text{Hysteresis} = 30mV$$

6. Supply Voltage:

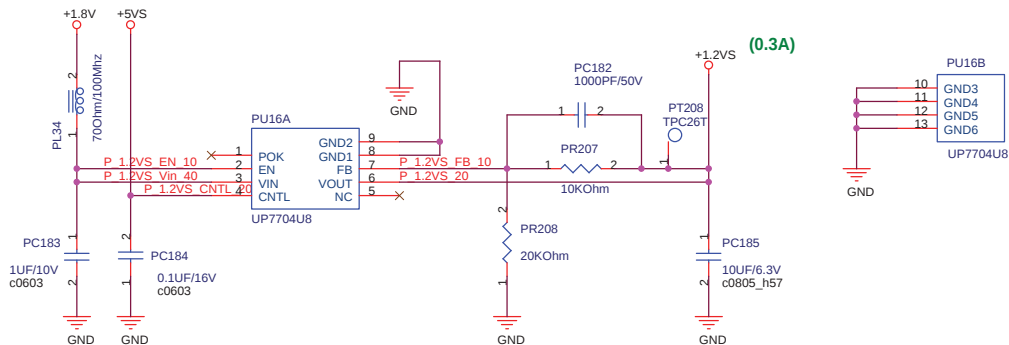
$$V_{\text{cc}} = 5V$$

7. Inrush current:

$$T_{\text{ss}} = 2ms$$

$$C_{\text{total}} = 20\mu F$$

$$I_{\text{inrush}} = 15mA$$



1.2VS @ 0.3A

1. Dropout Voltage:

$$\Delta V = 0.3V \text{ (Io = 1A)}$$

2.OCP:

$$I_{\text{ocp}} = 1.5A$$

3. Short Circuit Current Limit:

$$I_{\text{sc}} = 300mA$$

4. Power Dissipation:

$$R_{\text{thjc}} = 75^{\circ}\text{C/W}$$

$$P_d = 1.33W$$

5. EN Voltage:

$$V_{\text{en}} = 1.6V$$

$$V_{\text{sd}} = 0.4V$$

6. Power OK Voltage:

$$V_{\text{pokth}} = 92\% \cdot V_{\text{out}}$$

$$V_{\text{pokhys}} = 8\%$$

7. Inrush current:

$$T_{\text{ss}} = 5ms$$

$$C_{\text{total}} = 10\mu F$$

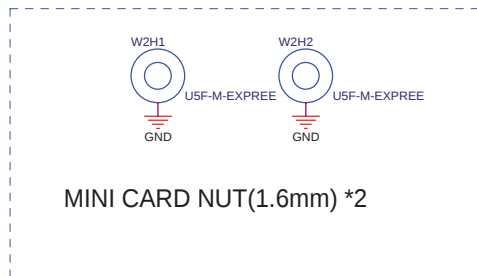
$$I_{\text{inrush}} = 2.4mA$$

8. FB Voltage:

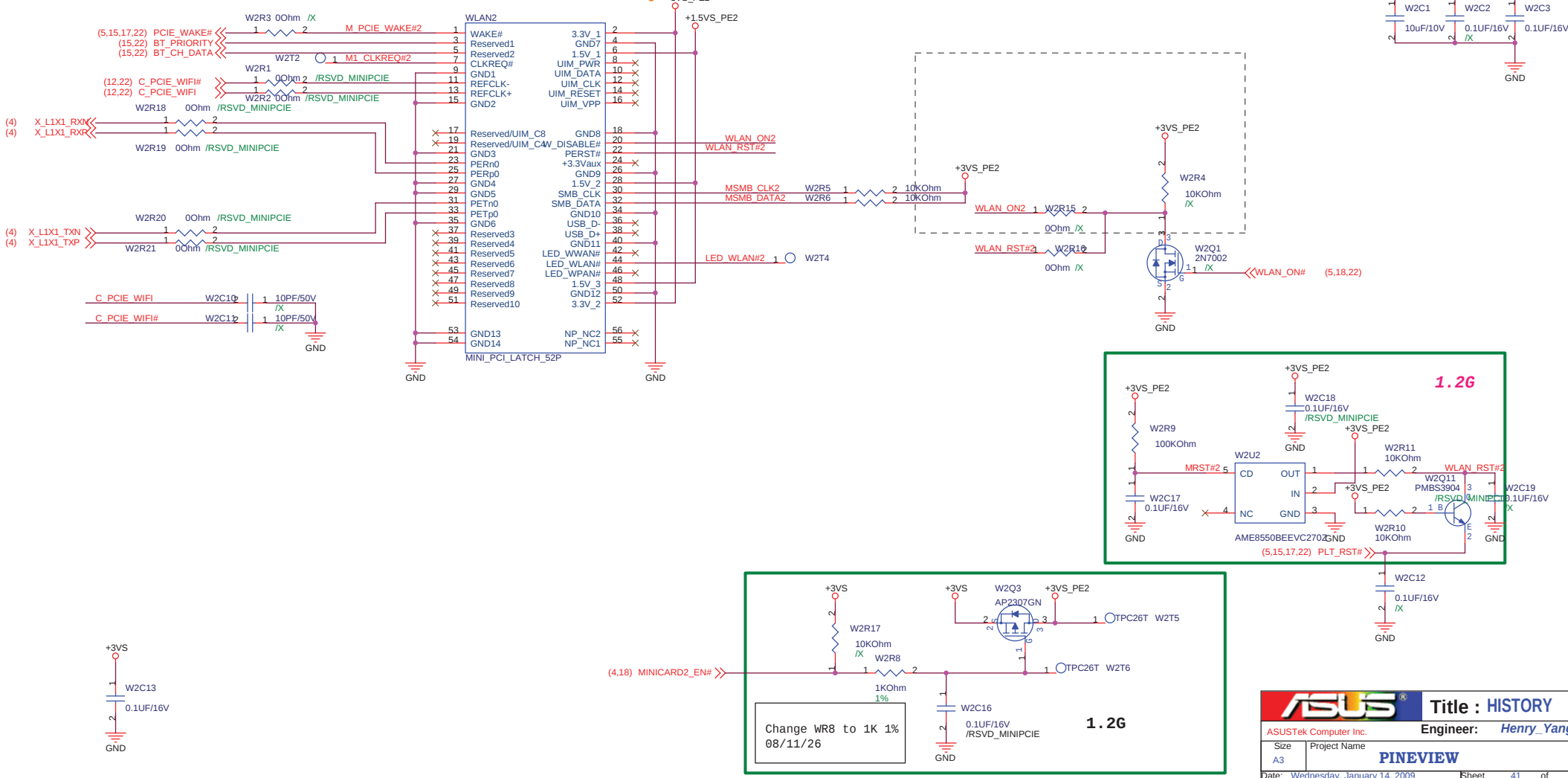
$$V_{\text{FB}} = 0.8V$$

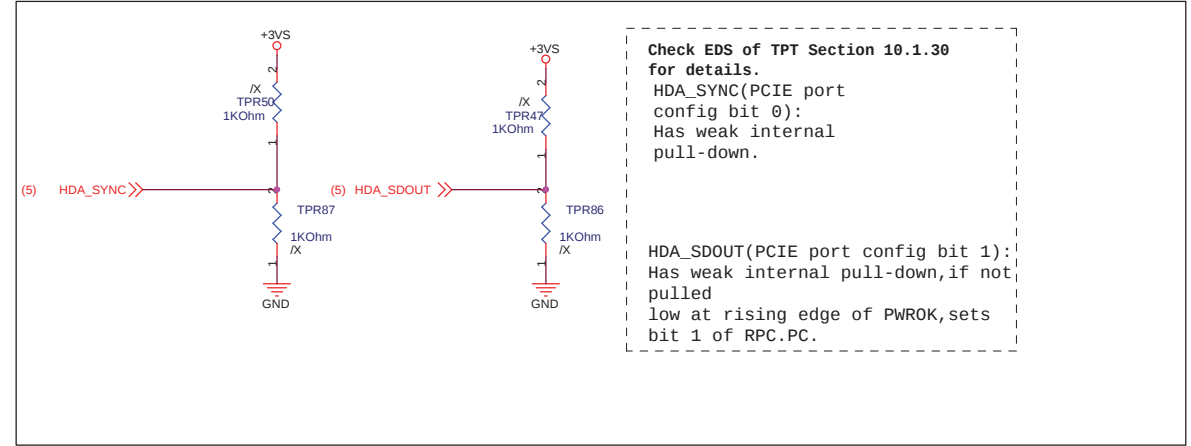
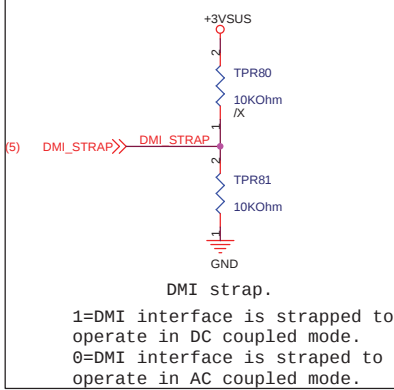
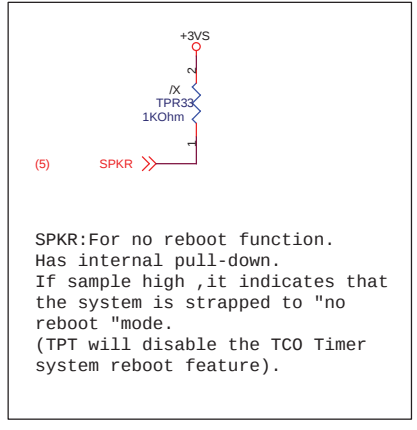
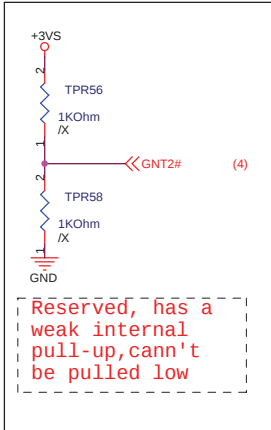
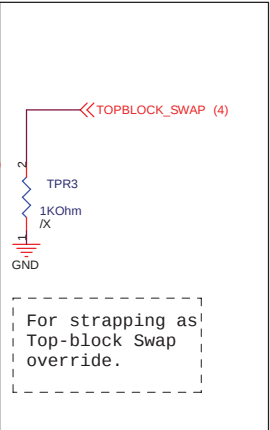
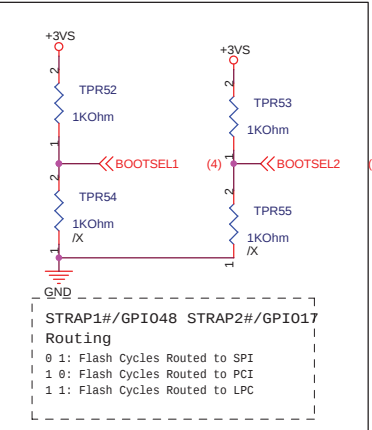
<Variant Name>

1.Delete LCD liner power sck .



MINICARD use 12G03010052Q





From 1225.1900 change 3GC1 to 47u, modify screw to 13G021050010.