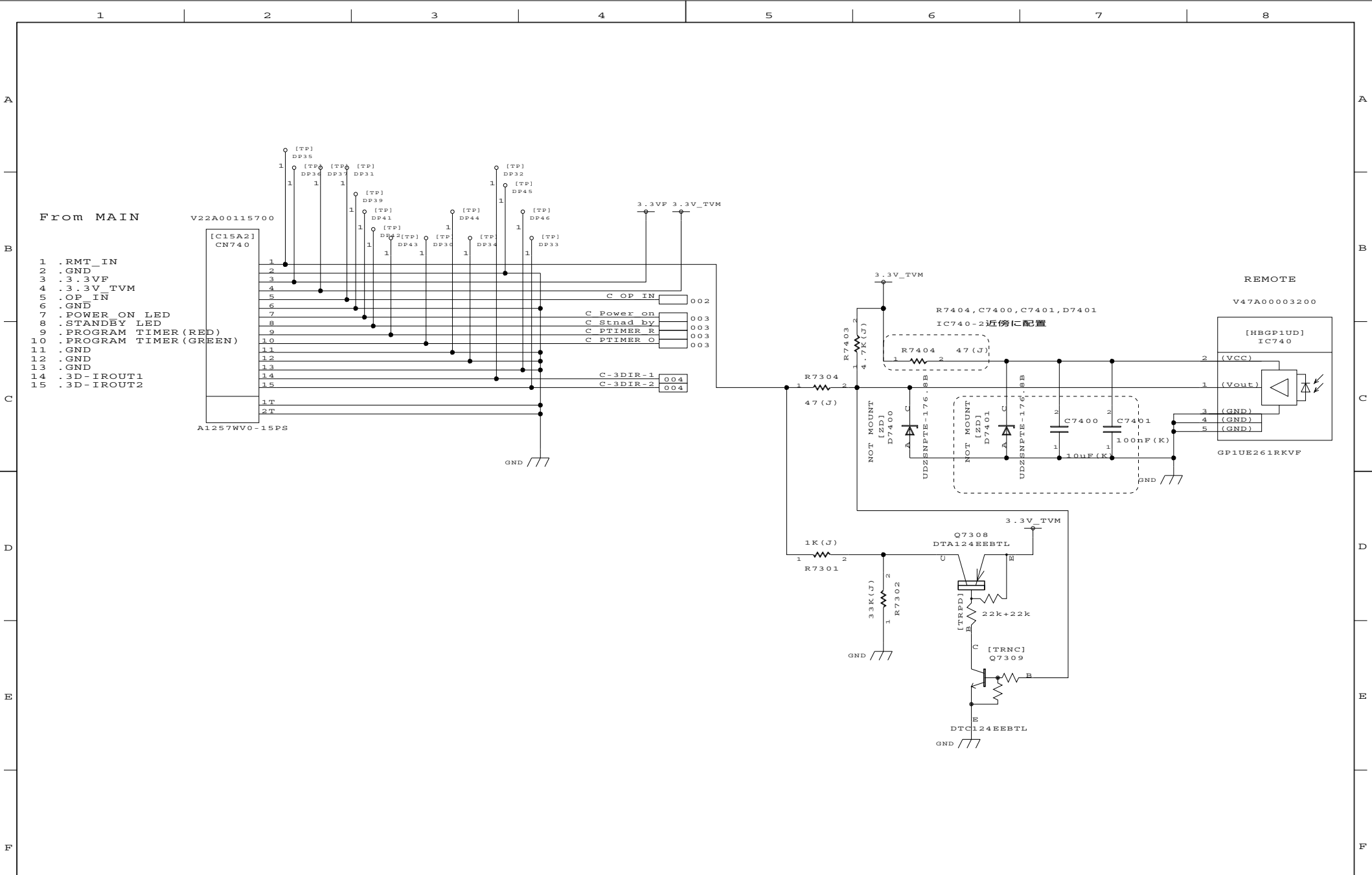
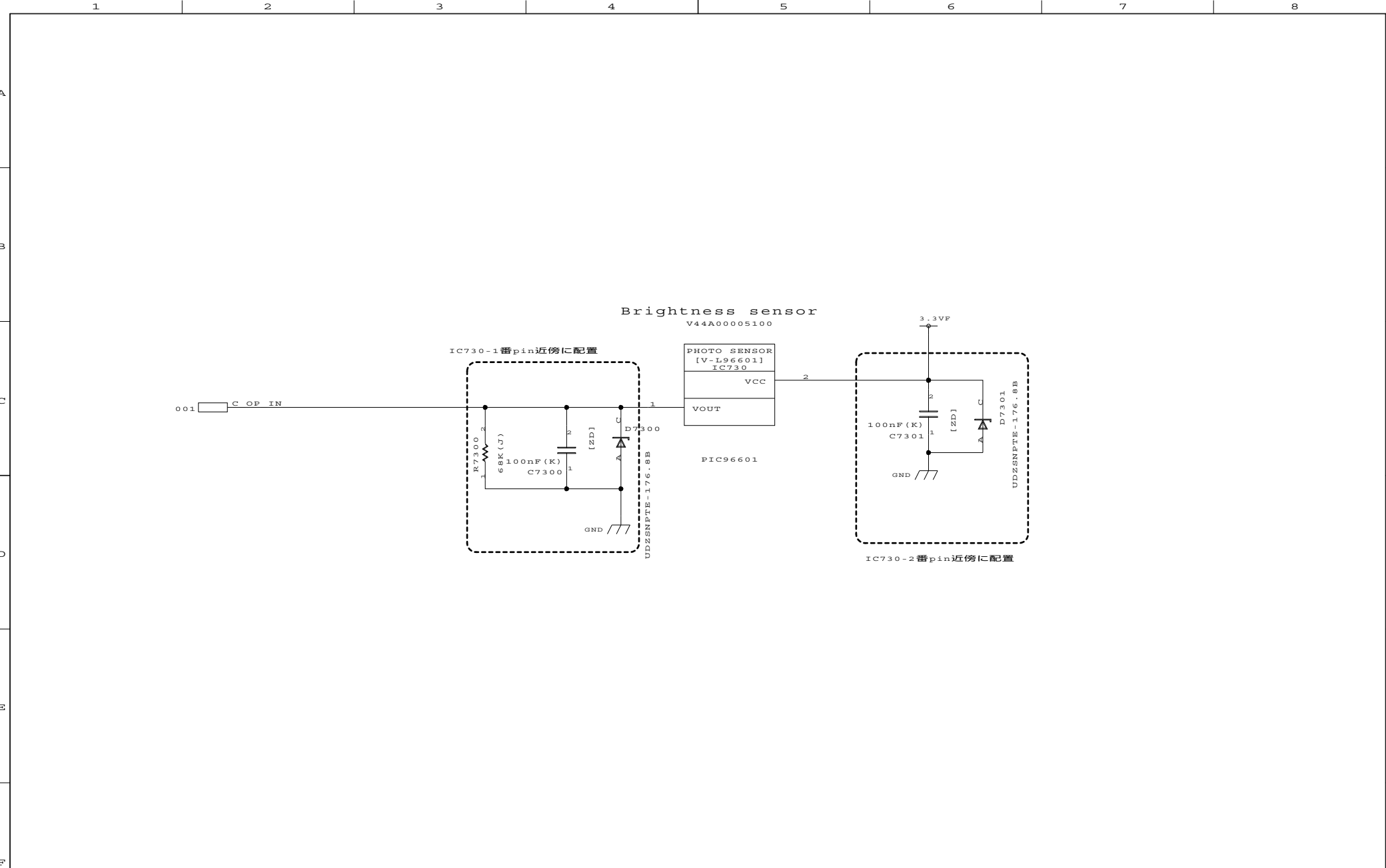
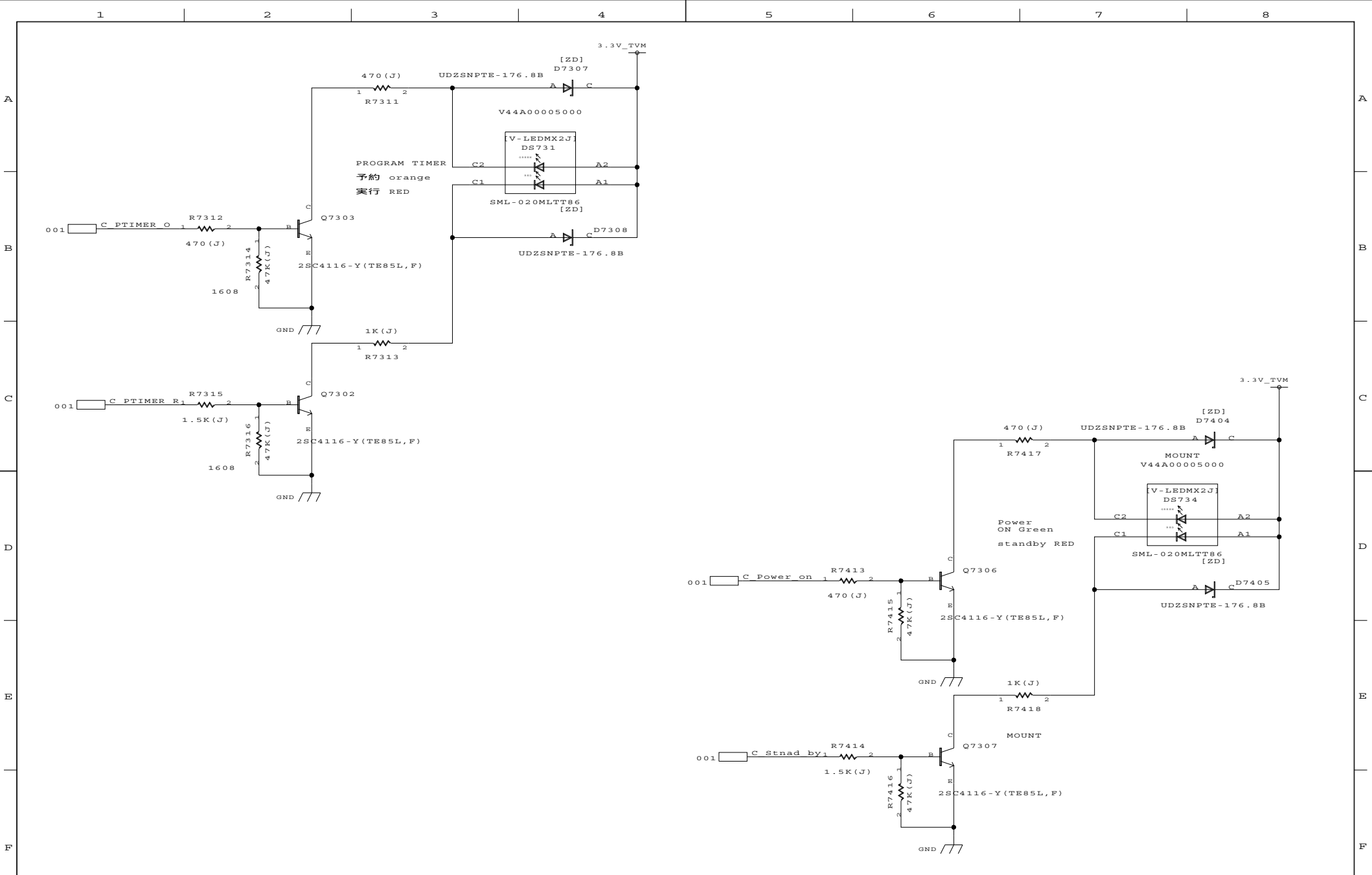




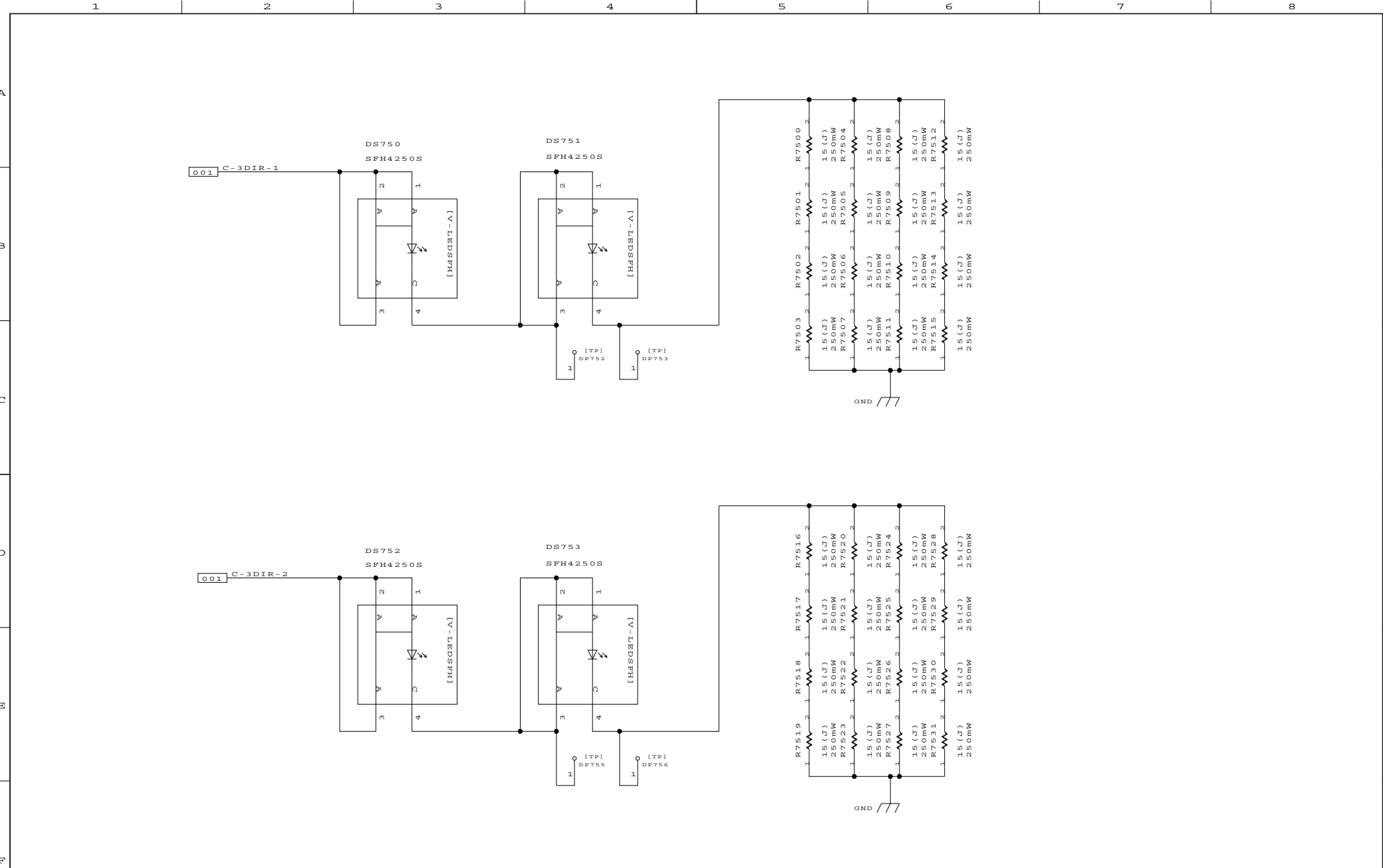
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T. IMURA	2010_LED_RMT	RMT	1	1	00	???
2011.07.26	11:45					TOSHIBA CORPORATION



DESIGNED BY T. IMURA	TITLE 2011_LED_RMT	FUNCTION LED SENSOR	SH.NO. 2	PAGE NO. 2	REV.MARK 00	DRAWING.NO. ???
2011.07.26 11:45	TOSHIBA CORPORATION					



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T. IMURA	2010_LED_RMT	LED Driver	3	3	00	**
2011.07.26	11:45					TOSHIBA CORPORATION



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
M. SATO	2011_LED_RMT	3D-IR	4	4	00	
2011.07.26 11:45			TOSHIBA CORPORATION			

SCHEMATIC DIAGRAMS

PRODUCT NAME RL838/858

UNIT NAME MAIN

UNIT NO. PE1014ZA

PWA NAME

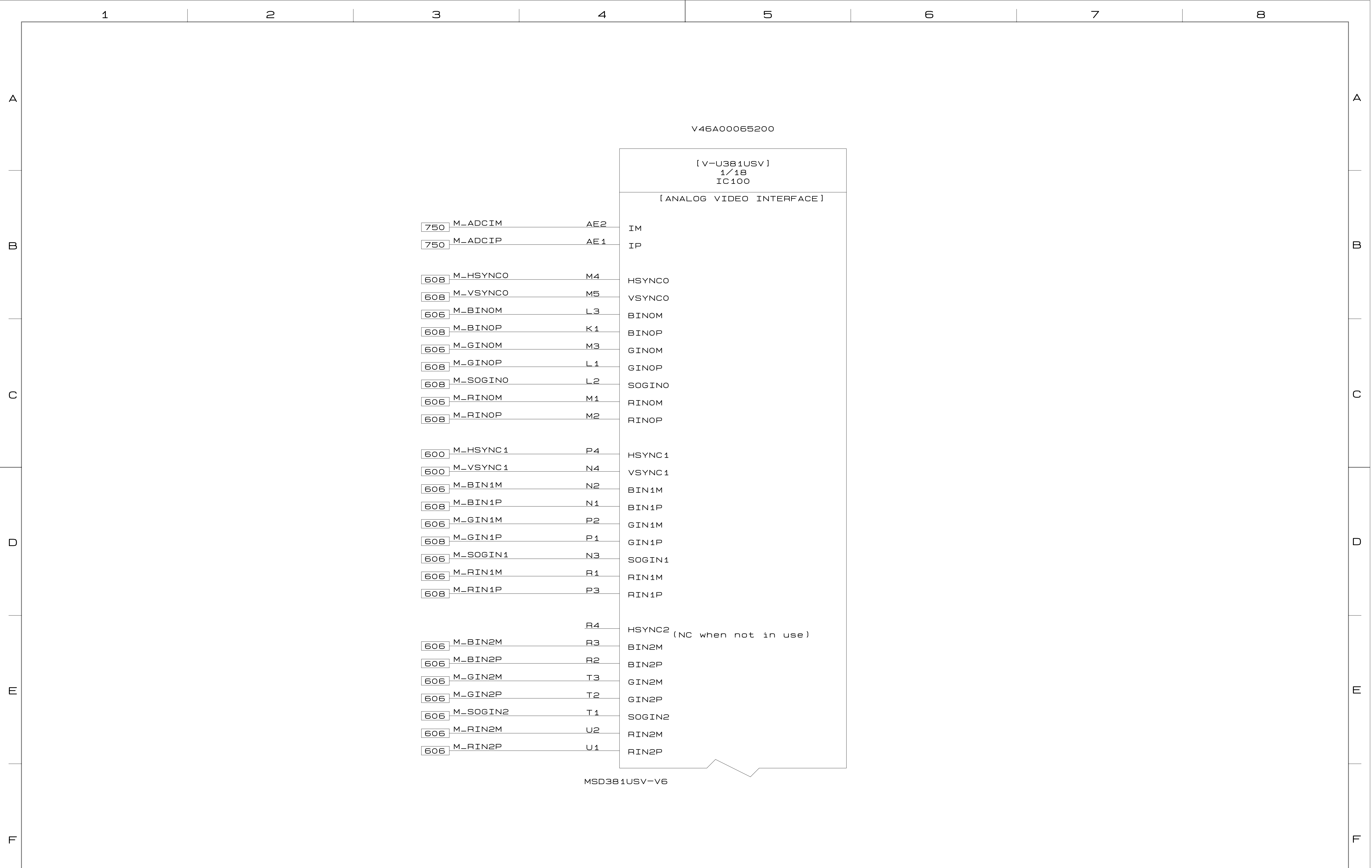
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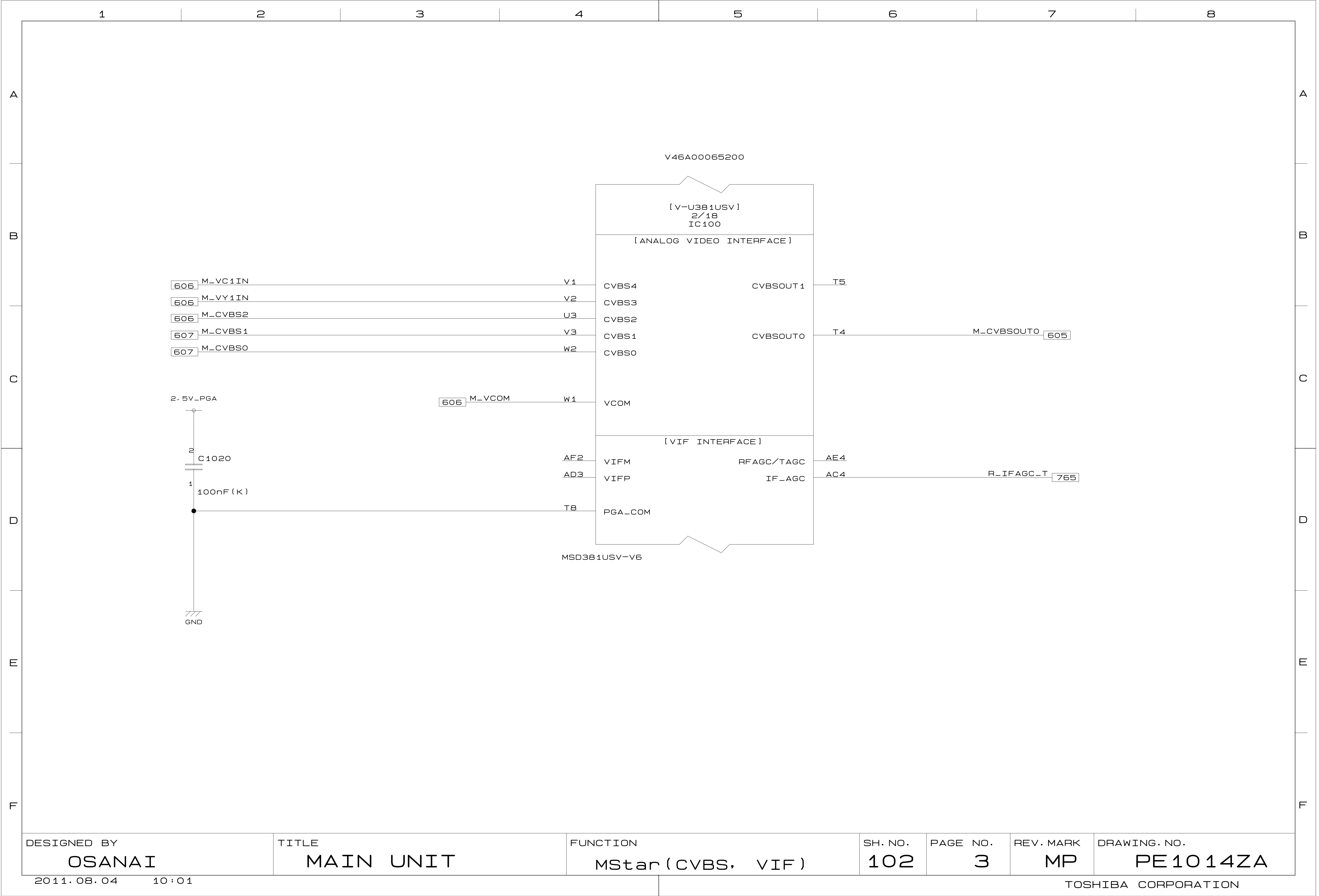
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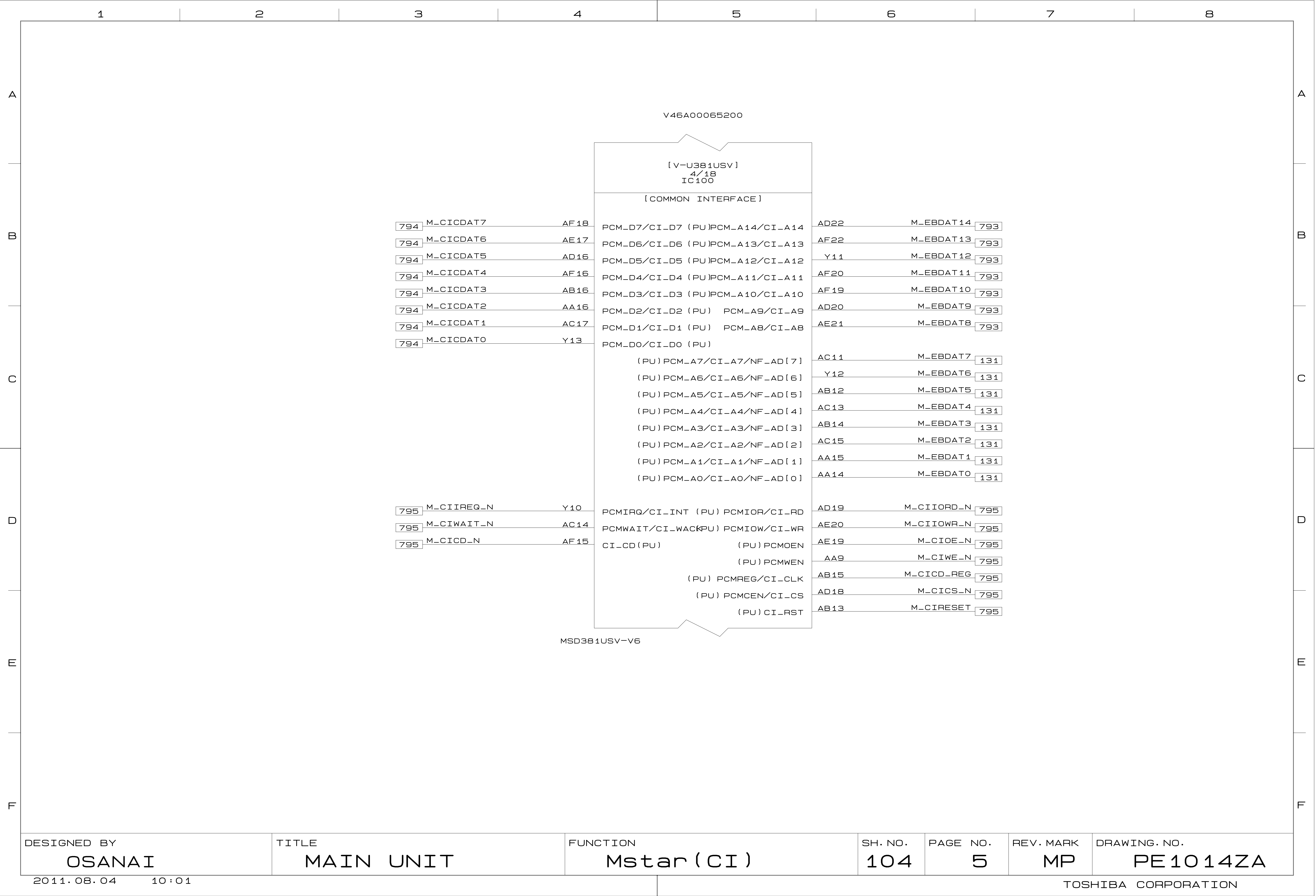
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PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV
1	---	COVER SHEET	MP	26	131	EBUS RESISTOR	MP	51	663	HDMI HPD(2)	MP	76			
2	101	MStar (Component Video)	MP	27	132	SPI FLASH	MP	52	664	HDMI DDC	MP	77			
3	102	MStar (CVBS, VIF)	MP	28	134	NAND FLASH	MP	53	665	3.3V_HDMI	MP	78			
4	103	MStar (Audio, SIF)	MP	29	150	ETHER PHY	MP	54	669	HDCP KEY ROM	MP	79			
5	104	Mstar (CI)	MP	30	151	RJ45 CONNECTOR	MP	55	700	LED/IR IF	MP	80			
6	105	Mstar (TS)	MP	31	160	USB CONNECTOR(side)	MP	56	701	5V LED/KEY	MP	81			
7	106	Mstar (HDMI 1/2)	MP	32	310	AUDIO TV OUTPUT	MP	57	704	TVM FAN CONTROL	MP	82			
8	107	Mstar (HDMI 2/2)	MP	33	321	Headphone AMP	MP	58	750	TUNER	MP	83			
9	108	MStar (LVDS 1/2)	MP	34	360	OPTICAL OUTPUT	MP	59	765	DEMOD DVB-T2/T/C	MP	84			
10	109	MStar (LVDS 2/2)	MP	35	370	AUDIO D-AMP (MAIN)	MP	60	780	TUNER POWER	MP	85			
11	110	MStar (DDR A)	MP	36	371	AUDIO D-AMP OUT (MAIN)	MP	61	790	CI TS RESISTOR	MP	86			
12	111	MStar (DDR B)	MP	37	373	AUDIO MUTE	MP	62	791	CI POWER/CONNECTOR	MP	87			
13	112	MStar (DDR C)	MP	38	600	EURO SCART	MP	63	793	CI ADDRESS BUFFER	MP	88			
14	113	Mstar (NAND, FRC)	MP	39	601	FRONT AV	MP	64	794	CI DATA RESISTOR	MP	89			
15	114	MStar (GPIO)	MP	40	602	DSUB PC IN	MP	65	795	CI CONTROL BUFFER	MP	90			
16	115	Mstar (USB, MISC)	MP	41	603	EURO COMPONENT IN	MP	66	796	CI TS OUTPUT BUFFER	MP	91			
17	116	MStar (Power 1/3)	MP	42	605	EURO TV OUT	MP	67	901	3.3V STANDBY	MP	92			
18	117	MStar (Power 2/3)	MP	43	606	AV INPUT1	MP	68	902	POWER 1.1V	MP	93			
19	118	MStar (Power 3/3)	MP	44	607	AV INPUT2	MP	69	904	FRC POWER 1.5V	MP	94			
20	120	PERI (XTAL, STRAPS)	MP	45	608	AV INPUT3	MP	70	905	POWER 3.3VD	MP	95			
21	121	RESET, I2C	MP	46	650	LVDS CONNECTOR	MP	71	906	POWER 6V	MP	96			
22	122	PERI (SERIAL)	MP	47	652	BL_CONTROL & DIMMING	MP	72	950	POWER CONNECTOR	MP	97			
23	126	DDR3 SDRAM	MP	48	660	HDMI CONNECTOR	MP	73	960	EMC CONTROL	MP	98			
24	127	DDR3 Damping Resistors	MP	49	661	HDMI 5VPWR	MP	74				99			
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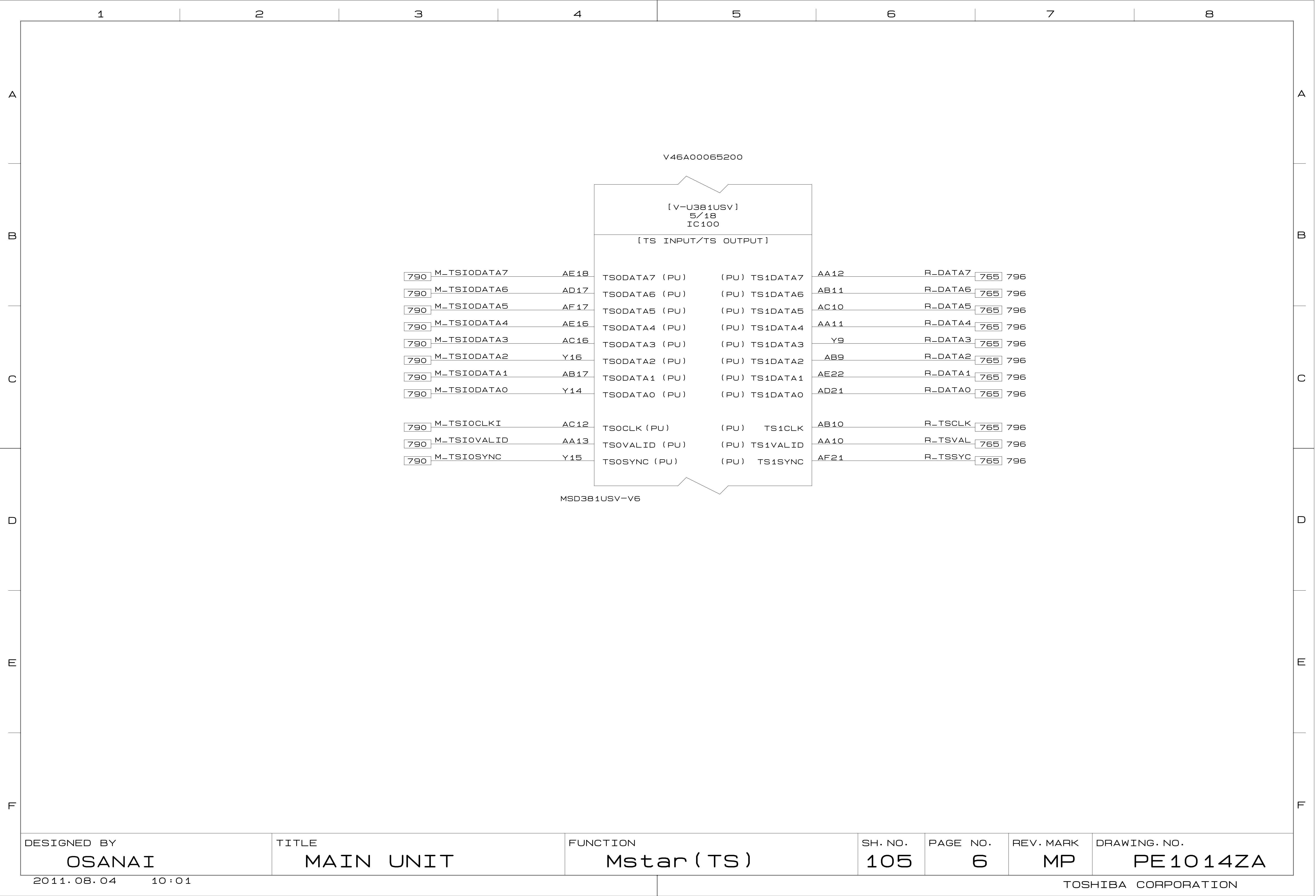
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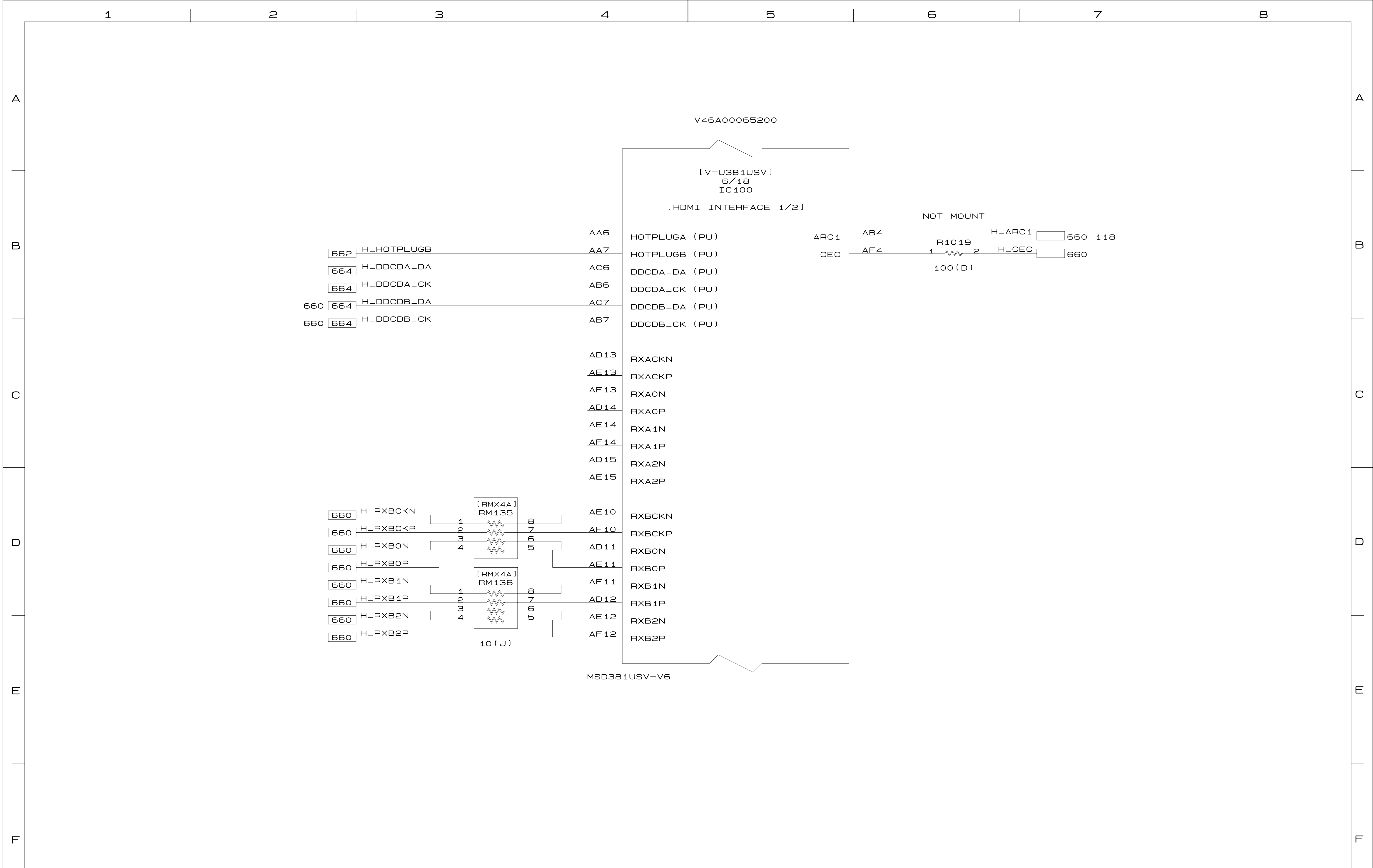


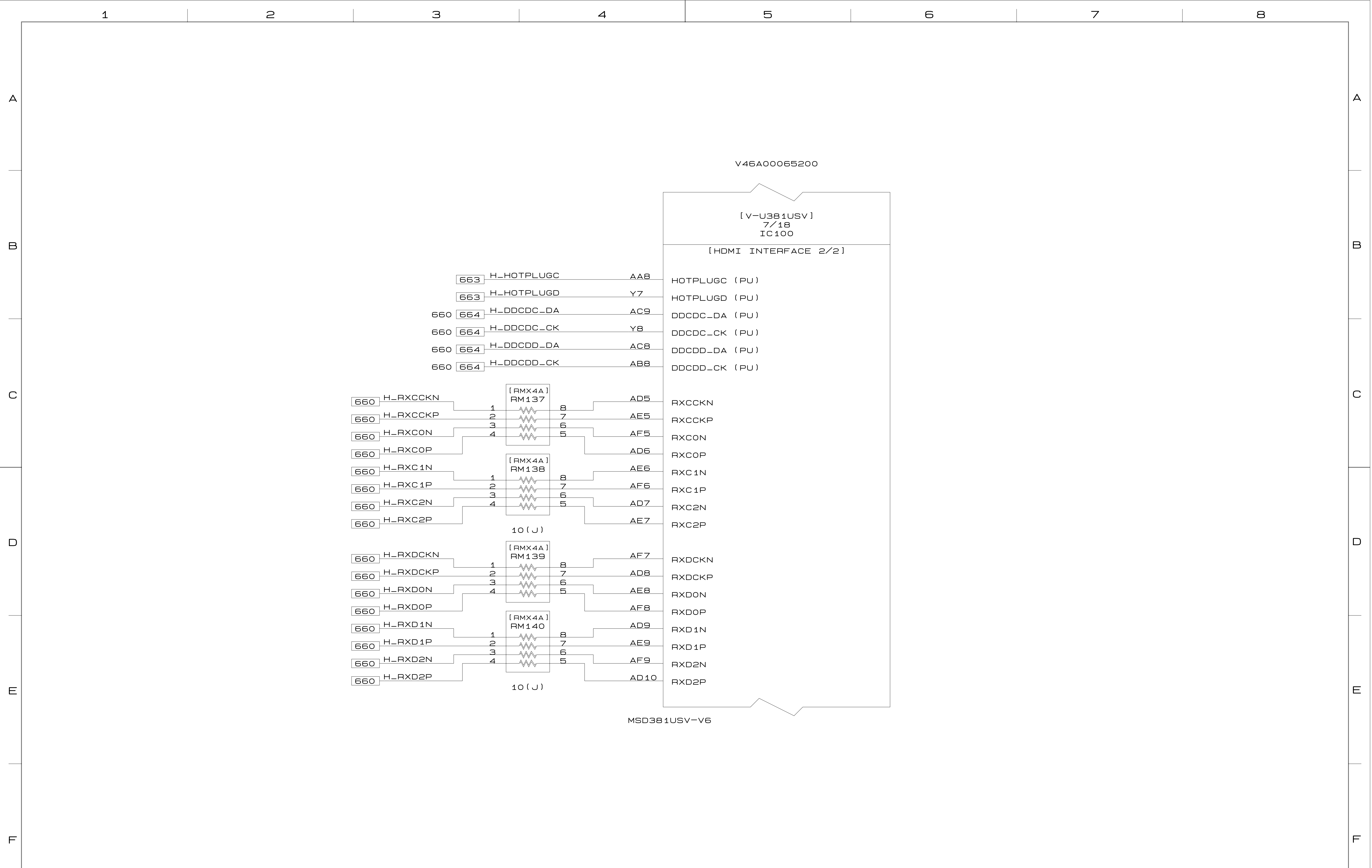


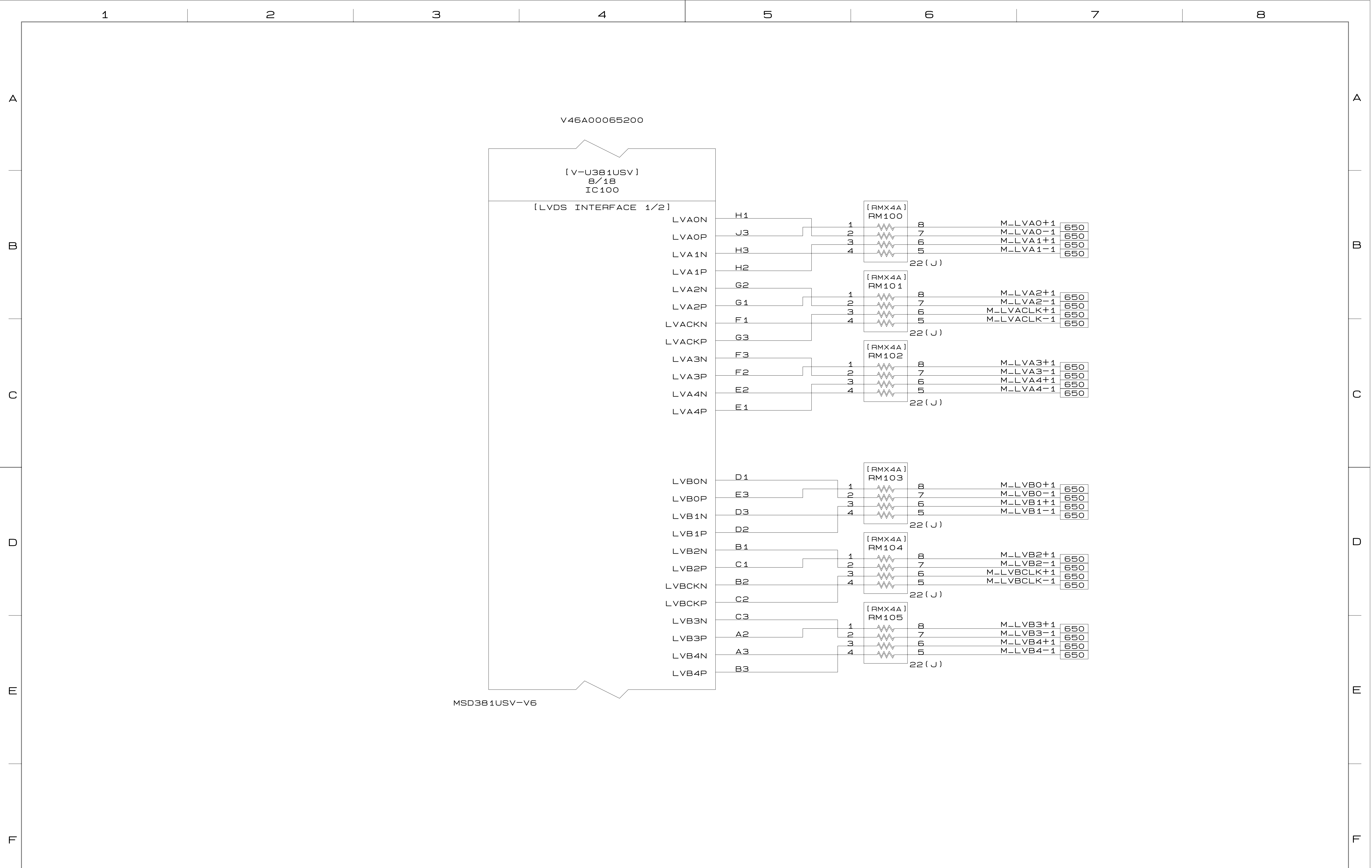


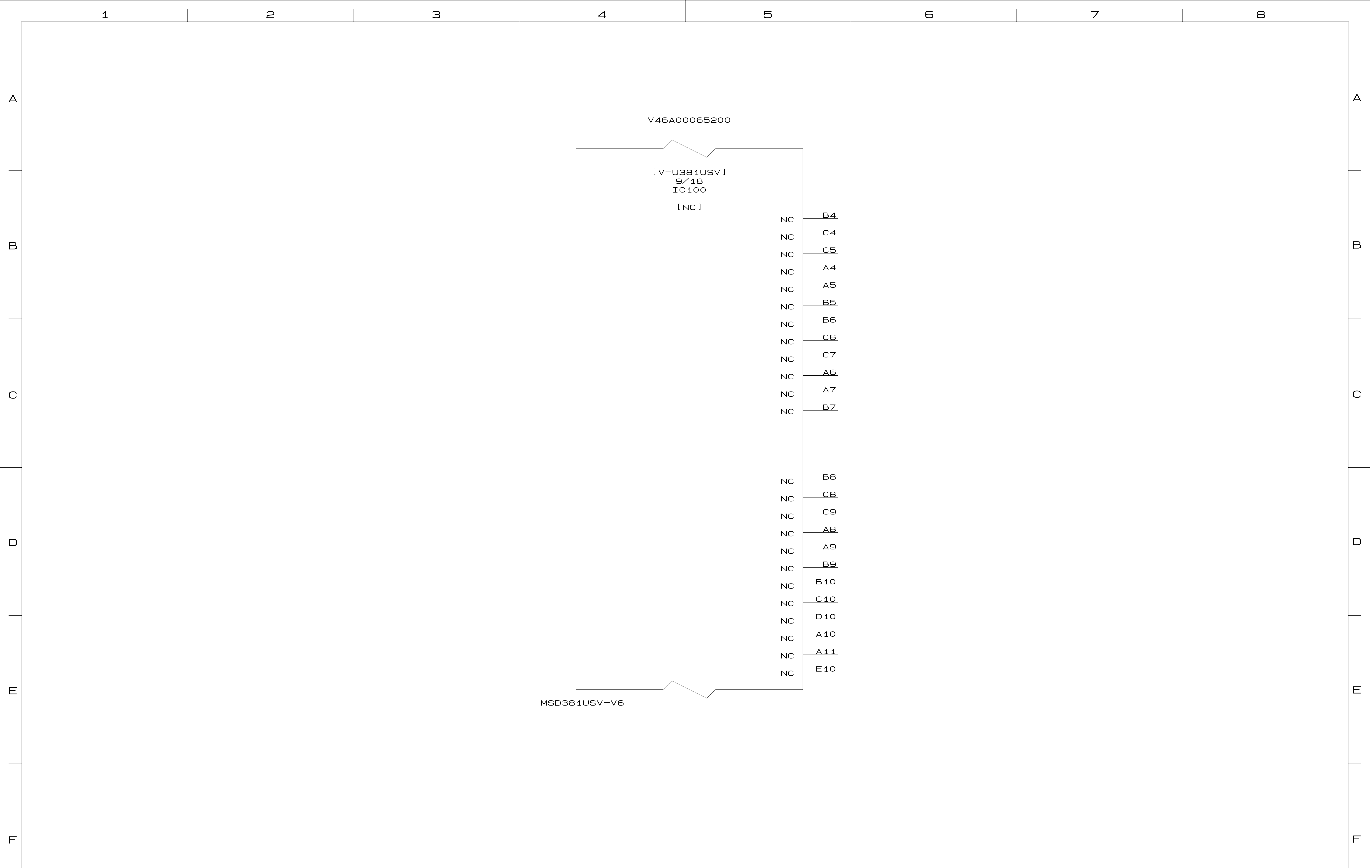
MSD381USV-V6



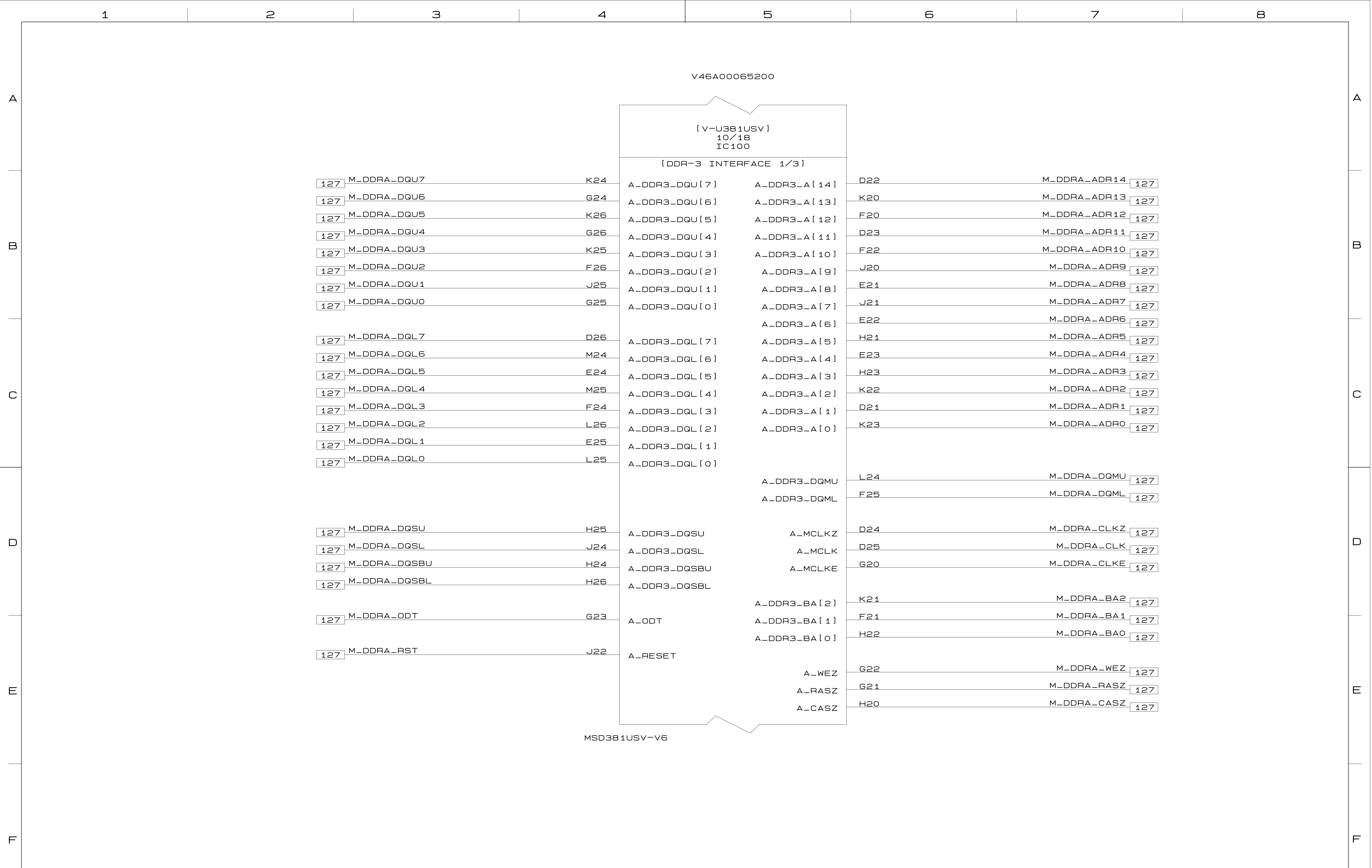








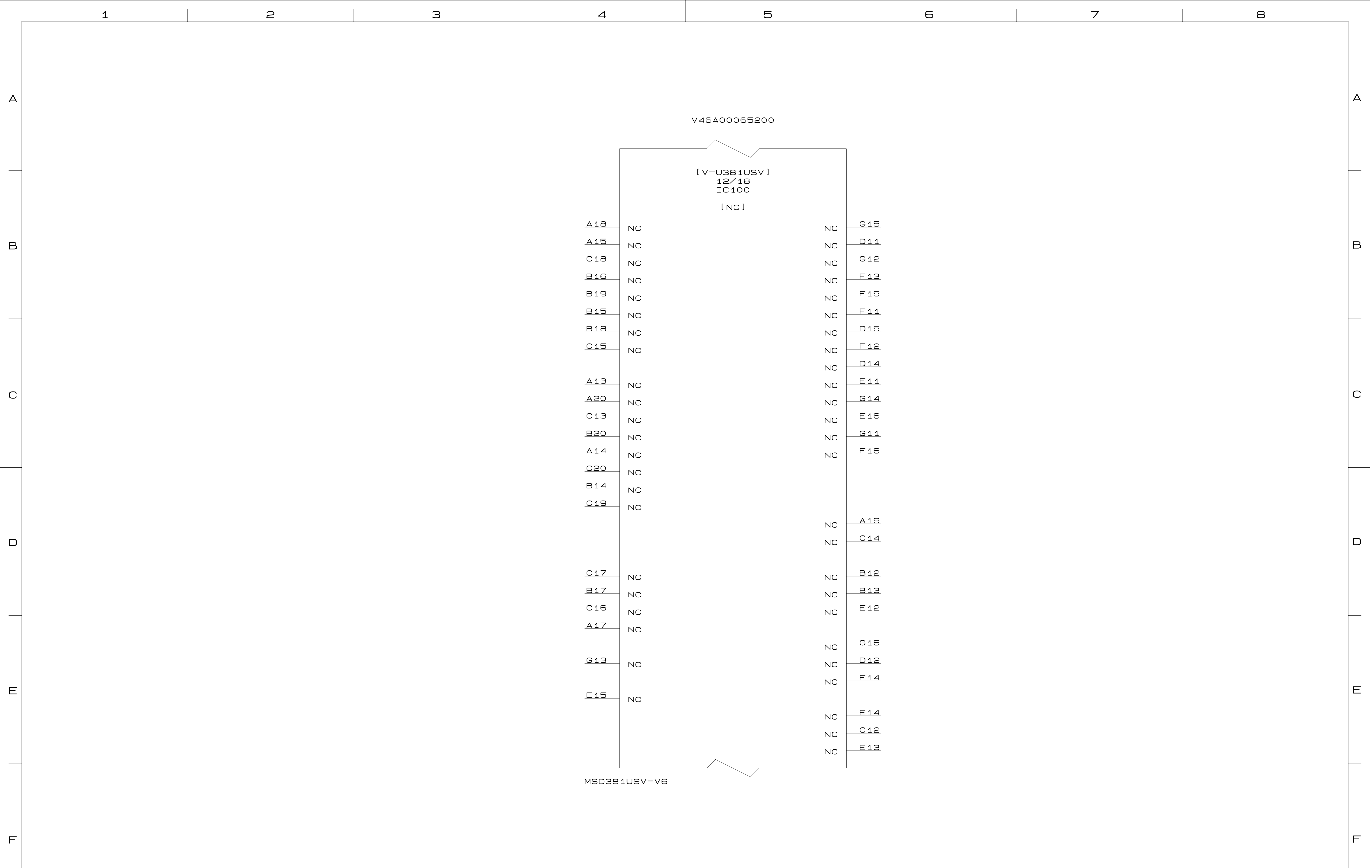
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OSANAI	MAIN UNIT	MStar (LVDS 2/2)	109	10	MP	PE1014ZA

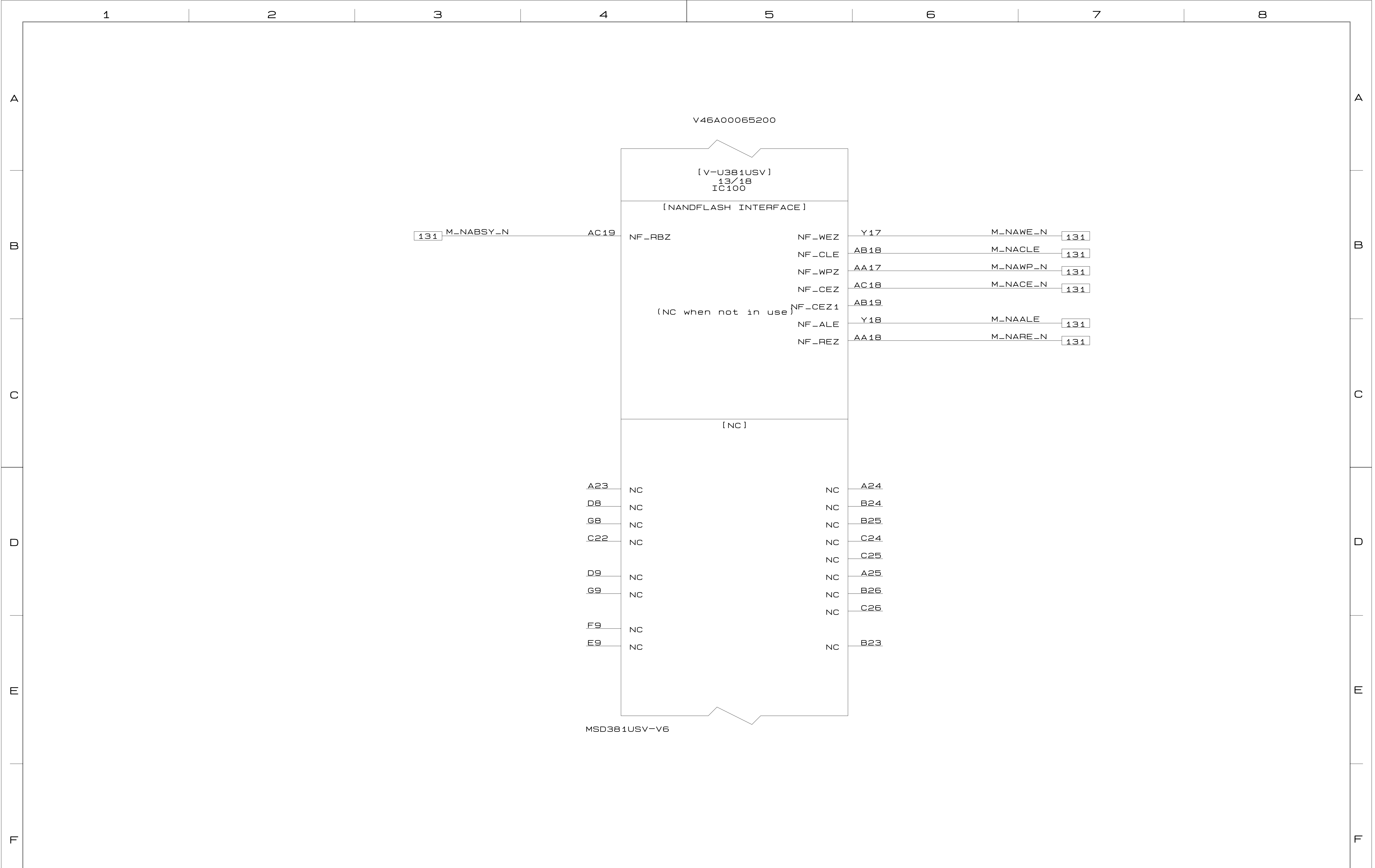


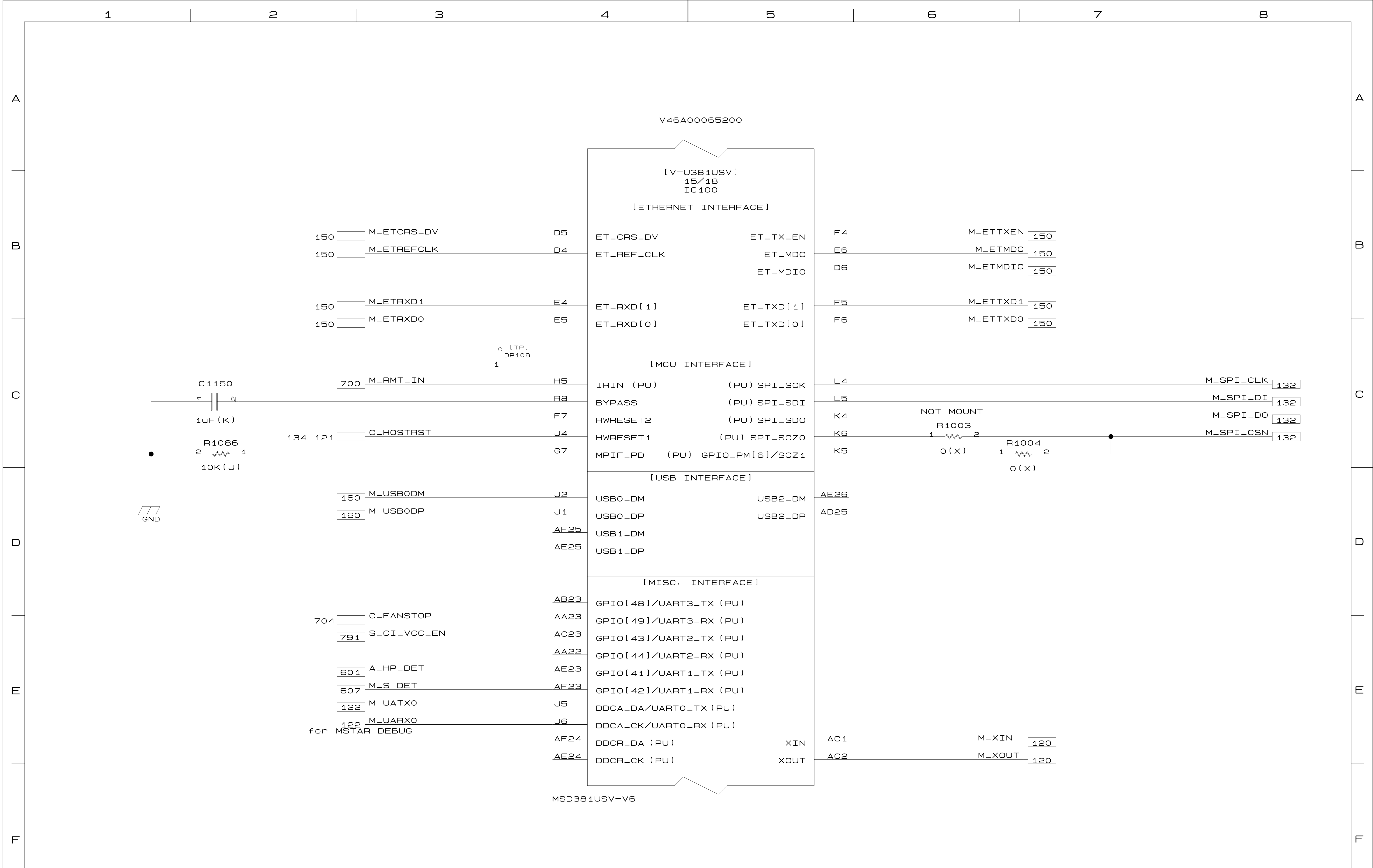
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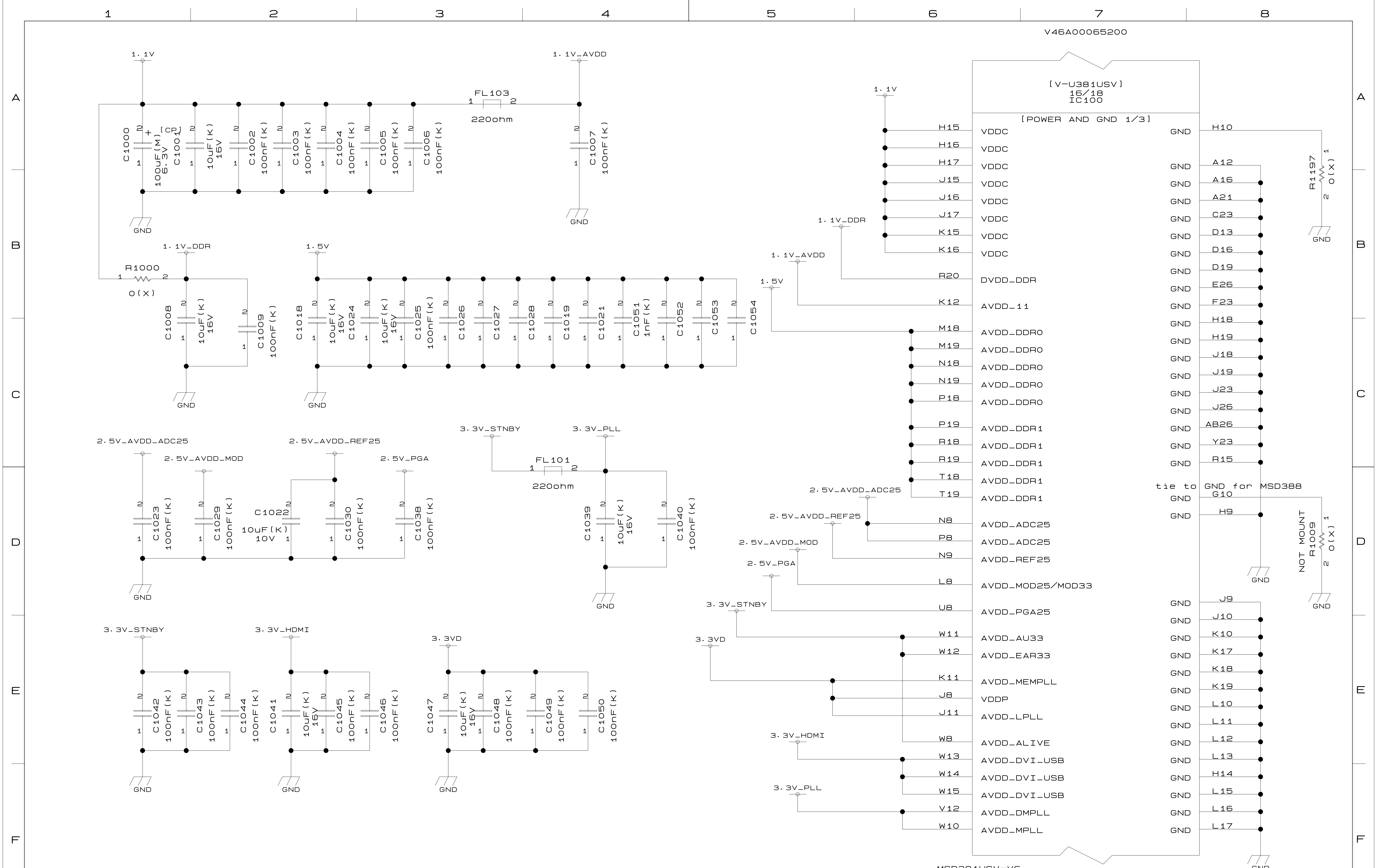
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A										A
	V46A00065200									
	[V-U381USV] 11/18 IC100									
	[DDR-3 INTERFACE 2/3]									
B	128	M_DDRB_DQU7	W25	B_DDR3_DQU[7]	B_DDR3_A[14]	M22	M_DDRB_ADR14	128		
	128	M_DDRB_DQU6	T26	B_DDR3_DQU[6]	B_DDR3_A[13]	U22	M_DDRB_ADR13	128		
	128	M_DDRB_DQU5	Y26	B_DDR3_DQU[5]	B_DDR3_A[12]	N24	M_DDRB_ADR12	128		
	128	M_DDRB_DQU4	U25	B_DDR3_DQU[4]	B_DDR3_A[11]	M23	M_DDRB_ADR11	128		
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	128	M_DDRB_DQU1	W26	B_DDR3_DQU[1]	B_DDR3_A[8]	N23	M_DDRB_ADR8	128		
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					B_DDR3_A[6]	M20	M_DDRB_ADR6	128		
C	128	M_DDRB_DQL7	P26	B_DDR3_DQL[7]	B_DDR3_A[5]	R21	M_DDRB_ADR5	128		
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	128	M_DDRB_DQL2	AA26	B_DDR3_DQL[2]	B_DDR3_A[0]	U21	M_DDRB_ADR0	128		
	128	M_DDRB_DQL1	R25	B_DDR3_DQL[1]						
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					B_DDR3_DQML	R24	M_DDRB_DQML	128		
D										
	128	M_DDRB_DGSU	V26	B_DDR3_DGSU	B_MCLKZ	N26	M_DDRB_CLKZ	128		
	128	M_DDRB_DQSL	V24	B_DDR3_DQSL	B_MCLK	N25	M_DDRB_CLK	128		
	128	M_DDRB_DQSBU	U24	B_DDR3_DQSBU	B_MCLKE	N20	M_DDRB_CLKE	128		
	128	M_DDRB_DQSBL	V25	B_DDR3_DQSBL						
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	128	M_DDRB_ODT	P22	B_ODT	B_DDR3_BA[1]	P23	M_DDRB_BA1	128		
					B_DDR3_BA[0]	T23	M_DDRB_BA0	128		
E	128	M_DDRB_RST	T22	B_RESET						
					B_WEZ	R23	M_DDRB_WEZ	128		
					B_RASZ	P21	M_DDRB_RASZ	128		
					B_CASZ	P20	M_DDRB_CASZ	128		
	MSD381USV-V6									
F										F

DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION MStar (DDR B)	SH. NO. 111	PAGE NO. 12	REV. MARK MP	DRAWING. NO. PE1014ZA
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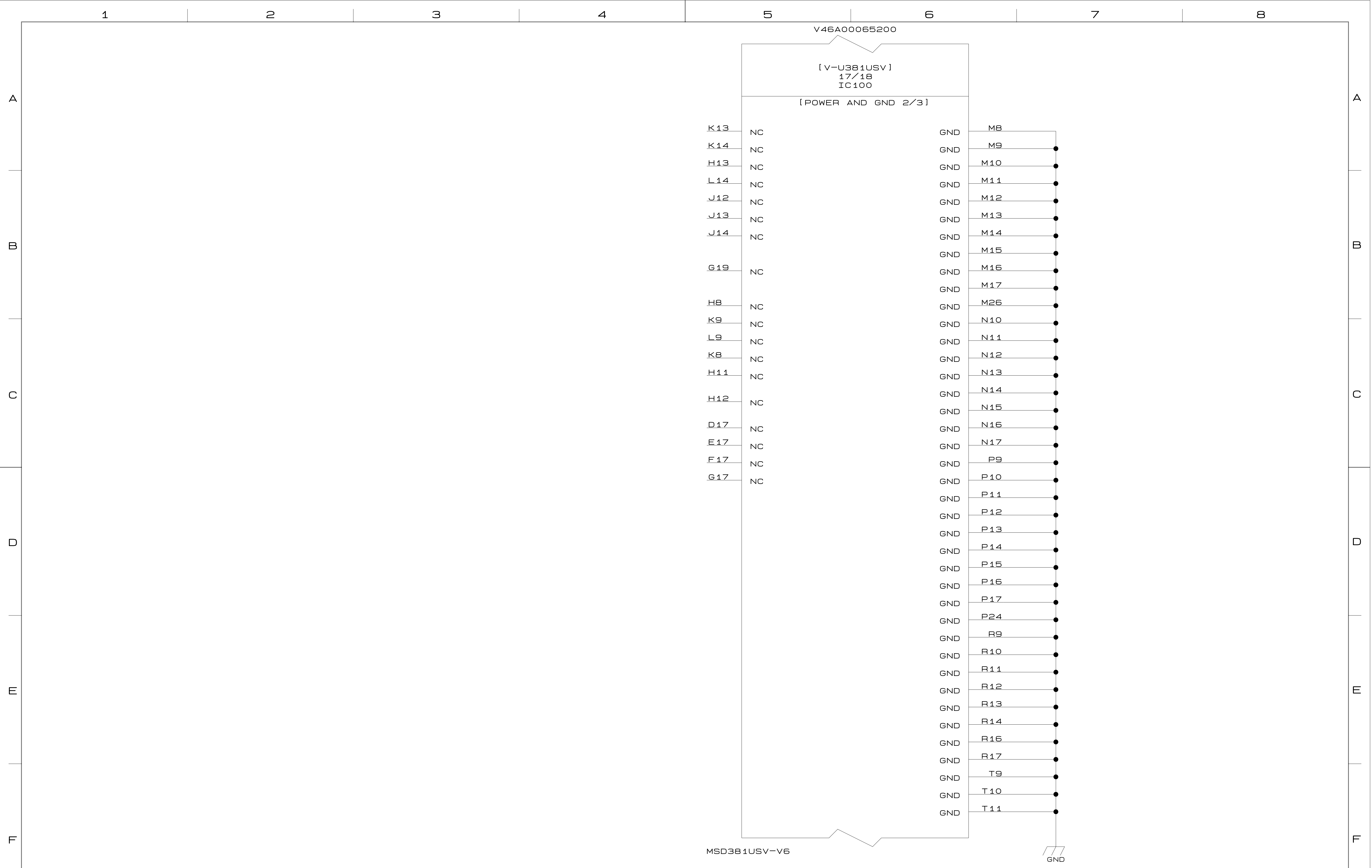


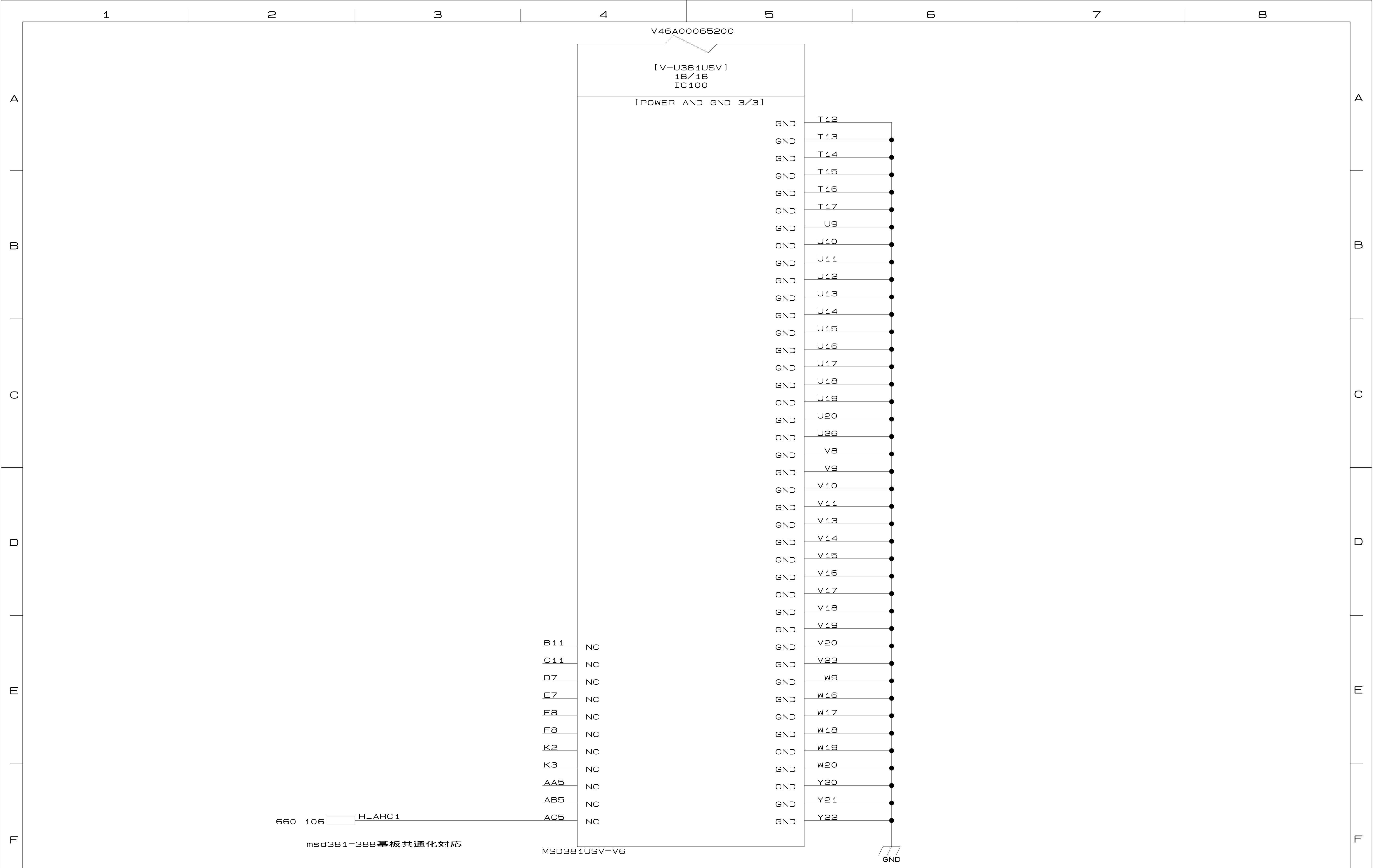




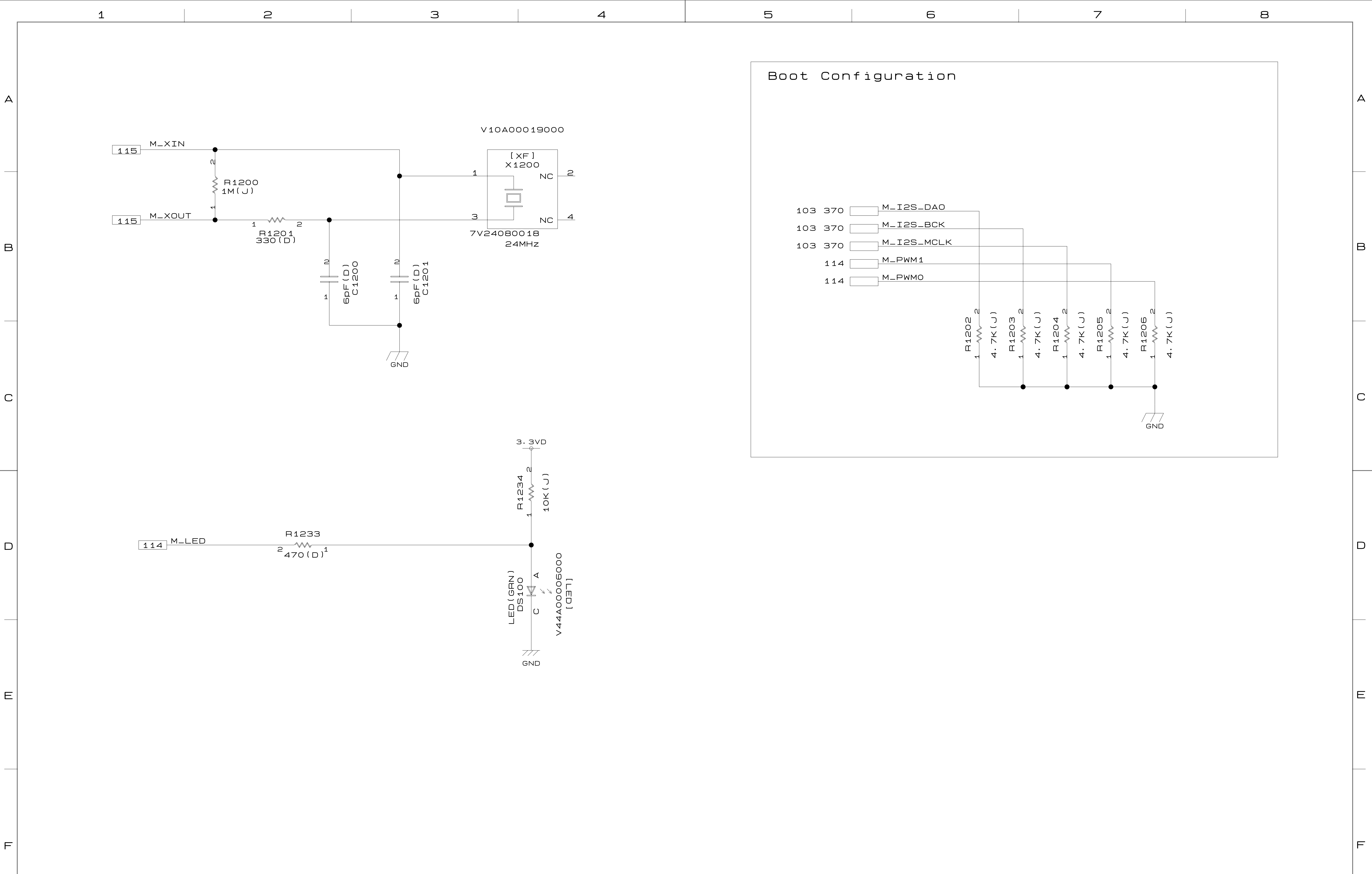


DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION MStar (Power 1/3)	SH. NO. 116	PAGE NO. 17	REV. MARK MP	DRAWING. NO. PE1014ZA
2011.08.04	10:01					TOSHIBA CORPORATION

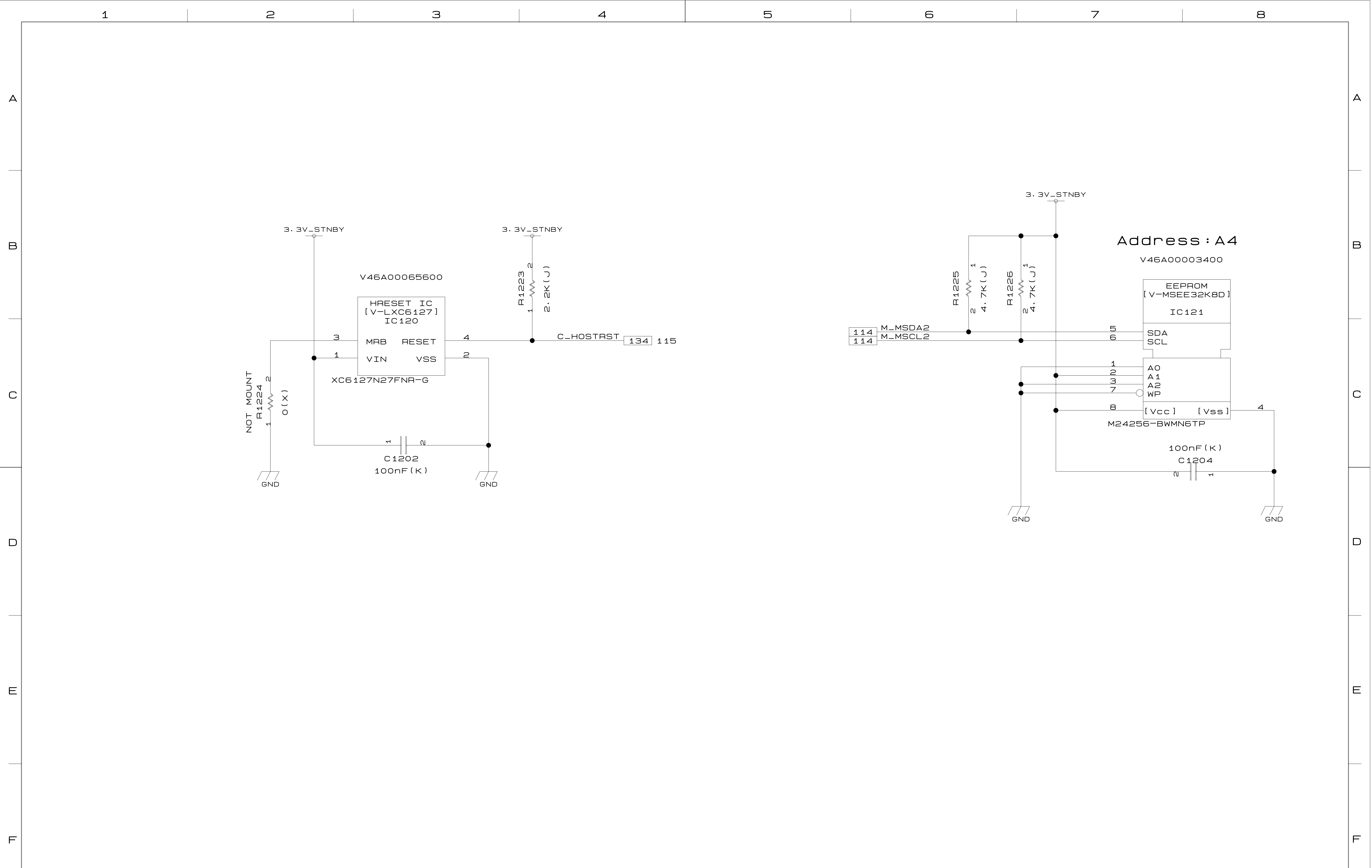




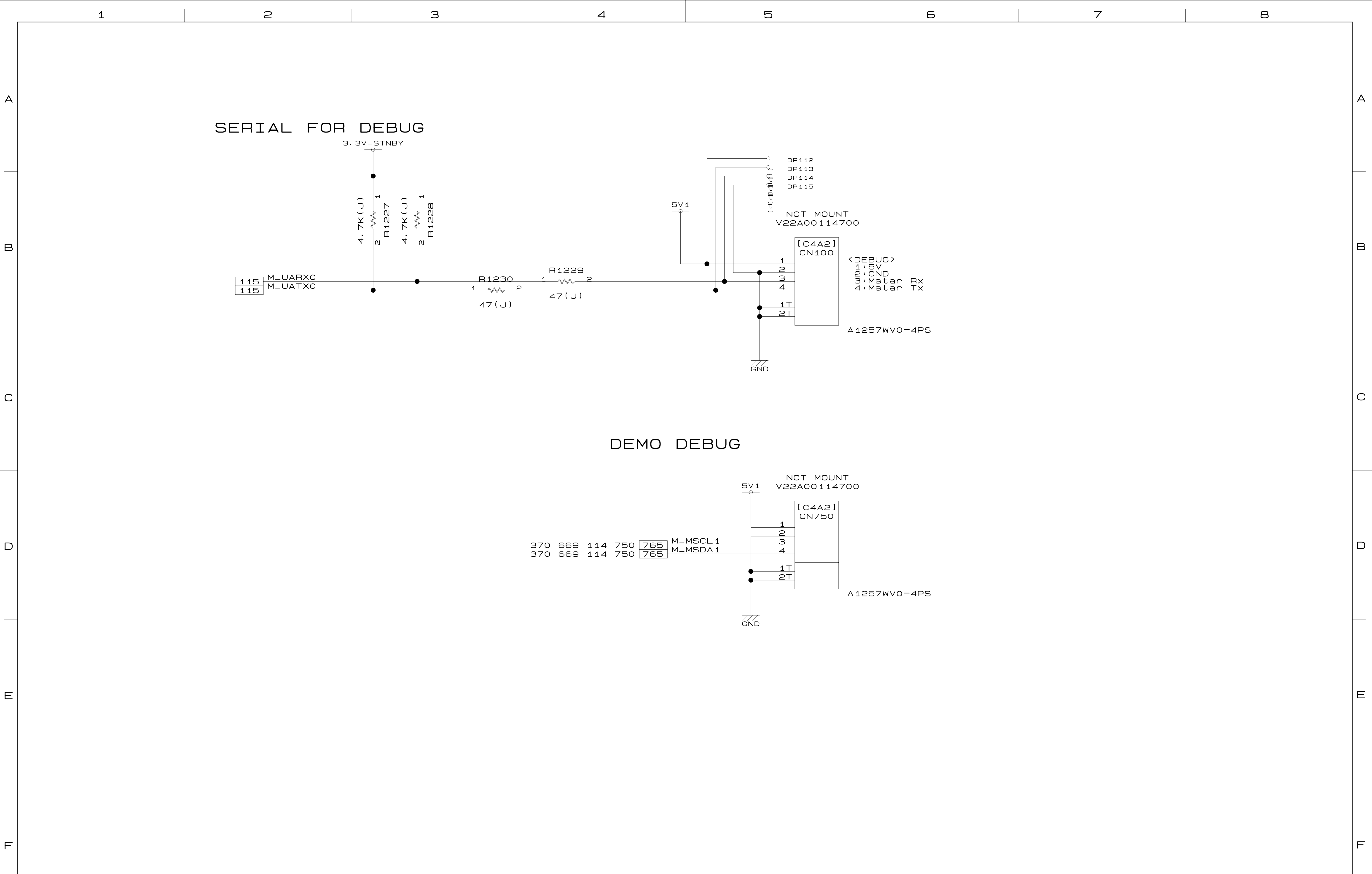
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2011.08.04	10:02					TOSHIBA CORPORATION

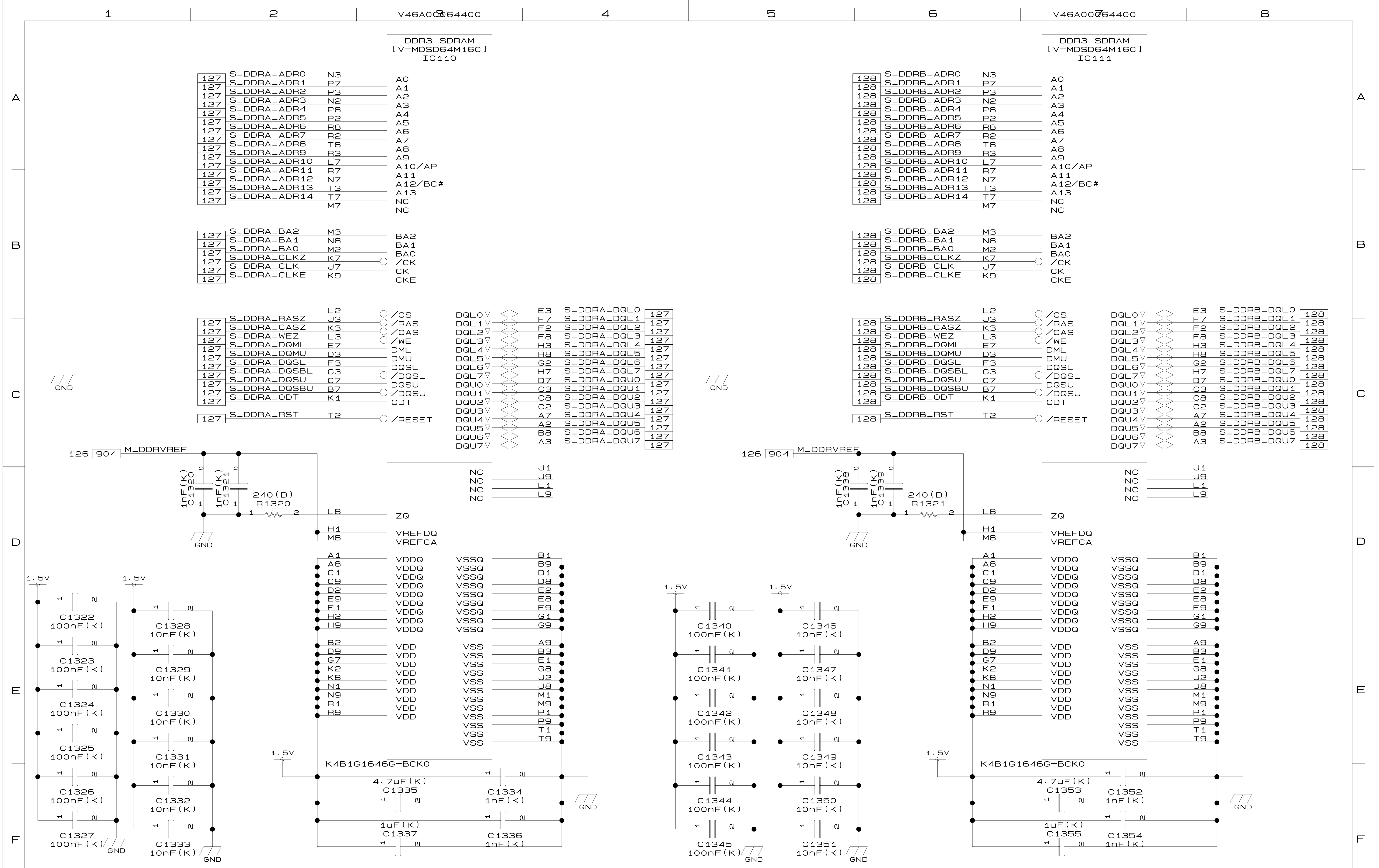


DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
OSANAI	MAIN UNIT	PERI (XTAL, STRAPS)	120	20	MP	PE1014ZA

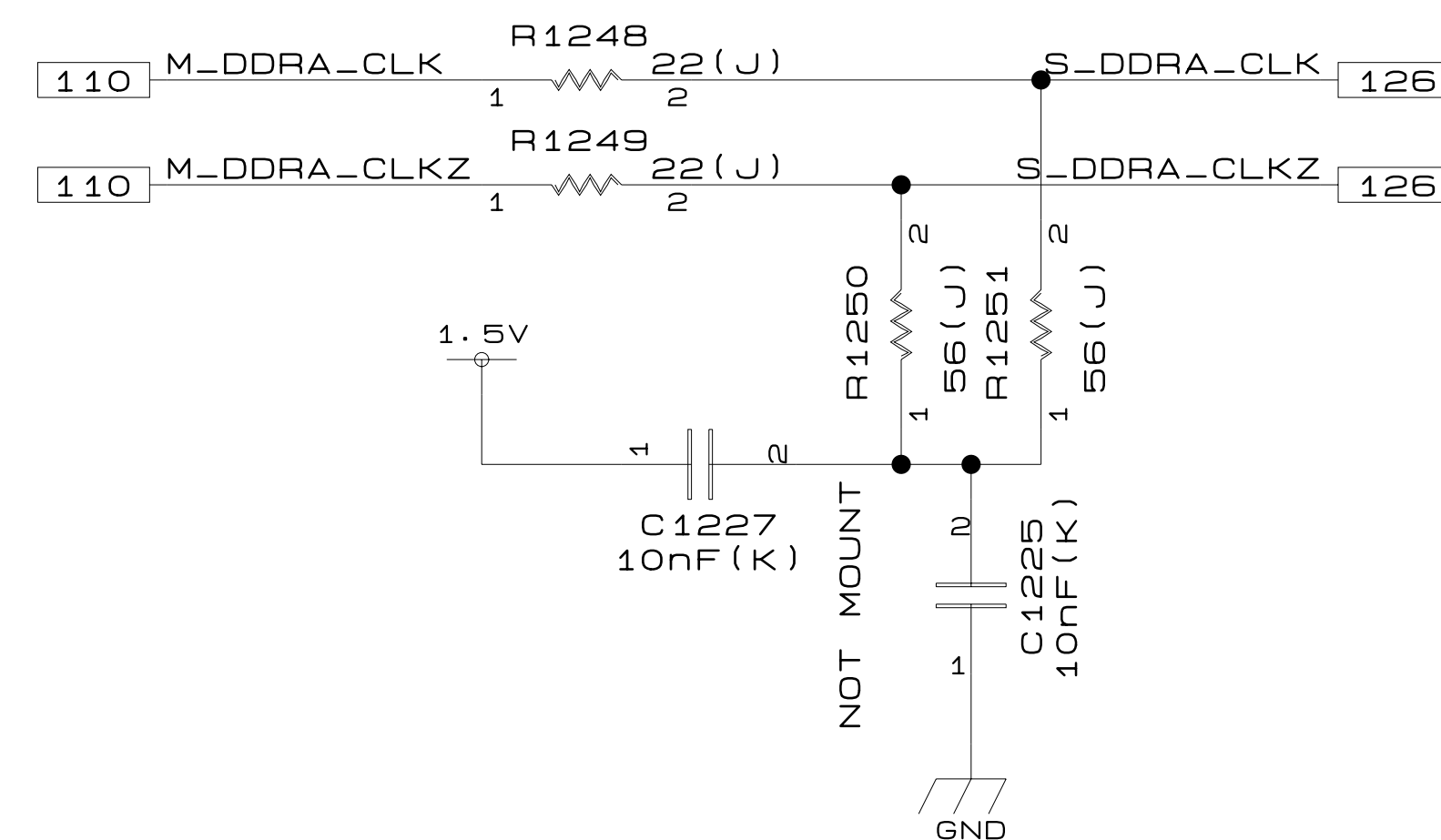
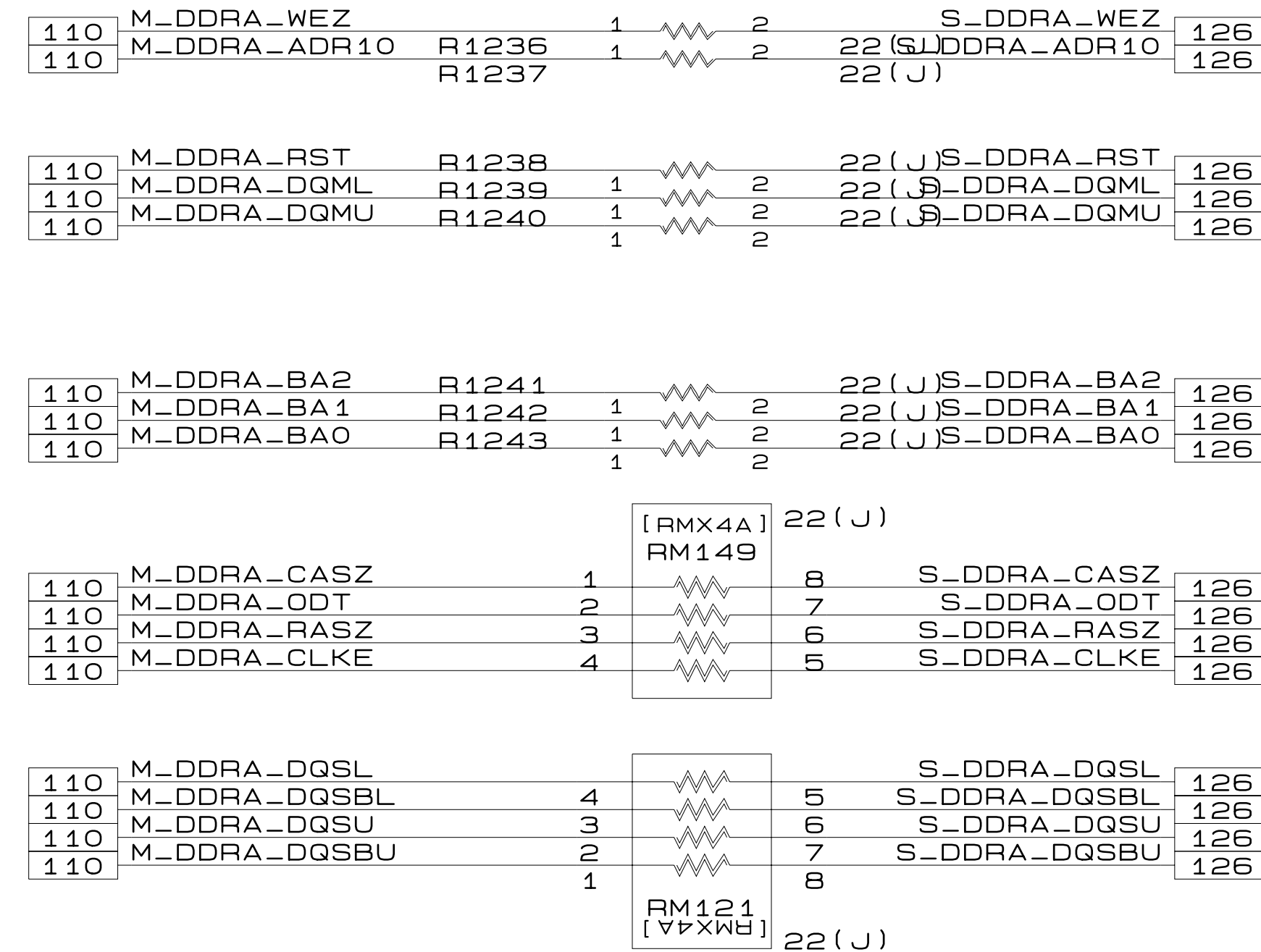


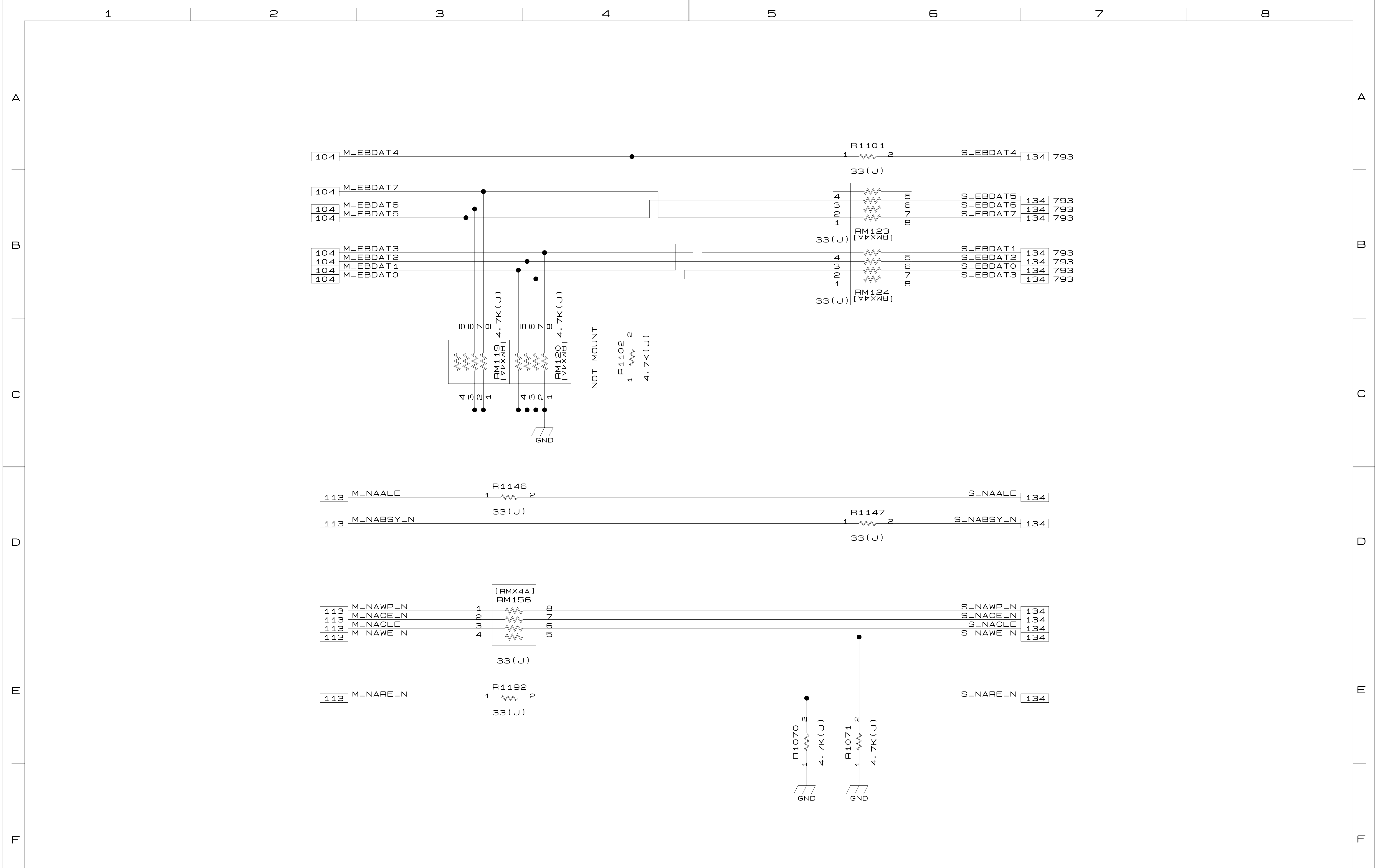
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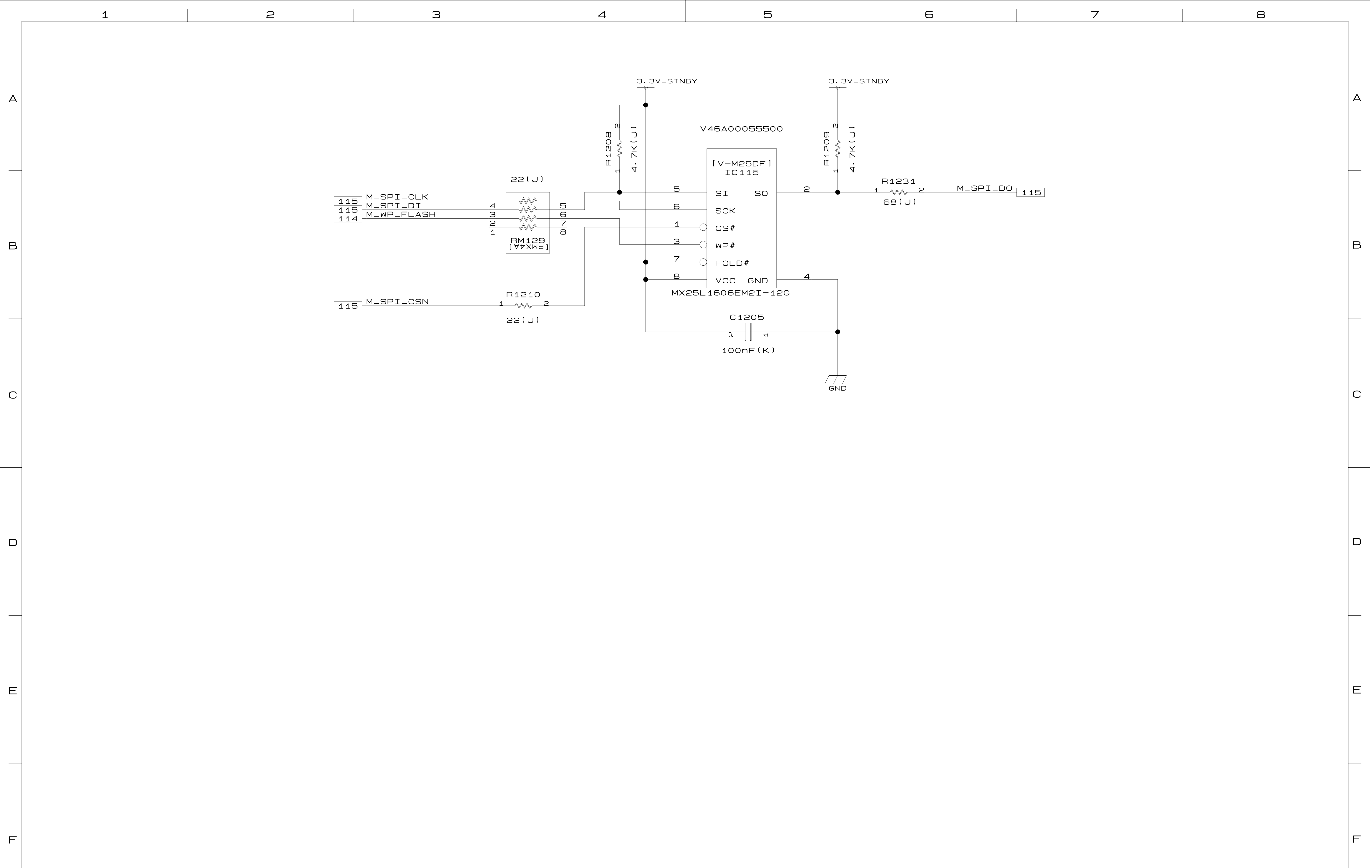




F

[illegible]





DESIGNED BY
OSANAI

TITLE
MAIN UNIT

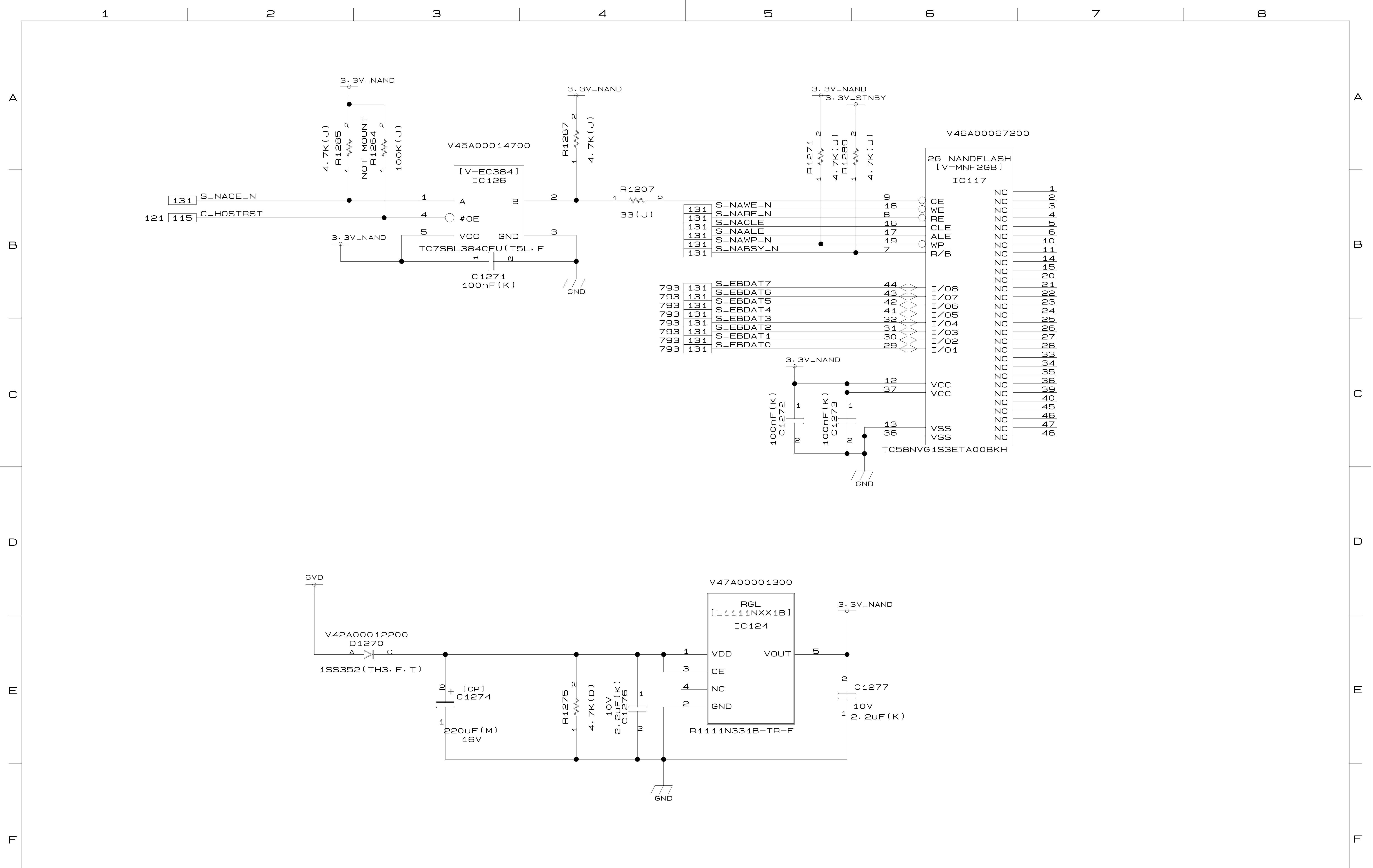
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SPI FLASH

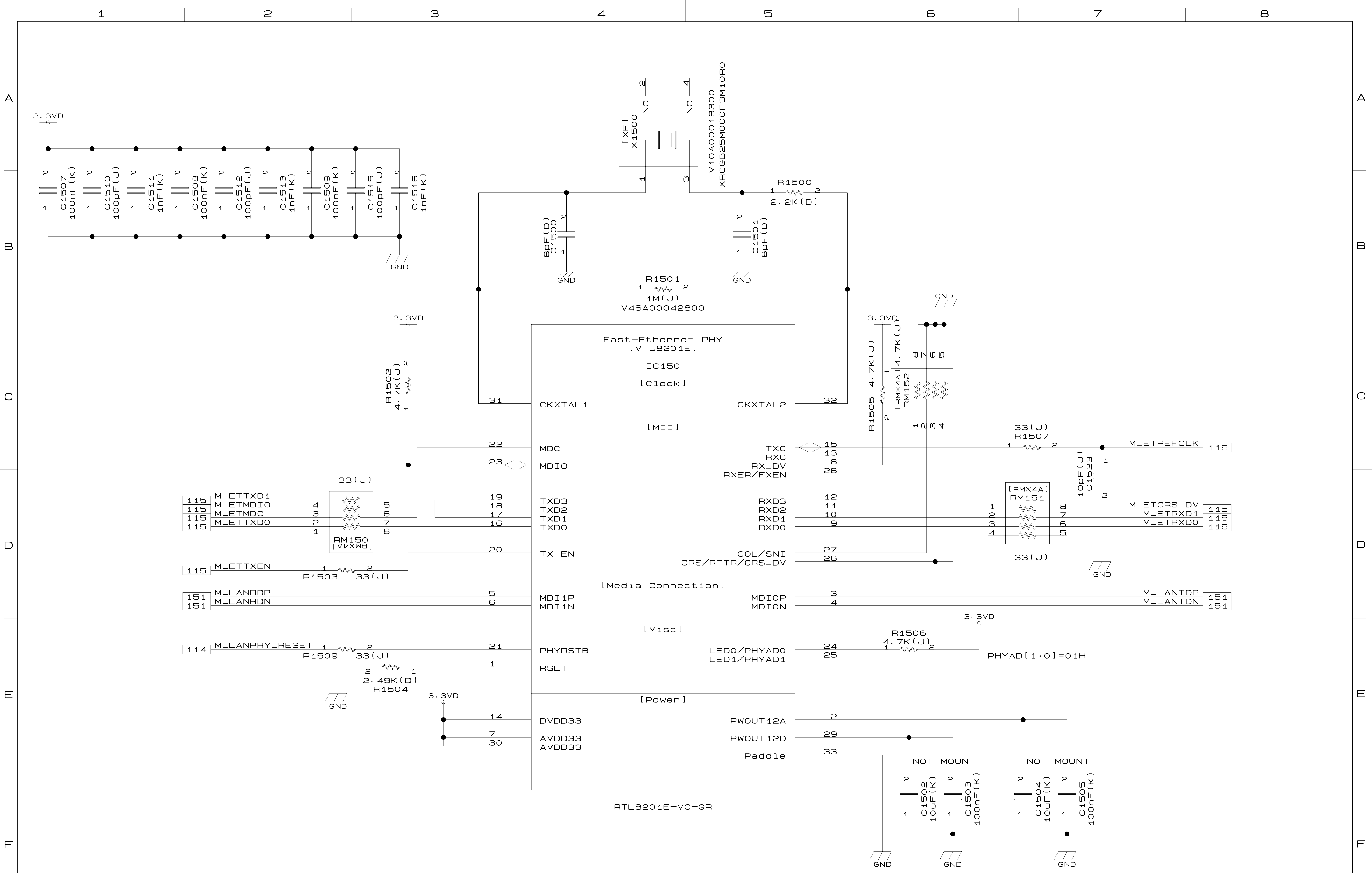
SH. NO.
132

PAGE NO.
27

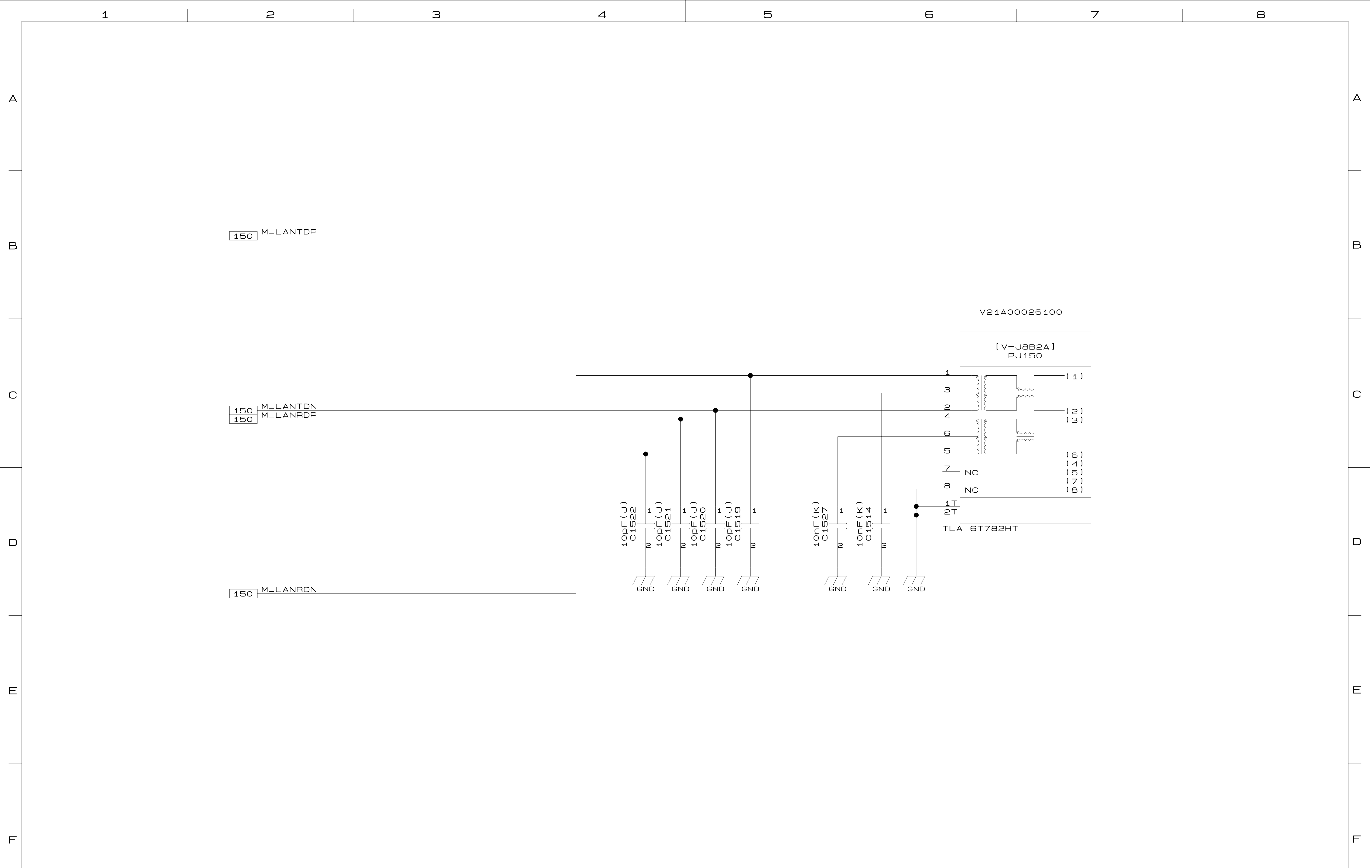
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DRAWING. NO.
PE1014ZA

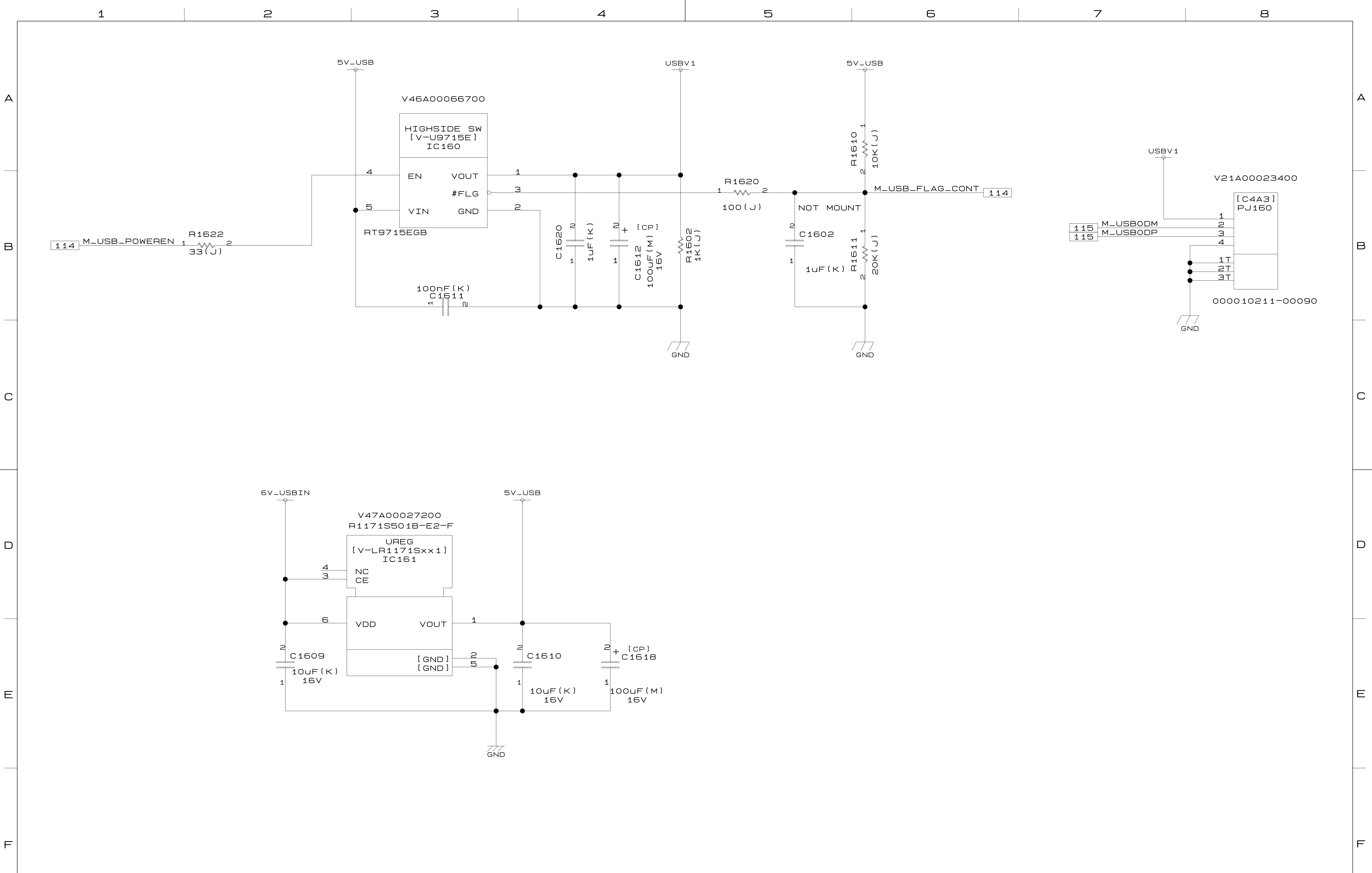




DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
KOJIMA	MAIN UNIT	ETHER PHY	150	29	MP	PE1014ZA



DESIGNED BY KOJIMA	TITLE MAIN UNIT	FUNCTION RJ45 CONNECTOR	SH. NO. 151	PAGE NO. 30	REV. MARK MP	DRAWING. NO. PE1014ZA
2011.08.04	10:02	TOSHIBA CORPORATION				



DESIGNED BY KOJIMA	TITLE MAIN UNIT	FUNCTION USB CONNECTOR(side) 160	SH. NO. 160	PAGE NO. 31	REV. MARK MP	DRAWING. NO. PE1014ZA
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A

B

C

D

E

F

A

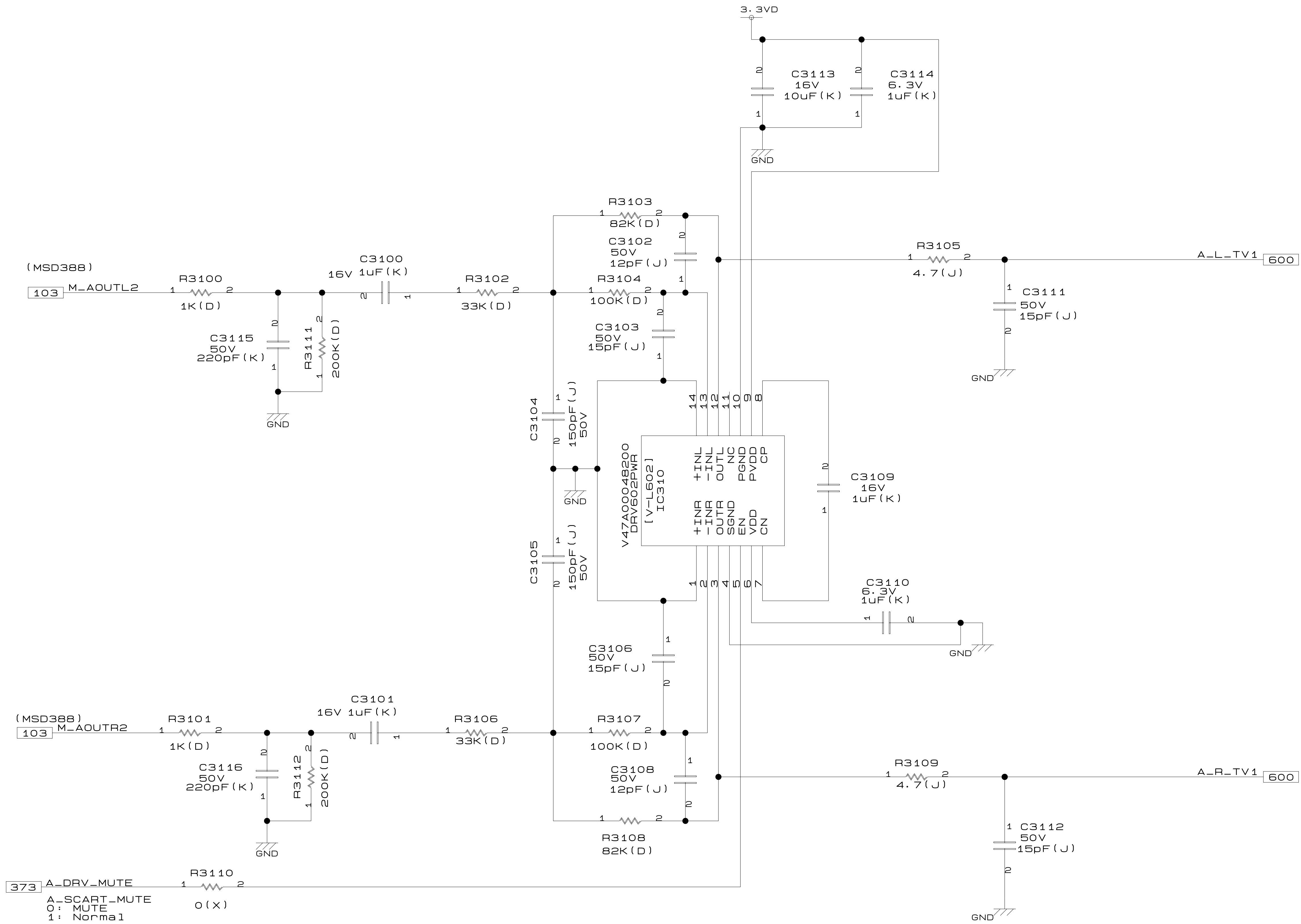
B

C

D

E

F



DESIGNED BY
ENDO

TITLE
MAIN UNIT

FUNCTION
AUDIO TV OUTPUT

SH. NO.
310

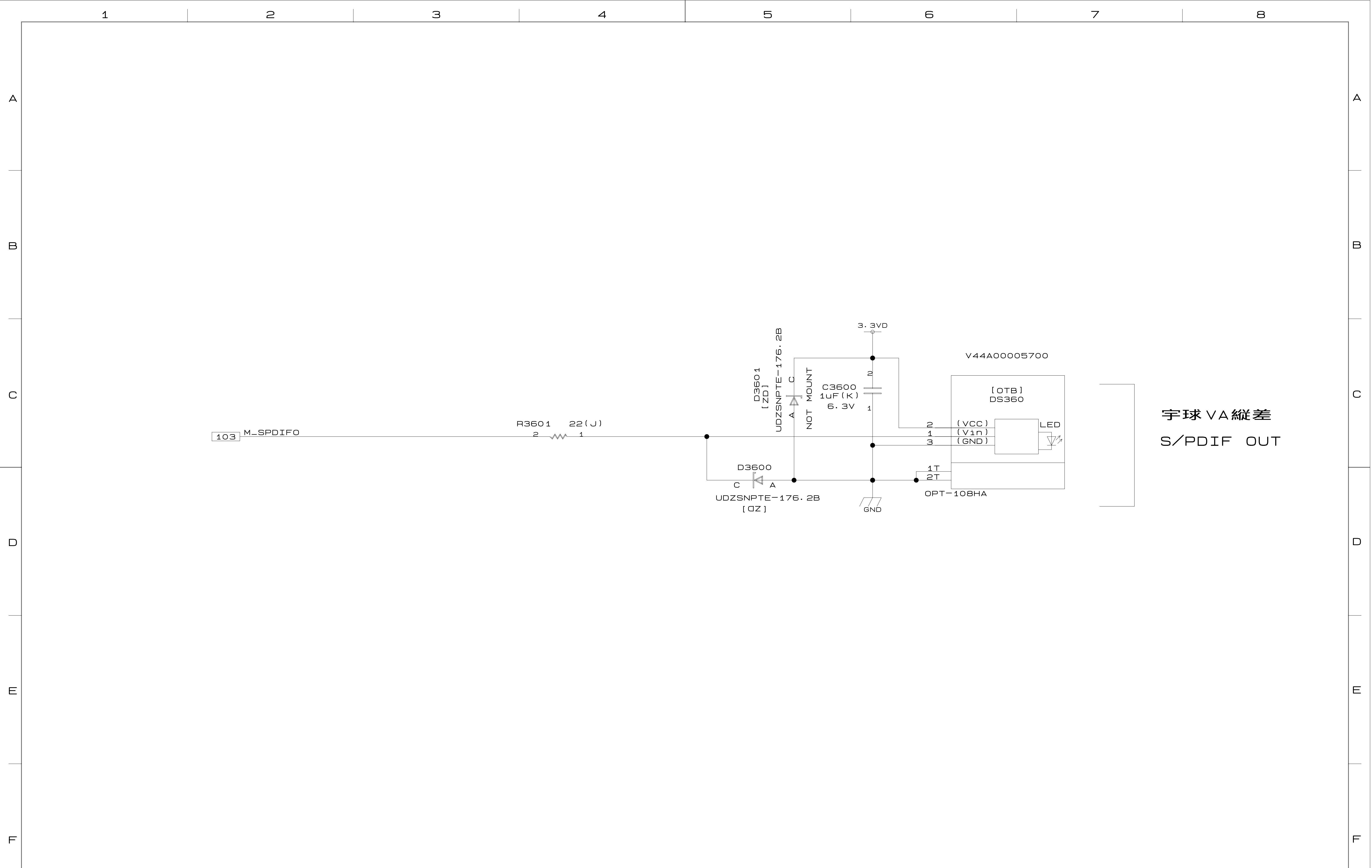
PAGE NO.
32

REV. MARK
MP

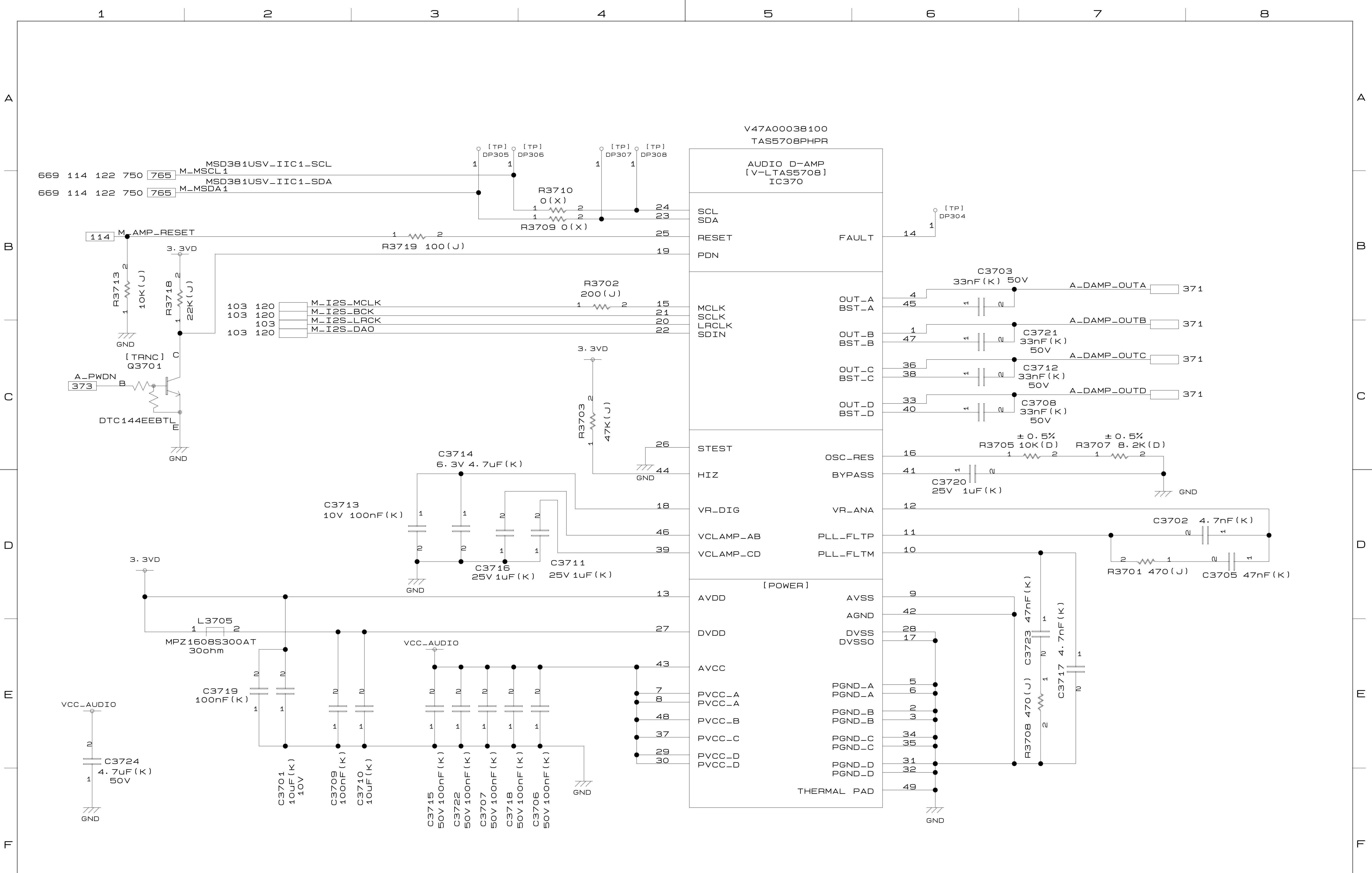
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PE1014ZA

2011.08.04 10:02

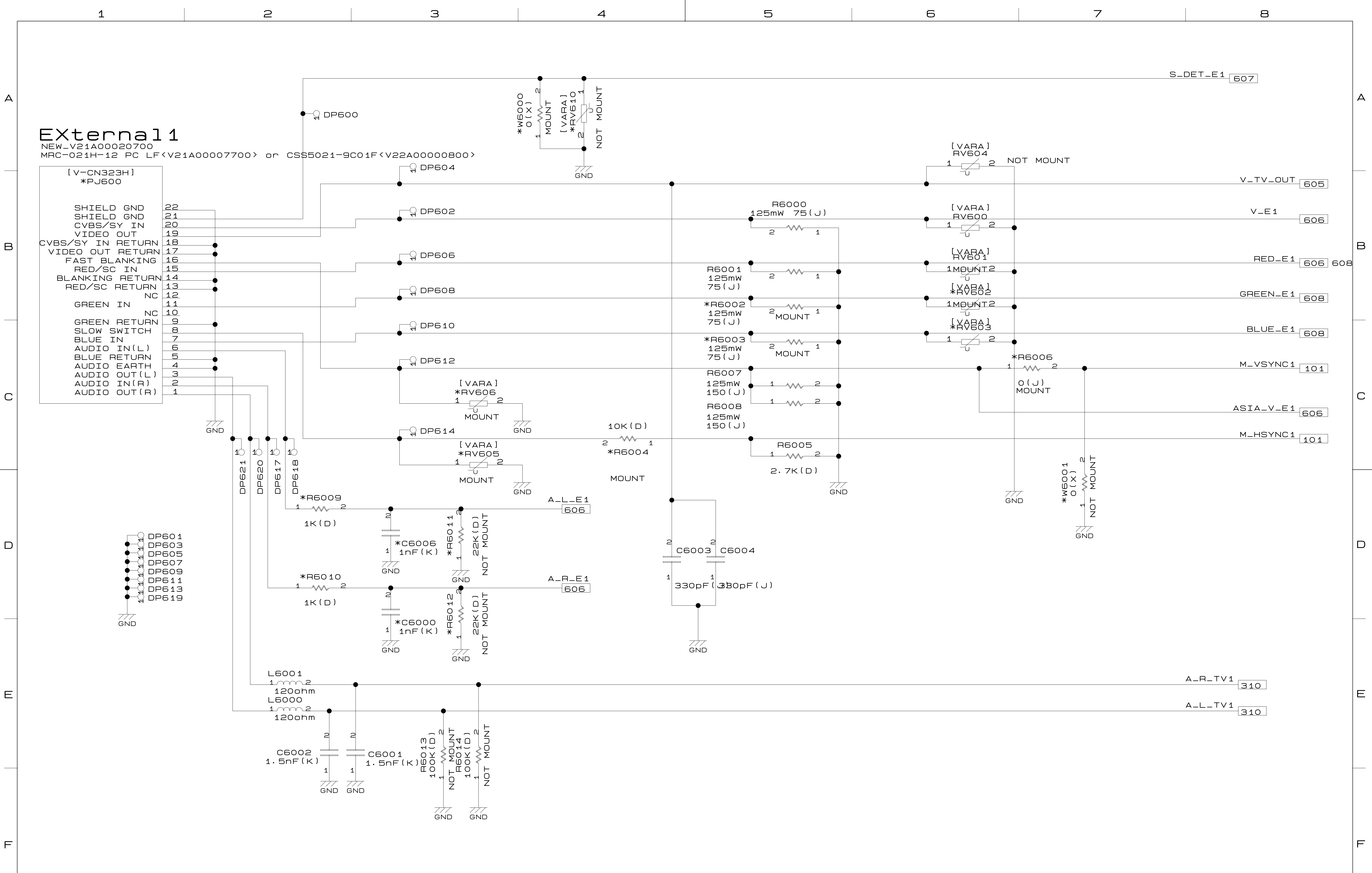
TOSHIBA CORPORATION



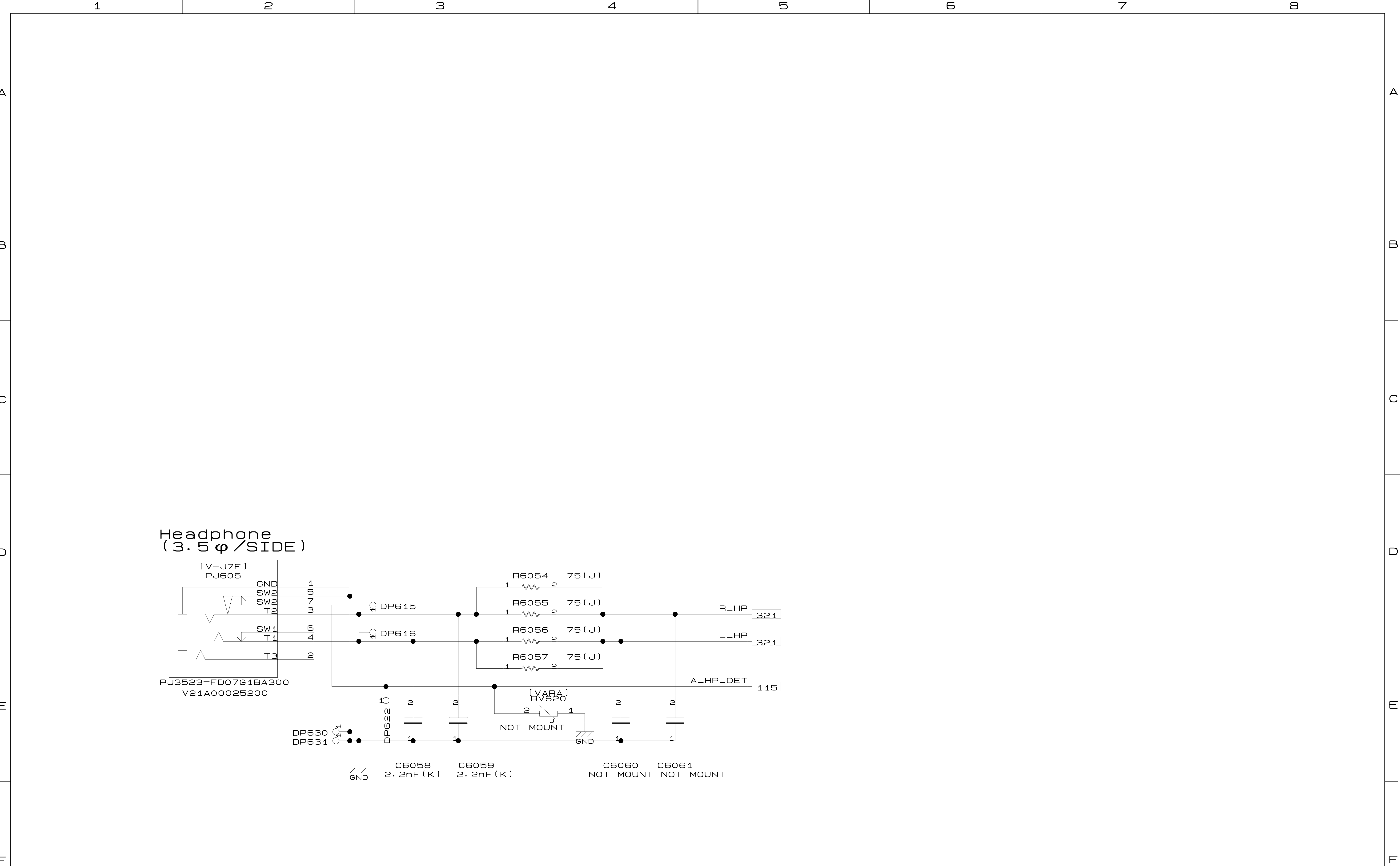
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2011.08.04 10:02	TOSHIBA CORPORATION					



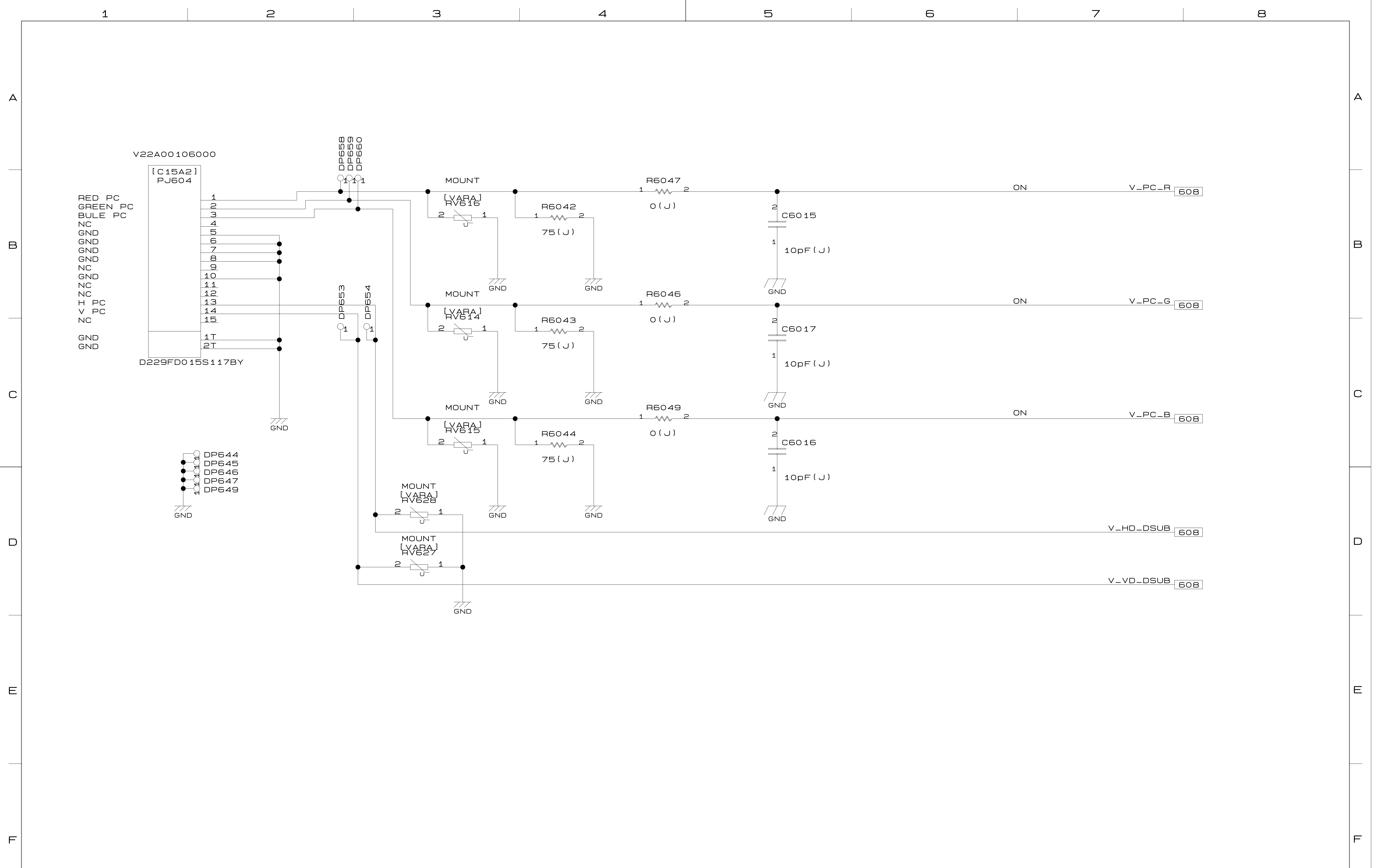
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ENDO	MAIN UNIT	AUDIO D-AMP (MAIN)	370	35	MP	PE1014ZA



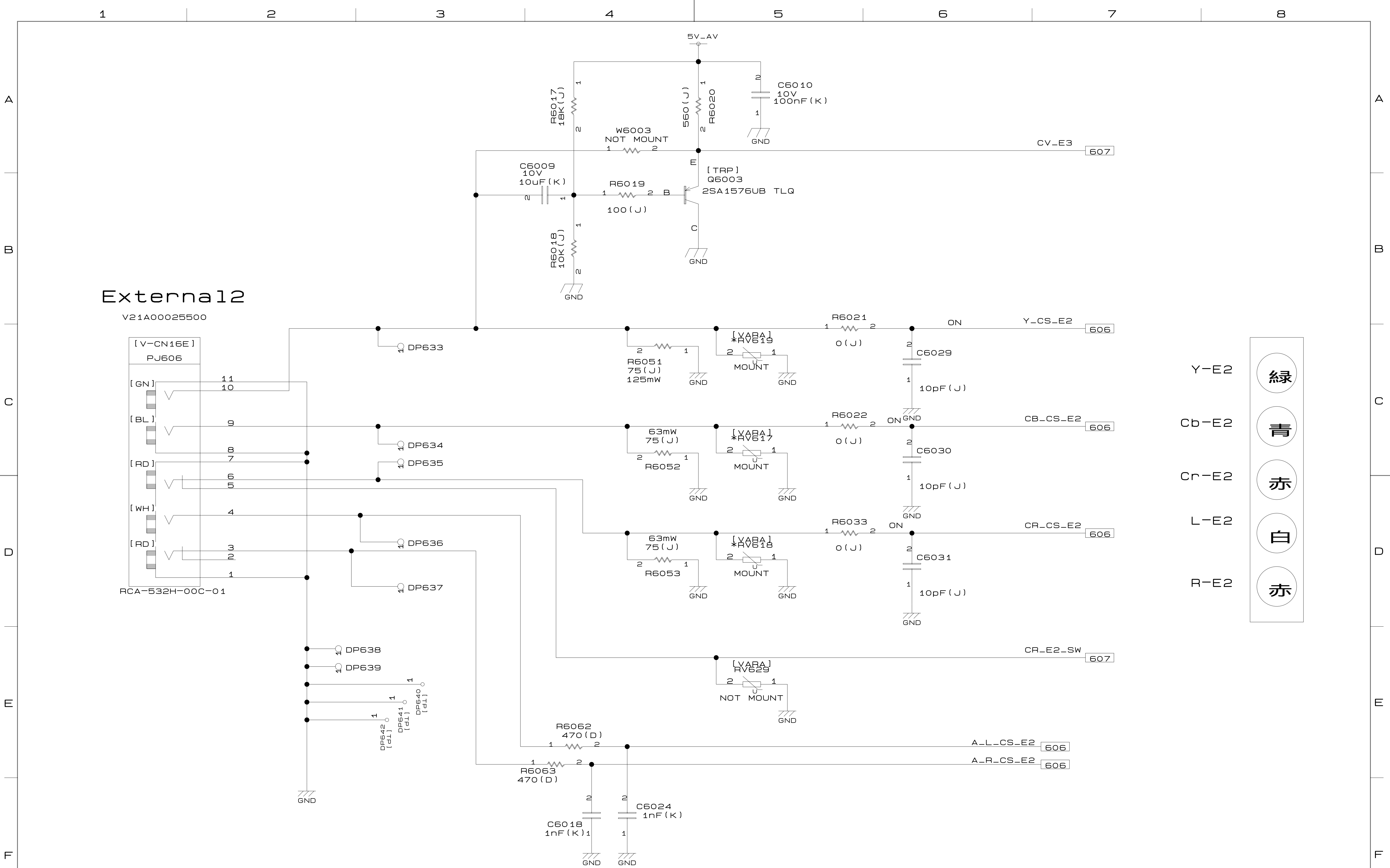
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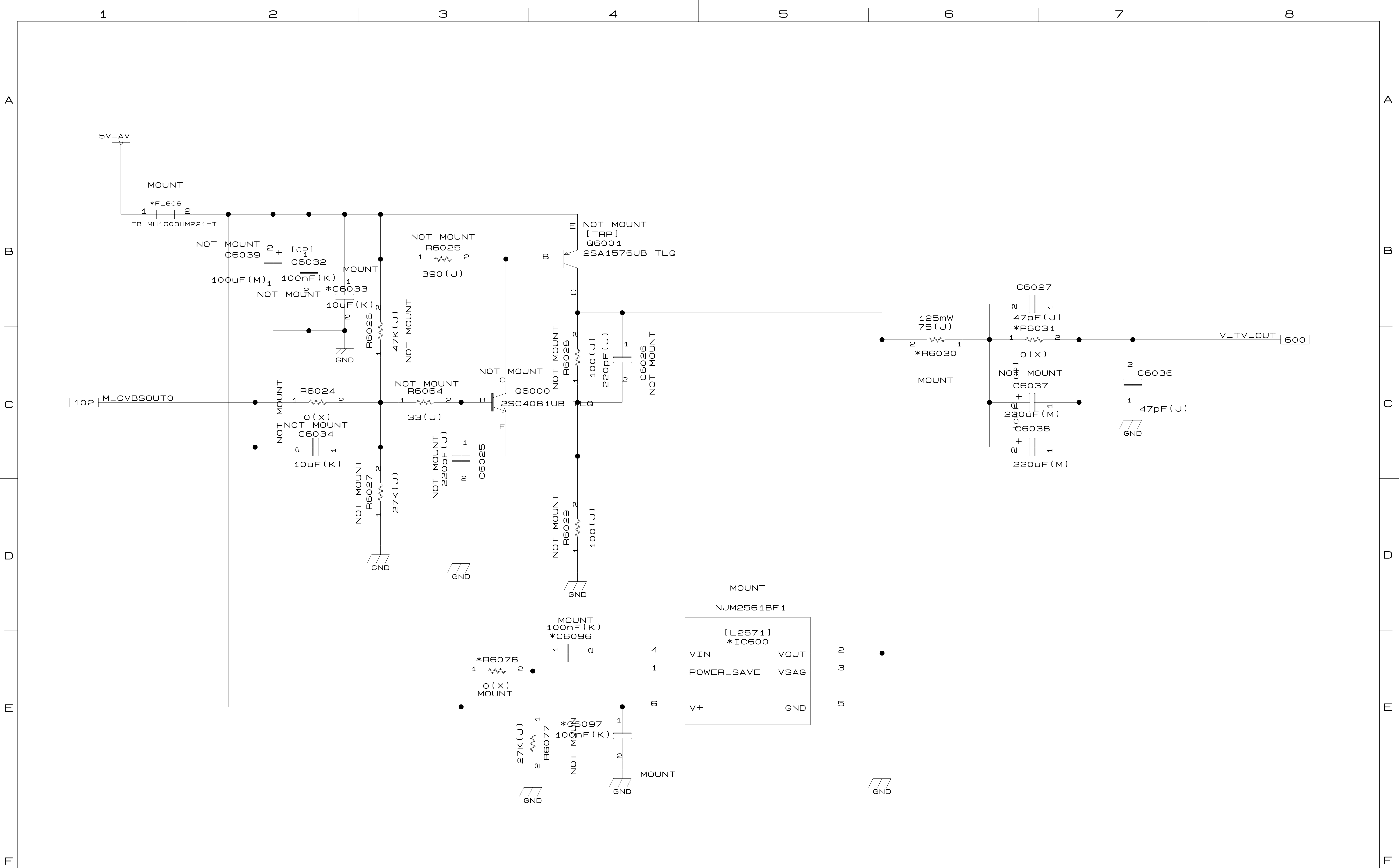


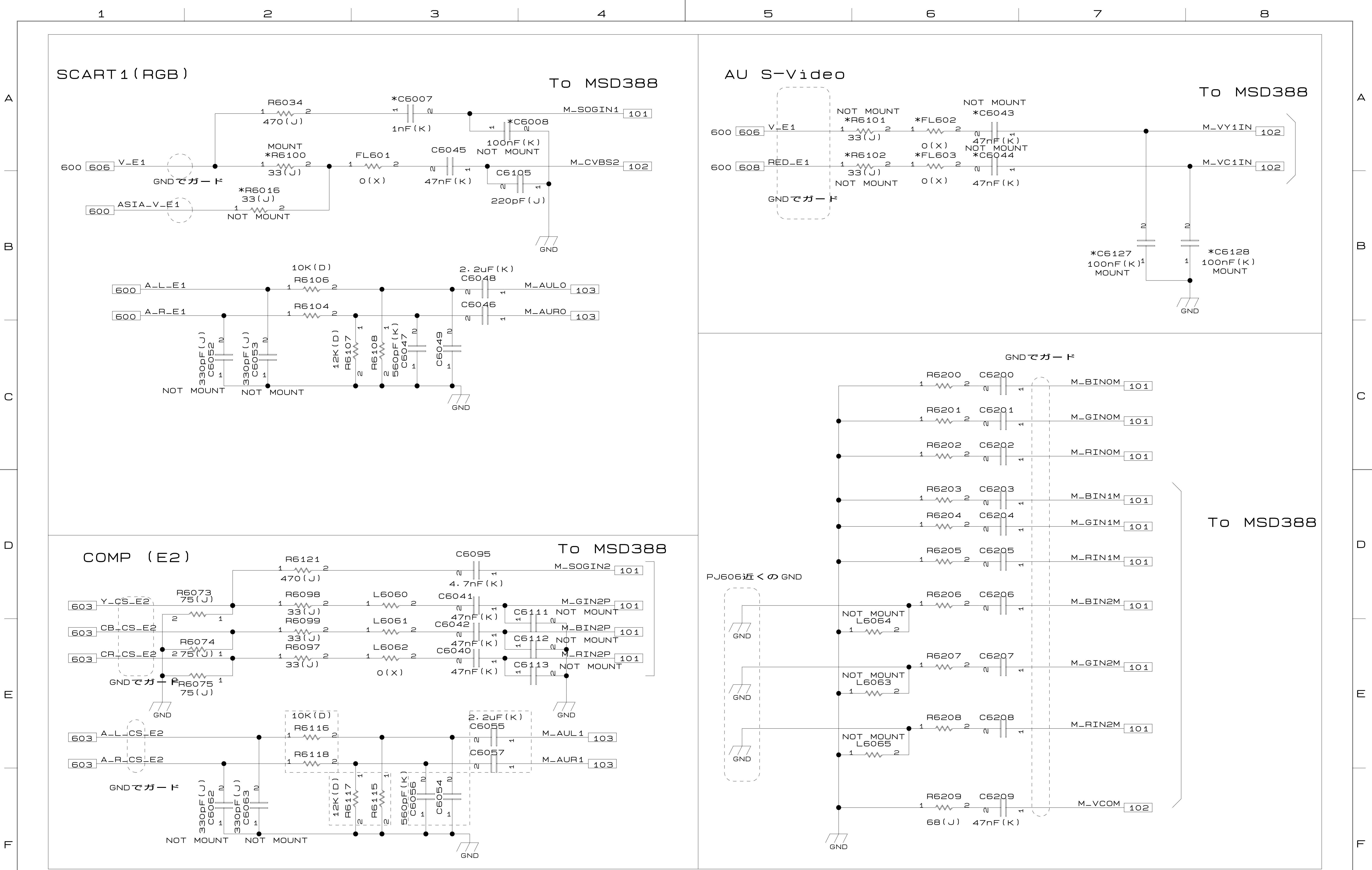
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2011.08.04 10:02	TOSHIBA CORPORATION					

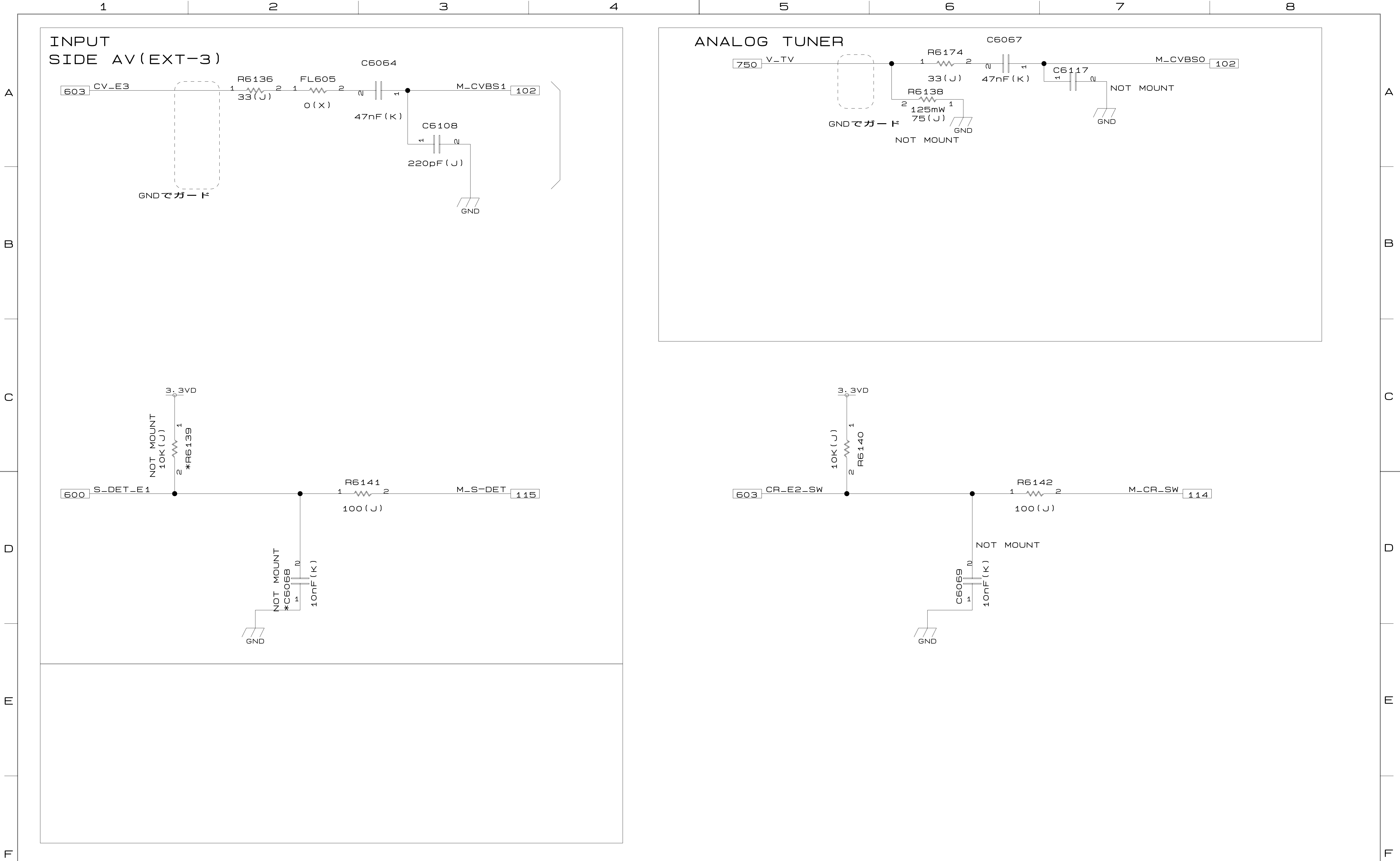


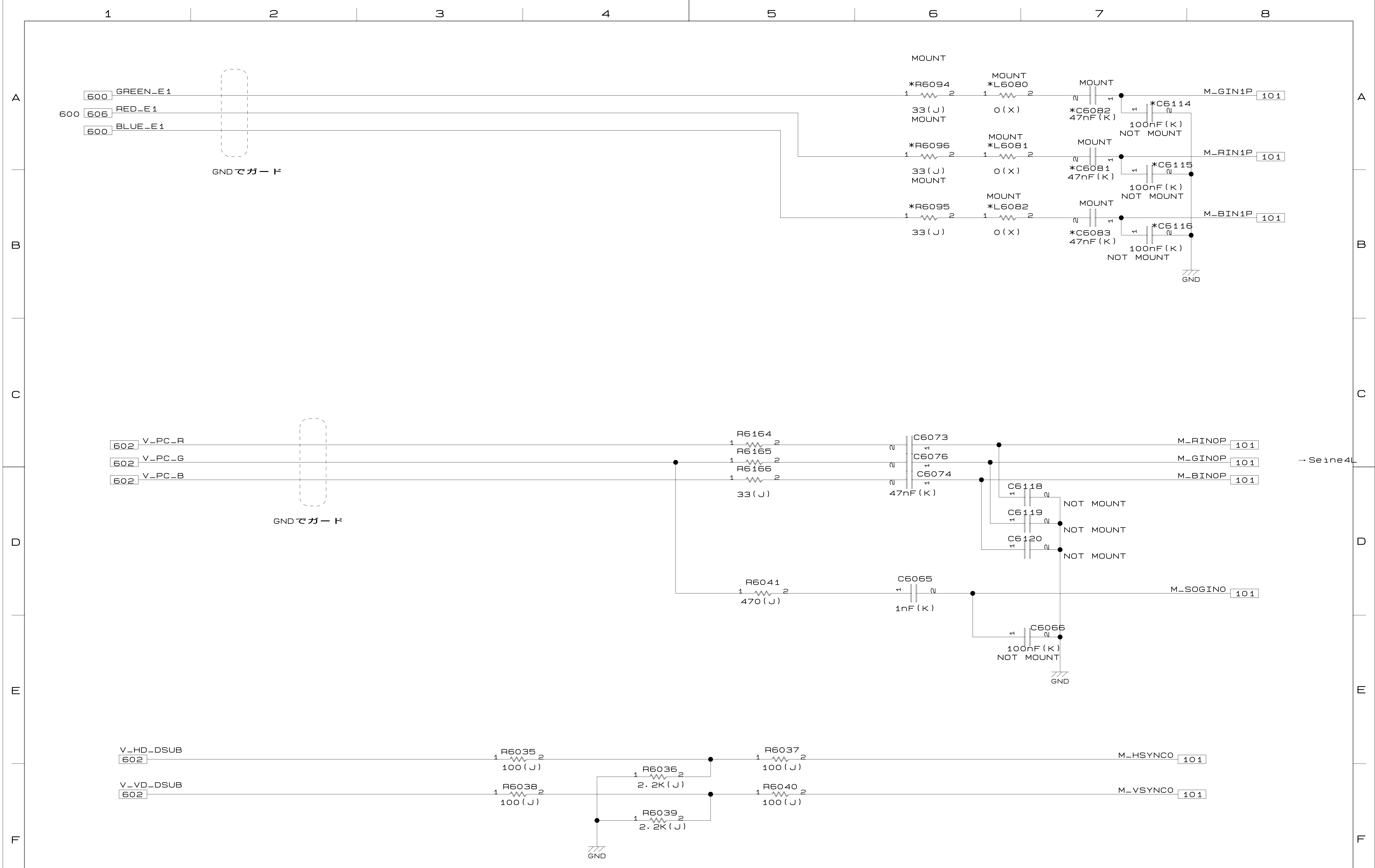
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2011.08.04	10:02					TOSHIBA CORPORATION



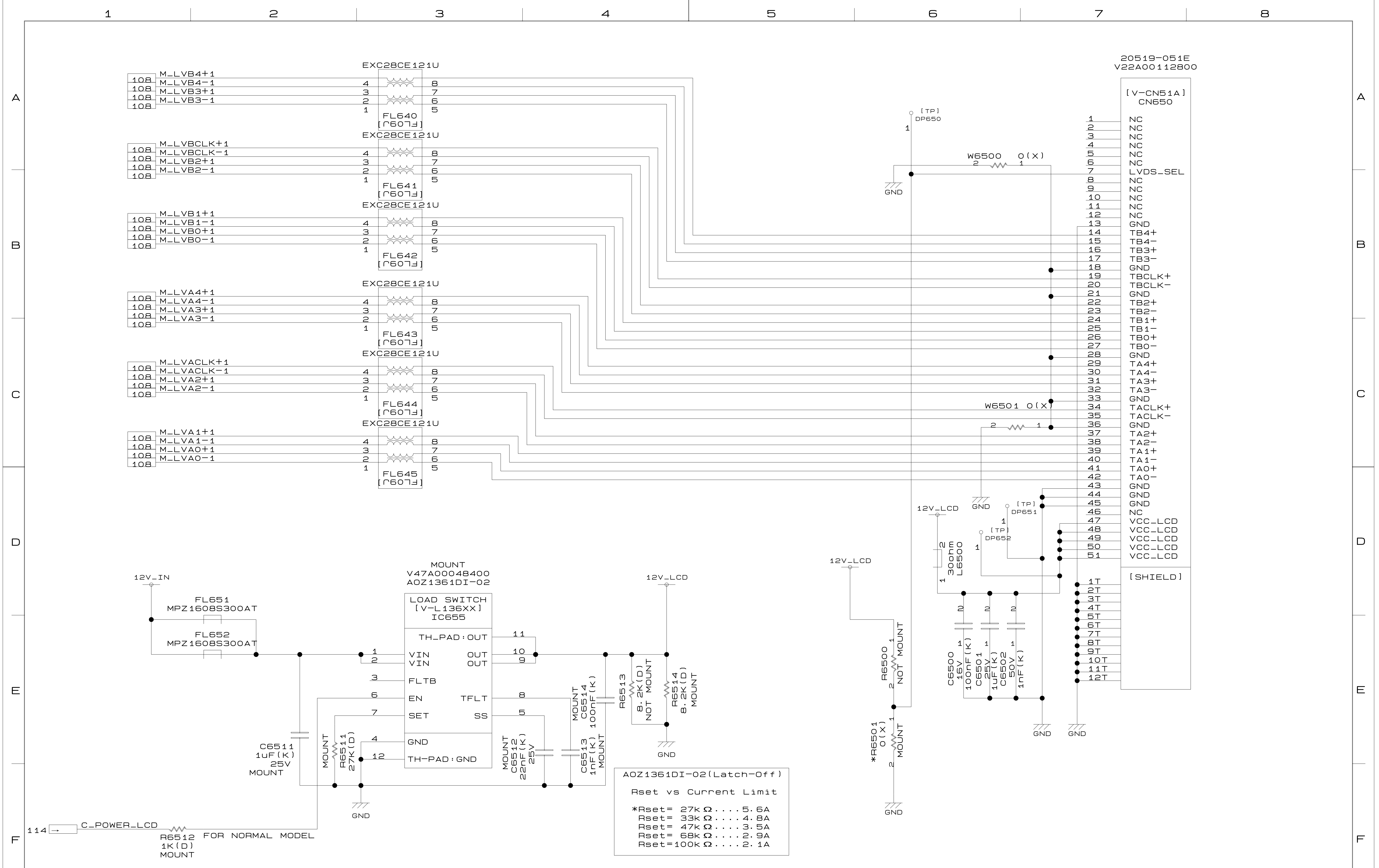


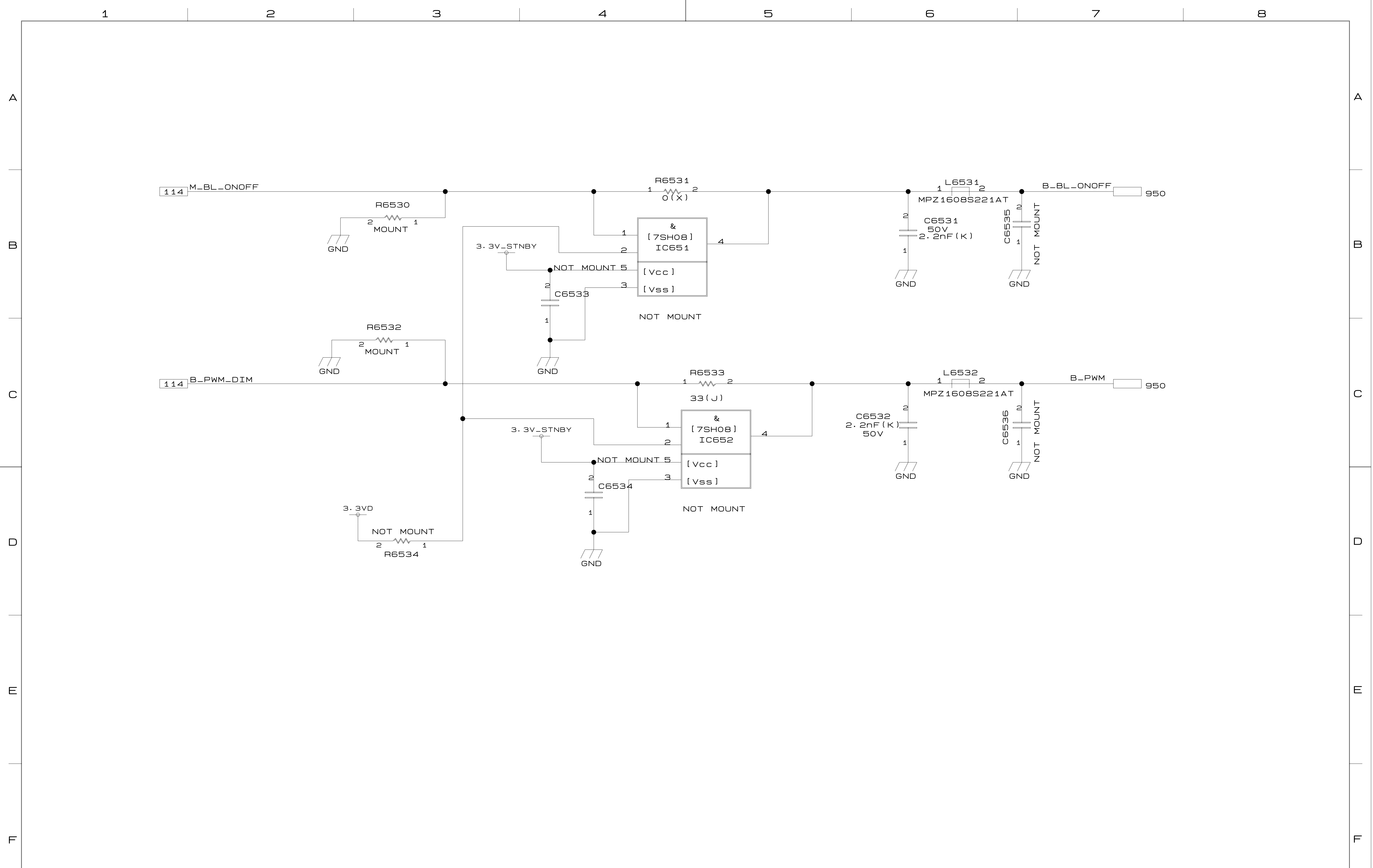




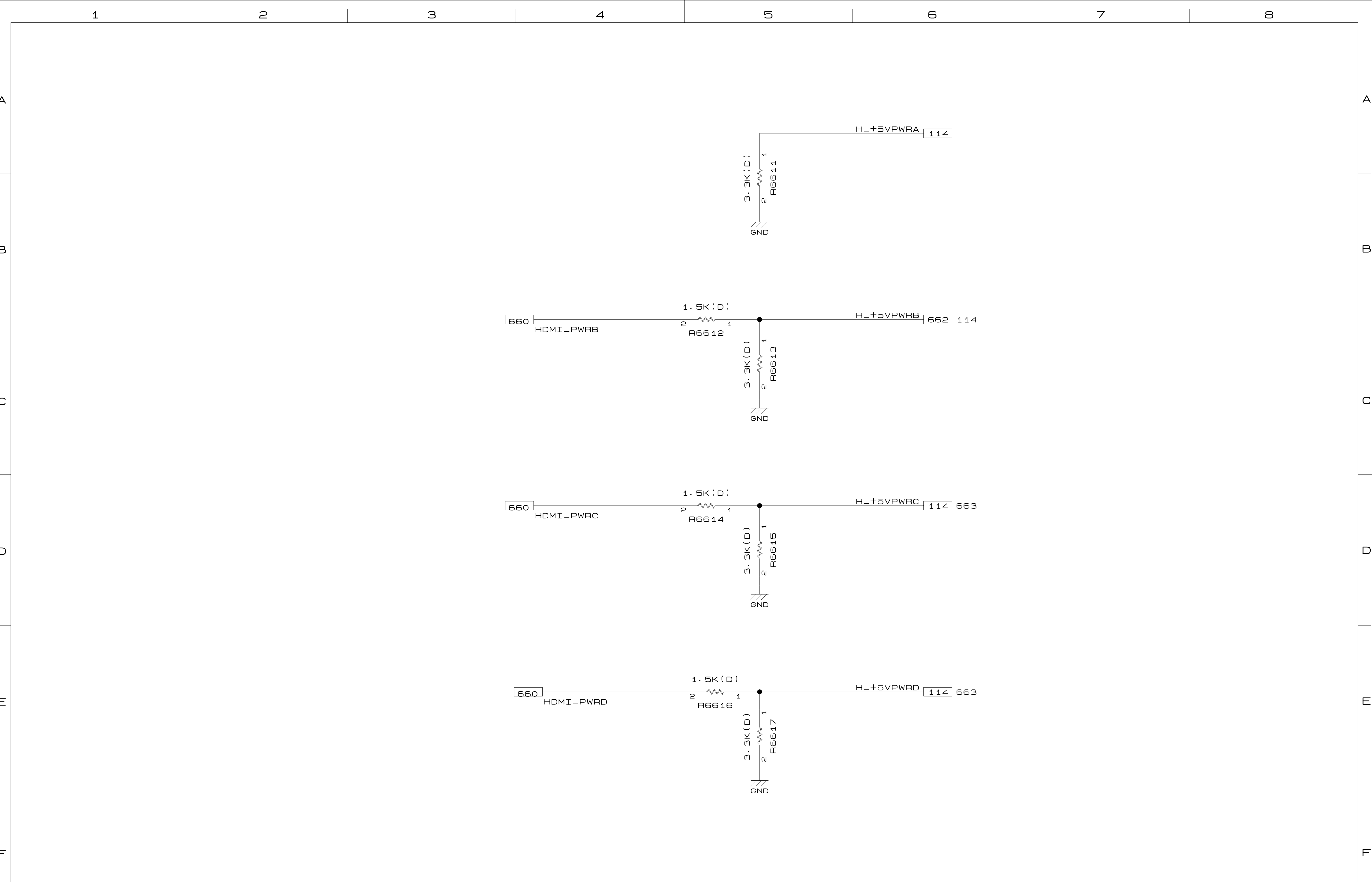


DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
SHIBATA/Endo	MAIN UNIT	AV INPUT3	608	45	MP	PE1014ZA
2011.08.04 10:02	TOSHIBA CORPORATION					

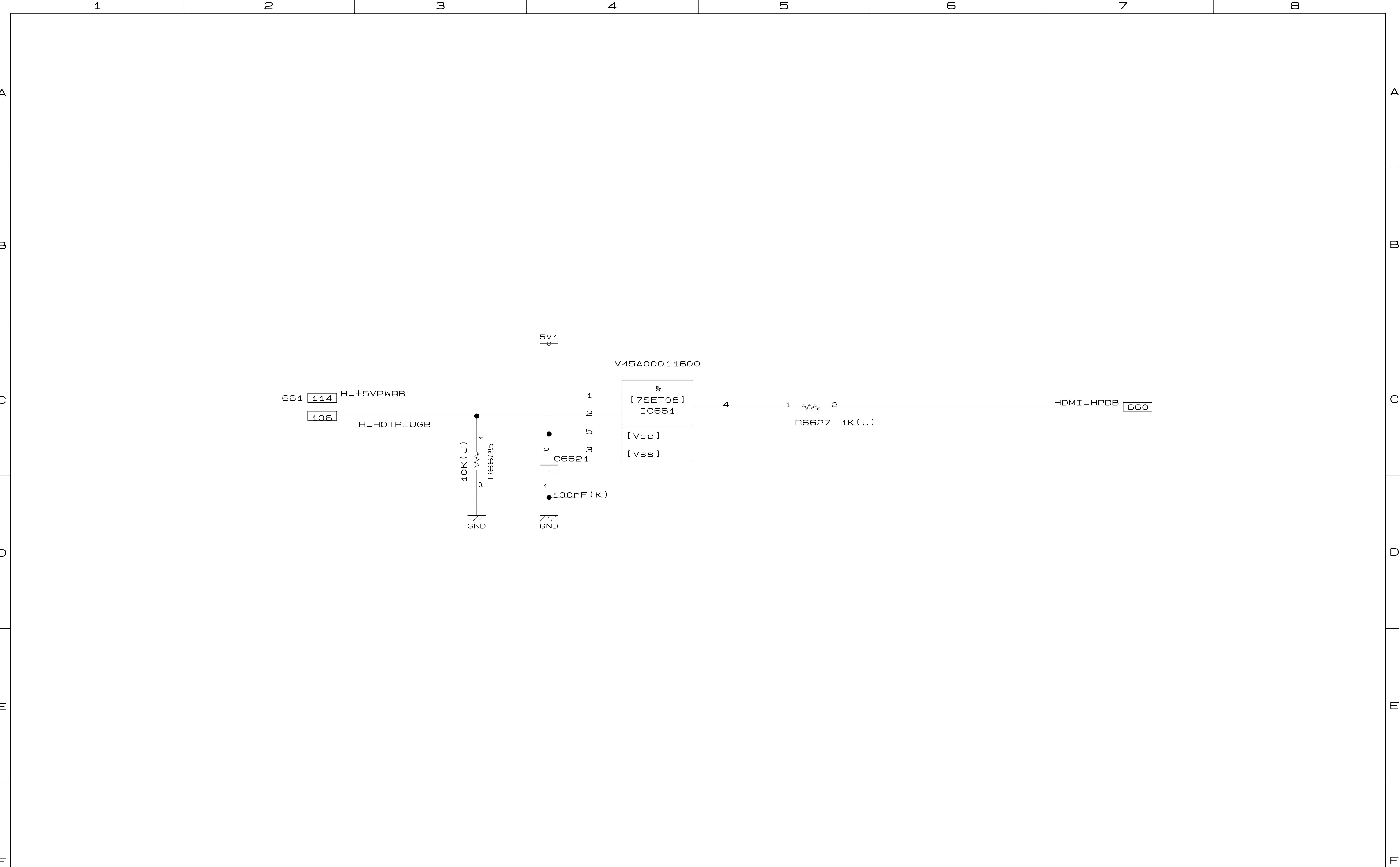


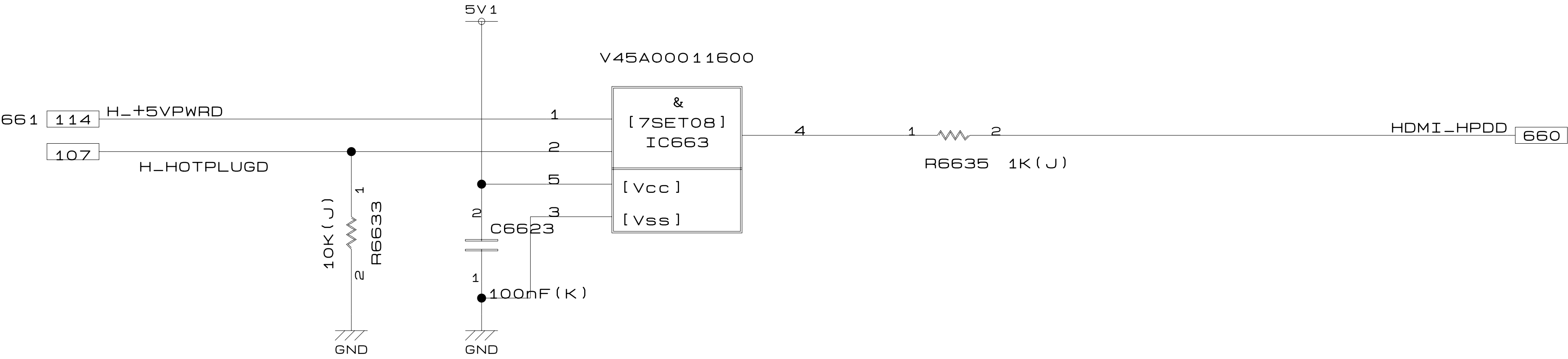
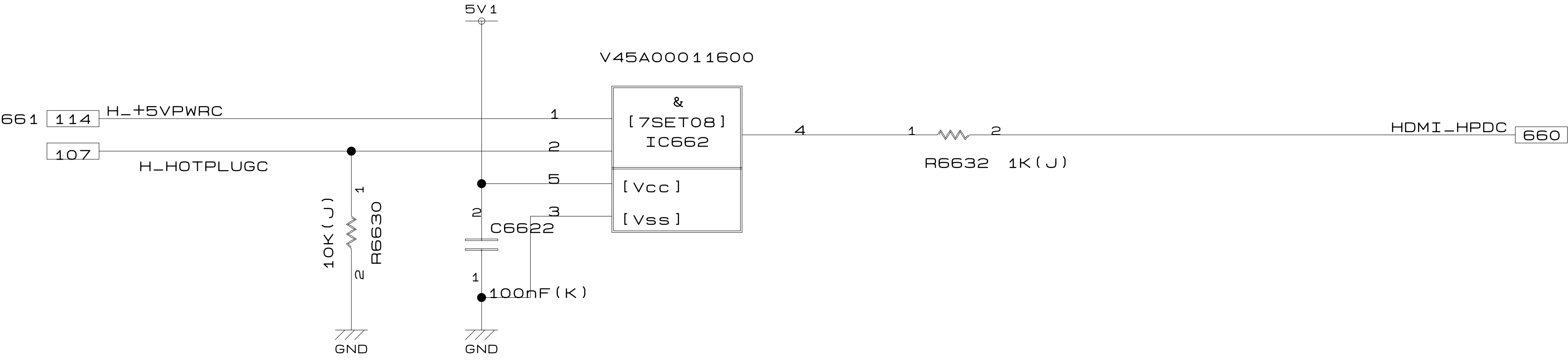


DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
S. KOMIYA	MAIN UNIT	BL_CONTROL & DIMMING	652	47	MP	PE1014ZA
2011.08.04	10:02					TOSHIBA CORPORATION

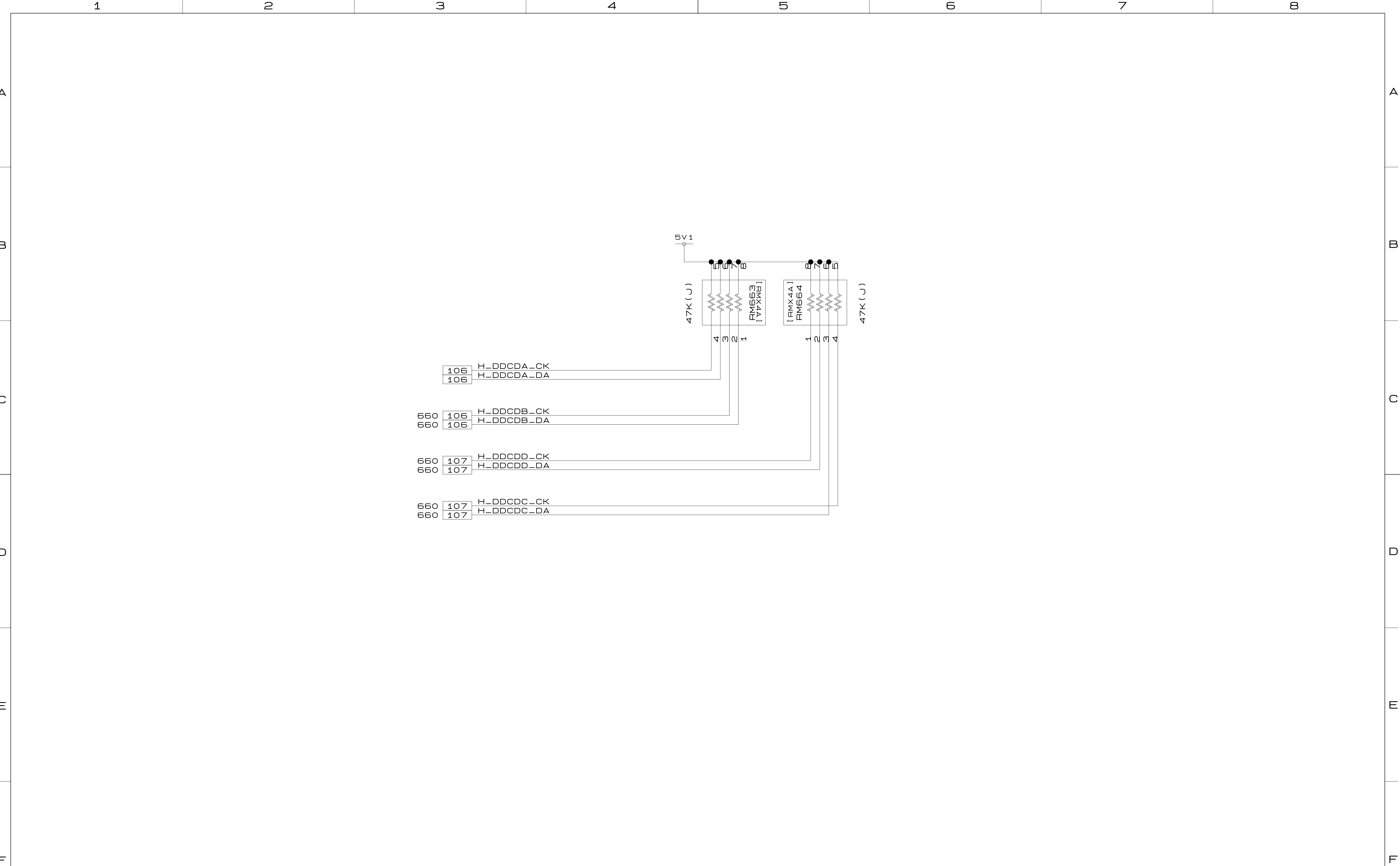


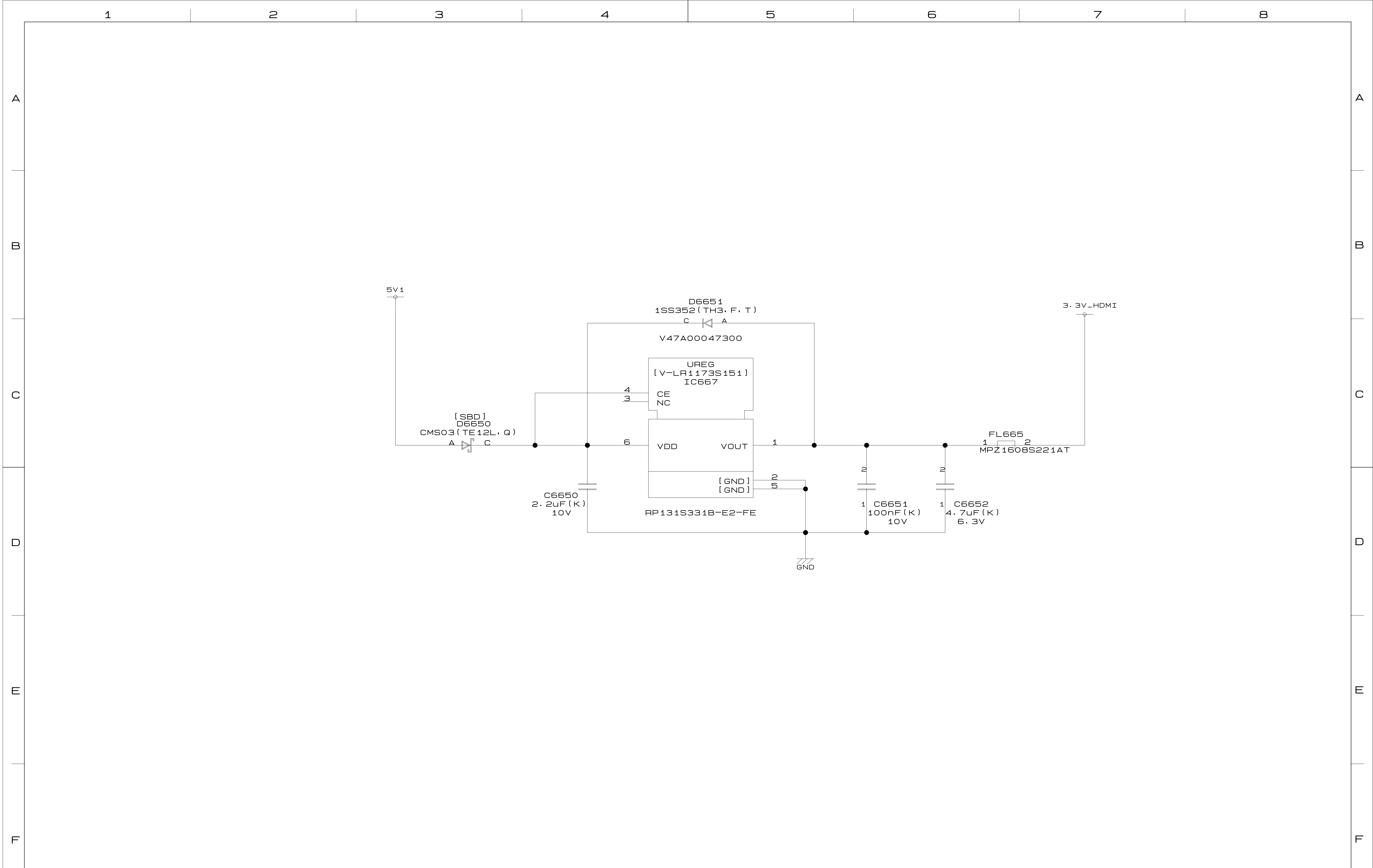
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Kuwata	MAIN UNIT	HDMI 5VPWR	661	49	MP	PE1014ZA
2011.08.04	10:02	TOSHIBA CORPORATION				



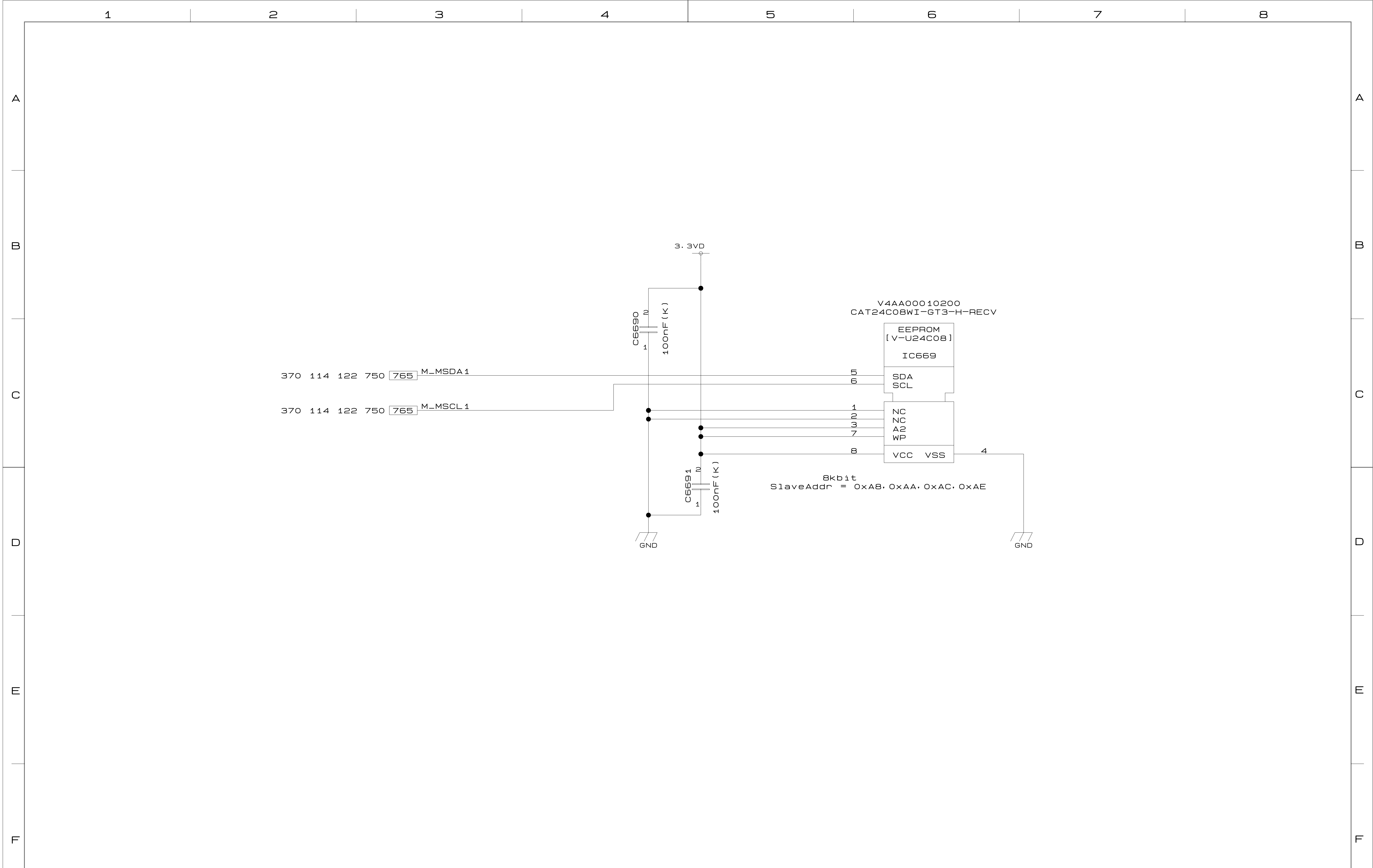


DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Kuwata	MAIN UNIT	HDMI HPD(2)	663	51	MP	PE1014ZA

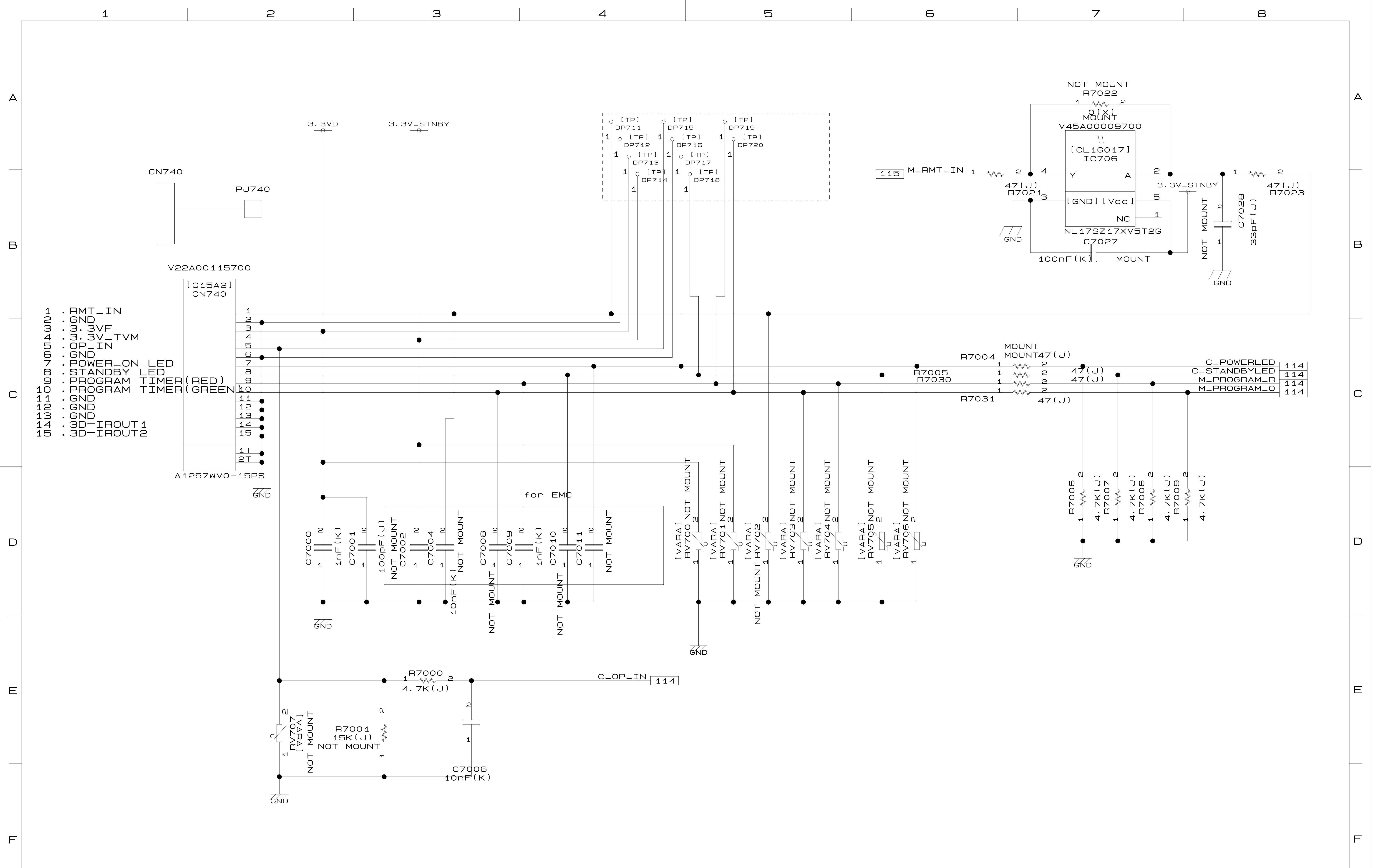


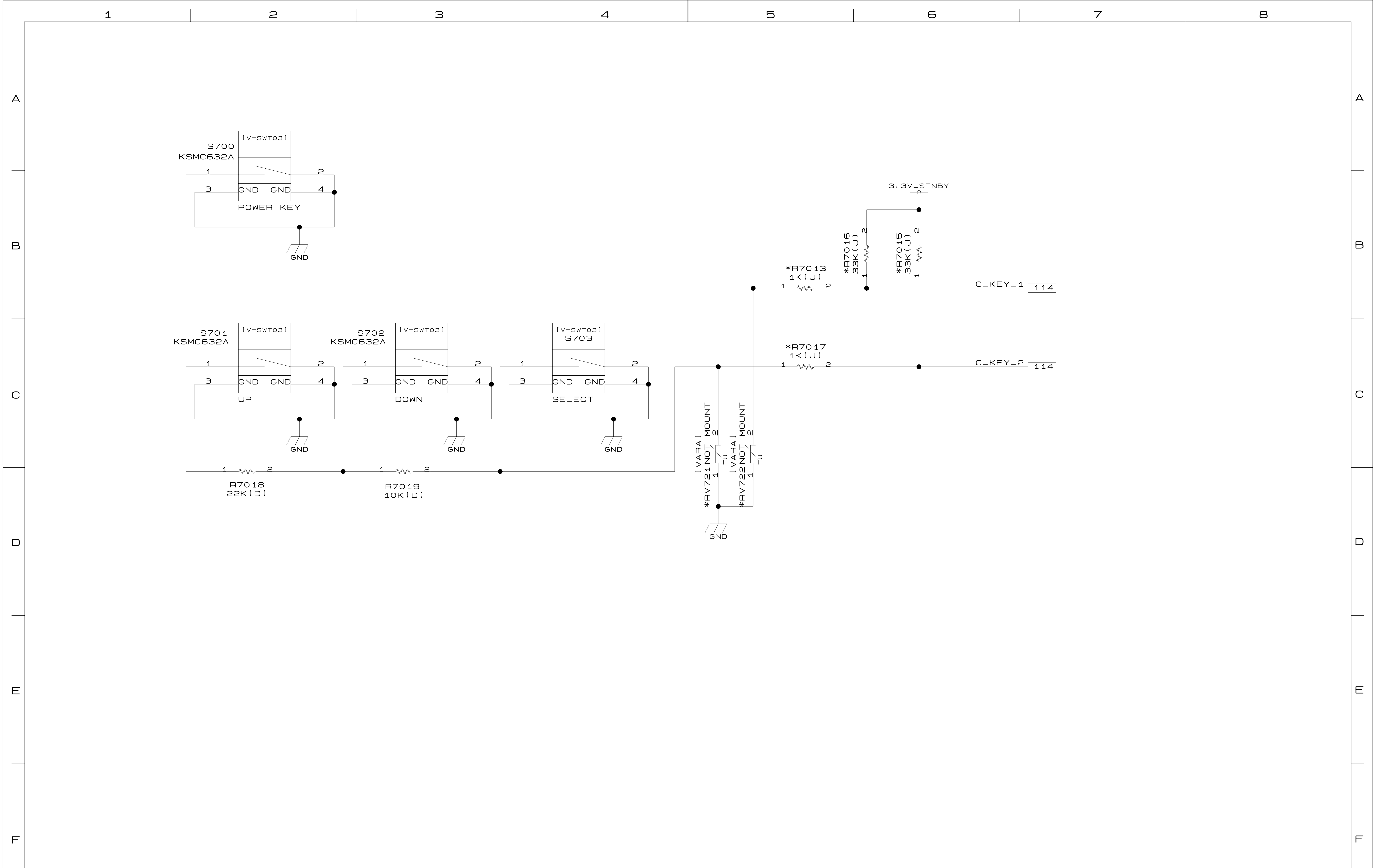


DESIGNED BY Kuwata	TITLE POWER TV2 [MAIN]	FUNCTION 3.3V_HDMI	SH. NO. 665	PAGE NO. 53	REV. MARK MP	DRAWING. NO.
2011.08.04 10:02	TOSHIBA CORPORATION					

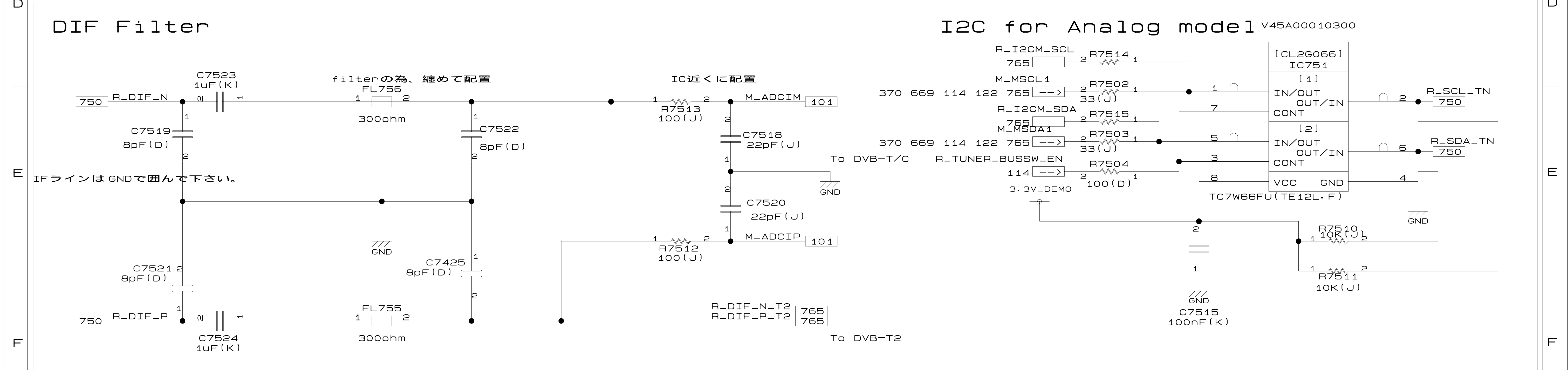
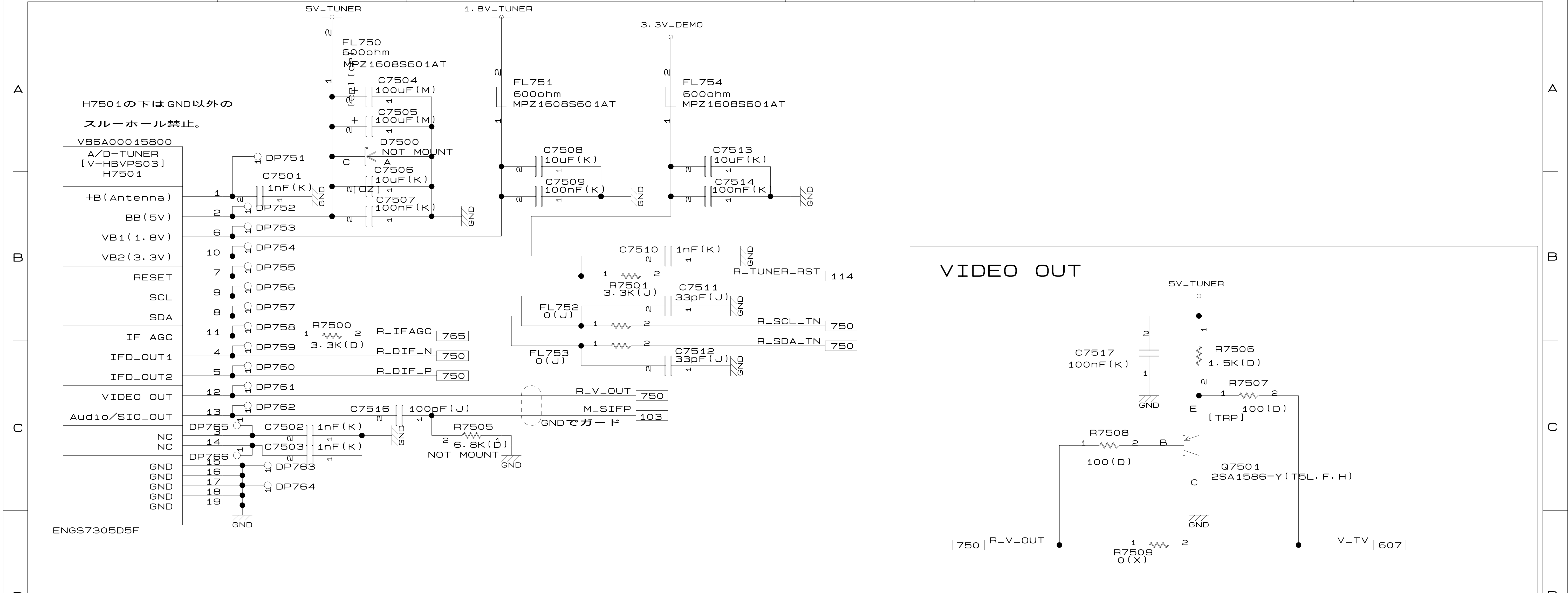


DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Noguchi/Kuwata	MAIN UNIT	HDPC KEY ROM	669	54	MP	PE1014ZA
2011.08.04	10:02	TOSHIBA CORPORATION				



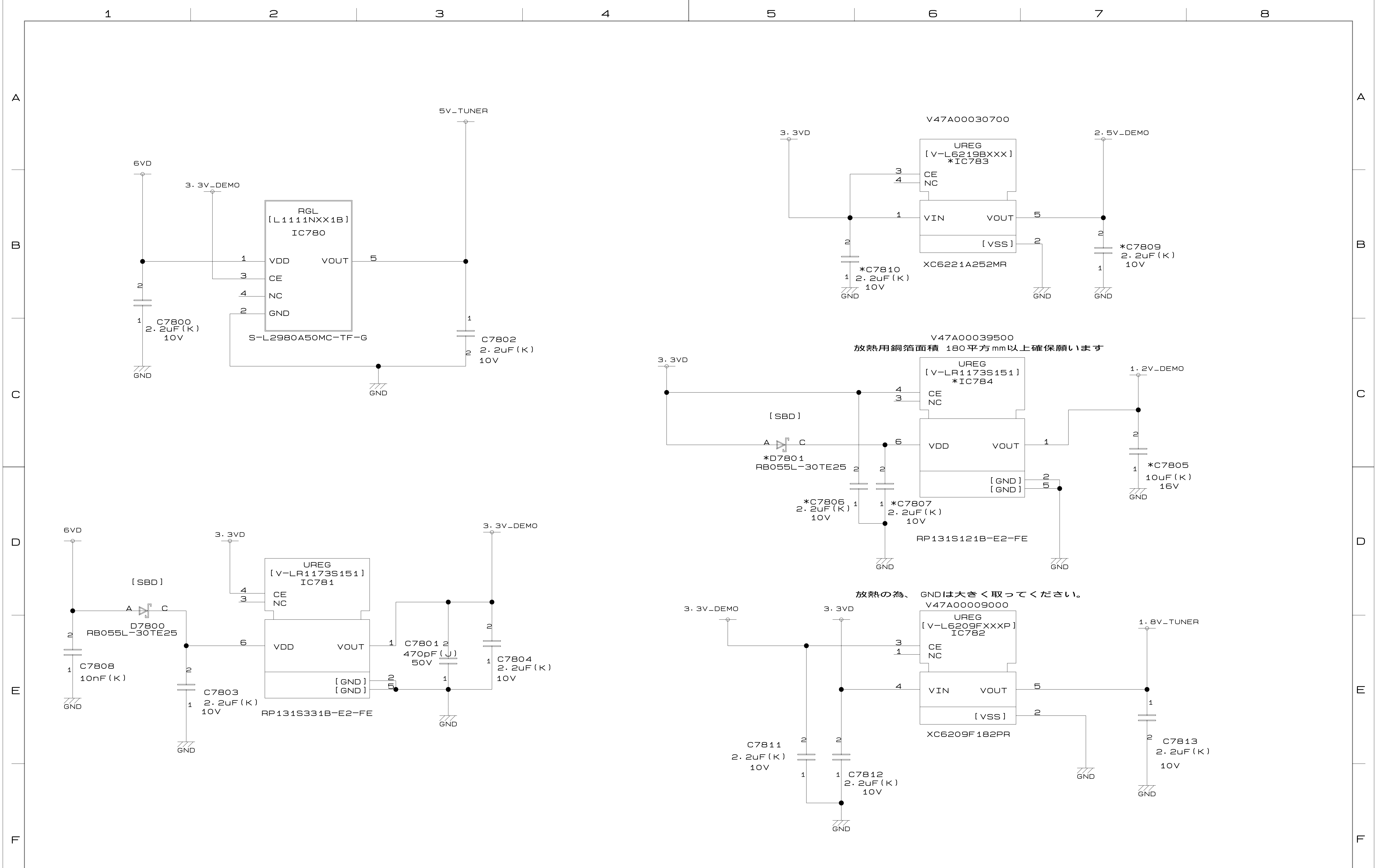


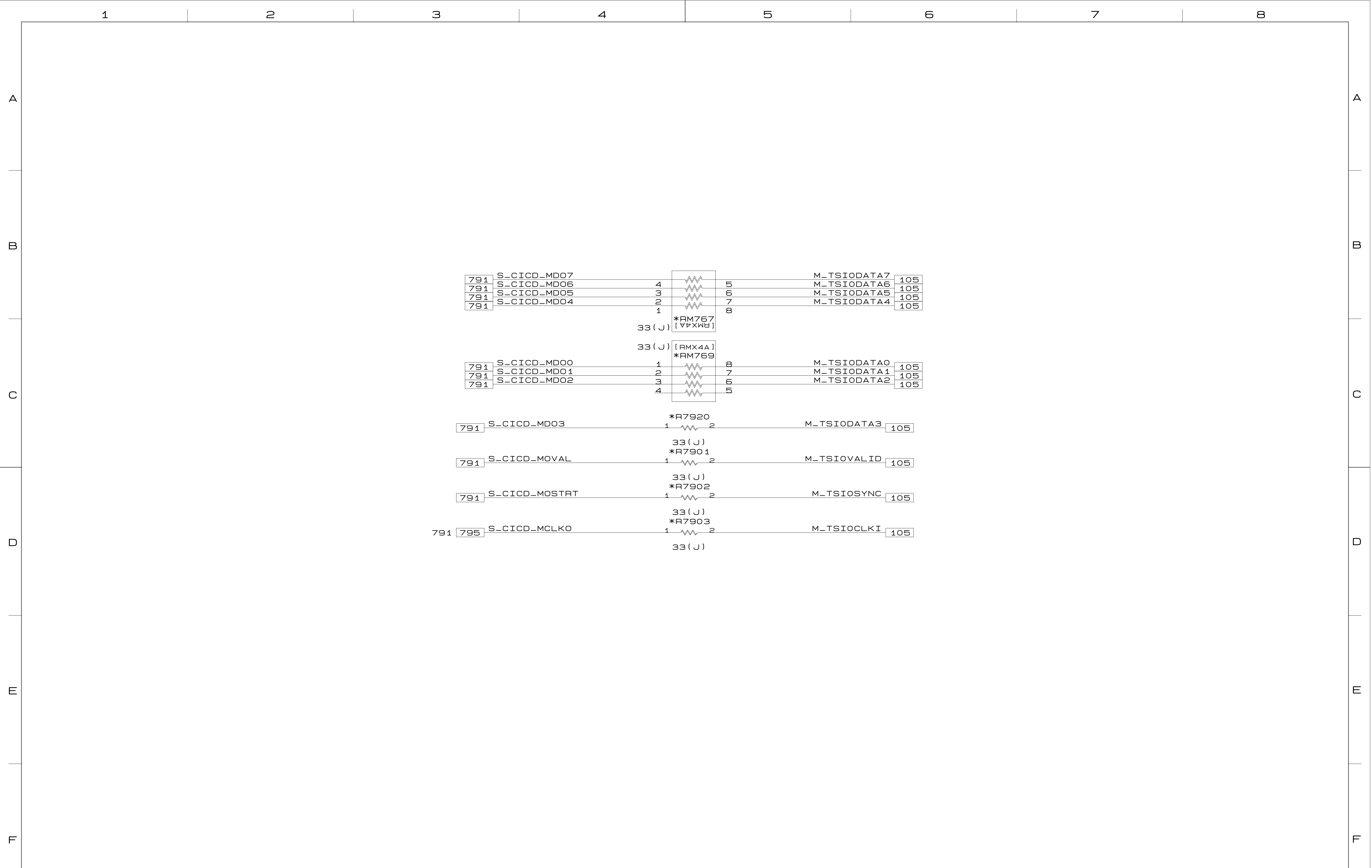
DESIGNED BY IMURA	TITLE MAIN UNIT	FUNCTION 5V LED/KEY	SH. NO. 701	PAGE NO. 56	REV. MARK MP	DRAWING. NO. PE1014ZA
2011.08.04 10:02	TOSHIBA CORPORATION					

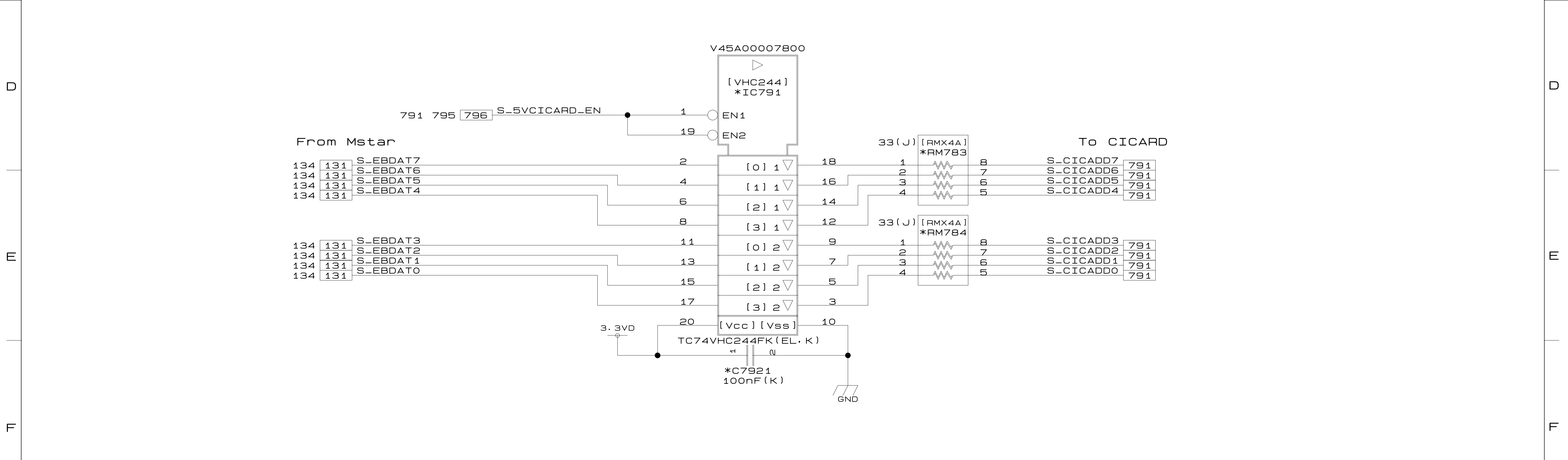
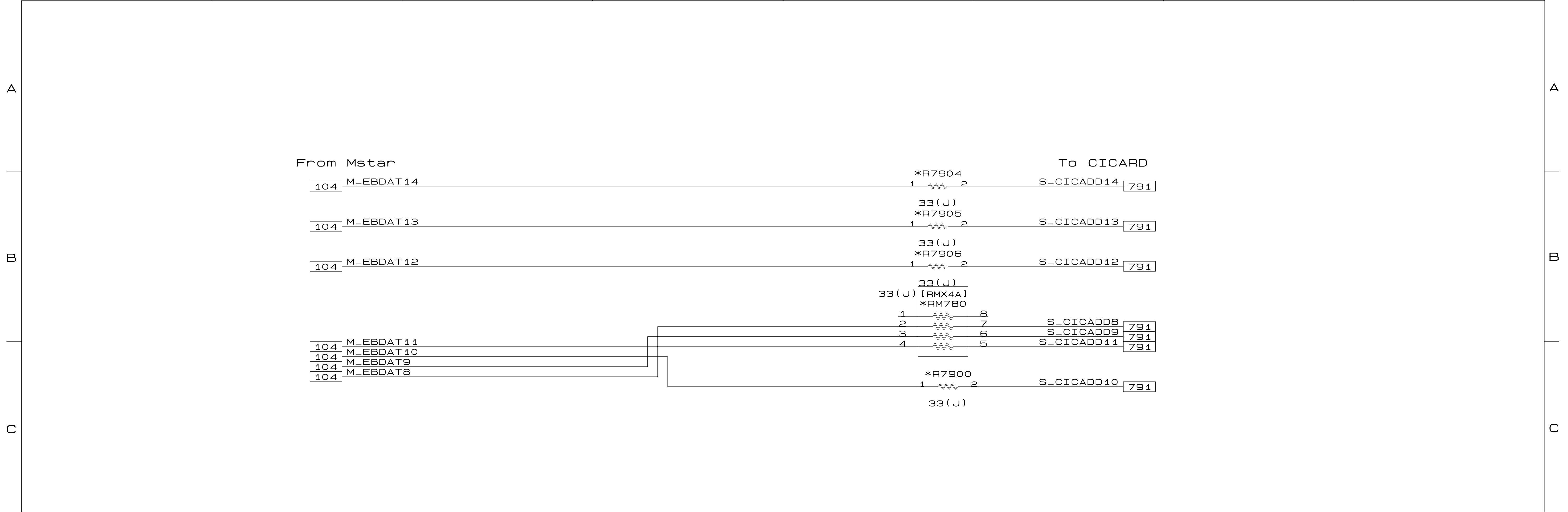


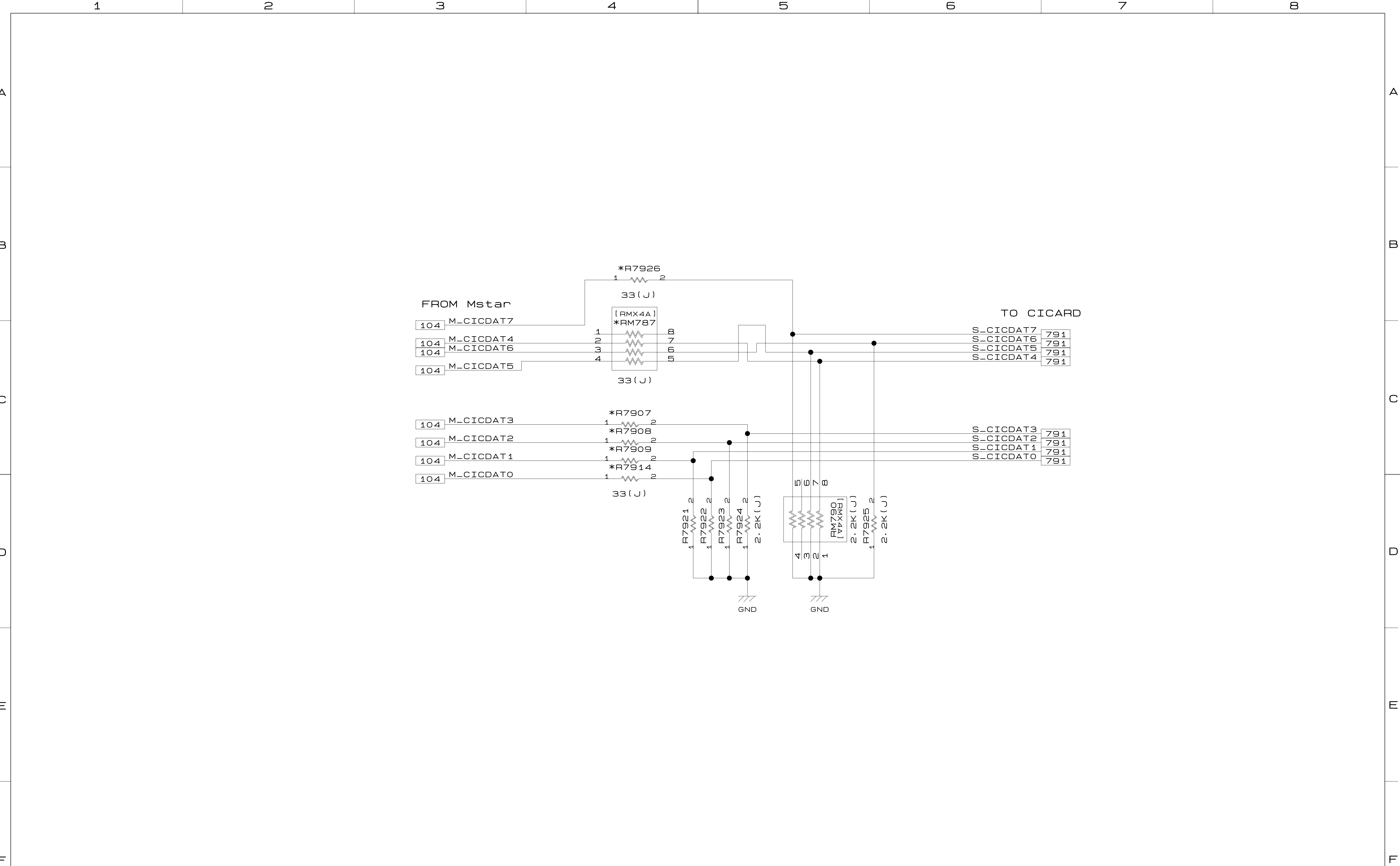
DESIGNED BY R. IZUMI	TITLE MAIN UNIT	FUNCTION TUNER	SH. NO. 750	PAGE NO. 58	REV. MARK MP	DRAWING. NO. PE1014ZA
2011.08.04 10:02			TOSHIBA CORPORATION			

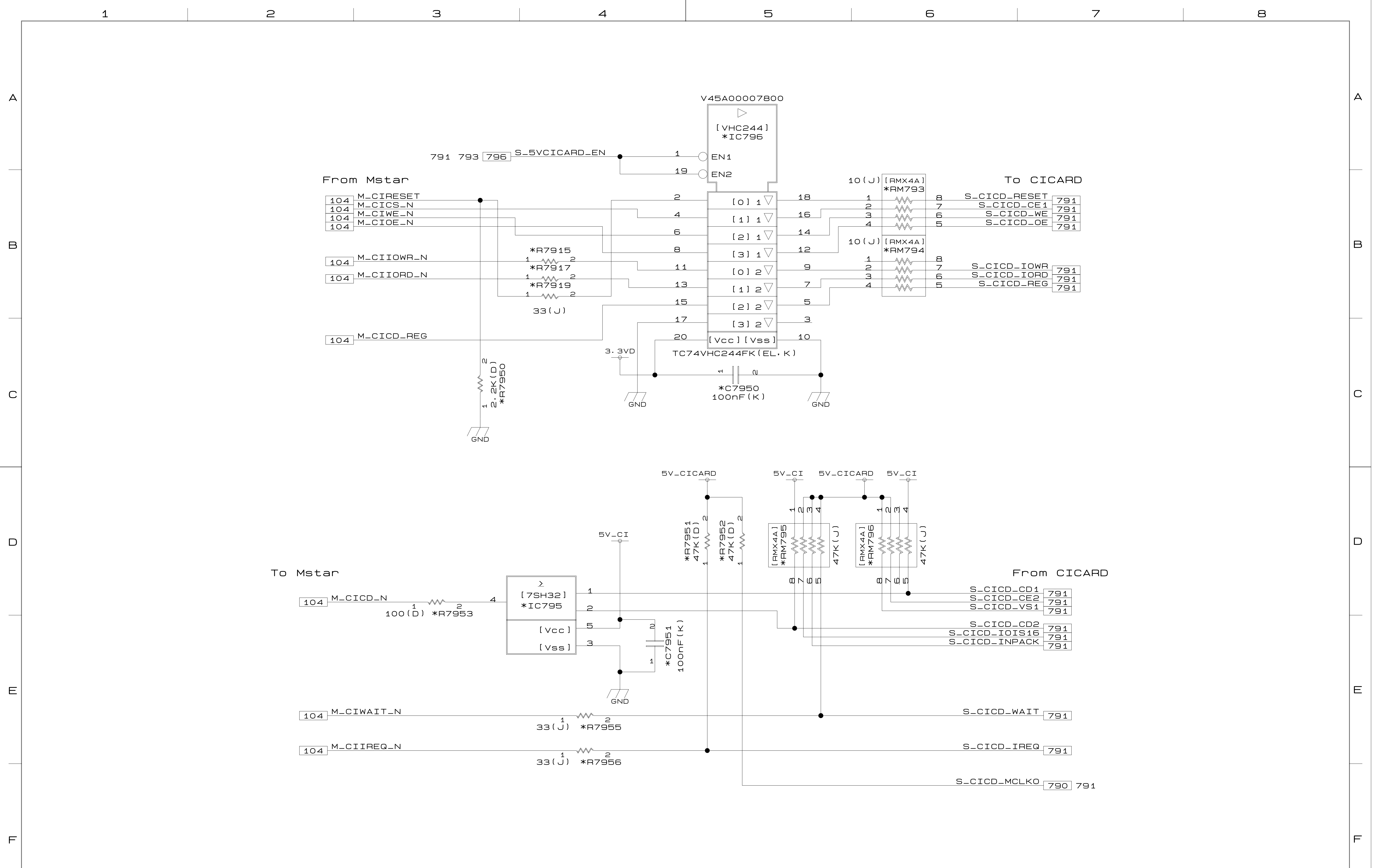












A

B

C

D

E

F

A

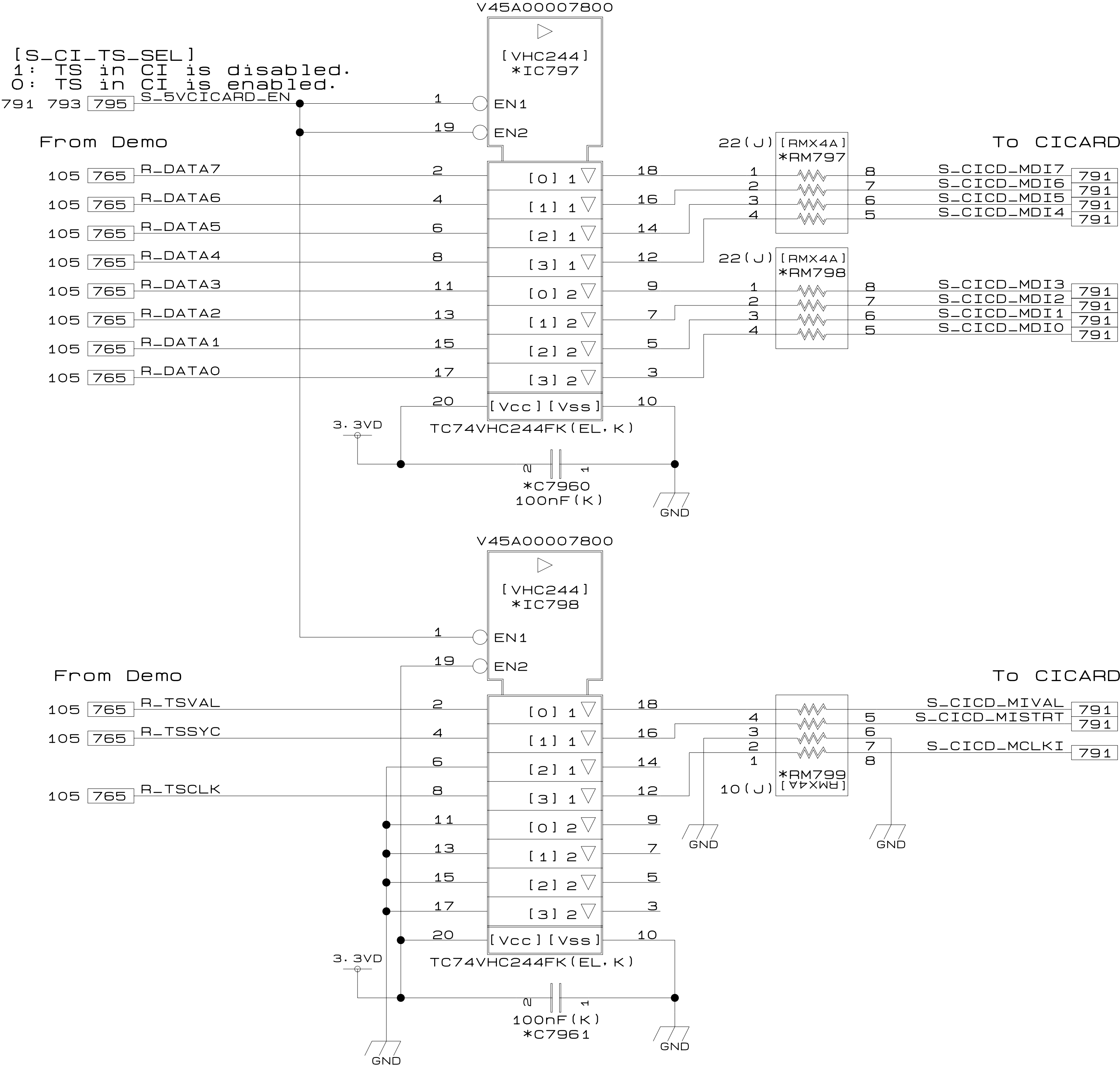
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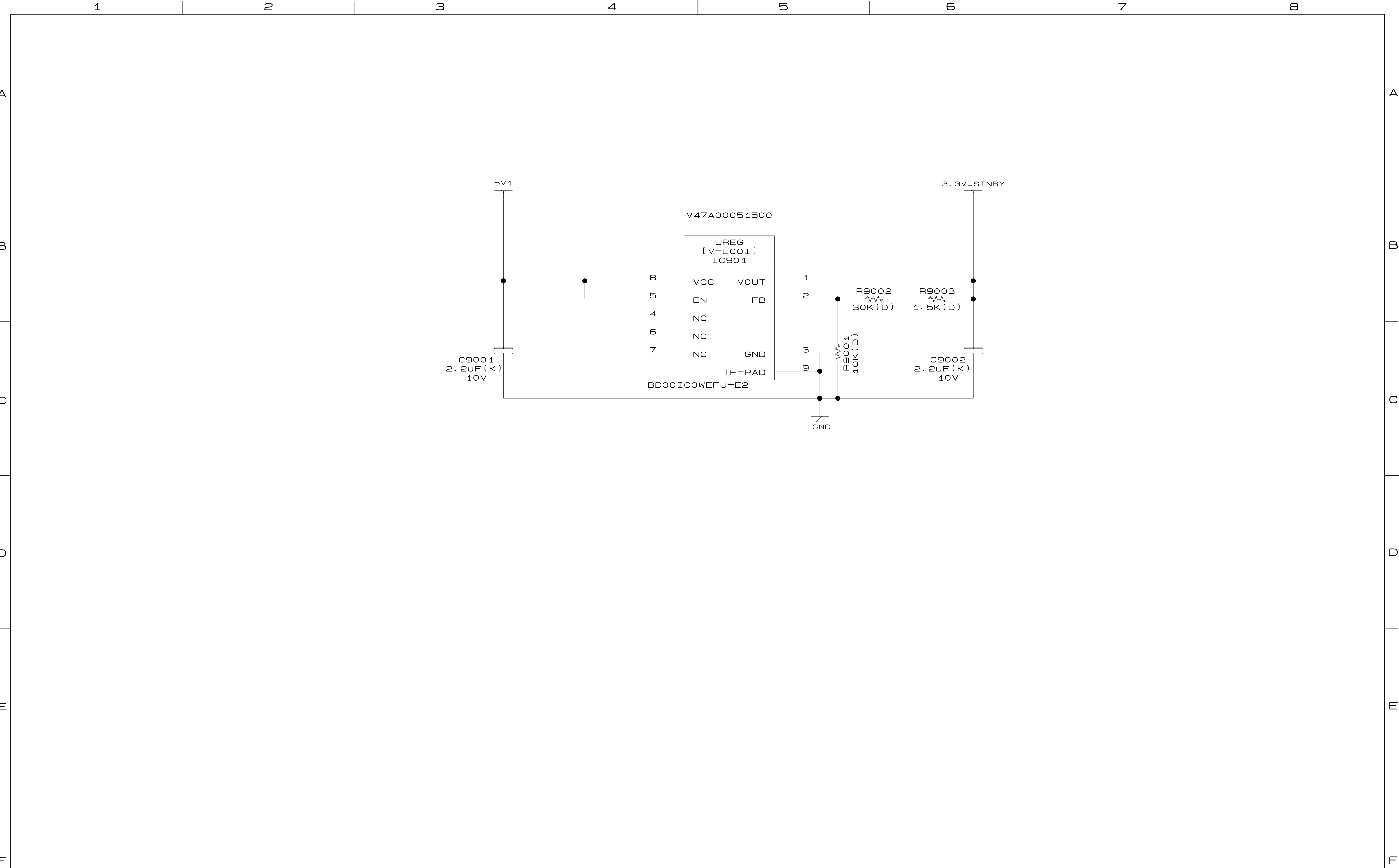
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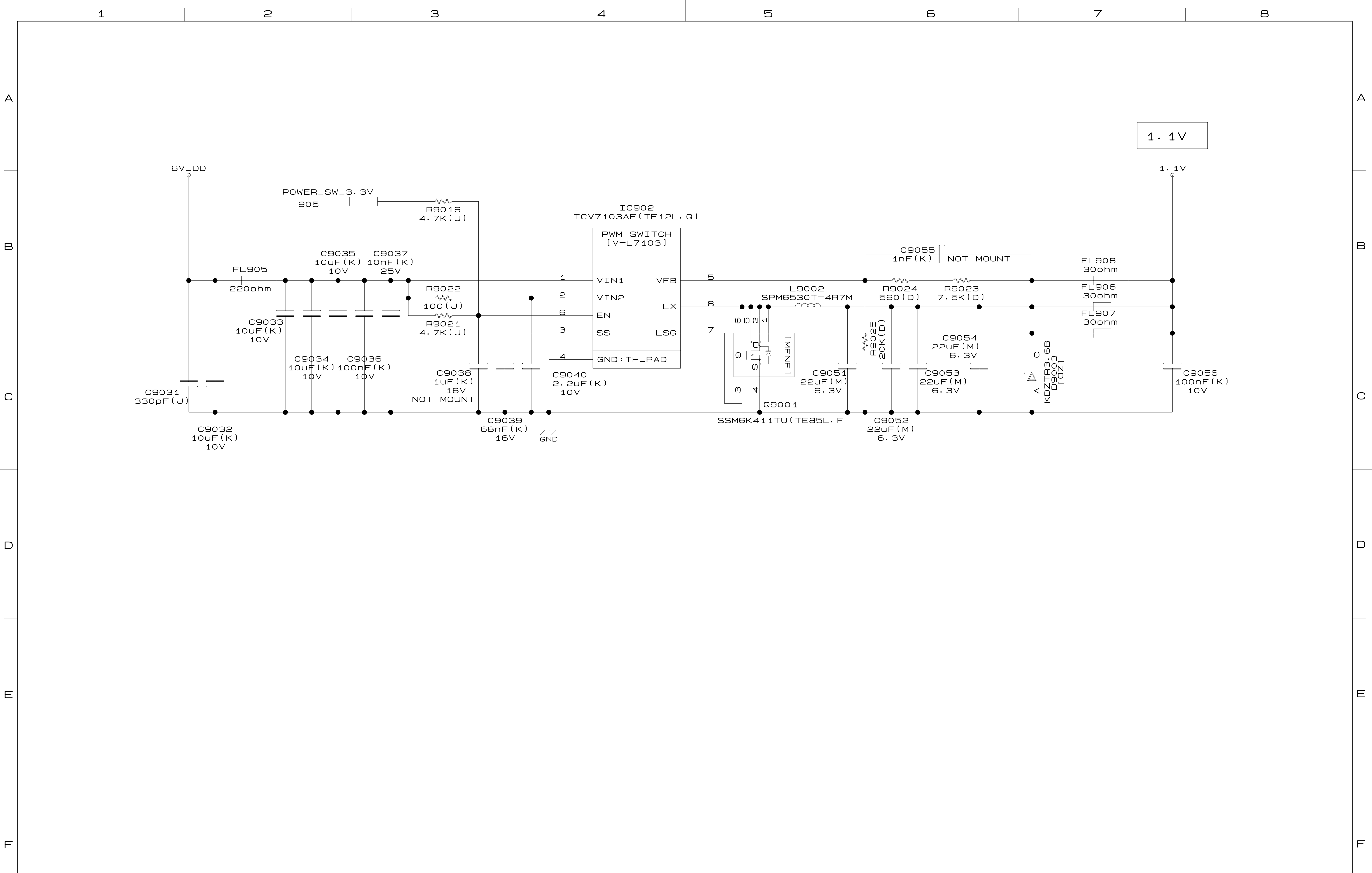
D

E

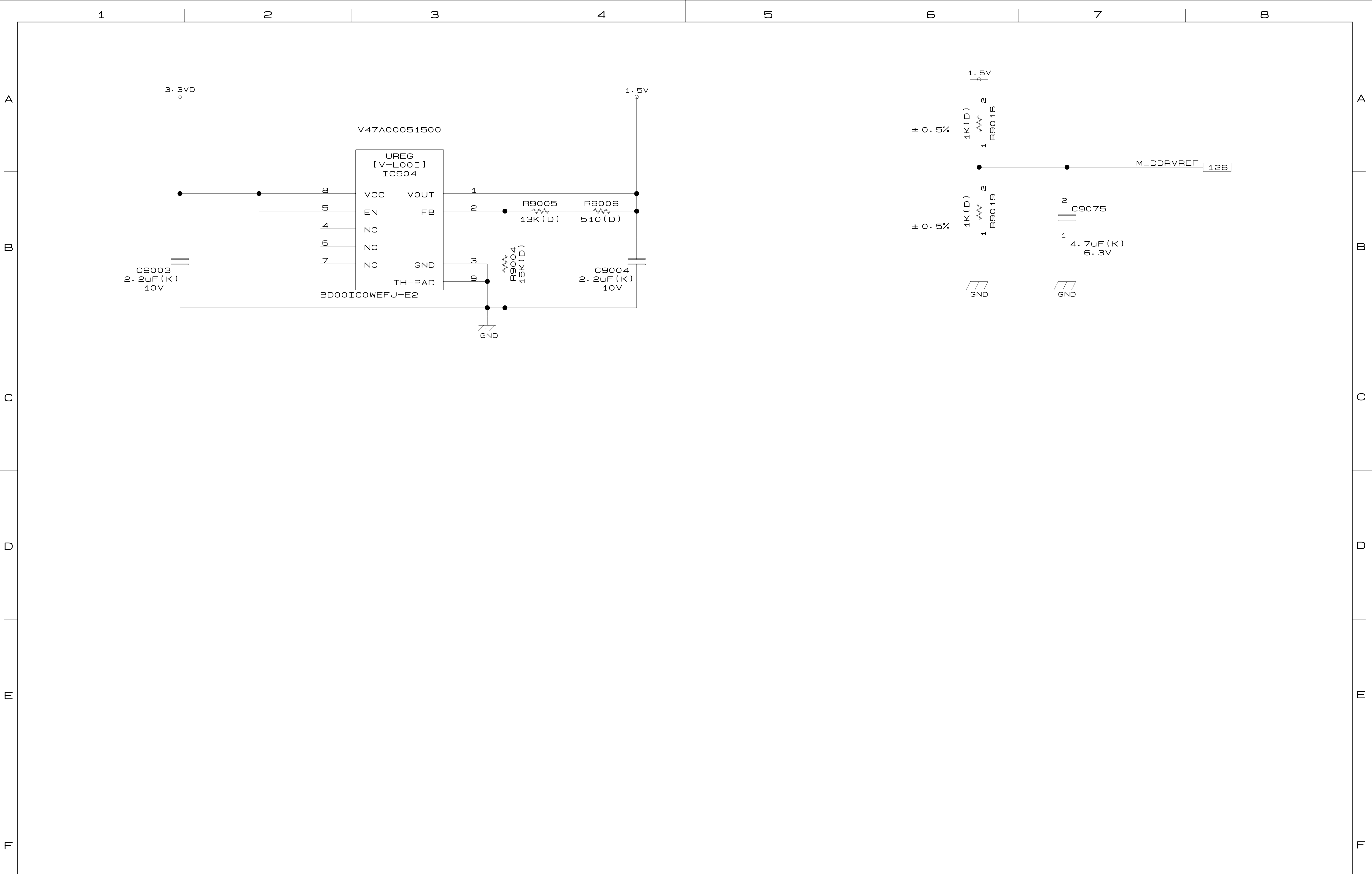
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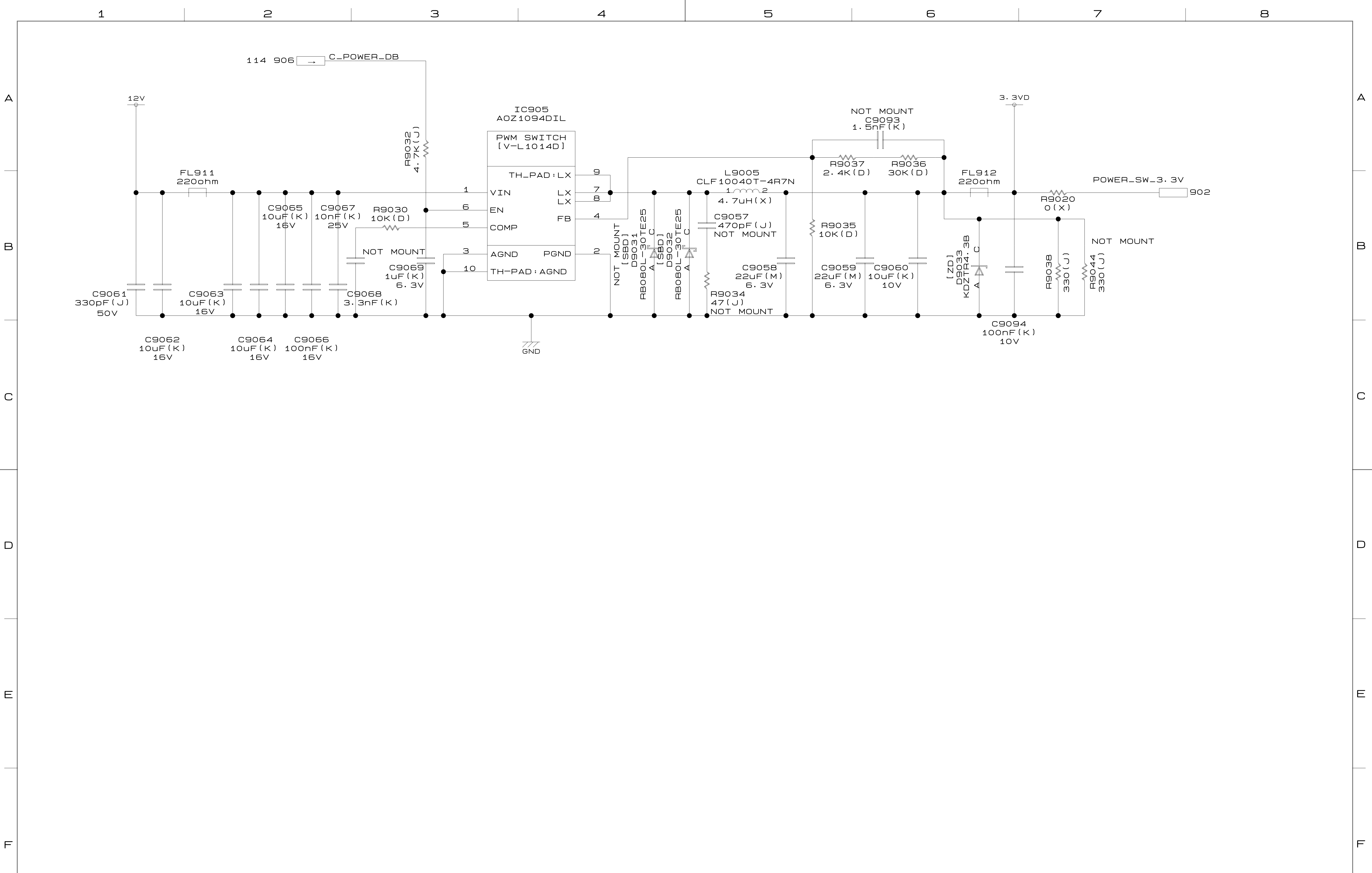




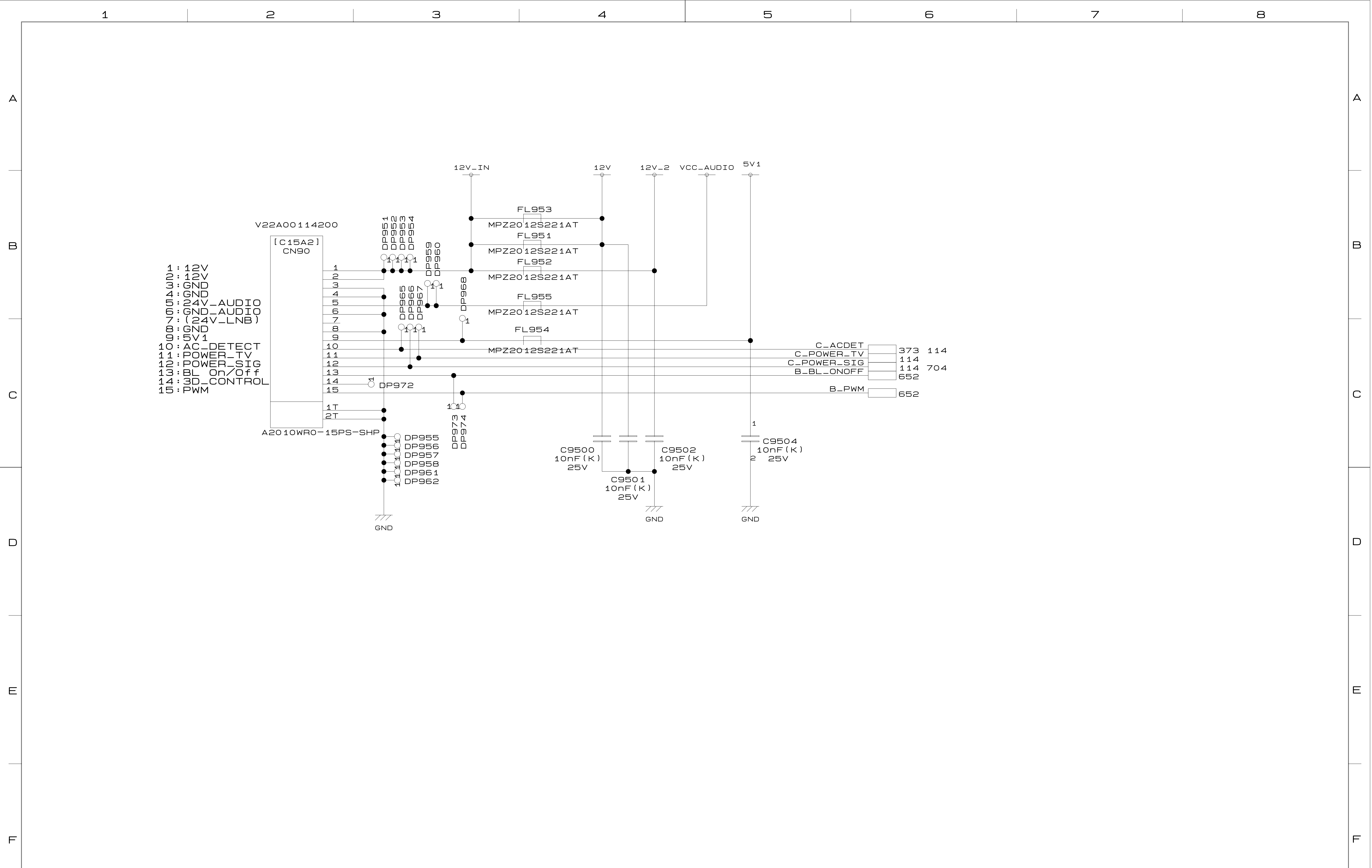
DESIGNED BY	2011/06/21	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
HASUMI		MAIN UNIT	POWER 1.1V	902	68	MP	PE1014ZA
2011.08.04	10:03						TOSHIBA CORPORATION

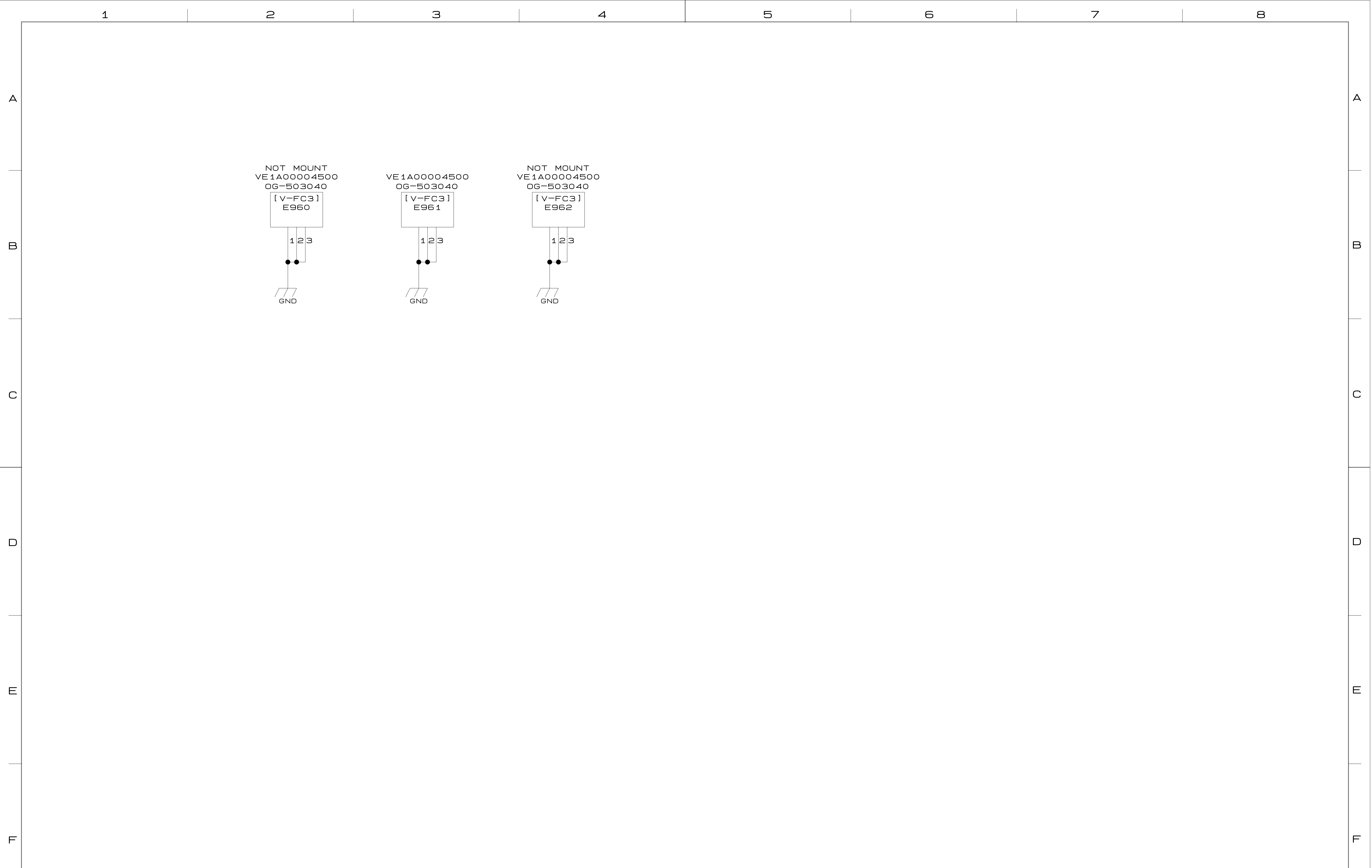


DESIGNED BY HASUMI	TITLE MAIN UNIT	FUNCTION FRC POWER 1.5V	SH. NO. 904	PAGE NO. 69	REV. MARK MP	DRAWING. NO. PE1014ZA
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DESIGNED BY	2011/06/21	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
HASUMI		MAIN UNIT	POWER 3.3VD	905	70	MP	PE1014ZA





DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION EMC CONTROL	SH. NO. 960	PAGE NO. 73	REV. MARK MP	DRAWING. NO. PE1014ZA
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