

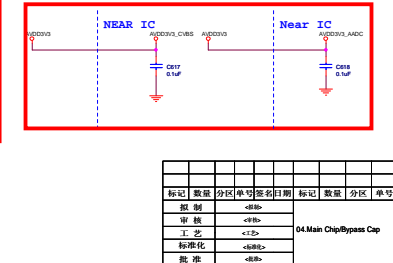
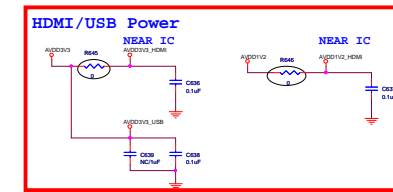
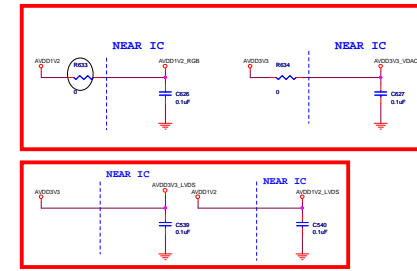
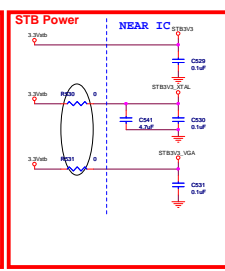
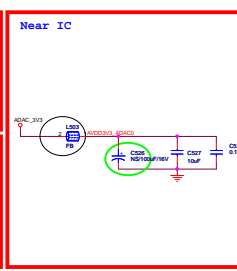
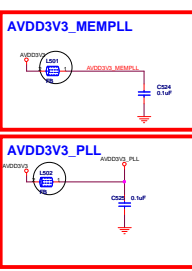
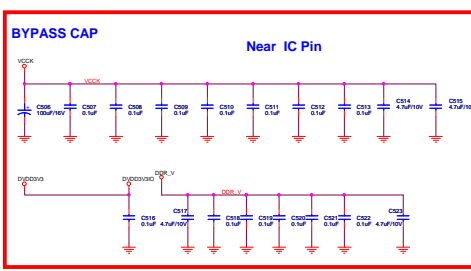
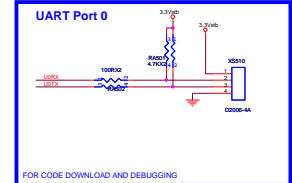
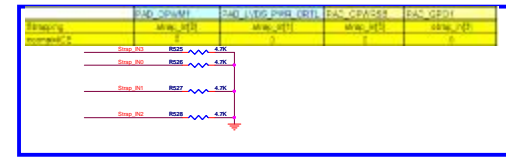
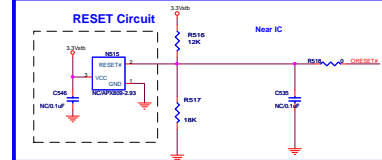
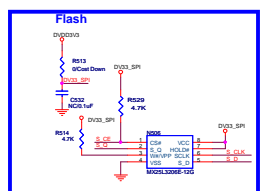
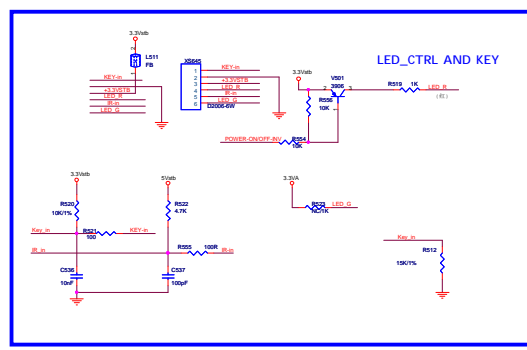
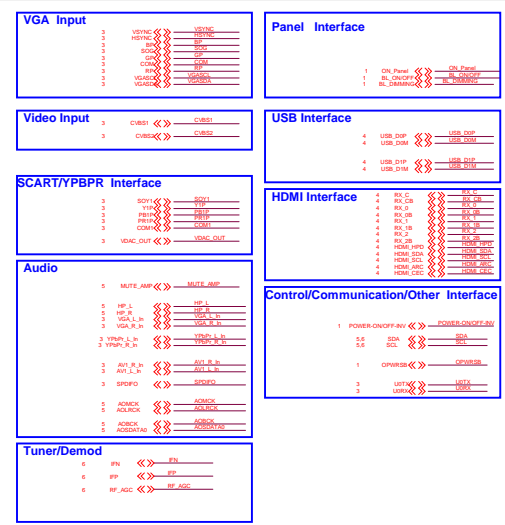
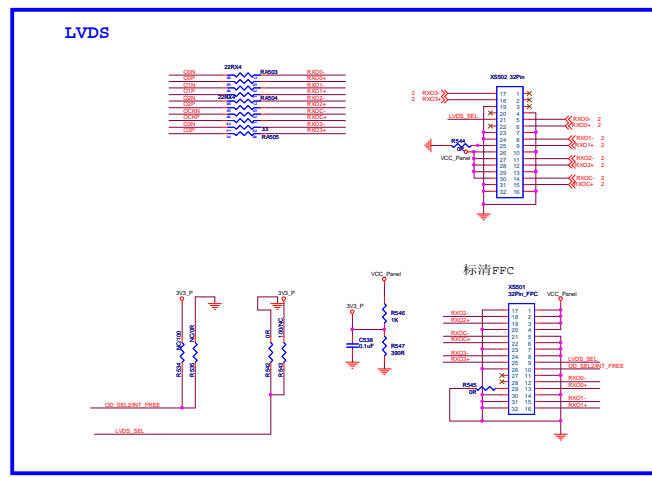
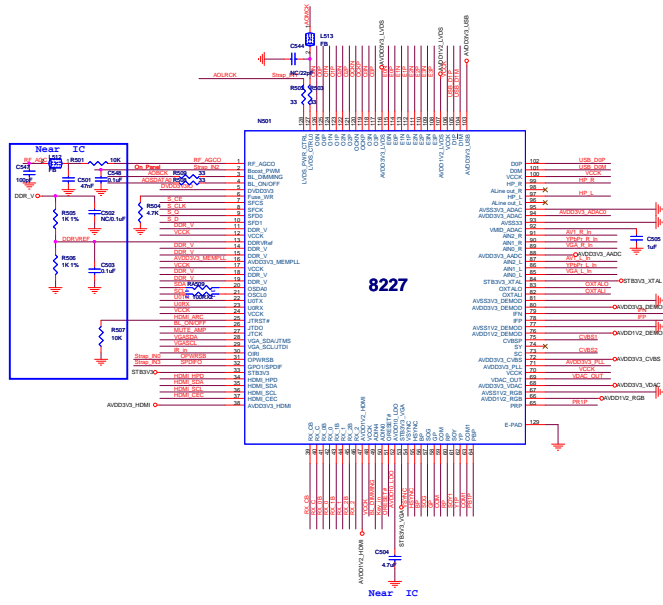
PIN Name	Function	STB GPIO
HDMI HPD	HDMI Hotplug	Y
JTRST#	SPI Flash Write Protect Control (L: WP Enable)	Y
JTDO	BL ON/OFF Control (L: ON)	Y
JTCK	AMP Mute Control (L: MUTE)	Y
VGA/SDA/JTMS	VGA/SDA	Y
VGA/SCL/JTDI	VGA/SCL	Y
OPWRSB	Power Module ON (L: ON)	Y
GPIO1/SPDIF	SPDIF OUT	Y
U0TX	U0TX	Y
U0RX	U0RX	Y
ADIN0	KEY IN	Y
ADIN4	BL_Dimming	Y
RF_AGCO	RF_AGCO	N
BoostPWM	LVDS Power Control(H: ON)	N
BL_Dimming	Aobck	N
BL_ON/OFF	Aosdata0	N
LVDS PWR Control	Aolrck	N
LVDS CTRL0	Aomck	N

LED TV Schematic Diagram

Models:  
KONKA LED32F3300C, LED42F3300C (MT8227 N501)  
KL26GT611, KL32GT615U  
SUPRA STV-LC22740WL, STV-LC42740FL, STV-LC26740WL  
Components:  
N501: MT8227  
N506: MX25L3206E  
N202: STA333W  
N110: TDA18273  
N801: SY8009A  
N802: FR9888  
N701: OZ9902C  
NW907: FAN6755W

Rev	History	P#	DATE
V0.1	INITIAL VERSION		
V1.0	3300 First VERSION		



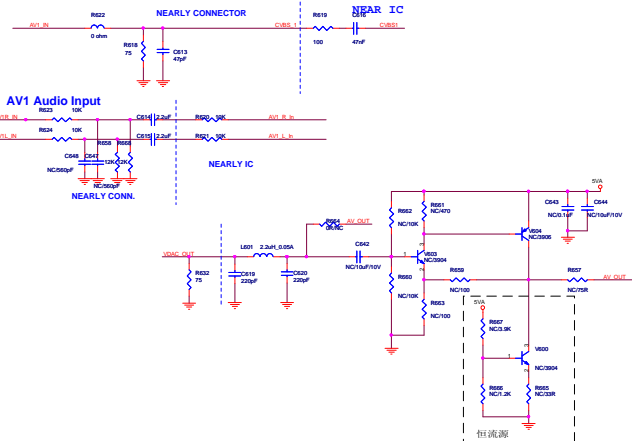
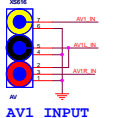


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审 核										
工 艺										
标准化										
批准										

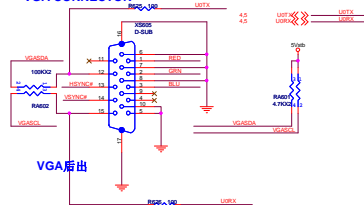
04 Main Chip/Bypass Cap

第 2 页 共 11 页

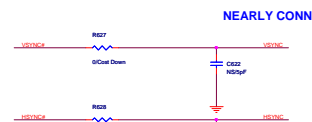
# AV1 Input



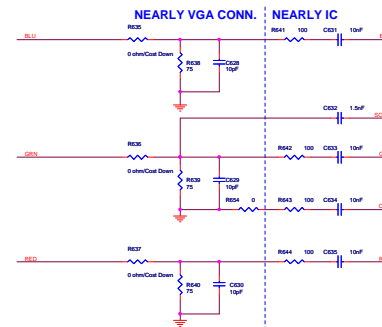
# VGA CONNECTOR



## VGA 输出

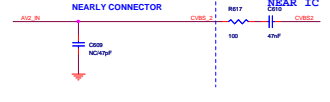


VGA Audio Input 使用YPbPr 的Audio

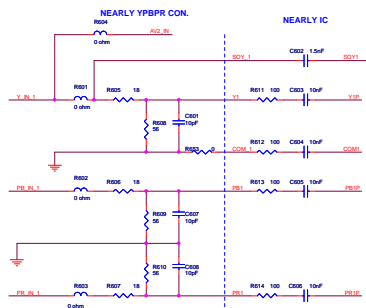
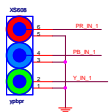
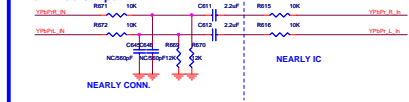


# YPbPr & AV2 Input

## AV2 Video Input



## YPbPr Audio Input



YPbPr&AV2&VGA INPUT

Audio	2	HP/OUT_L	HP/OUT_L
	2	HP/OUT_R	HP/OUT_R
	2	YPbPr_L_R	YPbPr_L_R
	2	YPbPr_L_R	YPbPr_L_R
	2	AV1_L_R	AV1_L_R
	2	VGA_L_R	VGA_L_R
	2	VGA_L_R	VGA_L_R
	2	SP/OUT	SP/OUT

Video Input	2	CIN01	CIN01
	2	CIN02	CIN02

SCART Interface	2	VH/OUT	VH/OUT
-----------------	---	--------	--------

VGA Input	2	VH/OUT	VH/OUT
	2	HP/OUT_L	HP/OUT_L
	2	HP/OUT_R	HP/OUT_R
	2	YPbPr_L_R	YPbPr_L_R
	2	YPbPr_L_R	YPbPr_L_R
	2	AV1_L_R	AV1_L_R
	2	VGA_L_R	VGA_L_R
	2	VGA_L_R	VGA_L_R

标记	数量	分区	单号	签名	日期	标记	数量	分区	单号	签名	日期	等级	标记
设计						设计							
审核						审核							
工艺						工艺							
标准化						标准化							
批准						批准							

06\_AV1YPbPr



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# Audio MUTE Circuit

12V

R203 NC390

R201 150

R242 NC3R

N205

RESET#

VCC

GND

APX009-2.53

R222 10K

R243 20K

AMP\_RESET

200ms

3.3Vsb

R586 10K

R223 1K

AMP\_EN

MUTE\_AMP

R214 NC47K

R219 4.7K

V214 2N14

3.3Vsb

R217 4.7K

R216 4.7K

V217 2N14

HP\_MUTE

HI => MUTE

LO => UN-MUTE

### Headphone output

Close to Mainchip

HPOL

HPOR

HPOLUT\_L

HPOLUT\_R

R224 0R20K

R228 NC47

C227 NC0.1uF

R226 0R20K

R227 NC47

C228 NC0.1uF

C239 220uF/16V

C238 220uF/16V

R232 NC47K

R231 NC47K

R235 10K Cost Down

R236 10K Cost Down

C224 NC470pF

C226 NC470pF

ESD

PESD3V3L2BT

**Audio AMP**

**N202**

1. GND\_SUB  
2. SA  
3. TEST\_MODE  
4. VSS  
5. VCC\_REG  
6. OUT2B  
7. GND2  
8. VCC2  
9. OUT2A  
10. OUT1B  
11. VCC1  
12. GND1  
13. OUT1A  
14. GND\_REG  
15. VDD  
16. CONFIG  
17. NC  
18. NC

**STA333W**

1. VDD\_DIG  
2. GND\_DIG  
3. SCL  
4. SDA  
5. INT\_LINE  
6. RESET  
7. SDI  
8. LRCKI  
9. BICKI  
10. XTI  
11. PLL\_GND  
12. Filter\_PLL  
13. VDD\_PLL  
14. PWRON  
15. GND\_DIG  
16. VDD\_DIG  
17. NC  
18. NC  
19. NC  
20. NC  
21. NC  
22. NC  
23. NC  
24. NC  
25. NC  
26. NC  
27. NC  
28. NC

**3.3V**

**12V**

**AMP\_12V**

**AMP\_RESET**

**PLL\_FILT**

**AMP\_EN**

**AMP\_Lout+**

**AMP\_Lout-**

**AMP\_Rout+**

**AMP\_Rout-**

**X3201**

**D2006-4W**

## Control

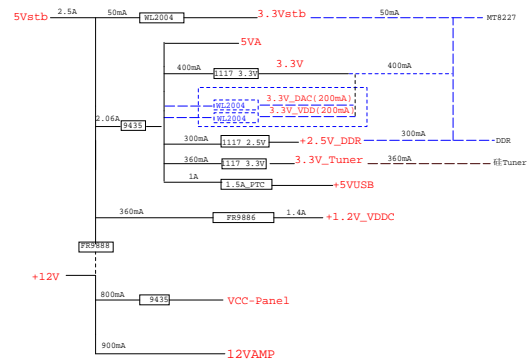
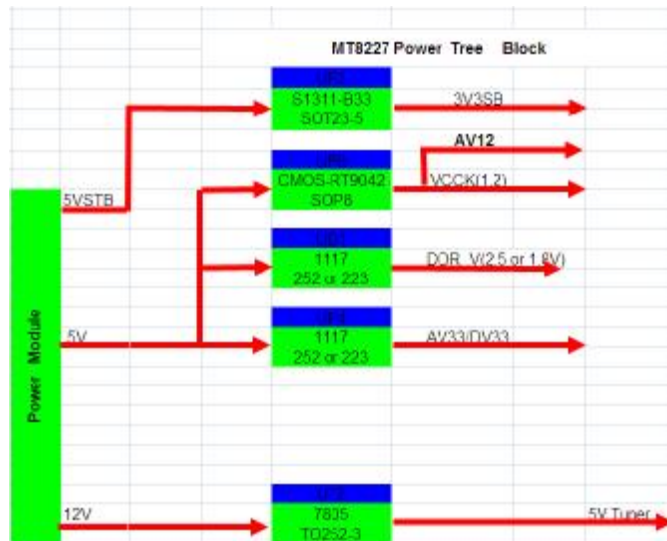
2	MUTE_AMP	<<>>	MUTE_AMP
2.6	SCL	<<>>	SCL
2.6	SDA	<<>>	SDA

## Audio

2	HP_L	《》	HP_L
2	HP_R	《》	HP_R
3	HPOUT_R	《》	HPOUT_R
3	HPOUT_L	《》	HPOUT_L
3	HP_MUTE	《》	HP_MUTE
2	AOMCK	《》	AOMCK
2	AOLRCK	《》	AOLRCK
2	AOBCK	《》	AOBCK
2	AOSDATA0	《》	AOSDATA0

[illegible]

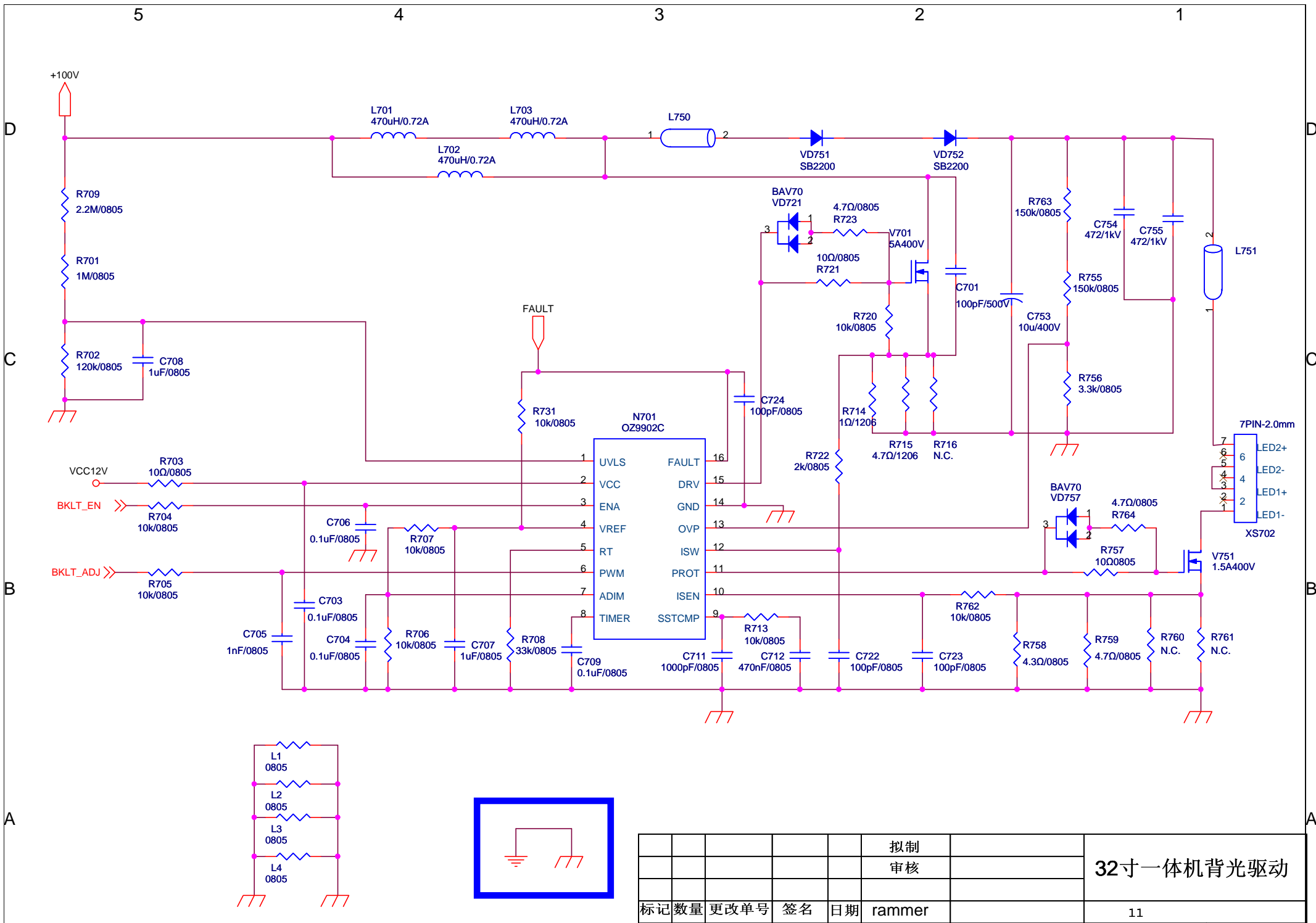


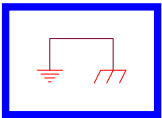


MediaTek Confidential

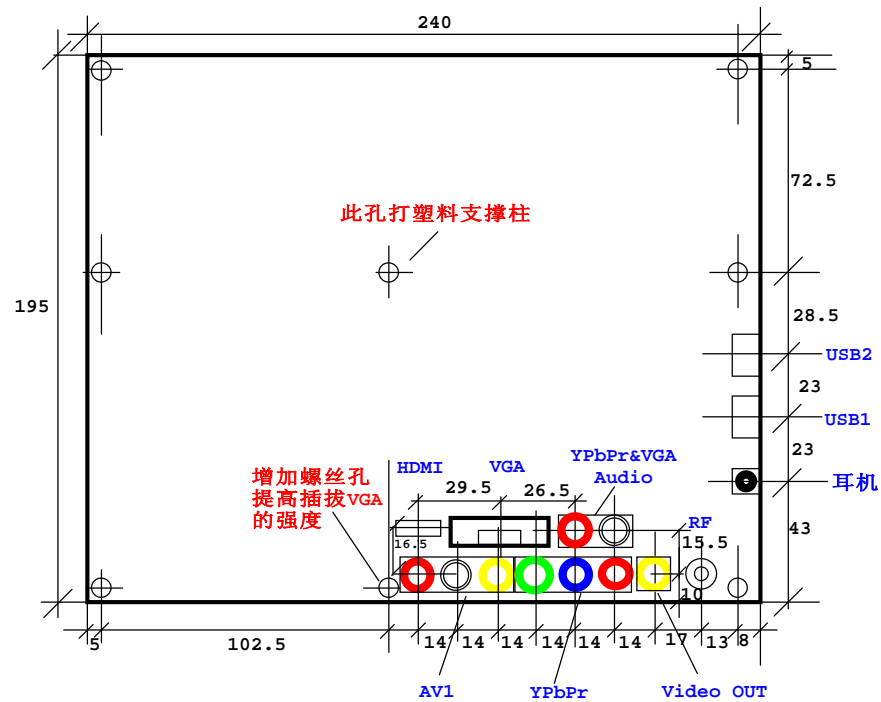
		<b>MediaTek Inc.</b> No. 1-2, Innovation Rd 1, S88P, Hsin-Chu City 300 TEL: 03527-0766 FAX: 03527-7610	
		<b>Placement/Sys Flow</b>	
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C		Max Yang	0.1
Date:	Wednesday, July 25, 2012	Sheet	7 of 11



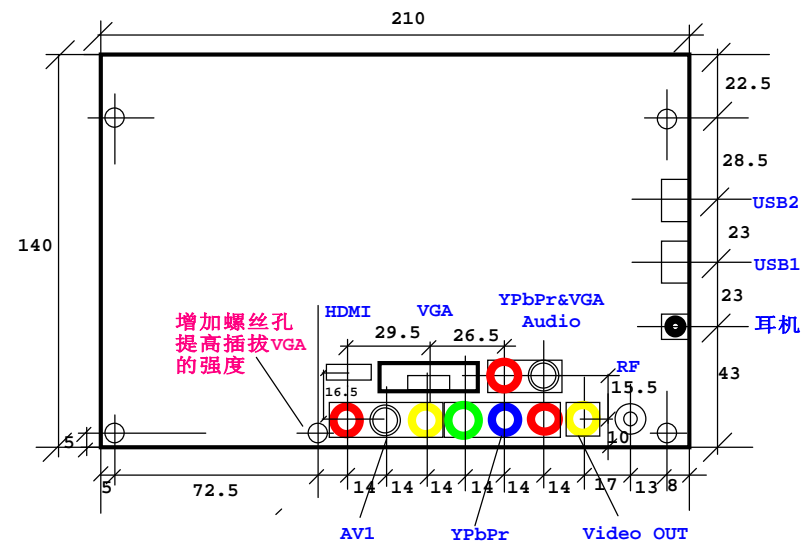




## LED40/42F3300C



## LED32F3300C



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Date:	Wednesday, July 25, 2012	Sheet 11 of 11
Rev		
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