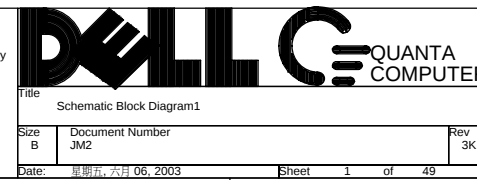
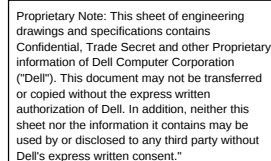
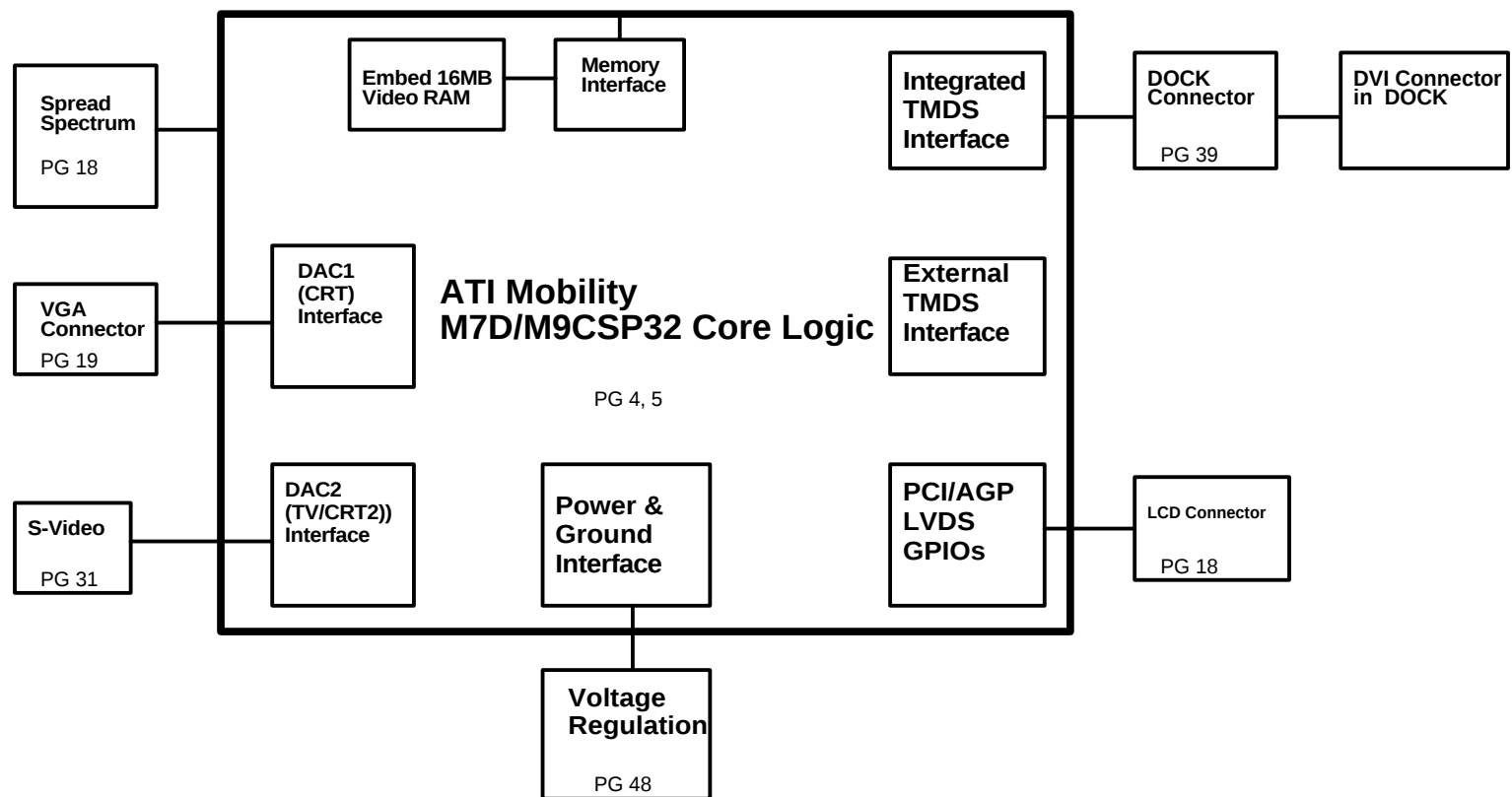


PWA:4U853, Rev A04
SCH: 7U239, Rev A03





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DELL		QUANTA COMPUTER	
Title: Schematic Block Diagram2			
Size: B	Document Number: JM2		Rev: 1A
Date: 星期五, 六月 06, 2003	Sheet: 2 of 49		

INDEX		
Pg#	Description	DNI LIST
1	Schematic Block Diagram 1	
2	Schematic Block Diagram 2	
3	FRONTPAGE	
4-5	Banias	
6-8	Odem MCH-M	
9-11	ICH4	
12-13	DDR SO-DIMM(200P)	
14	CLOCK GENERATOR	
15-17	M7/M9 Graphics	
18-19	LCD CONN,CY25245 & CRT CONN	
20	IDE (HDD&CD_ROM)	
21	PAD& SCREW	
22	O2_OZ711E1&CONN	
23-24	MINI-PCI & MDC CONN	
25-26	SIO	
27-28	SERIAL & PARALLEL	
29	FLASH	
30-31	TOUCH PAD & SWITCH&LED	
32	FAN & THERMAL	
33-34	AC97 CODEC & connector	
35-36	LAN(KENAI32-LAVON) & JACK	
37	FIR	
38-39	DOCKING Buffer and connector	
40	Reset circuit	
41	RTC BATTERY	
42-43	Battery Selector & Charger	
44	CPU Power	
45	1.2V,1.5V,1.8VS.2.5VS	
46	D/D Power	
47	RUN POWER SW	
48	VGA DC/DC	
49	DCIN,Batt	
50	CPU I/O Power	

Power & Ground			
Label	Pg#	Description	Control Signal
DC_IN+		AC ADAPTER (20V)	
PBATT+		MAIN BATTERY + (10~17V)	
PWR_SRC		MAIN POWER (10~20V)	
RTC_PWR3_3V		RTC & PCL POWER (3.3V)	
+12V		+12V	DRUNPWROK
VHCORE		CPU CORE POWER (1.25/1.15V)	RUNPWROK
V1_2RUN		AGTL+ POWER (1.2V)	RUNPWROK
+3VRUN		SLP_S3# CTRLD POWER	RUN_ON
+3VSUS		SLP_S5# CTRLD POWER	SUS_ON
+5VALW		8051 POWER (5V)	
+5VRUN		SLP_S3# CTRLD POWER	RUN_ON
+5VSUS		SLP_S5# CTRLD POWER	SUS_ON
+5VHDD		HDD POWER (5V)	HDDC_EN#
+5VMOD		MODULE POWER (5V)	MODC_EN#
STRB#5V		EXTERNAL FDD POWER (5V)	FDD/LPT#
+5VFAN1, +5VFAN2		FAN POWER (5V)	FAN_OFF/ON#
VDDA		AUDIO ANALOG POWER (5V)	RUN_ON
1_8VSUS		RESUME WELL IN ICH	
1_8VRUN		SLP_S3# CTRLD POWER	
+3VALW		8051 POWER (3V)	
V1_5RUN		AGP I/O POWER	
 GND	ALL PAGES	DIGITAL GROUND	
 AGND		AUDIO GND	
 GNDP		CPU POWER GND	
 CGNDP		CHARGER GND	
 DGNDP		DC/DC POWER GND	
 LANGND		COMBO CONN GND	

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TitleIndex, DNI, Power & Ground

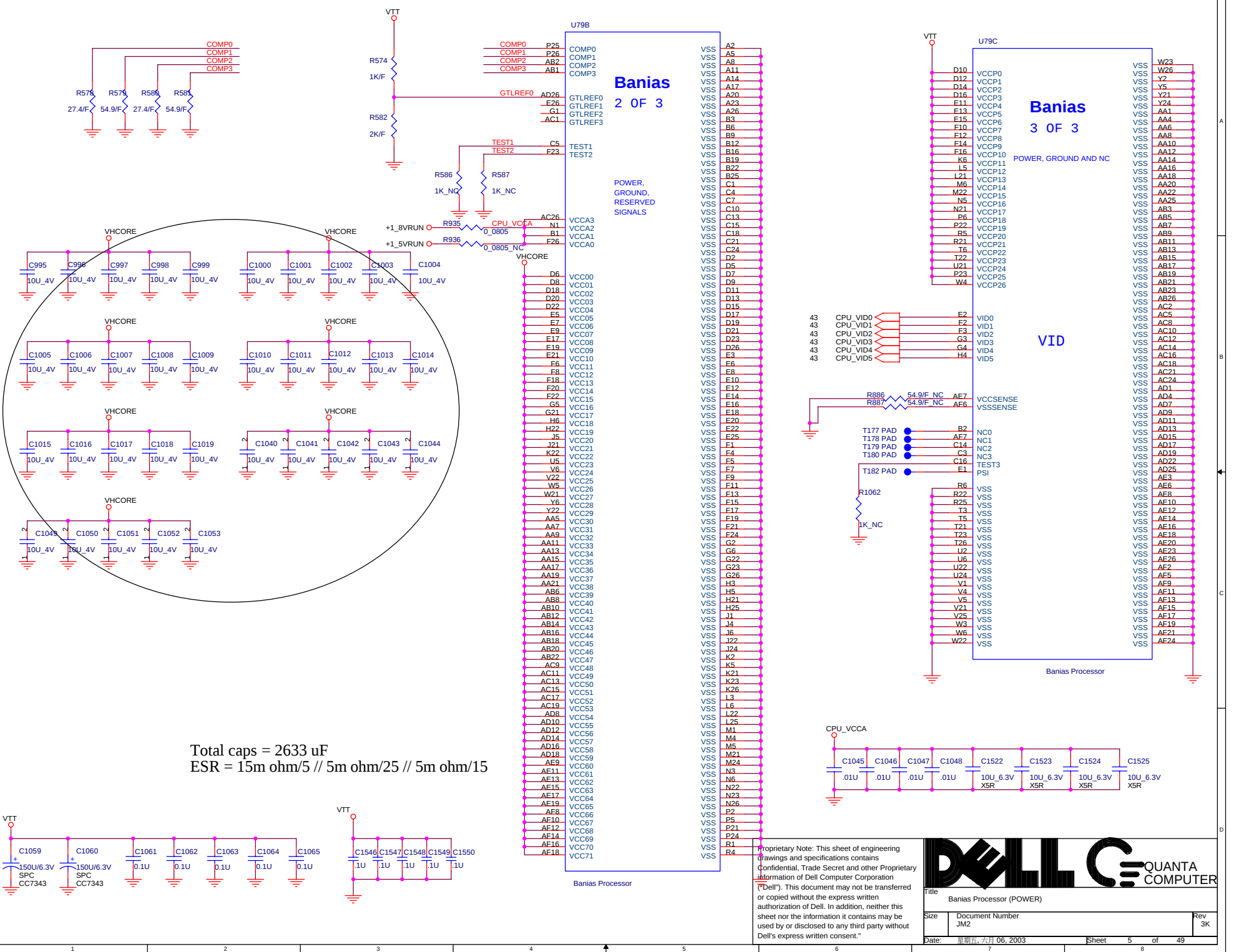
SizeB

Document NumberJM2

Rev1A

Date: 星期五, 六月 06, 2003

Sheet3 of 49



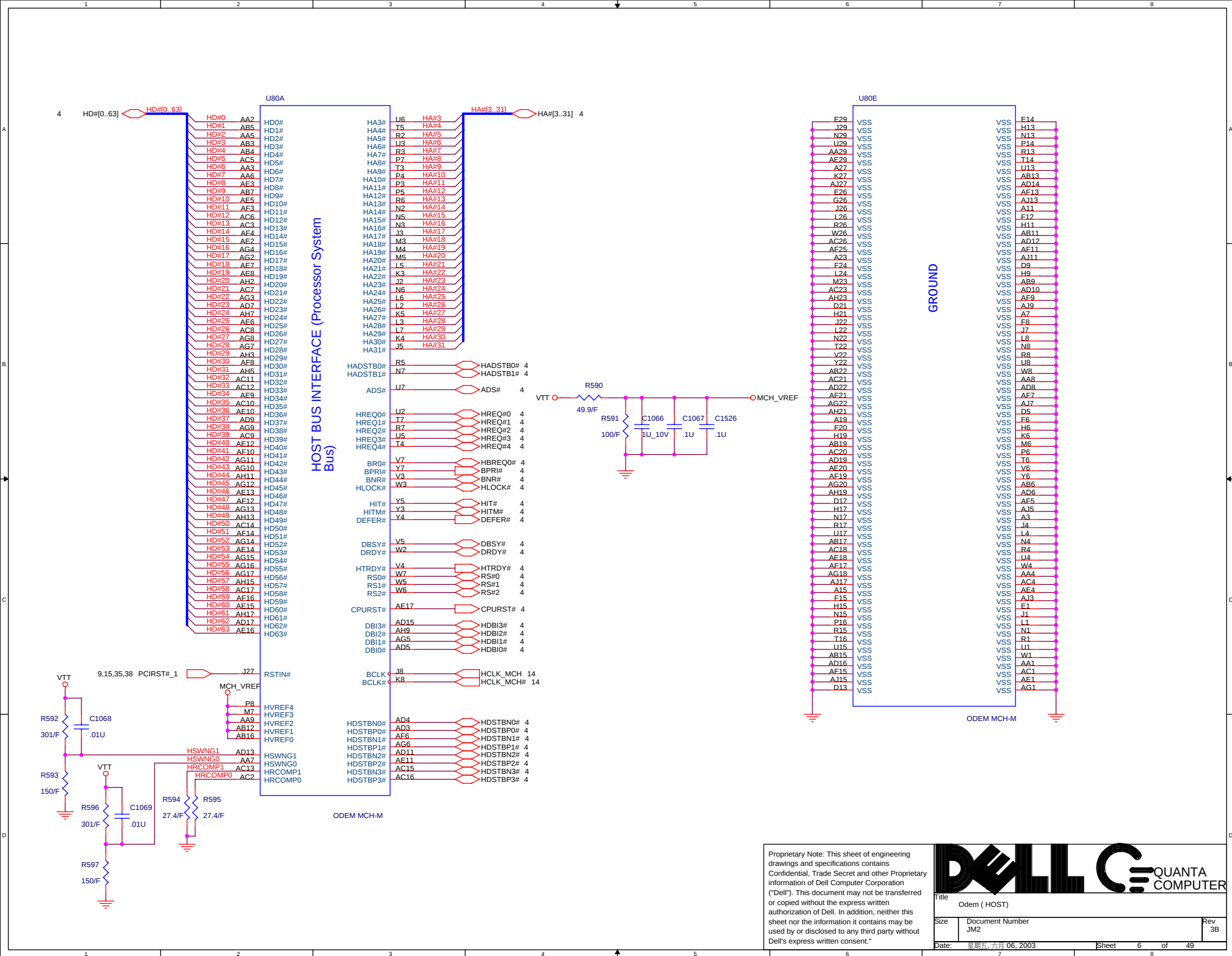
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ESR = 15m ohm/5 // 5m ohm/25 // 5m ohm/15

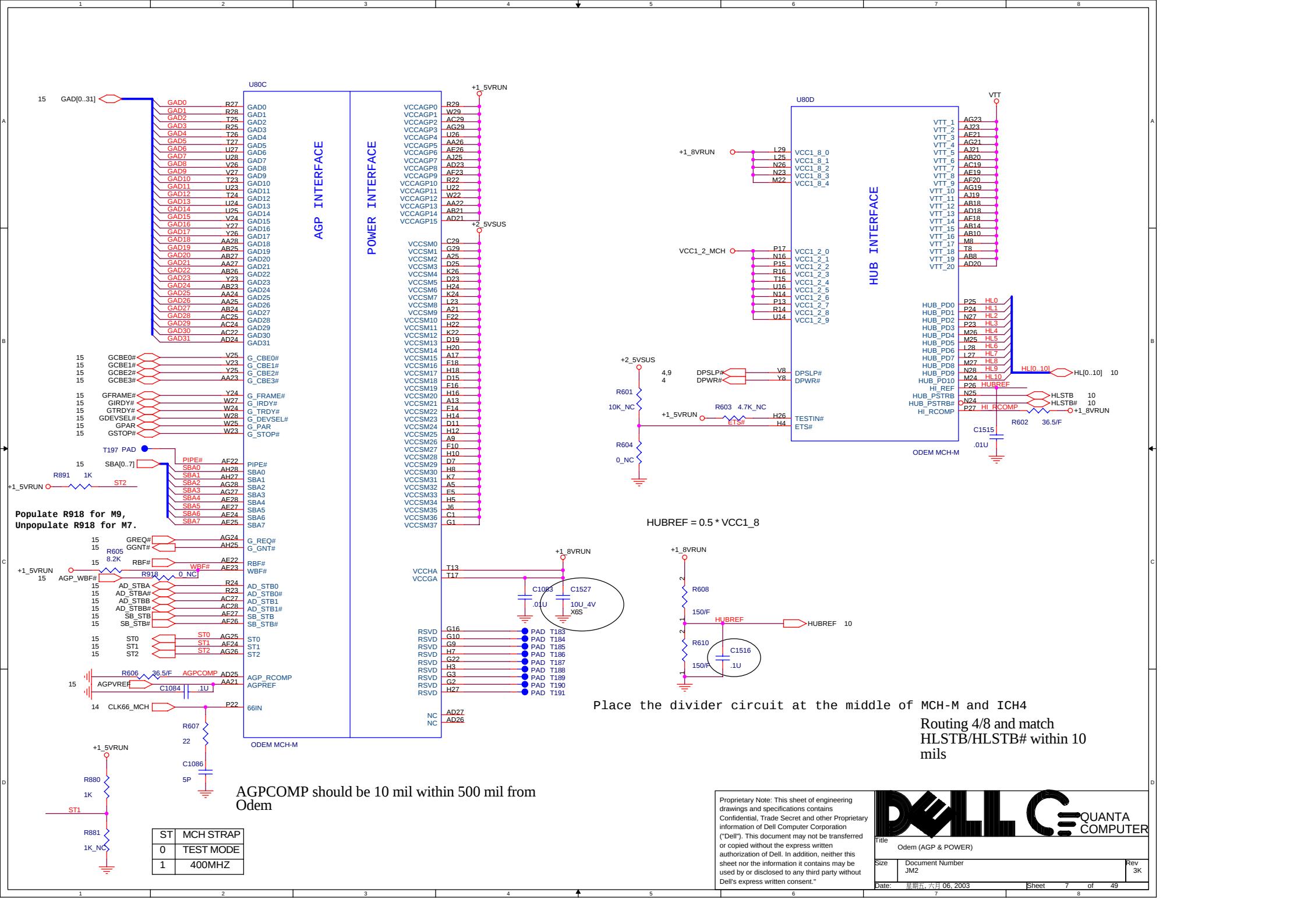
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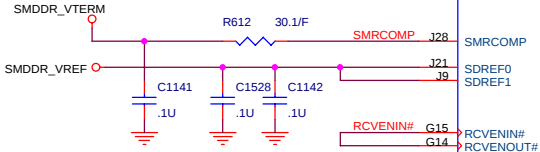
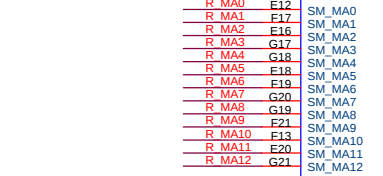
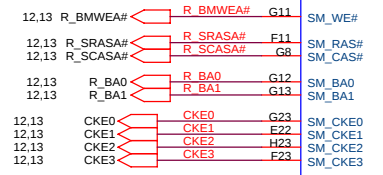
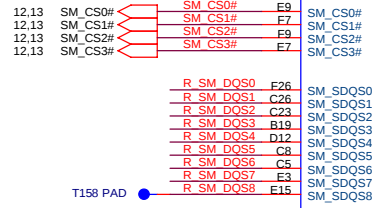
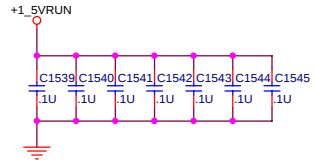
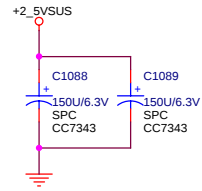
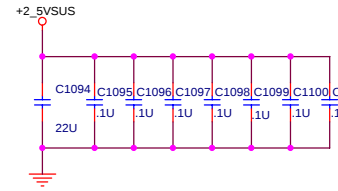
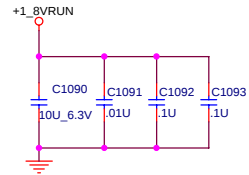
Title Banias Processor (POWER)		
Size	Document Number JM2	Rev 3K
Date	星期五, 六月 06, 2003	Sheet 5 of 49





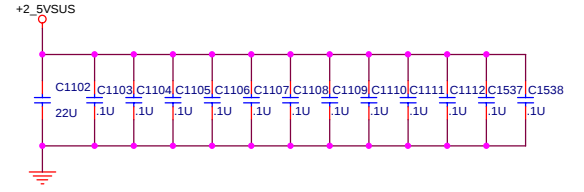
MD[0..63] 12.13
MA[0..12] 12.13

R_MD[0..63] 13
R_MA[0..12] 12.13
R_SM_DQS[0..7] 13

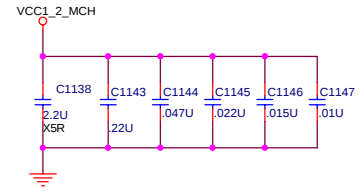
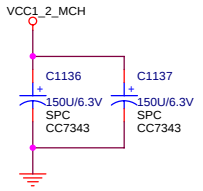
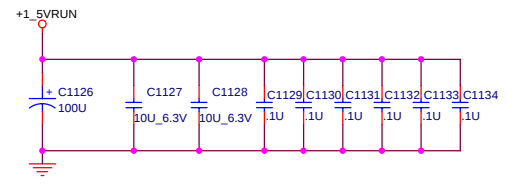
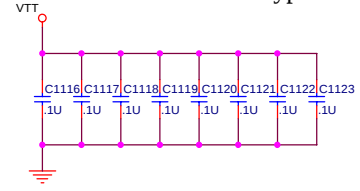


DDR INTERFACE

ODEM MCH-M



Place the bypass caps close to Odem MCH-M

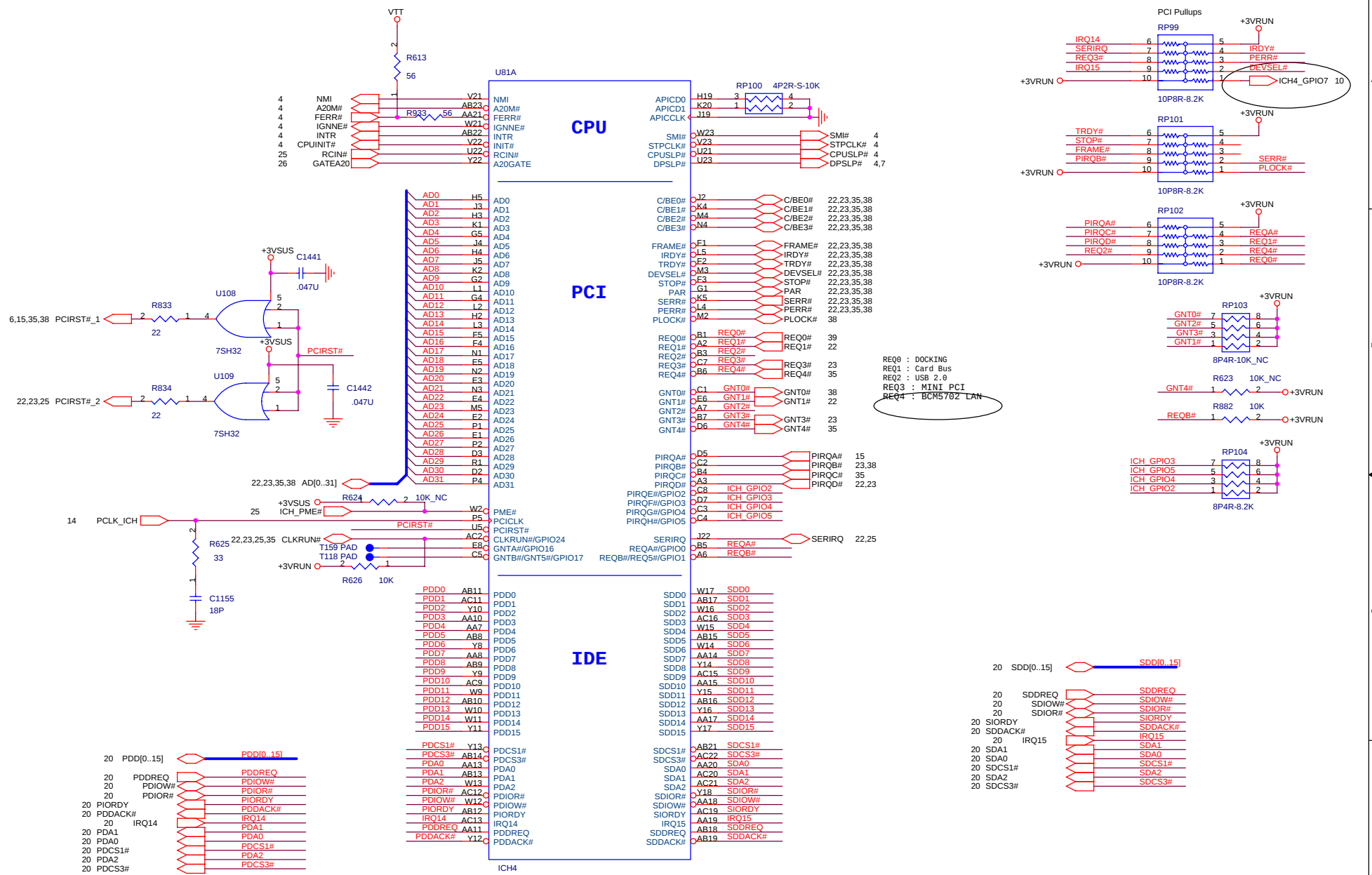


RCVENIN# and RCVENOUT#
ROUTING RULE
L1 - MCH-M Feedback Signal Pin to Signal Via: Max = 40 mils
L2 - MCH-M RCVENOUT# Signal Via to RCVENIN# Signal Via: Must = 100 mils +/- 5mils
L3 - Signal Via to MCH-M Feedback Signal Pin: Max = 40 mils

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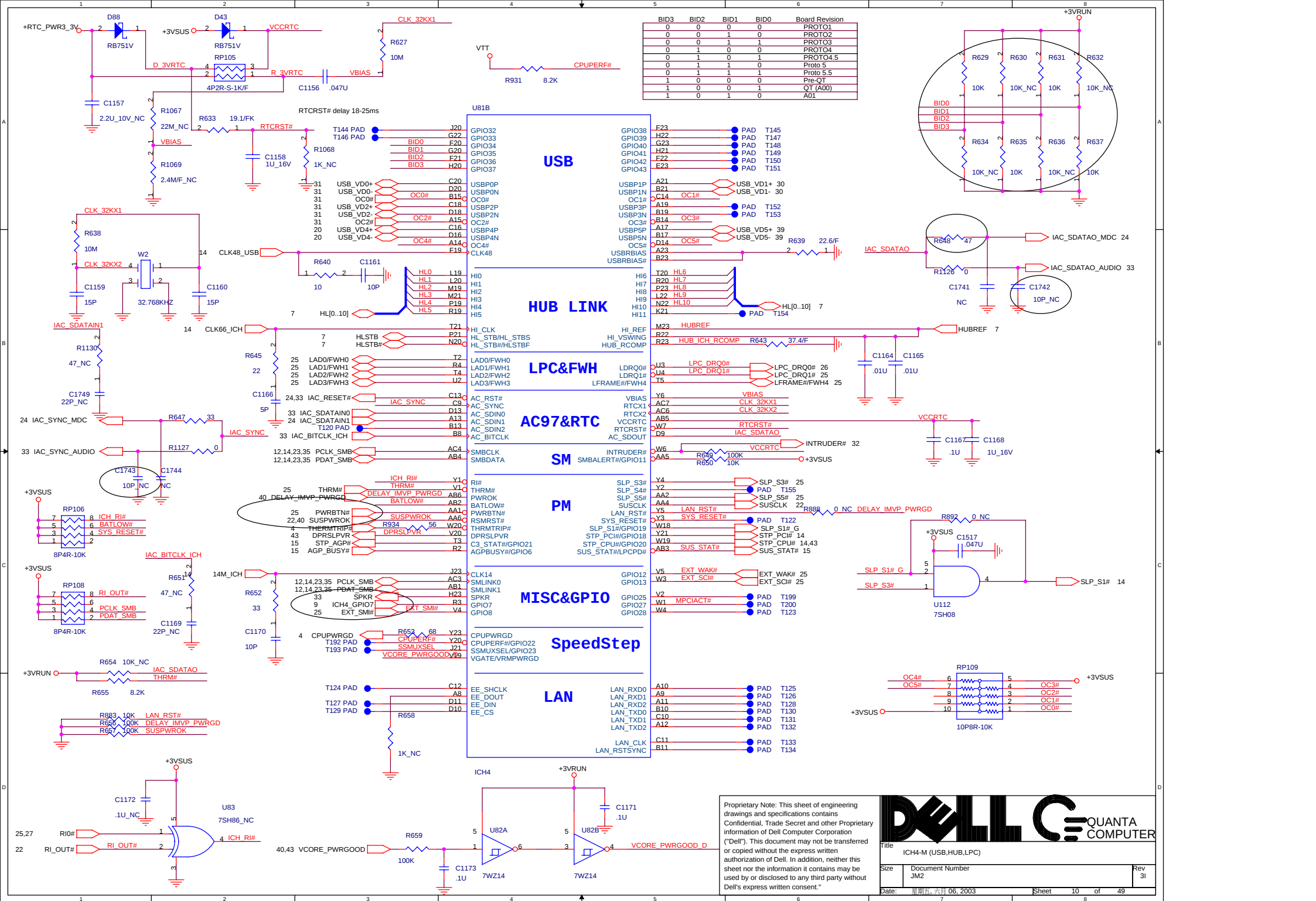


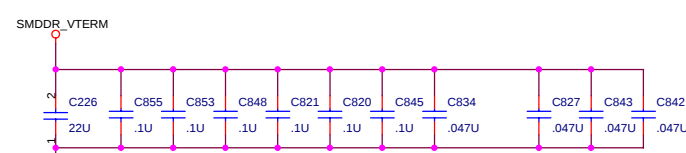
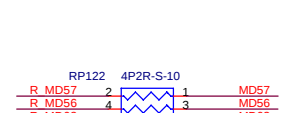
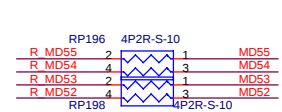
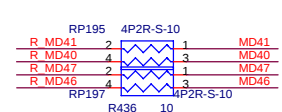
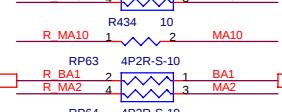
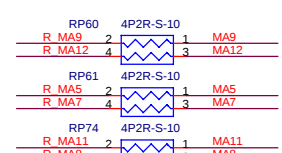
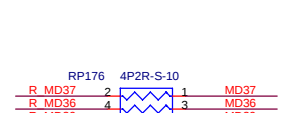
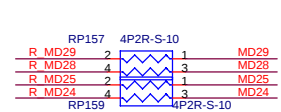
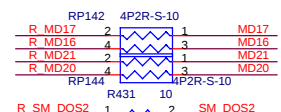
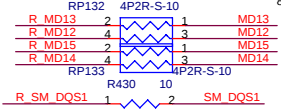
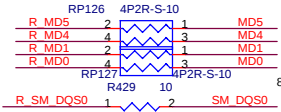
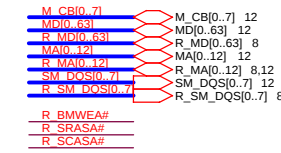
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Size	Document Number JM2		
Date:	星期五, 六月 06, 2003	Sheet	8 of 49



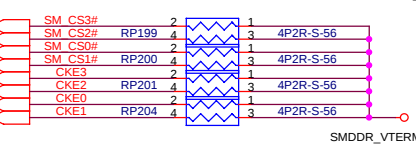
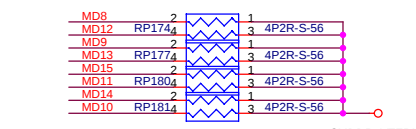
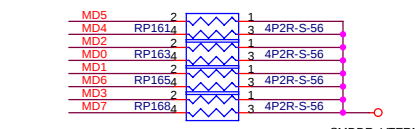
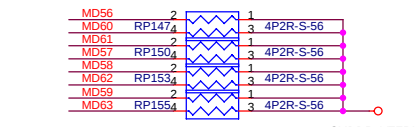
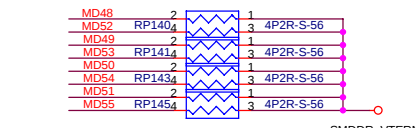
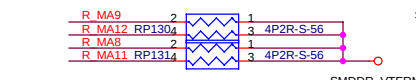
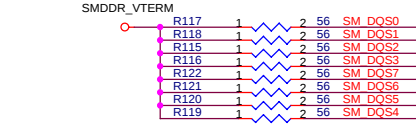
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DELL QUANTA COMPUTER	
Title ICH4-M (CPU,PCI,IDE)	
Size	Document Number JM2
Date: 星期五, 六月 06, 2003	Sheet 9 of 49
Rev 3C	

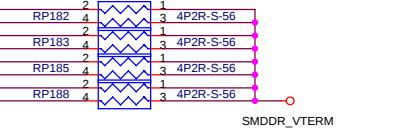
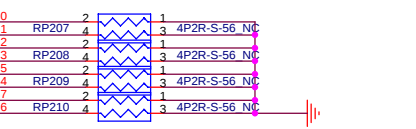
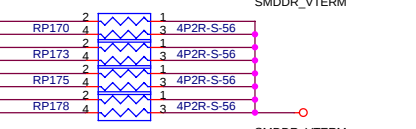
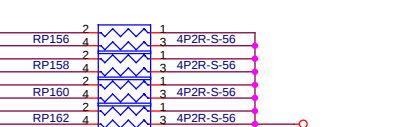
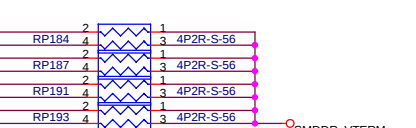
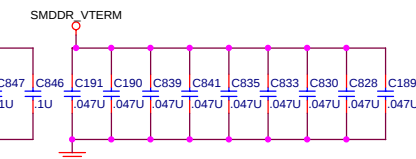
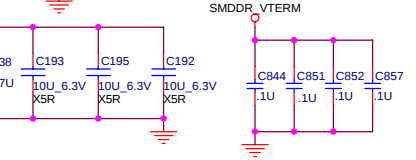
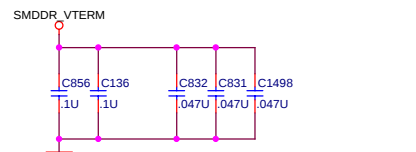




Place Rterms close to JDIMM 1



For terminal R-pack.



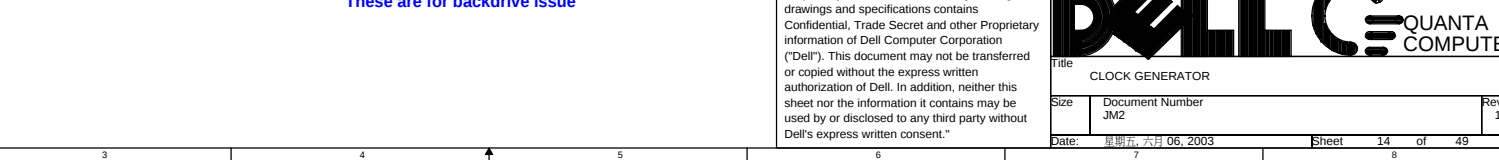
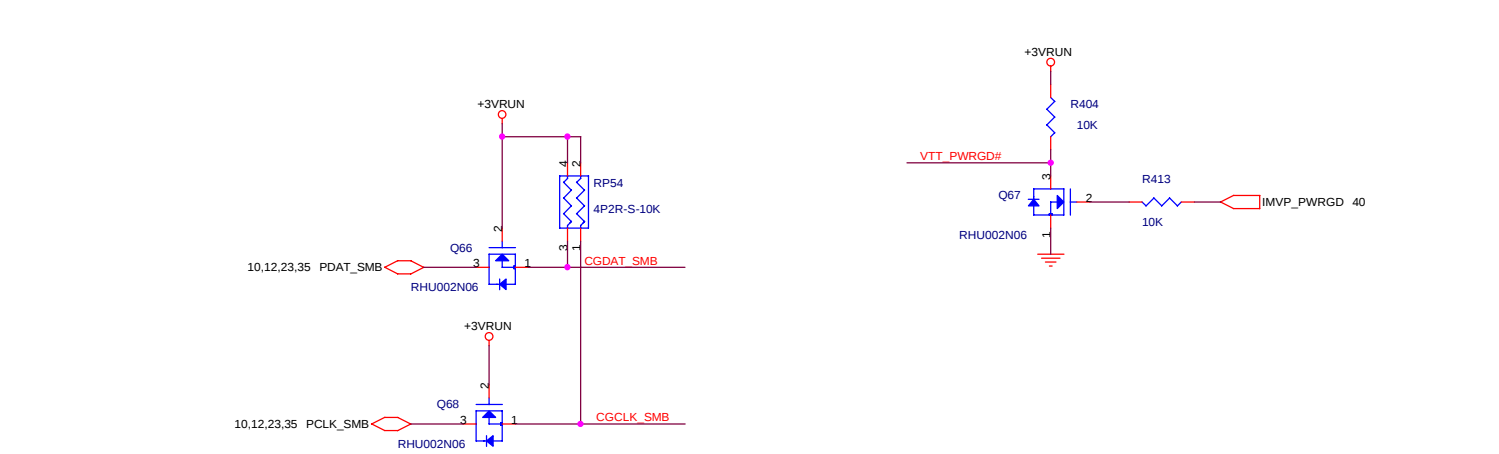
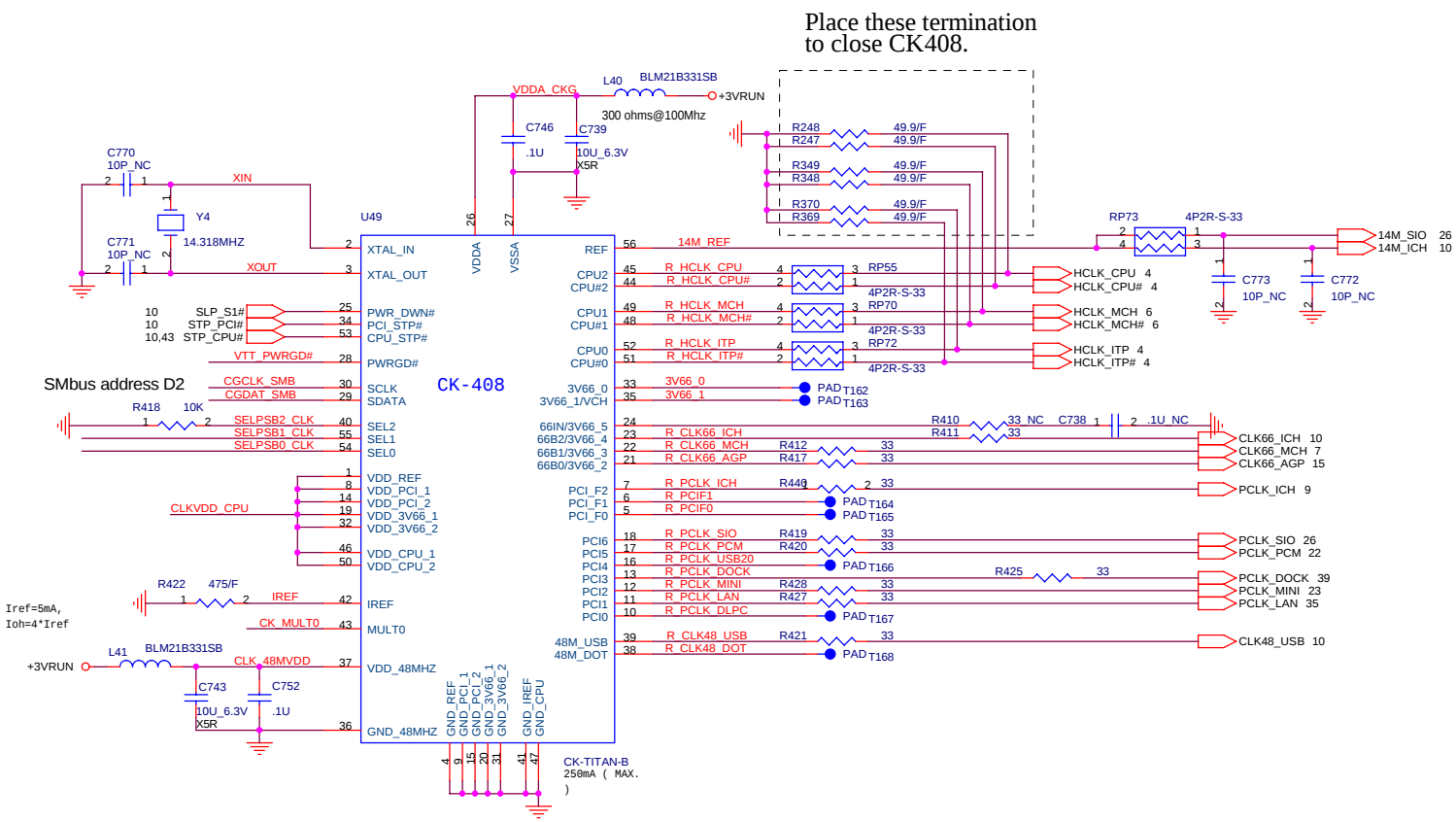
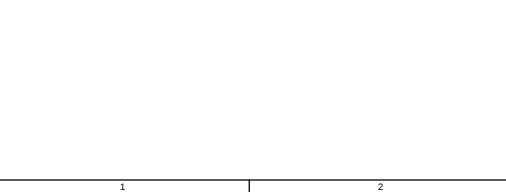
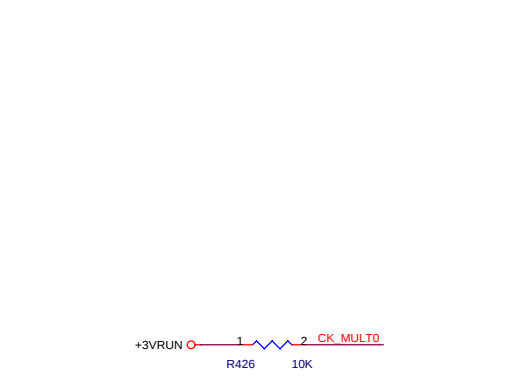
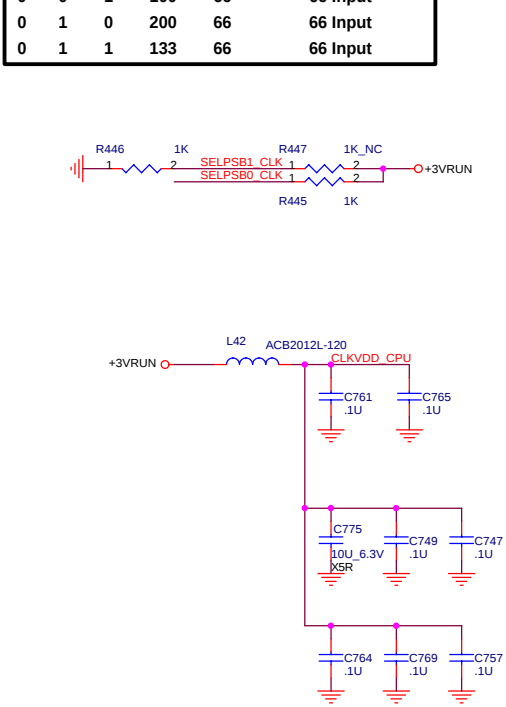
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QUANTA
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File		DDR RES.ARRAY	
Size	Document Number	Rev	
	JM2	1A	
Date:	星期二, 六月 06, 2003	Sheet	13 of 49

S2	S1	S0	CPU	3V66[0..4]	3V66_5/66IN
1	0	0	66	66IN	66 Input
1	0	1	100	66IN	66 Input
1	1	0	200	66IN	66 Input
1	1	1	133	66IN	66 Input
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0	0	1	100	66	66 Input
0	1	0	200	66	66 Input
0	1	1	133	66	66 Input



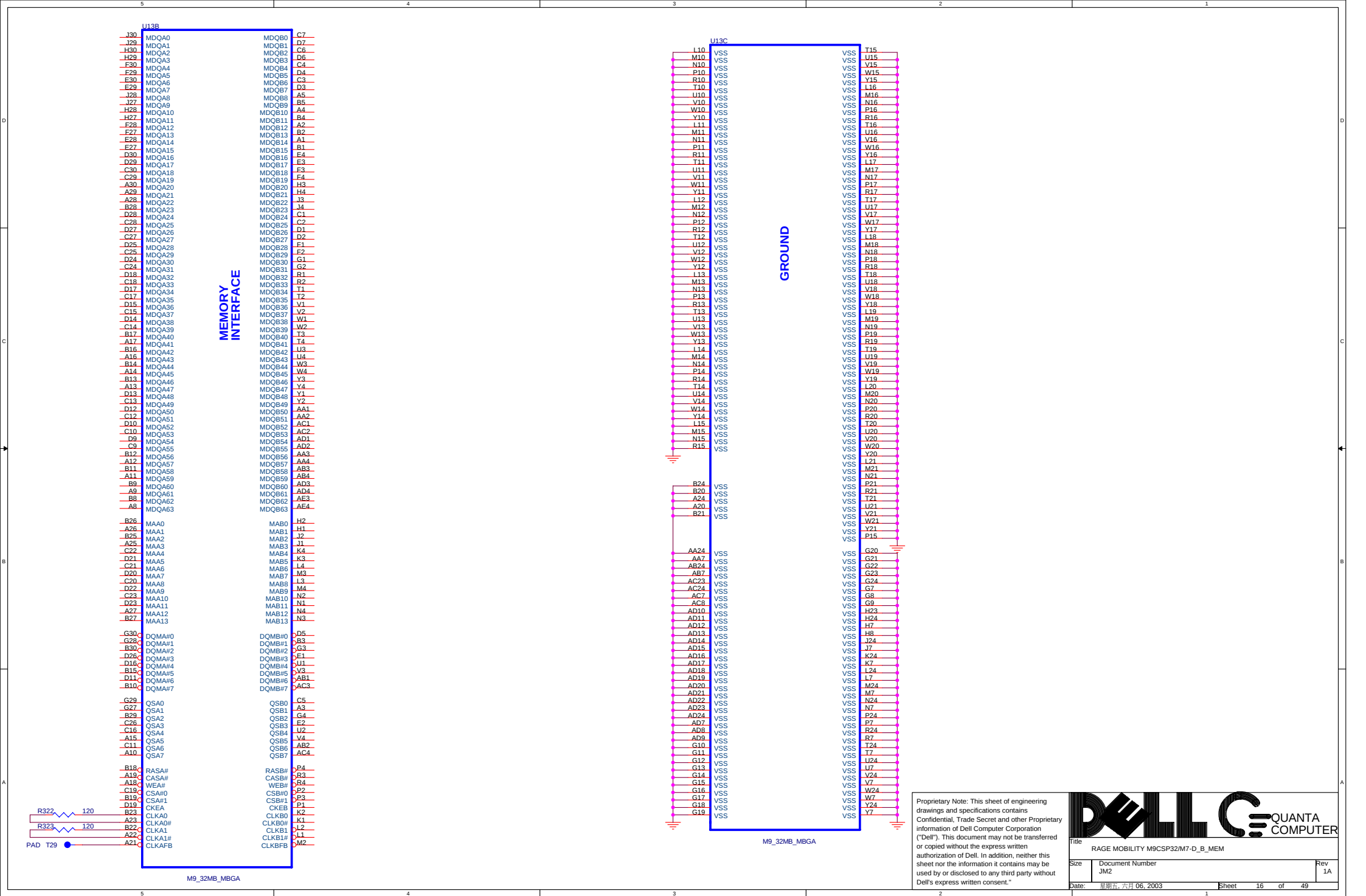
Place these termination to close CK408.

These are for backdrive issue

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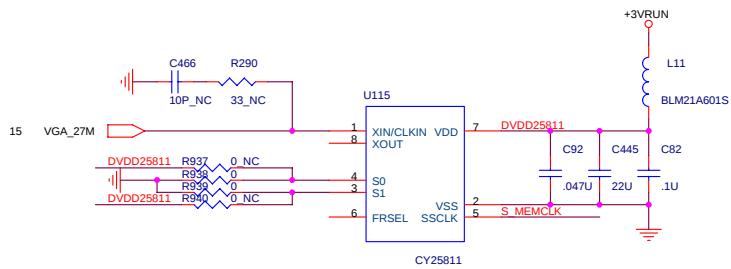
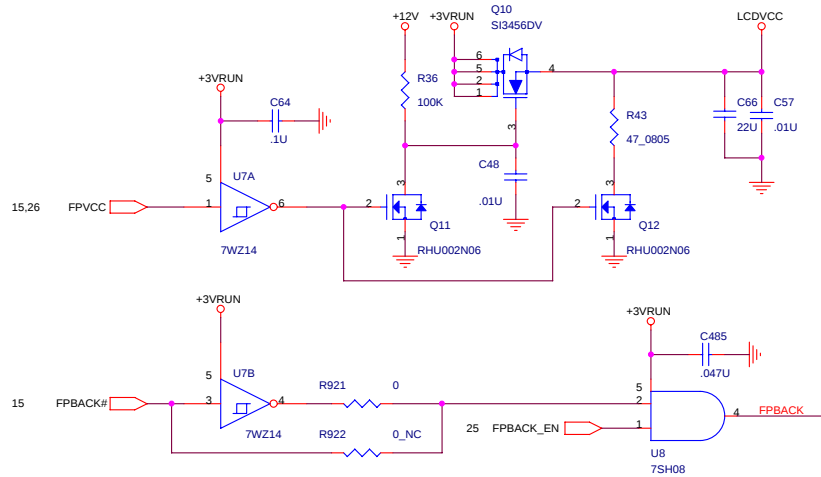
QUANTA COMPUTER

File		CLOCK GENERATOR	
Size	Document Number	Rev	
	JM2	1A	
Date:	星期五, 六月 06, 2003	Sheet	14 of 49

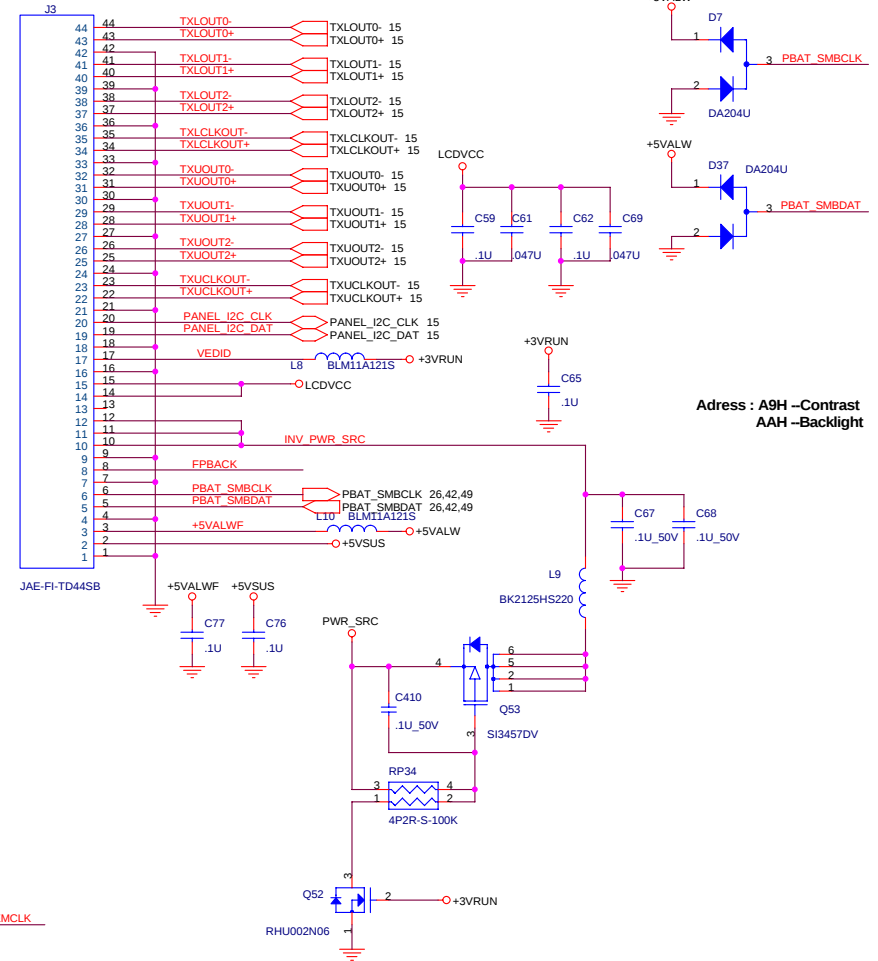


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DELL C QUANTA COMPUTER	
Title RAGE MOBILITY M9CSP32M7-D_B_MEM	
Size	Document Number JM2
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S1	0	0	0	NC	1	1	NC	1	NC
S0	0	NC	1	0	1	0	1	NC	NC
Mode	Center	Center	Center	Center	Down	Down	Down	Down	No spread
Rate	+ -1.2%	+ -0.9%	+ -0.5%	+ -0.4%	-2.5%	-1.8%	-1.5%	-0.6%	0



Address : A9H --Contrast
AAH --Backlight

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DELL C QUANTA COMPUTER

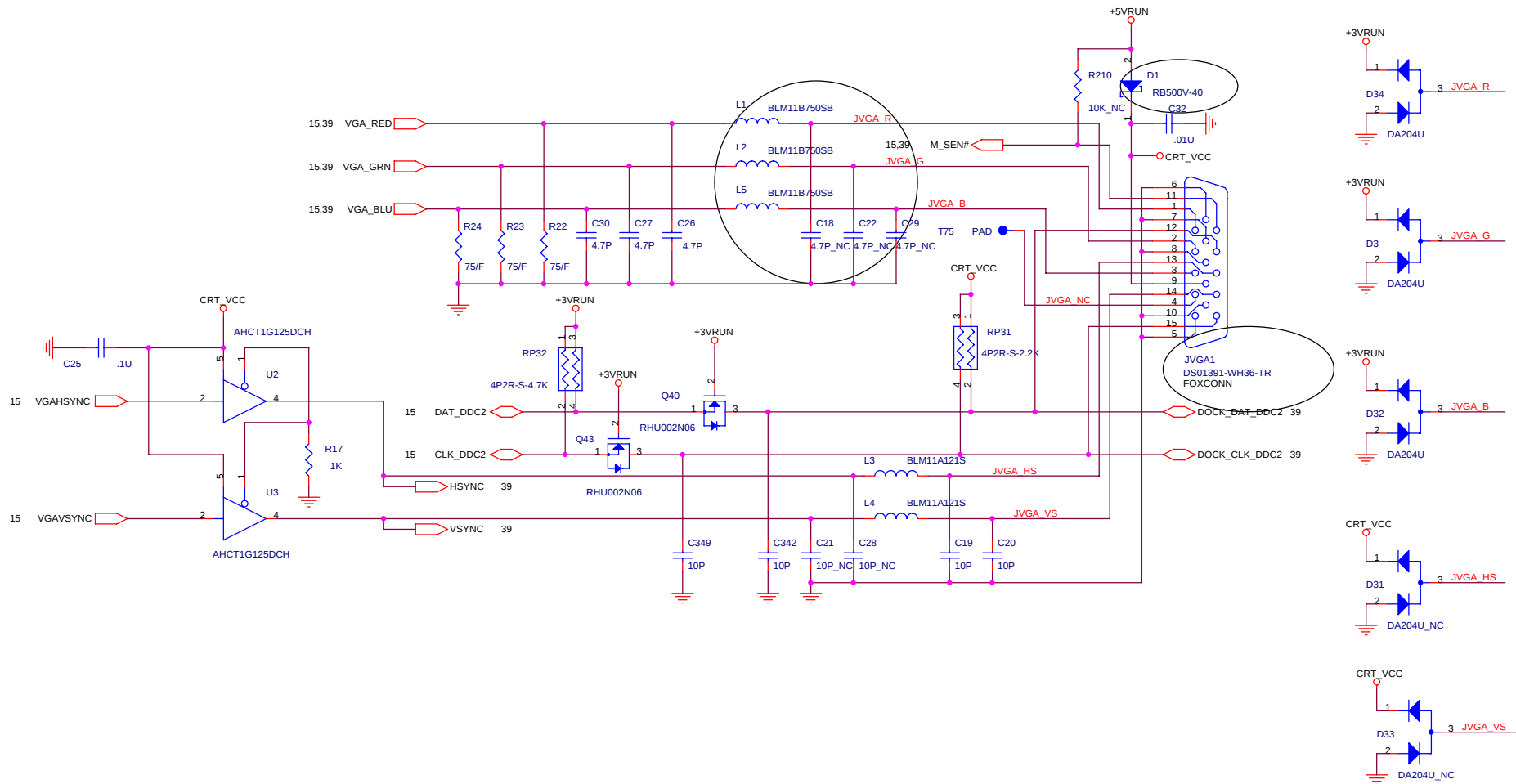
Title: LCD CONN&CY25245

Size: Document Number JM2

Date: 星期五, 六月 06, 2003

Sheet 18 of 49

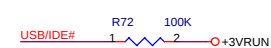
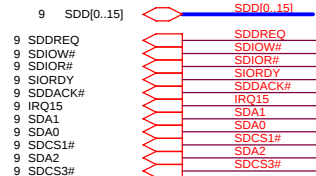
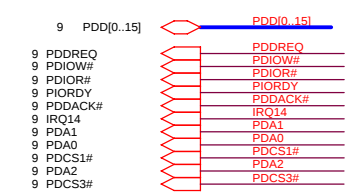
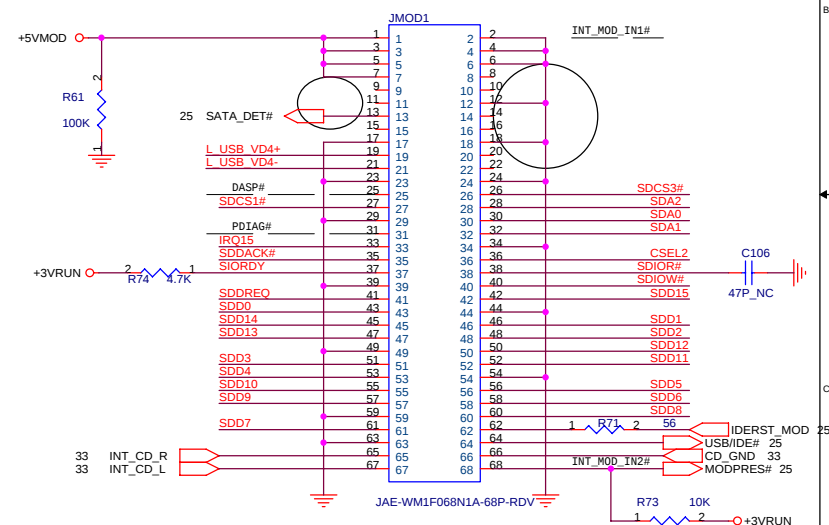
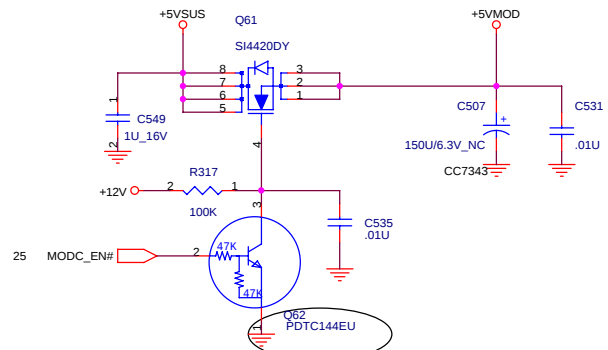
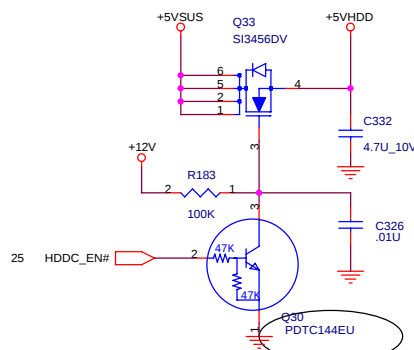
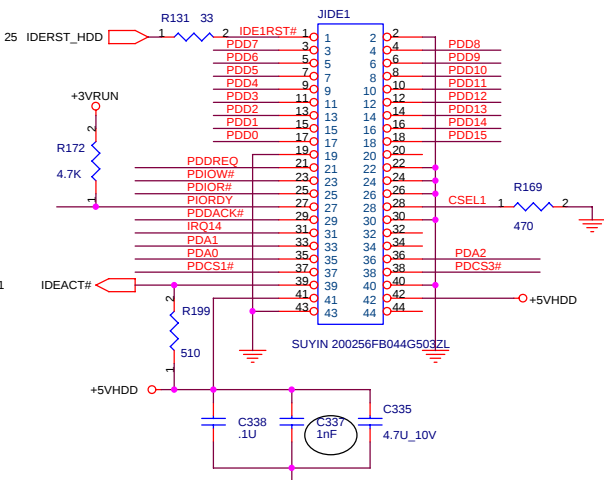
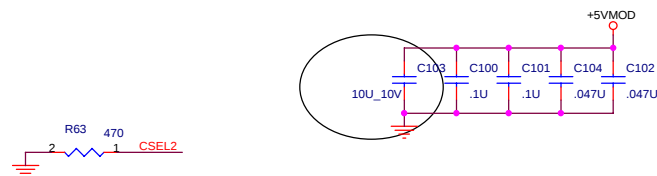
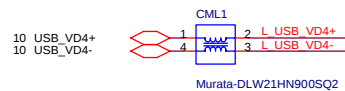
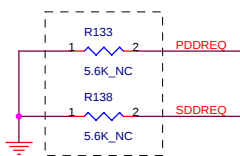
Rev 1A



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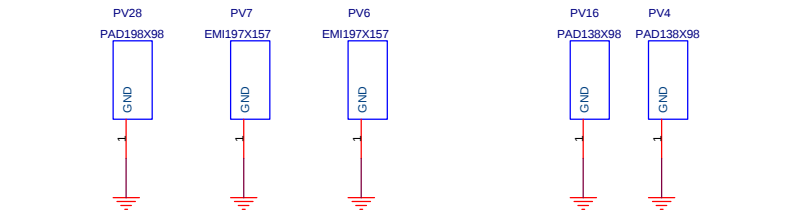
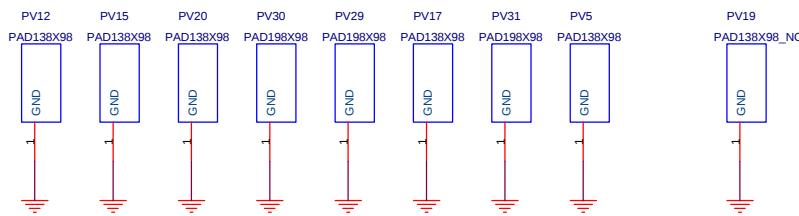
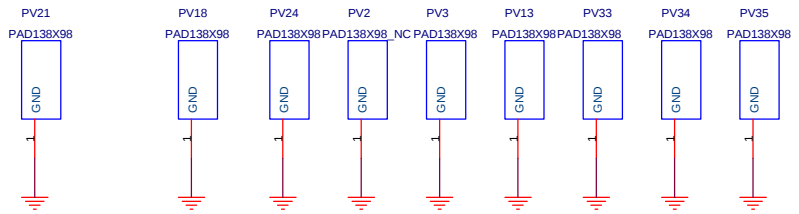
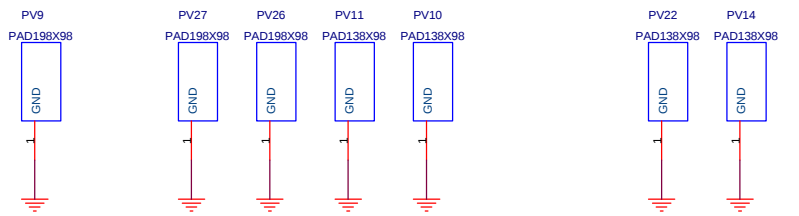
File	CRT CONN		
Size	Document Number JM2	Rev	3F
Date:	星期五, 六月 06, 2003	Sheet	19 of 49



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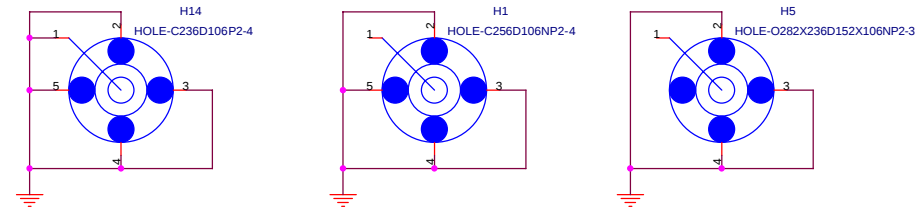
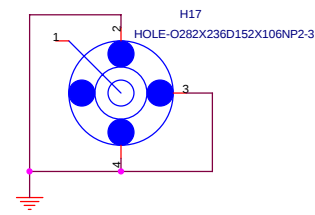
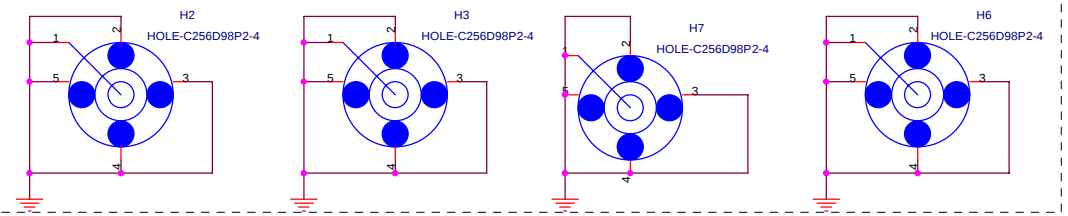


File	IDE (HDD&CD_ROM)
Size	Document Number
	JM2
Date	星期日, 六月 06, 2003
Sheet	20 of 49
Rev	3K

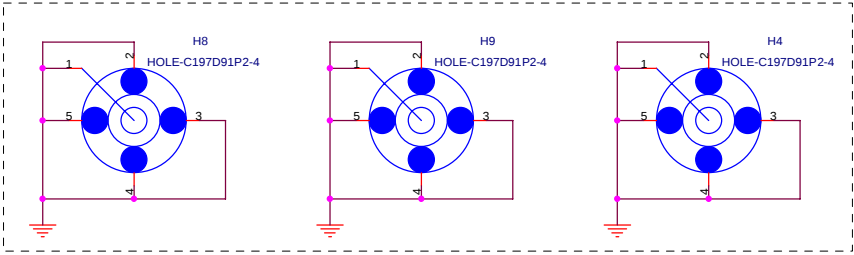


FOR AUDIO FOR DOCKING

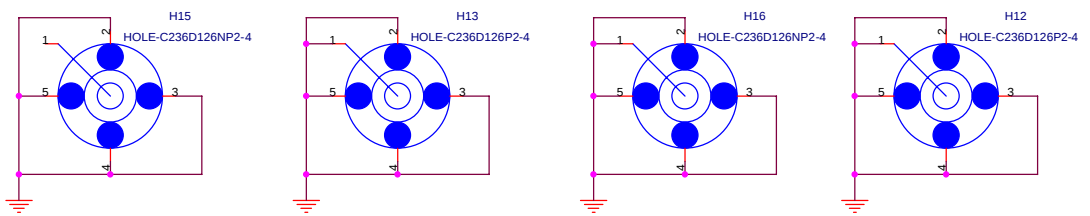
CPU



HEAT SINK



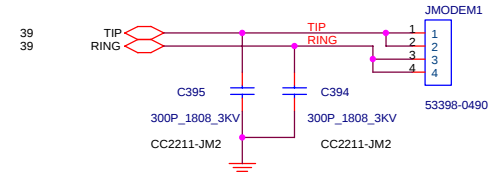
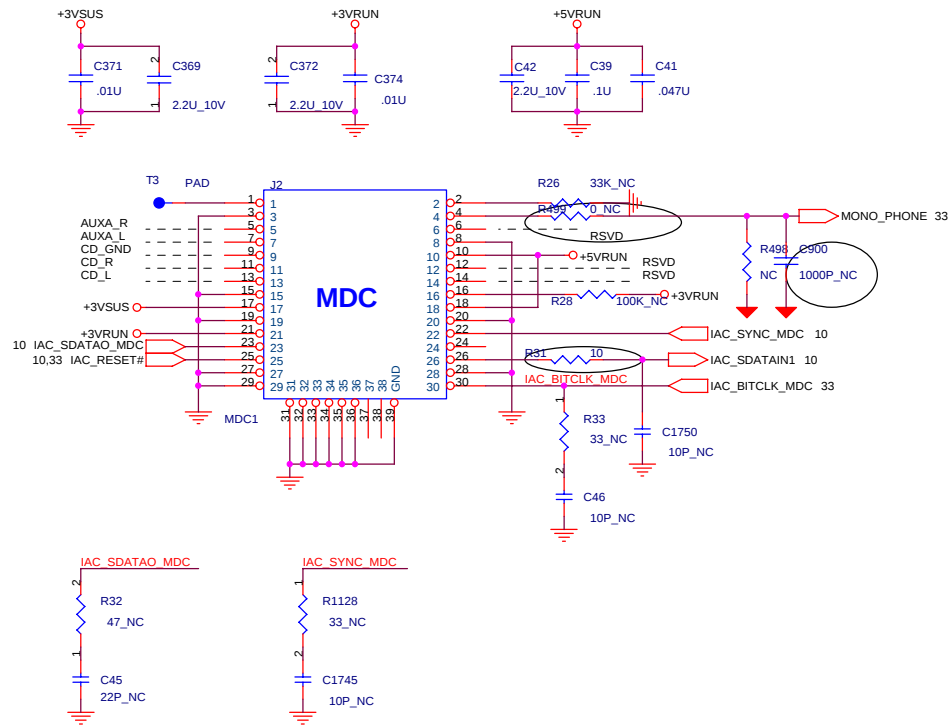
HDD



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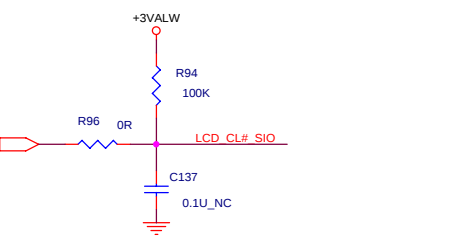
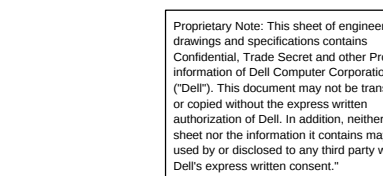
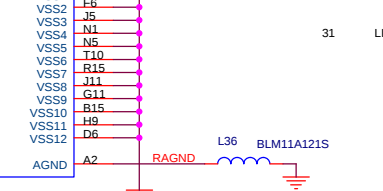
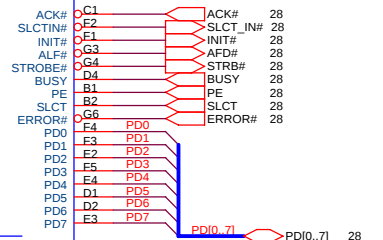
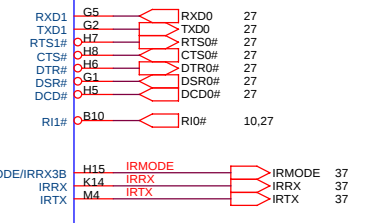
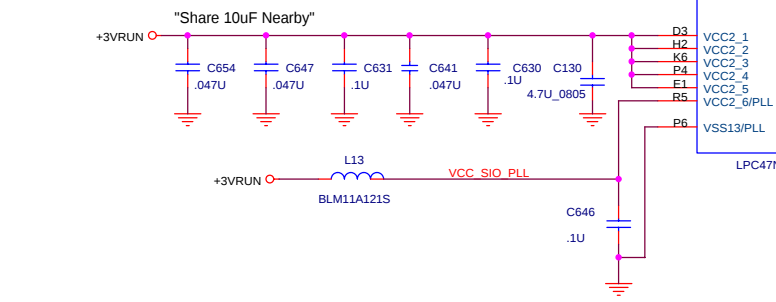
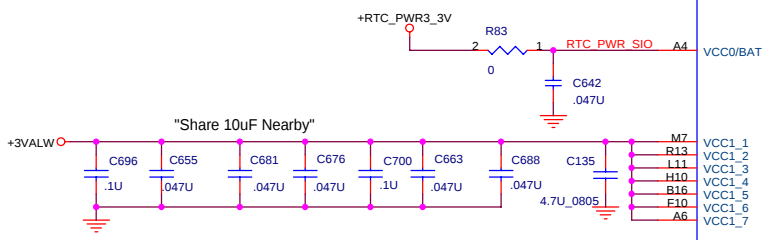
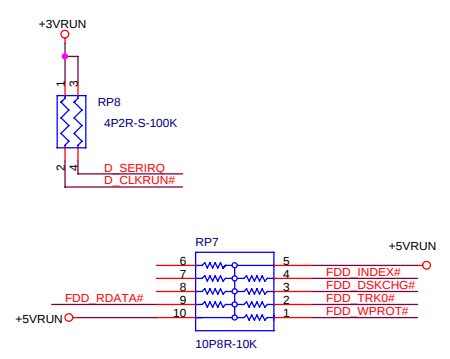
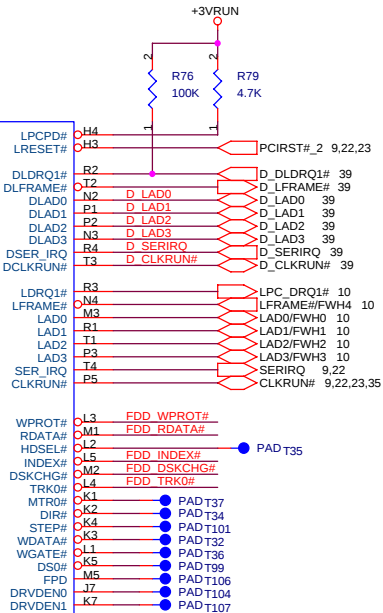
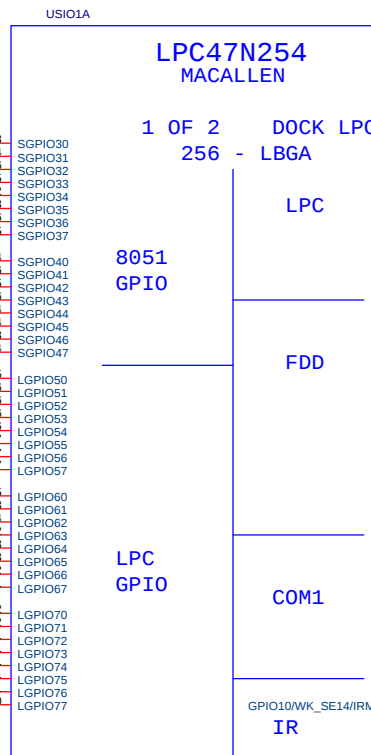
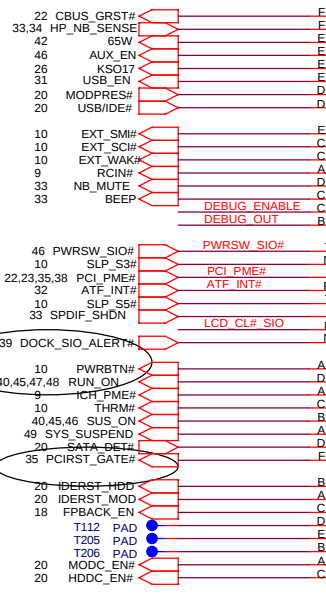
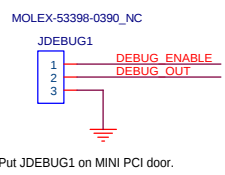
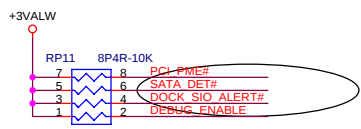
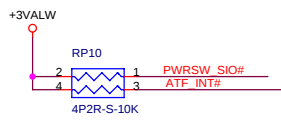
Title		SCREW PAD	
Size	Document Number	Rev	
	JM2	3A	
Date:	星期五, 六月 06, 2003	Sheet	21 of 49



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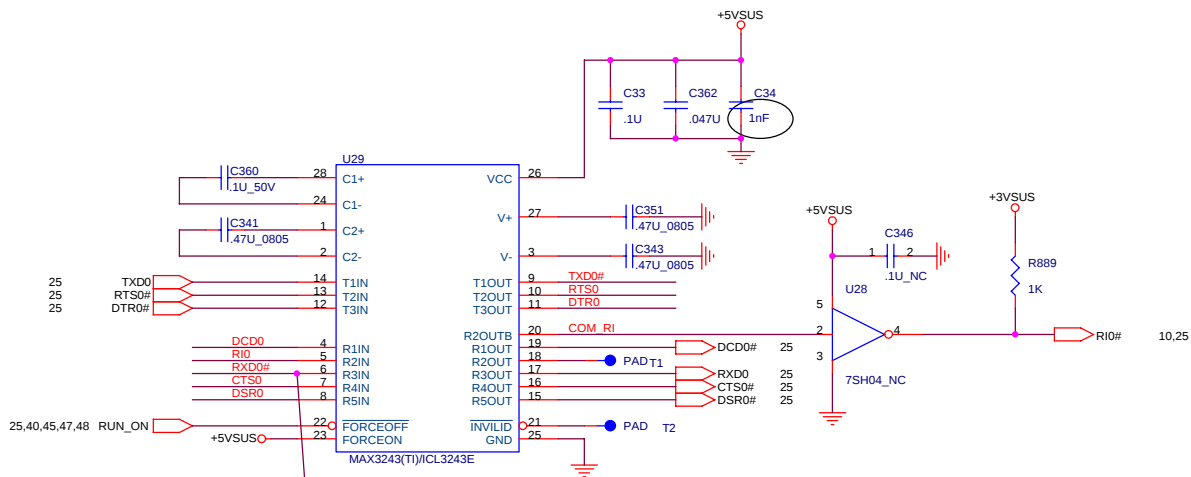
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Size	Document Number	Rev
	JM2	3A
Date:	星期五, 六月 06, 2003	Sheet 24 of 49



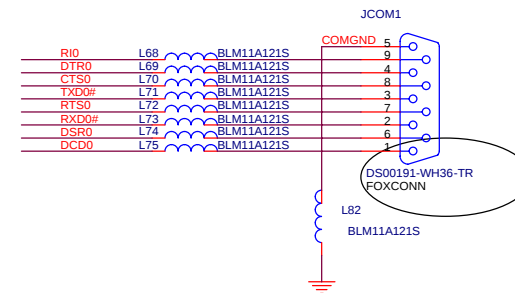
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Ultra I/O Controller LPC47N254		
Size	Document Number JM2	Rev 3D
Date:	星期五, 六月 06, 2003	Sheet 25 of 49



If MAX3243 pin 22 tied to RUN_ON, then it can not support Ring Out

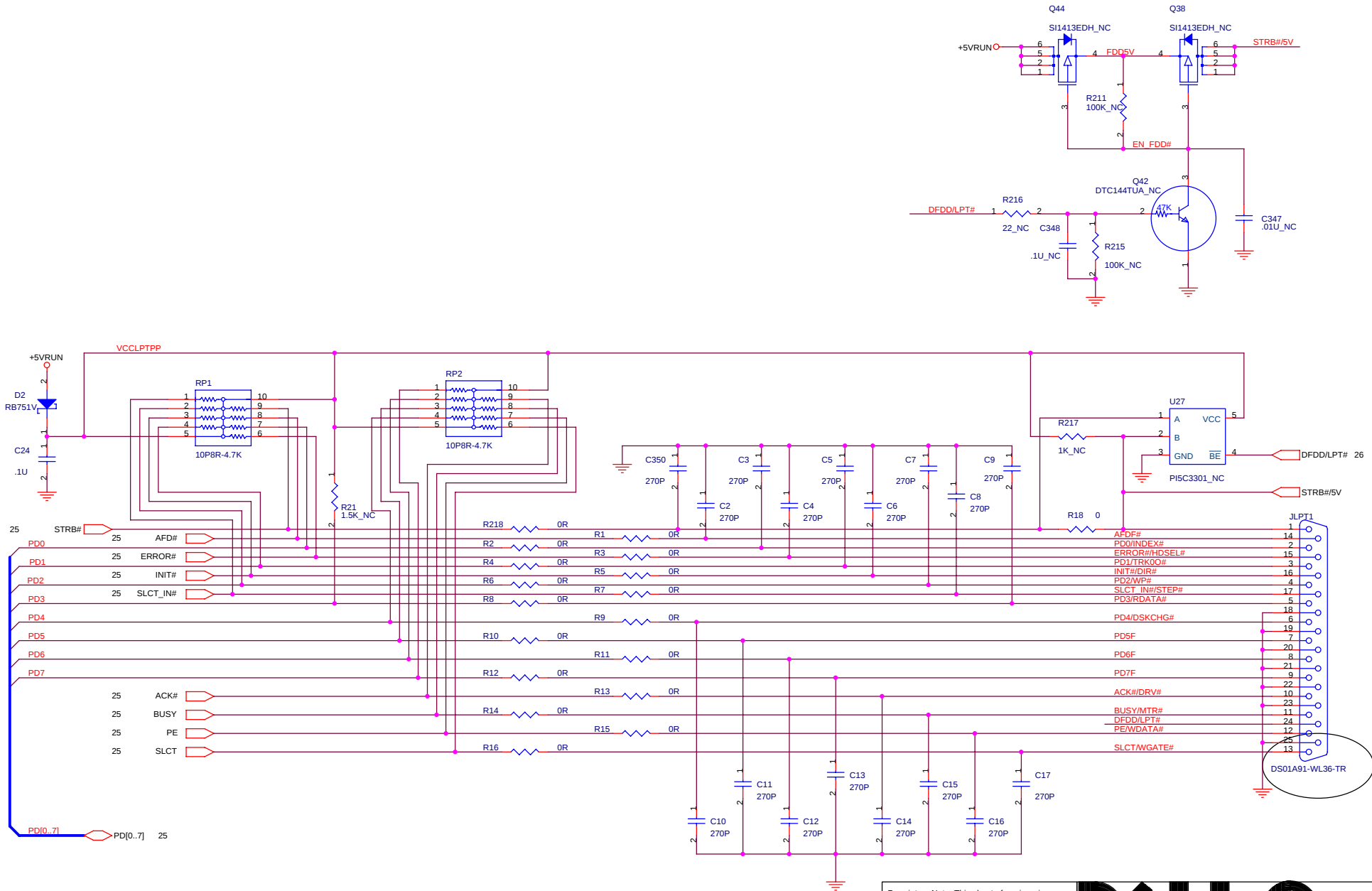


Place these beads close to JCOM1 as soon as possible

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File	SERIAL PORT	Rev	3K
Size	Document Number JM2		
Date	星期五, 六月 06, 2003	Sheet	27 of 49

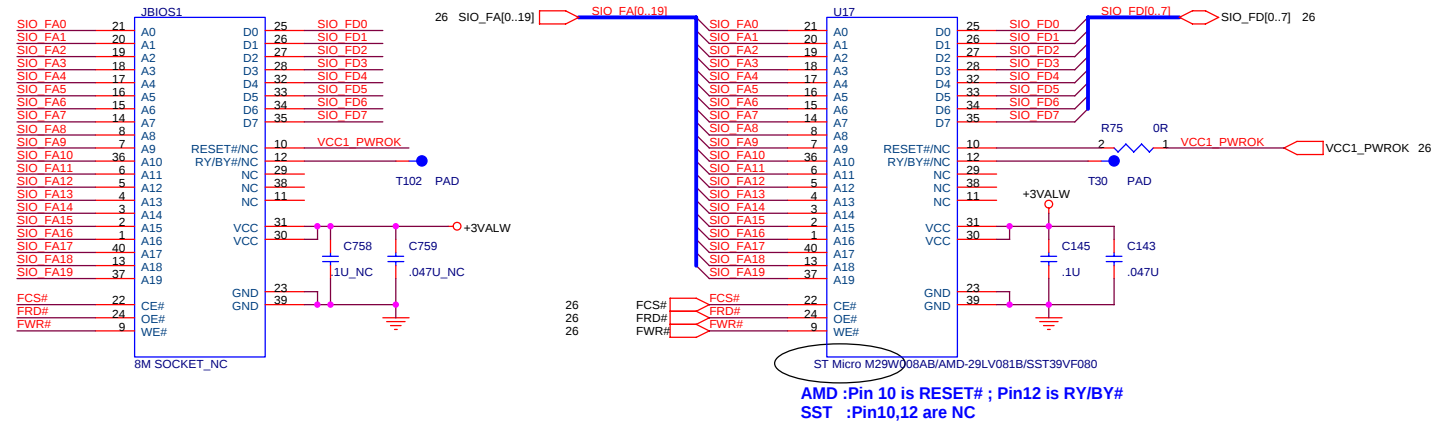


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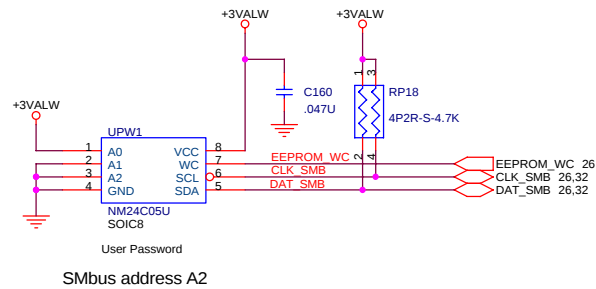


Title		PARALLEL CONN.
Size	Document Number	Rev
	JM2	3F
Date:	2003.06.06	Sheet 28 of 49

8Mbit (1M Byte),NO PLCC TYPE



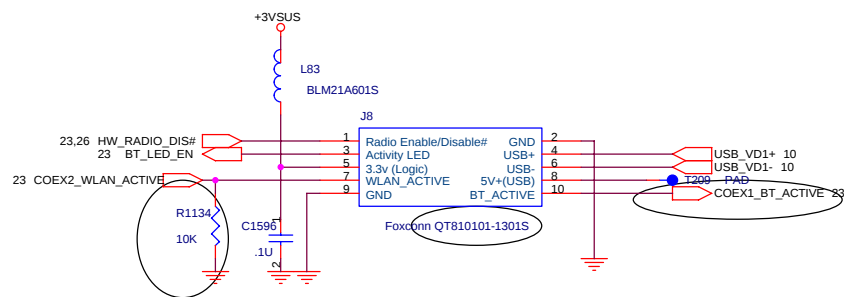
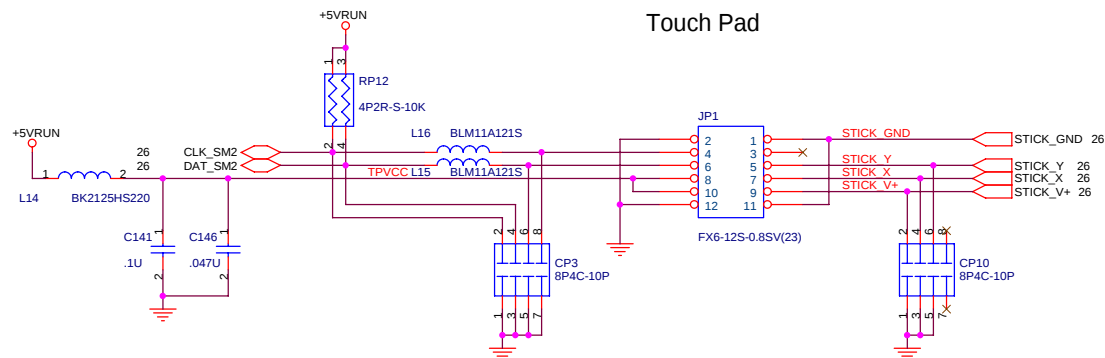
- 1.AMD-29LV081B require MAX 500nS Tready for it's hardware reset.And MAX6326_UR29 has >100mS reset timing.So we can tie it's reset# pin to +3VALW directly.
- 2.SIO has internal 20 mS delay of VCC1_PWROK



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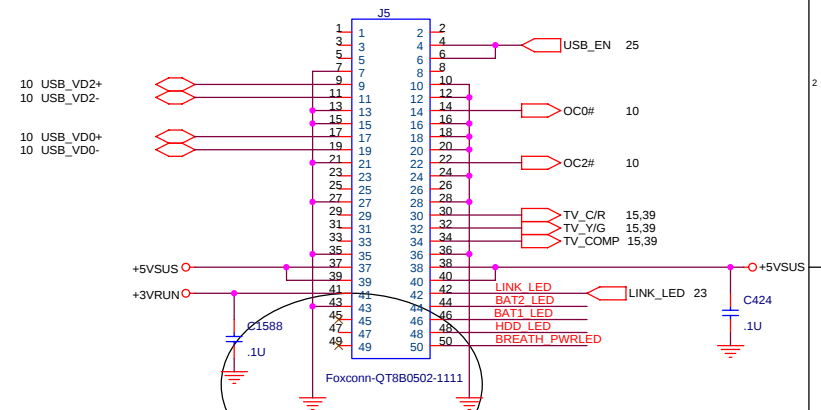
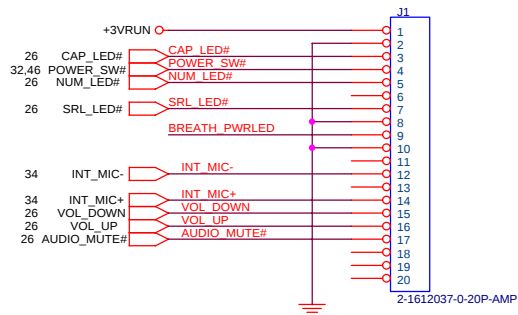
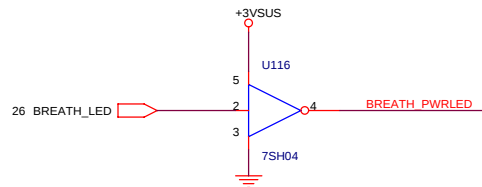
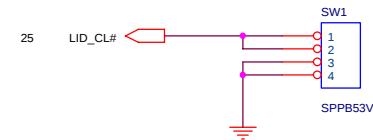
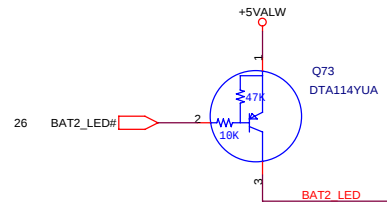
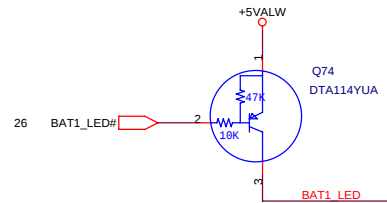
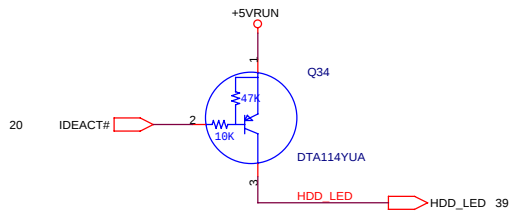


File	FLASH	Rev	3A
Size	Document Number JM2		
Date:	星期五, 六月 06, 2003	Sheet	29 of 49



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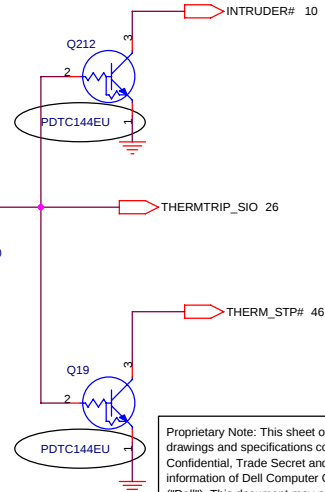
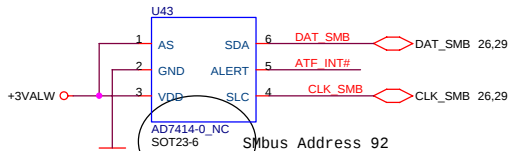
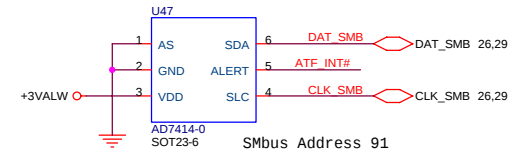
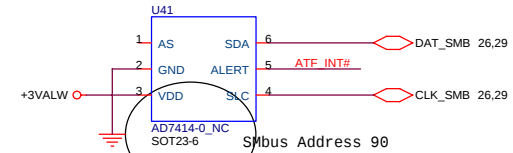
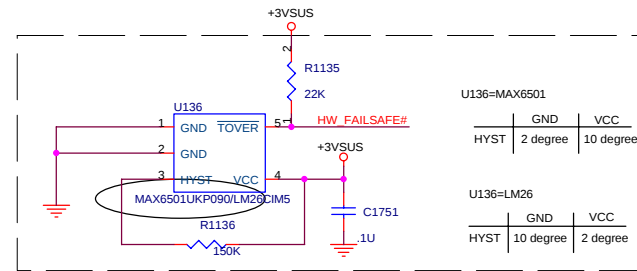
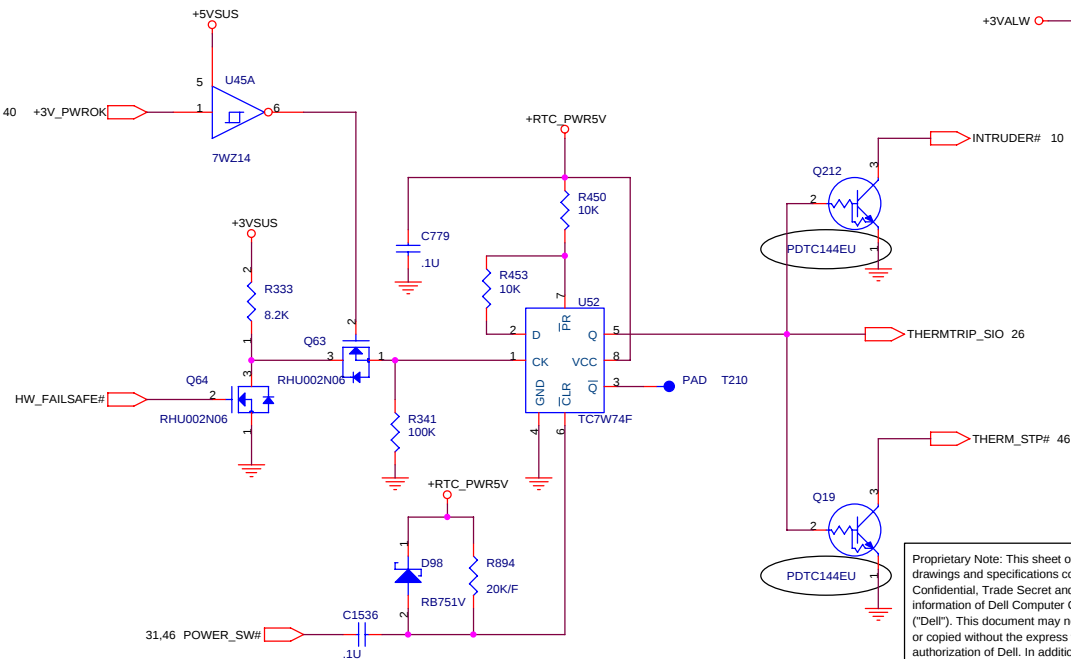
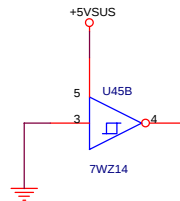
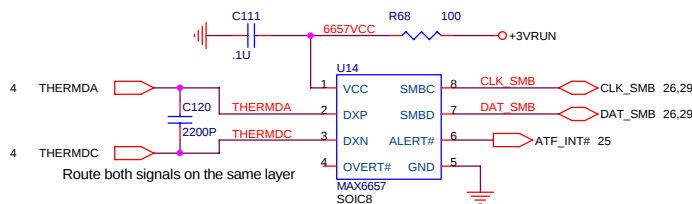
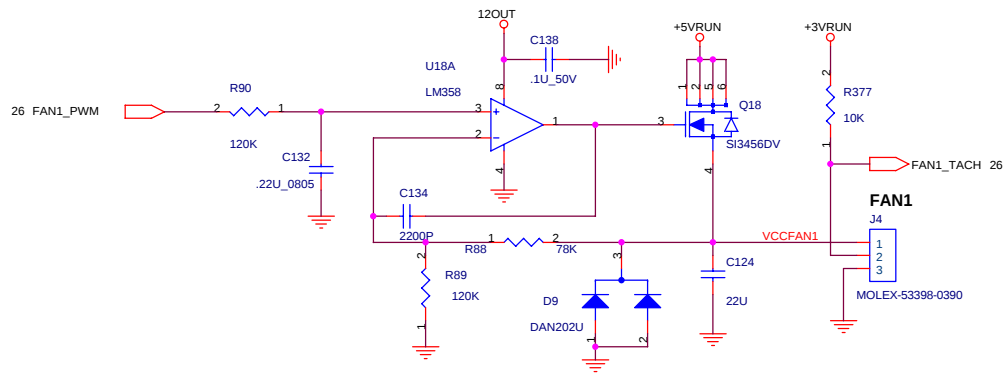
		Title TOUCH PAD & BULE TOOTH	
		Size Document Number JM2	Rev 3A
Date: 星期六, 六月 06, 2003		Sheet 30	of 49



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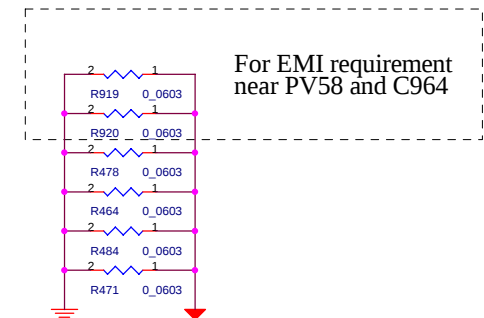
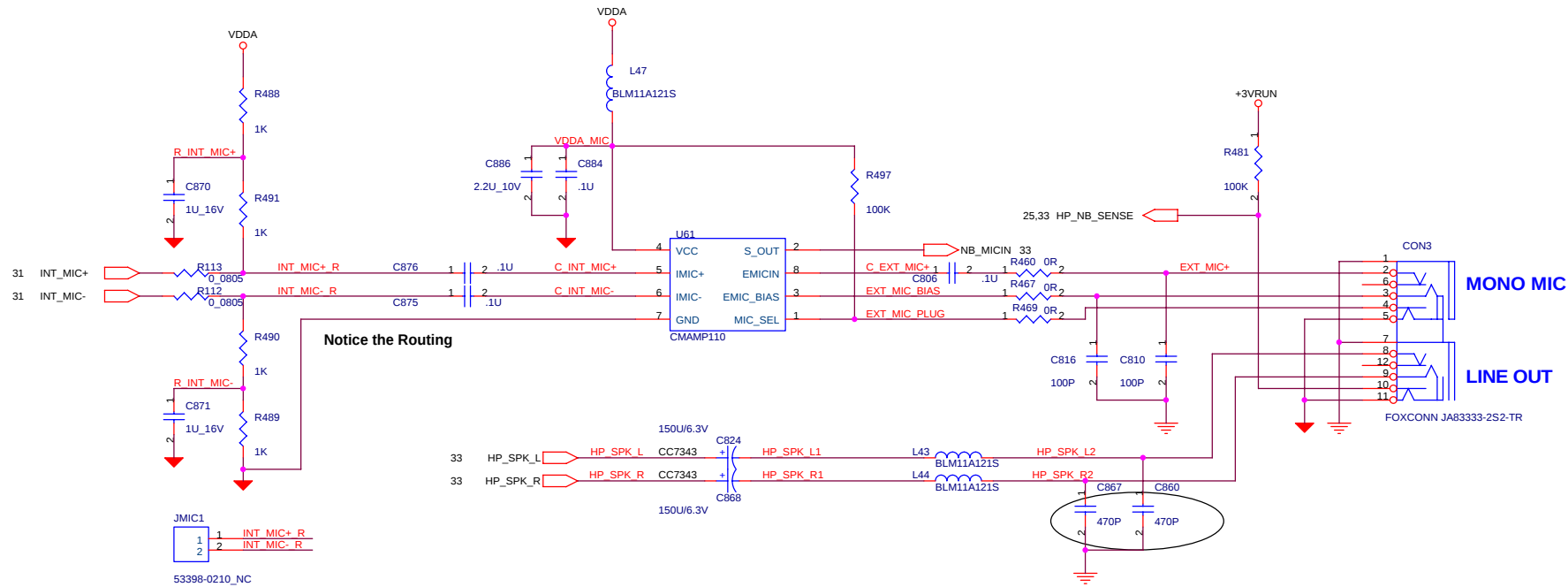
File		SWITCH & LED	
Size	Document Number	Rev	
	JM2	3G	
Date:	星期五, 六月 06, 2003	Sheet	31 of 49



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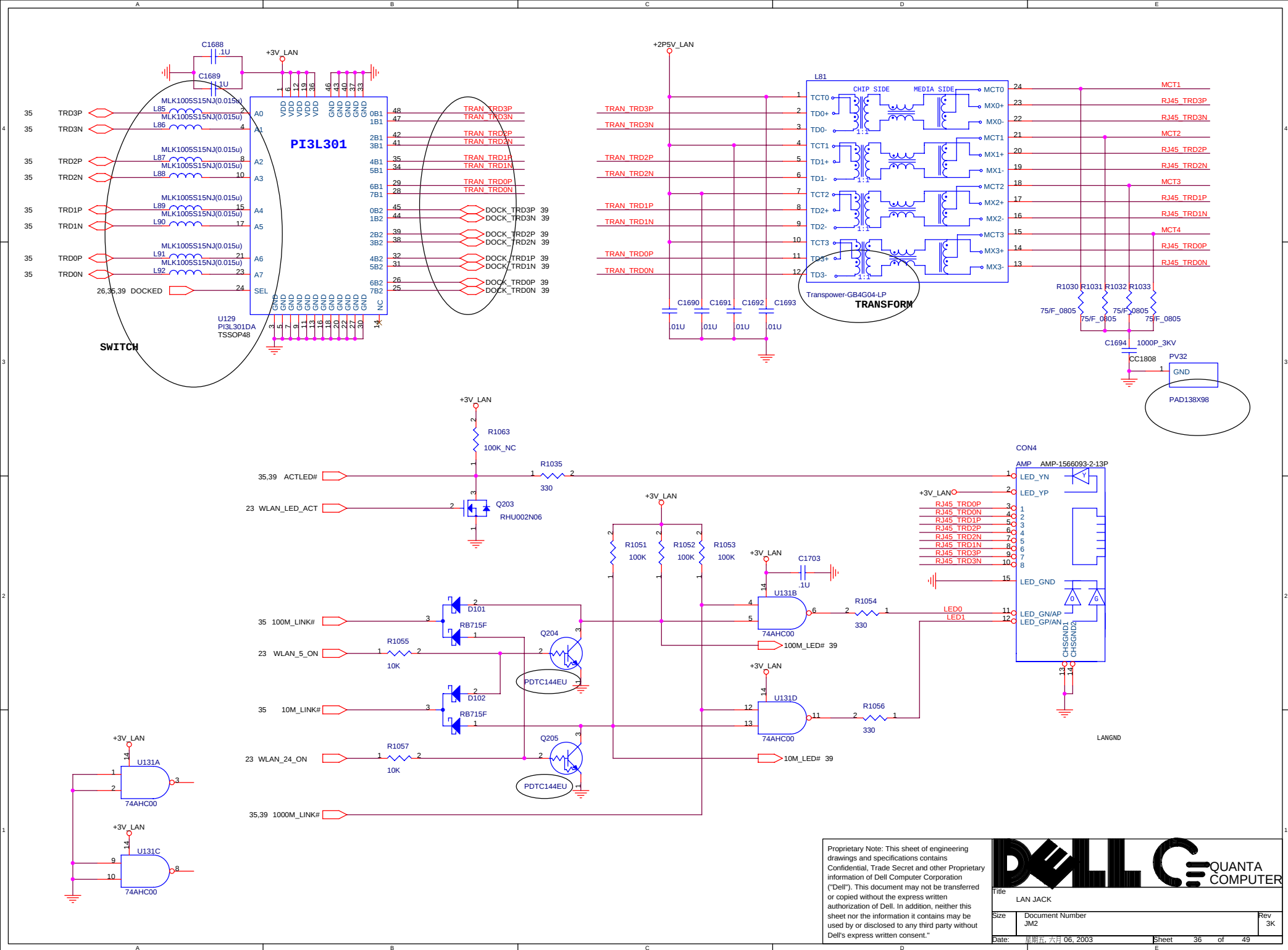
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Size	Document Number	Rev
	JM2	3K
Date:	星期五, 六月 06, 2003	Sheet 32 of 49

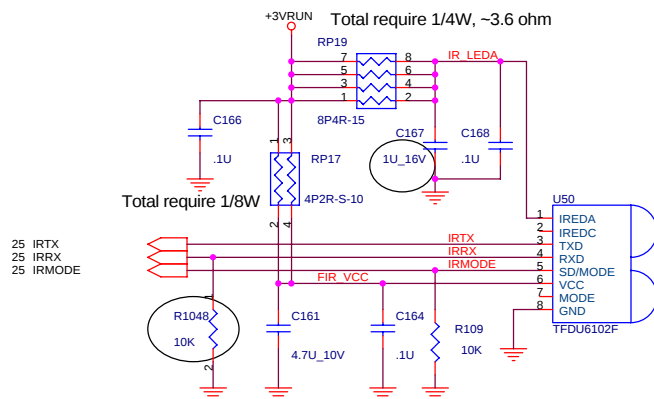


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DELL **QUANTA** **COMPUTER**

Title AUDIO CONN		Rev 3F
Size JM2	Document Number JM2	
Date 星期日, 六月 06, 2003	Sheet 34 of 49	

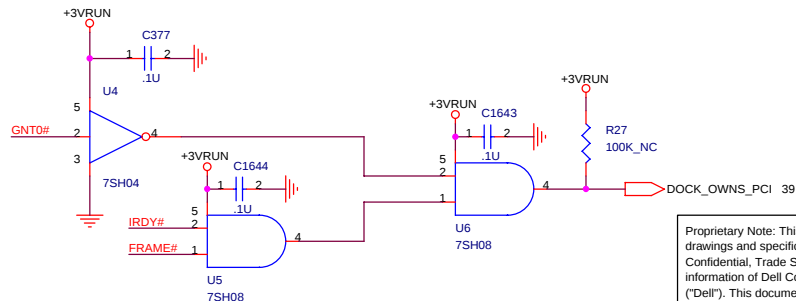
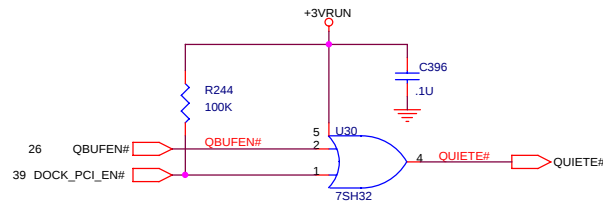
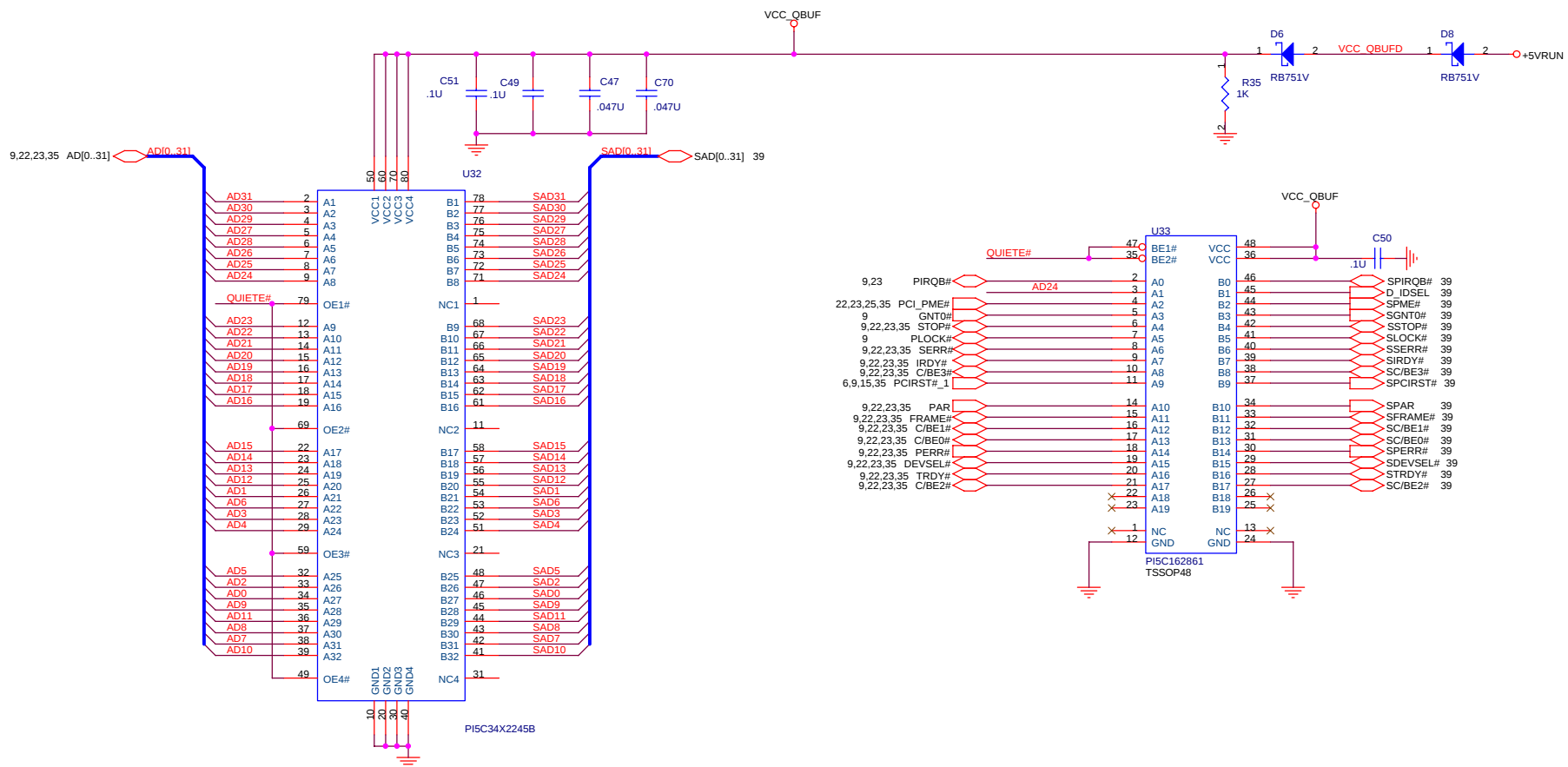




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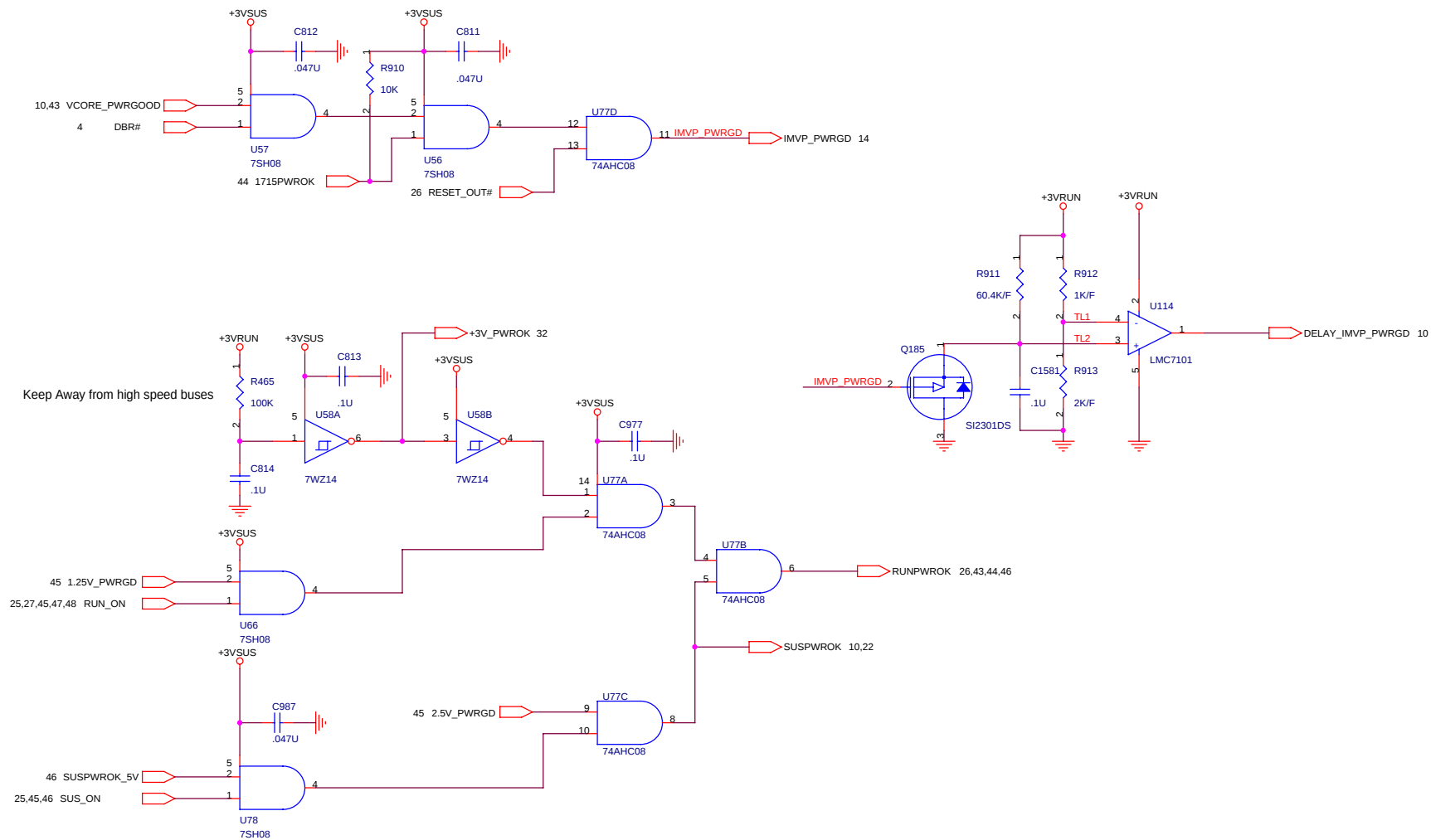


Title		FIR	
Size	Document Number	Rev	
	JM2	3G	
Date:	星期五, 六月 06, 2003	Sheet	37 of 49
	7	8	



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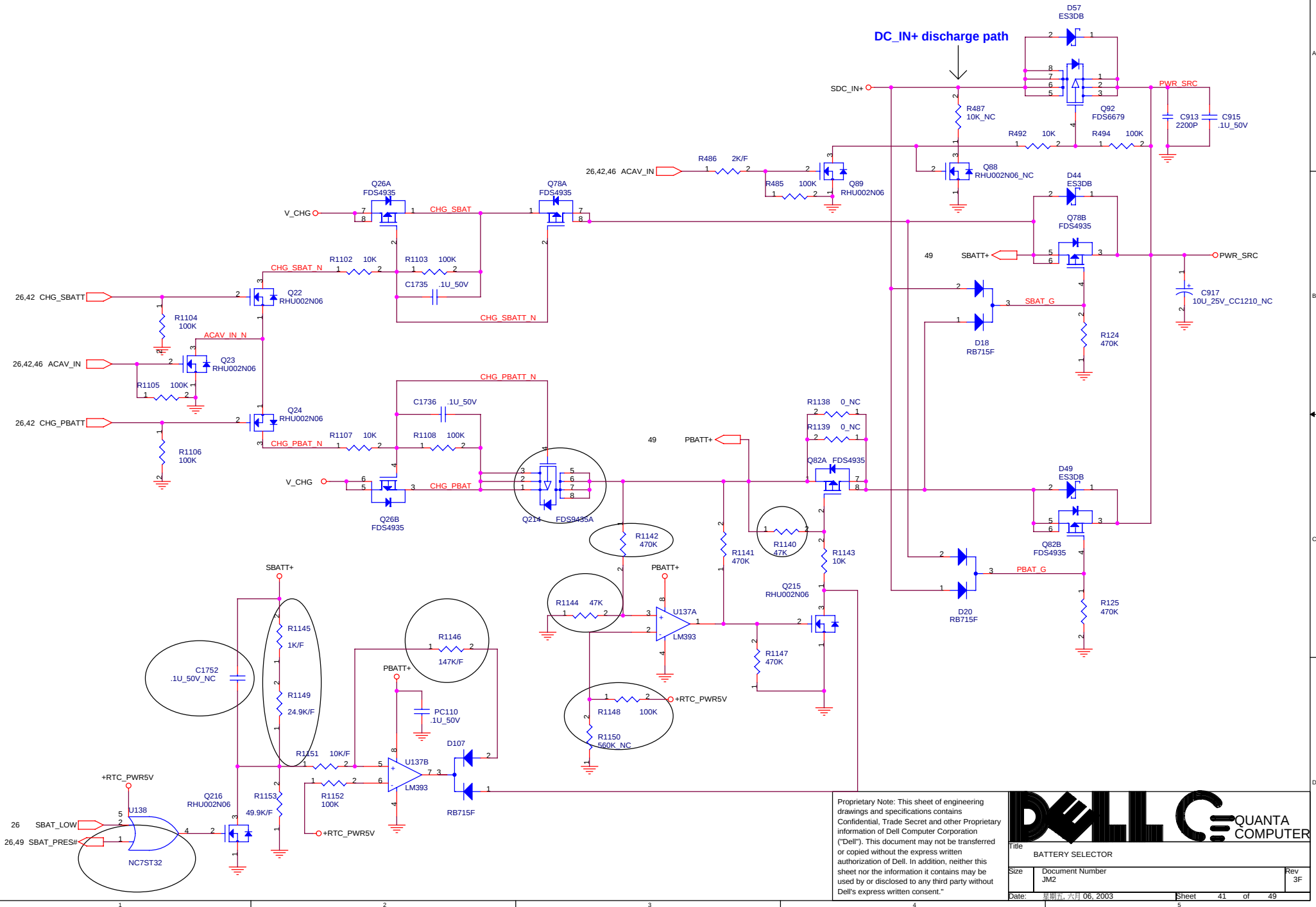
File: Docking Q-SWITCH		Rev: 2E
Size: Document Number JM2		
Date: 星期五, 六月 06, 2003	Sheet: 38 of 49	



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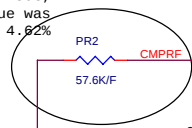


File	POWER GOOD		
Size	Document Number JM2		Rev 1A
Date	星期五, 六月 06, 2003		Sheet 40 of 49



File: BATTERY SELECTOR		
Size: JM2	Document Number: JM2	Rev: 3F
Date: 2003.06.06	Sheet: 41	of 49

value provided
2.67% offset;
old value was
for old 4.62%
offset.



BG waveforms
improved. may
delete from
future
revision.

- CPU_VID5 CPU_VID5
- CPU_VID4 CPU_VID4
- CPU_VID3 CPU_VID3
- CPU_VID2 CPU_VID2
- CPU_VID1 CPU_VID1
- CPU_VID0 CPU_VID0

Set up for
constant-ripple
mode. Was
constantfrequency
mode

20 mil Trace list for layout
The +5VRUN (PIN38
AND PIN20)
BST1_VCORE
BST2_VCORE

Added filter for
PBOOT

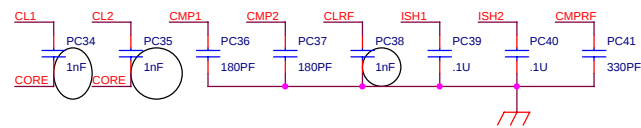
V I D							Vcore
VID 5	VID 4	VID 3	VID 2	VID 1	VID 0	V	V
0	1	0	1	1	1	1.340	
0	1	1	0	0	0	1.324	
0	1	1	0	1	0	1.292	
0	1	1	1	0	0	1.260	
0	1	1	1	0	1	1.244	
0	1	1	1	1	1	1.212	
1	0	0	0	0	1	1.180	
1	0	0	0	1	1	1.148	
1	0	0	1	1	0	1.100	
1	0	1	0	0	1	1.052	
1	0	1	0	1	1	1.020	
1	0	1	1	1	0	0.972	
1	1	0	0	0	0	0.940	

100 mil Trace list for layout

DH_VCORE
LX_VCORE
DL_VCORE
DH_VCORE2
LX_VCORE2
DL_VCORE2

10 mil Trace list for layout

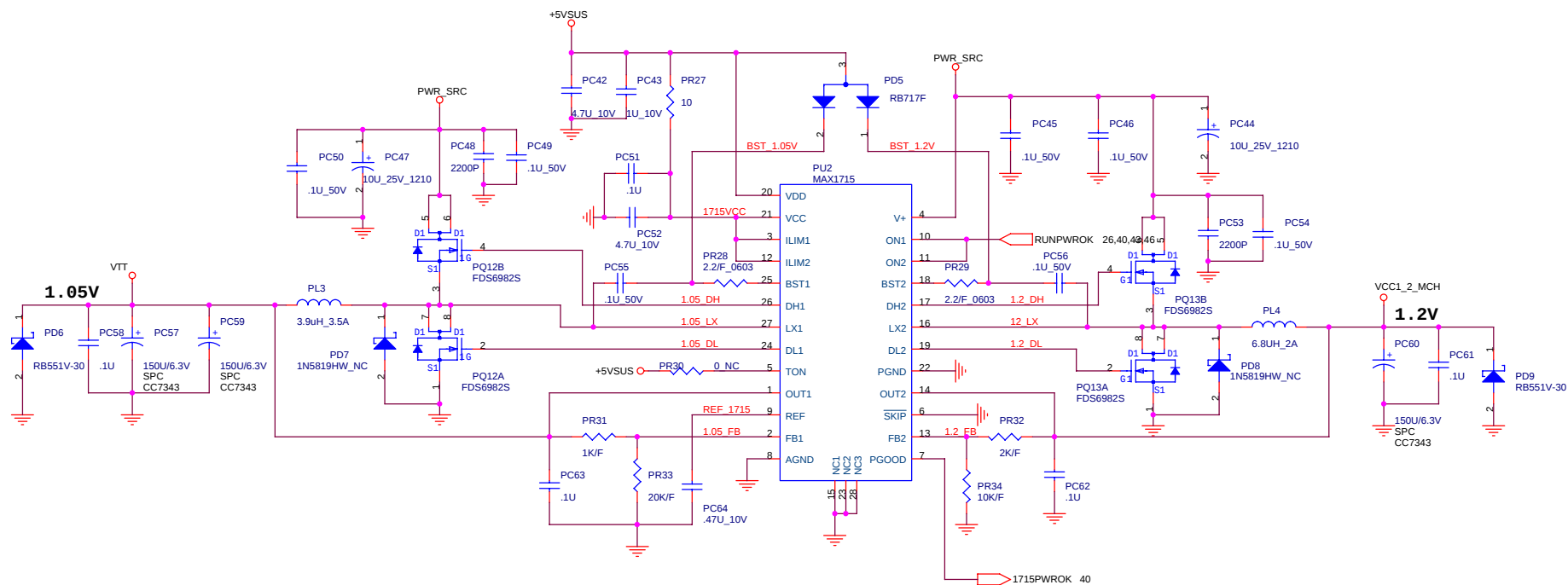
SC1476
pin 4 pin
5 pin 7
pin 25
pin 30



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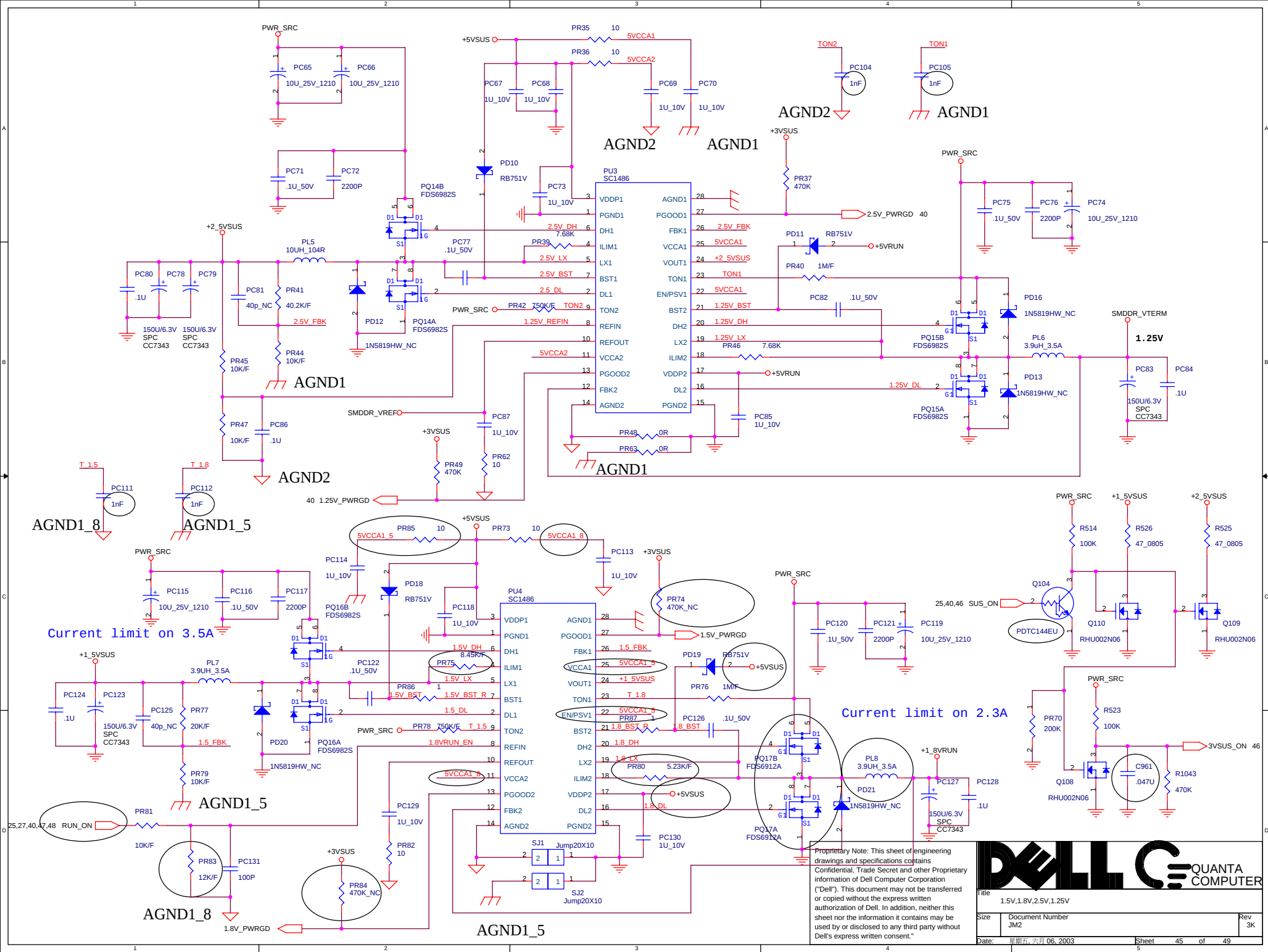


Title CPU POWER			
Size	Document Number JM2	Rev 3K	
Date:	星期五, 六月 06, 2003	Sheet	43 of 49



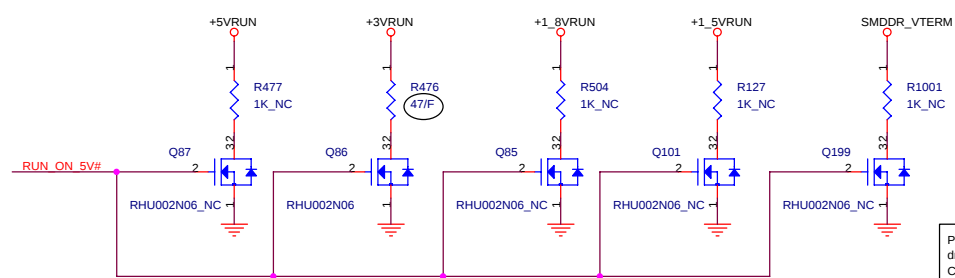
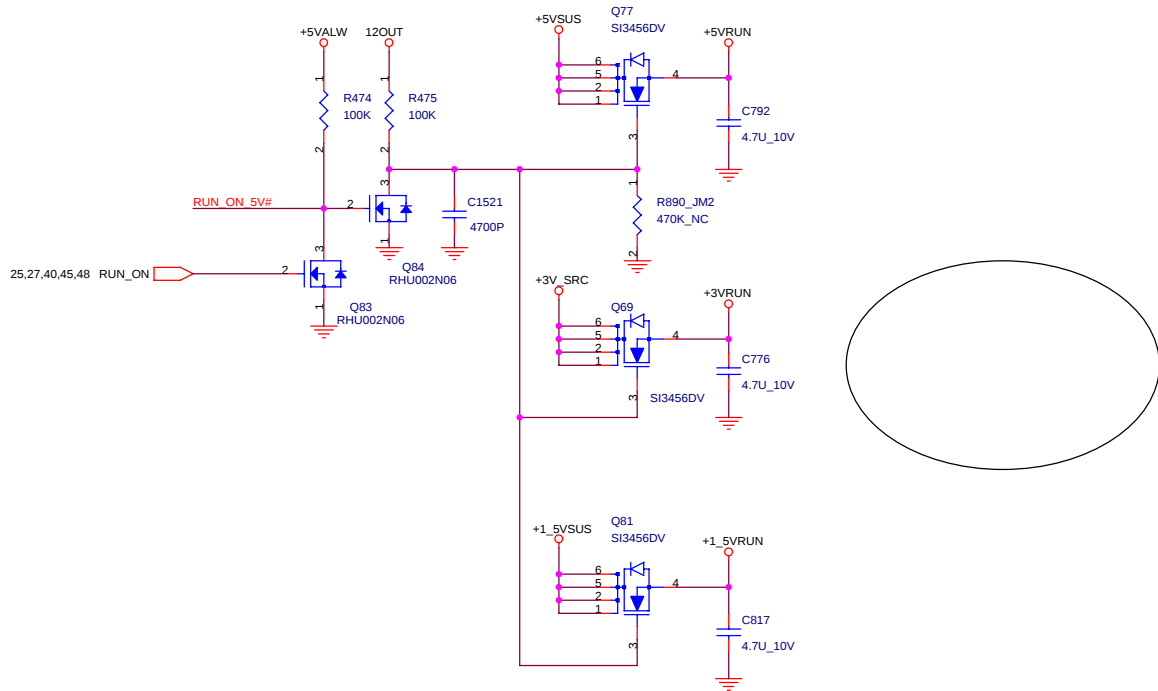
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Title VTT_1.05V,VCC1_2_MCH			
Size	Document Number JM2		Rev 1A
Date: 星期五, 六月 06, 2003	Sheet 44	of 49	



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Title		1.5V,1.8V,2.5V,1.25V	
Size	Document Number	JM2	
Date	Rev	3K	
Sheet	45	of	49



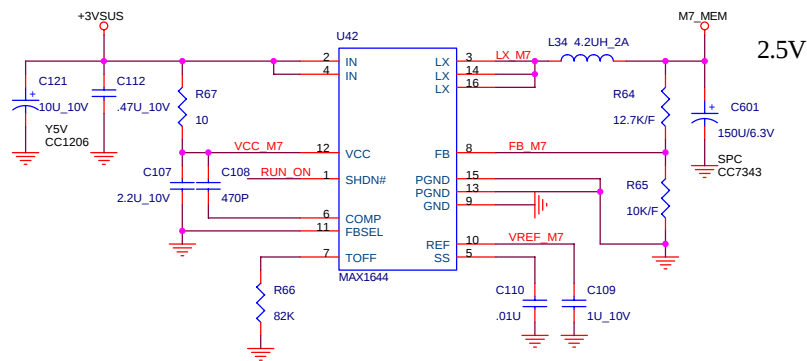
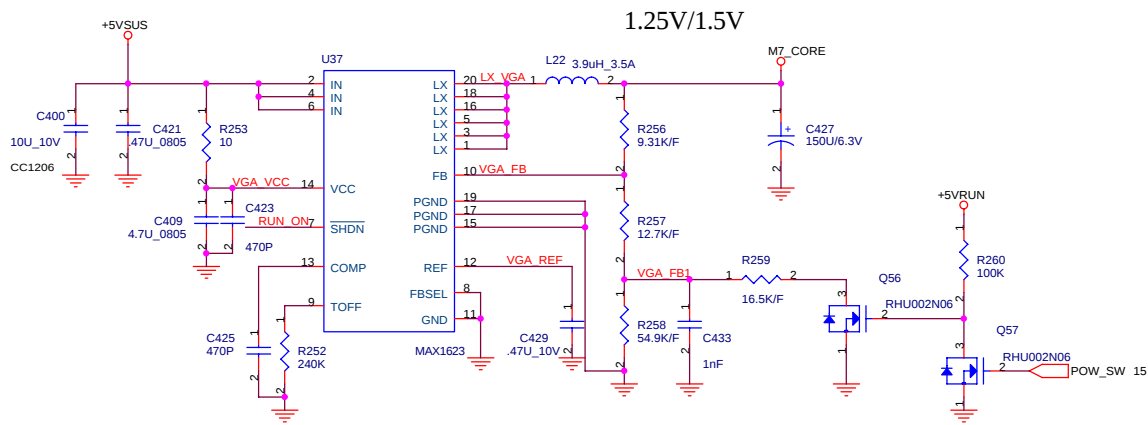
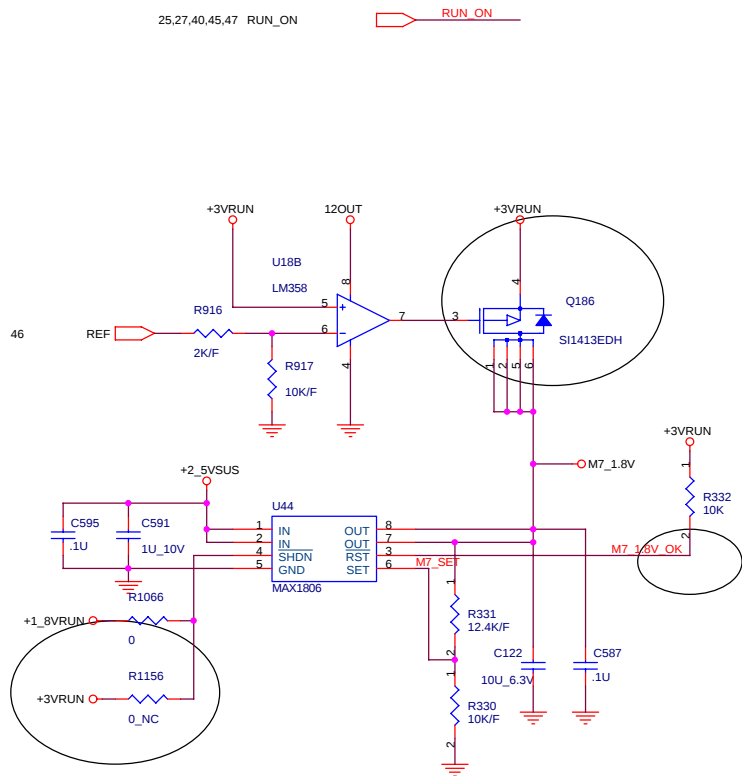
Reserve discharge path

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File		RUN POWER SW	
Size	Document Number	Rev	
	JM2	3K	
Date:	原明市, 六月 06, 2003	Sheet	47 of 49

POW_SW	M7D/M9 CSP 16 32
0	1.5V
1	1.25V



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DELL **QUANTA** **COMPUTER**

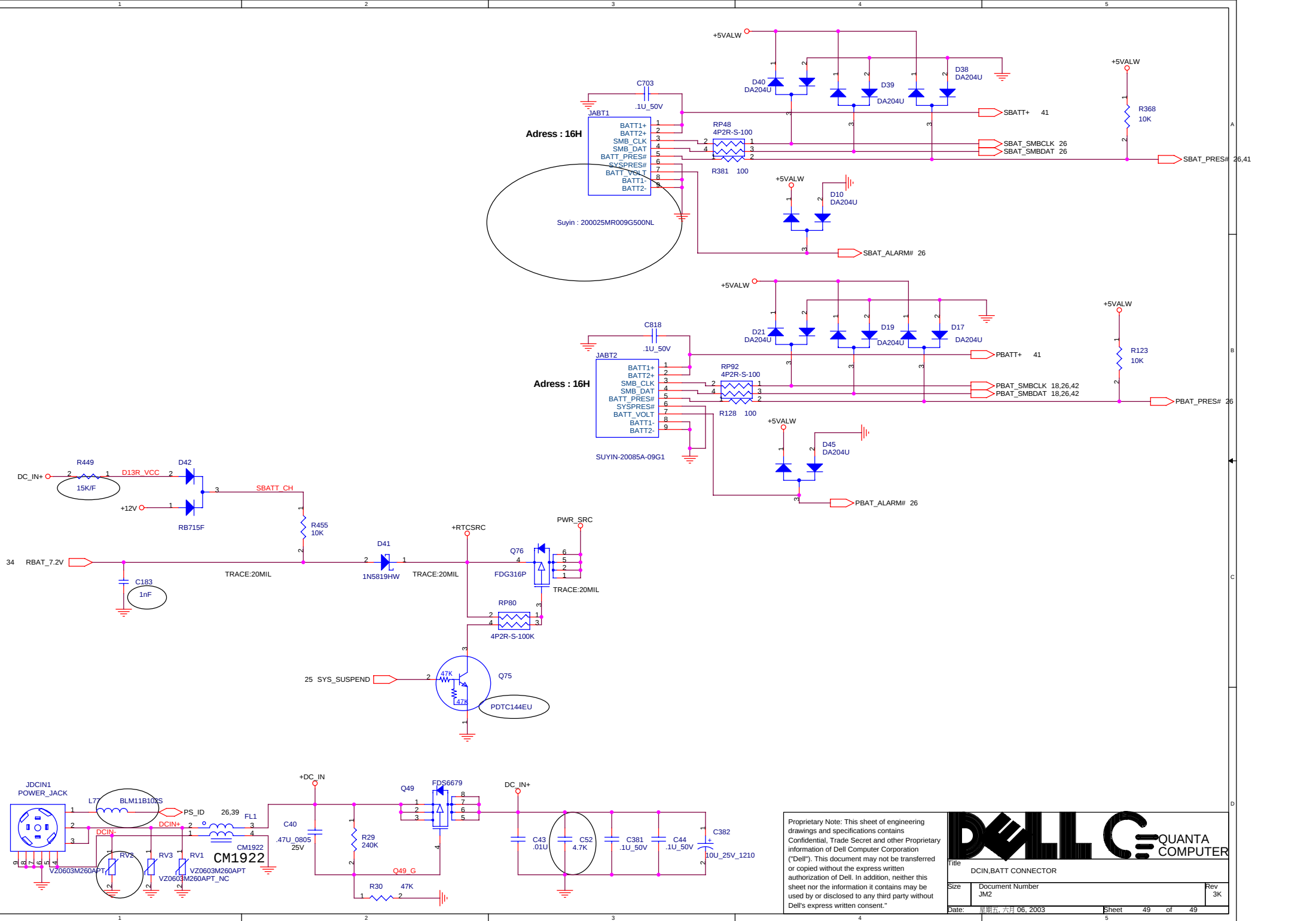
File: VGA DC/DC

Size: Document Number JM2

Date: 2003.06.06

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Rev: 3F



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File DCIN.BATT CONNECTOR		
Size JM2	Document Number Rev 3K	
Date 星期五, 六月 06, 2003	Sheet 49	of 49

MODEL	REV	CHANGE LIST	Model	JM2	MB
			Page	FM	TO
JM2	1A	Preliminary Release	1	3I	
			2	1A	
	1B	Refer to ECN No. E200205-0561	3	1A	
			4	1A	3K
	2A	Refer to ECN No. E200206-0072	5	2E	3K
			6	3B	
	2B	Refer to ECN No. E200206-0273	7	3A	3K
			8	2E	
	2C	Refer to ECN No. E200207-0115	9	3C	
			10	3I	
	2D	Refer to ECN No. E200207-1038	11	3A	
			12	3F	
	2E	Refer to ECN No. E200207-1219, E200207-2119	13	1A	
			14	1A	
	3A	Refer to ECN No. E200208-1667, E200209-1836	15	3A	
			16	1A	
	3B	Refer to ECN No. E200210-0812, E200210-1418	17	3A	
			18	1A	
	3C	Refer to ECN No. E200210-2031, E200211-0167	19	3F	
			20	3F	3K
	3D	Refer to ECN No. E200211-0897	21	3A	
			22	3G	
	3E	Refer to ECN No. E200211-1860, E200211-2101, E200211-2472	23	3A	
			24	3A	
	3F	Refer to ECN No. E200211-3189	25	3D	
			26	3F	
	3G	Refer to ECN No. E200301-0228, E200301-1028	27	3F	3K
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	3H	Refer to ECN No. E200301-1742, E200301-2344, E200302-0074, E200302-0685	29	3A	
			30	3A	
	3I	Refer to ECN No. E200302-1751	31	3G	
			32	3A	3K
	3J	Refer to ECN No. E200302-2889	33	3C	3K
			34	3F	
	3K	Page 43 : De-pop PQ4 and PQ8 for save cost Page 43 : Change PR72 from 100K NC to 62K/F for correct schematic. Page 43 : Change PR2 from 22.6K/F to 57.6K/F for correct schematic. Page 27 : Change C34 from 1000P to 1nF for description same as BOM. Page 49 : Change C183 from 1000P to 1nF for description same as BOM. Page 20 : Change C337 from 1000P to 1nF for description same as BOM. Page 39 : Change C358, C359 from 1000P to 1nF for description same as BOM. Page 33 : Change C1589 from 1000P to 1nF for description same as BOM. Page 43 : Change PC16, PC32, PC33, PC34, PC35, PC38 from 1000P to 1nF for description same as BOM. Page 45 : Change PC104, PC105, PC111, PC112 from 1000PF to 1nF for description same as BOM. Page 20 : Change Q30, Q62 from DTC144EUA to PDTC144EU for description same as BOM. Page 32 : Change Q19, Q212 from DTC144EUA to PDTC144EU for description same as BOM. Page 36 : Change Q204, Q205 from DTC144EUA to PDTC144EU for description same as BOM. Page 39 : Change Q45 from DTC144EUA to PDTC144EU for description same as BOM. Page 45 : Change Q104 from DTC144EUA to PDTC144EU for description same as BOM. Page 46 : Change Q36 from DTC144EUA to PDTC144EU for description same as BOM.	35	3C	3K
			36	3G	3K
			37	3G	
			38	2E	
			39	2E	3K
			40	1A	
			41	3F	
			42	3F	
			43	3C	3K
			44	1A	
			45	3D	3K
			46	3H	3K
			47	3C	3K
			48	3F	
			49	3F	3K
 QUANTA COMPUTER		PROJECT : JM2			
		DOC. NO. 204			
		REV: 3K			
		ASSY: 31JM2MB0036			
APPROVED BY : Eric Liu		CHECKED BY:Kevin Chien		DRAWN BY : Lit Wu	
		DATE : JUN. 06, 2003		SHEET 1 OF 1	