

ZZZ1 LA-4761P
PCB
DAZ06H00100

Compal Confidential

KIU10-Tiger M/B Schematics Document

2009-01-13

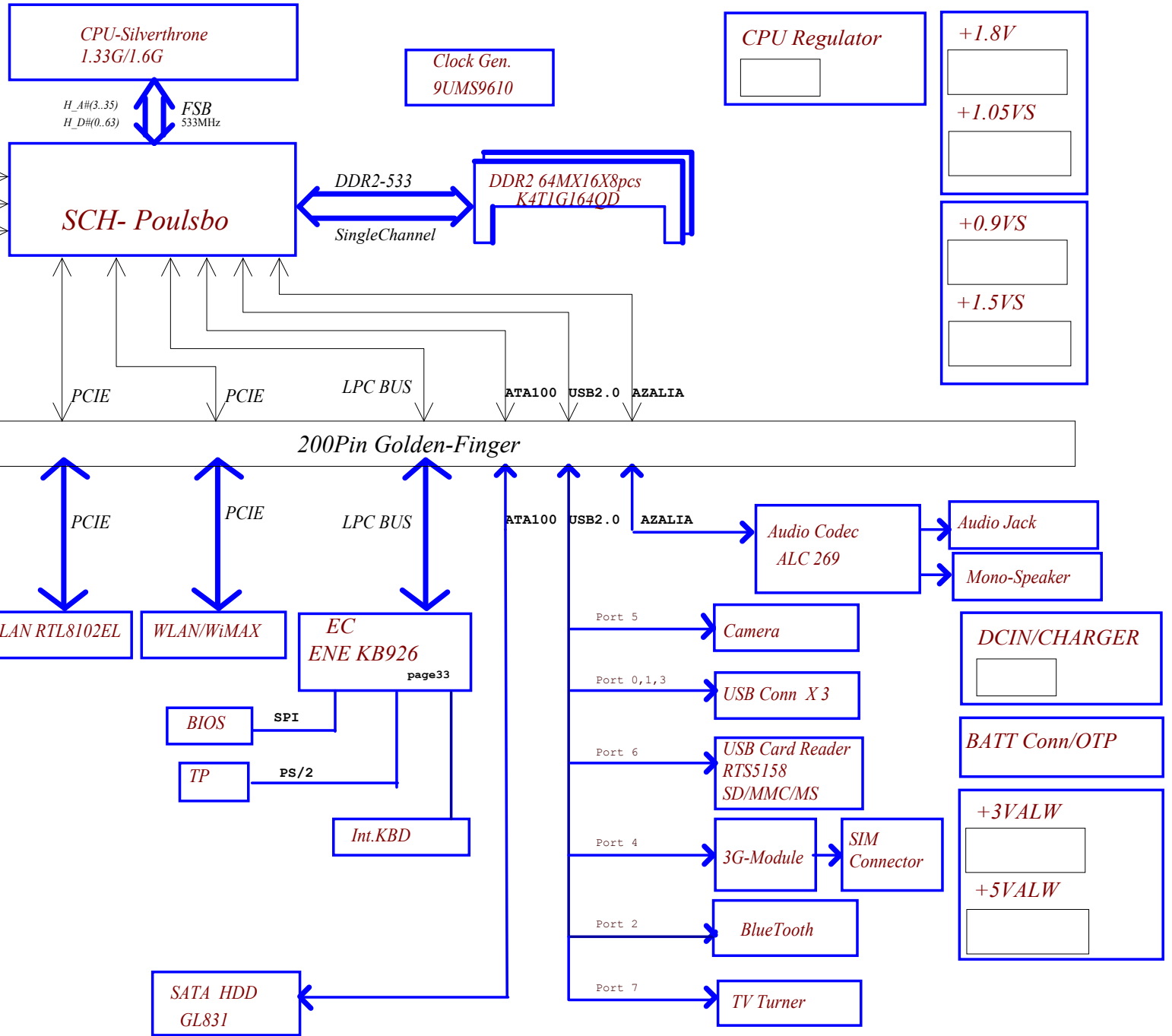
REV: 1.0

Security Classification	Compal Secret Data		Title		
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			Custom	1.0	
Date:	Document Number	Sheet	1	of	22
	KIU10 LA-4761P				

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Model Name : Kxxxxx

Tiger System-Board

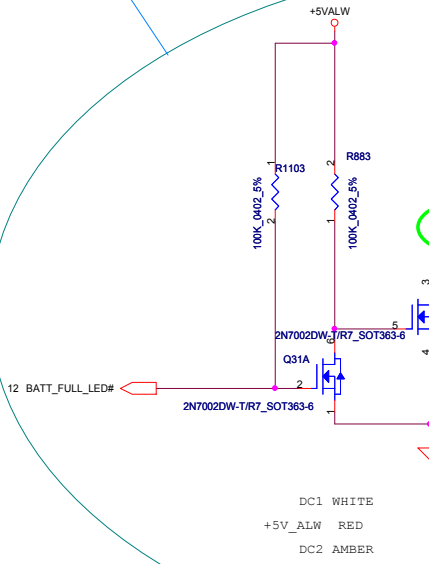


File Name: Tiger Main-Board

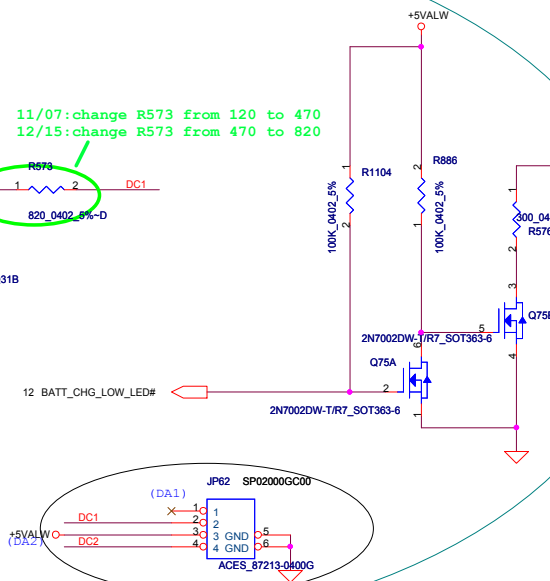
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Issued Date		Deciphered Date		Block Diagram		
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12/09:Chaneg LED board CONN to 3PIN

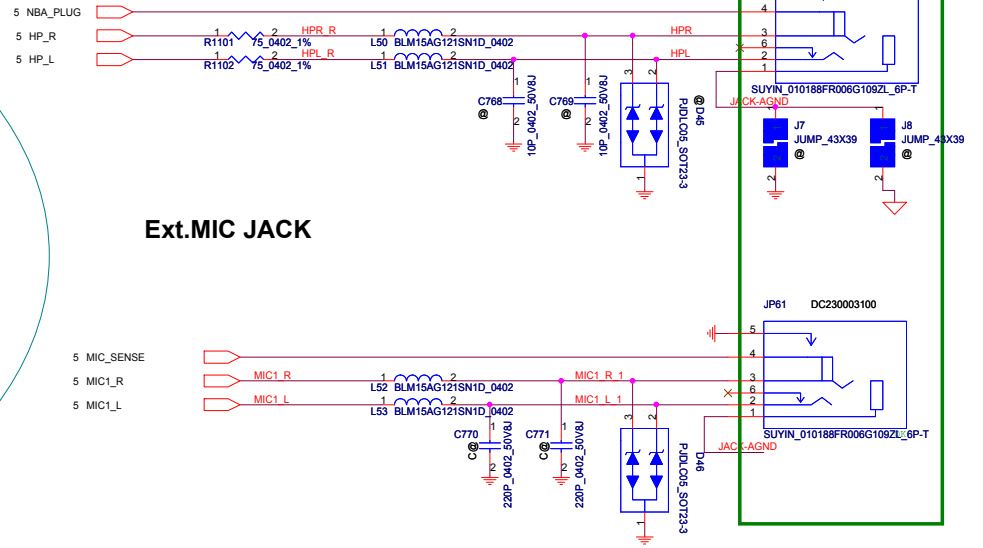
FULL LED



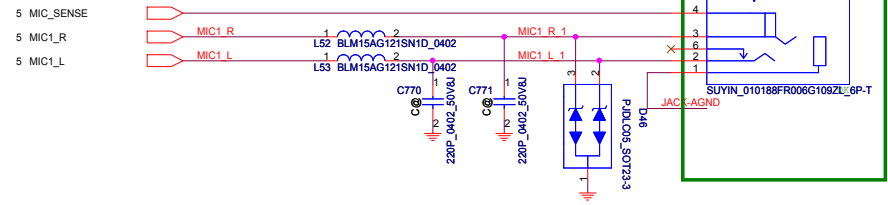
BATT CHARGE



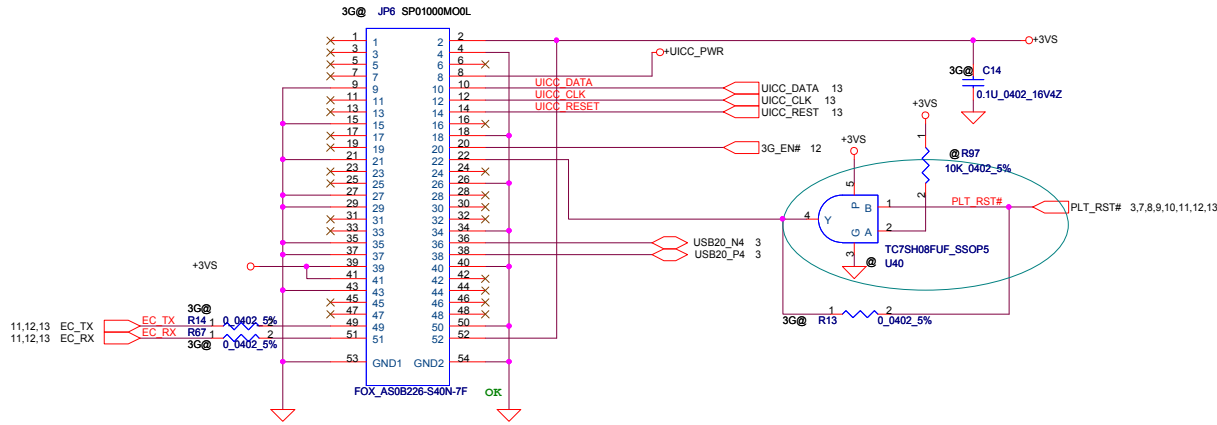
HeadPhone JACK



Ext.MIC JACK

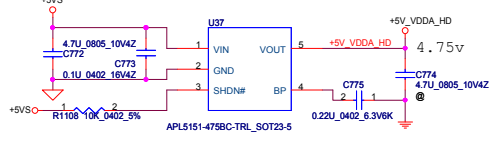


PCIe Mini Card for 3G



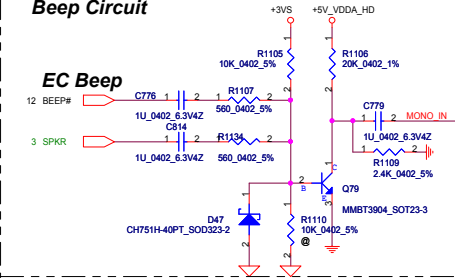
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Issued Date		Deciphered Date			
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Codec Regulator

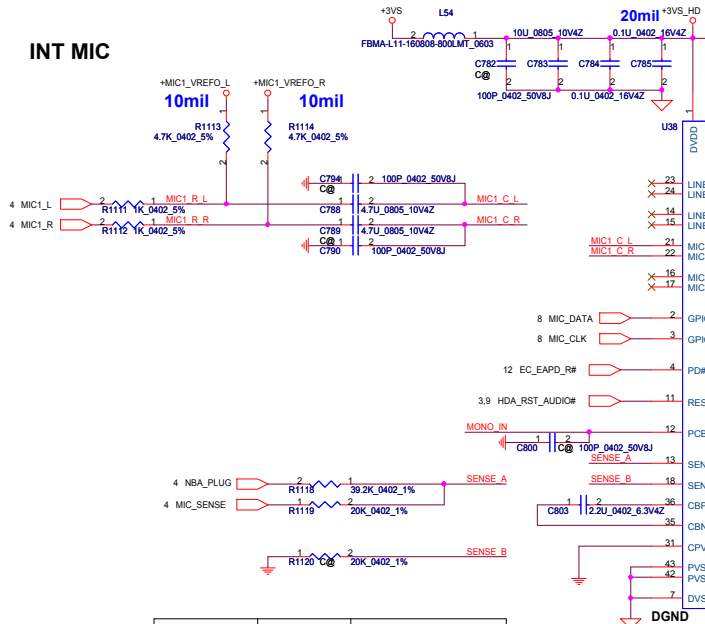


Beep Circuit

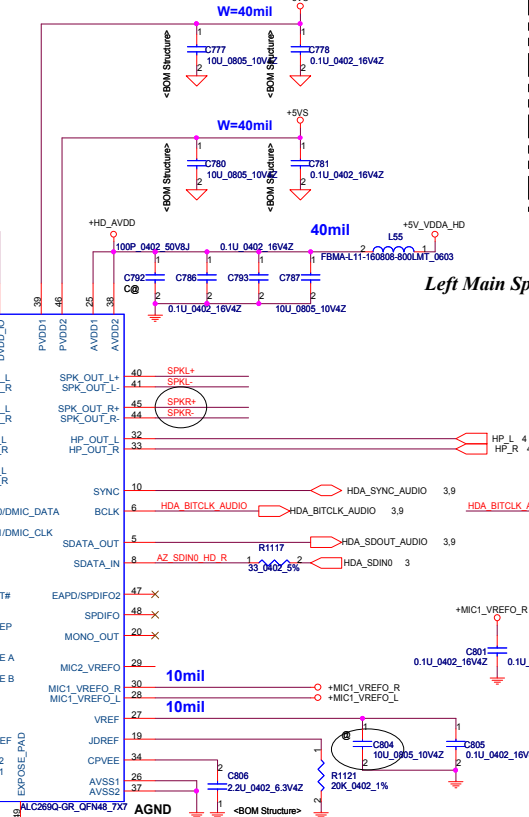
EC Beep



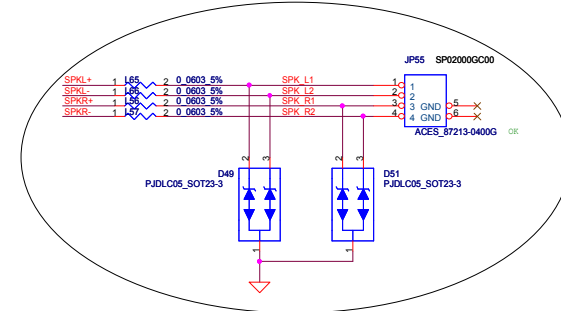
INT MIC



Left Main Speaker Connector

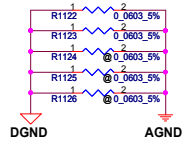


Speaker Conn.

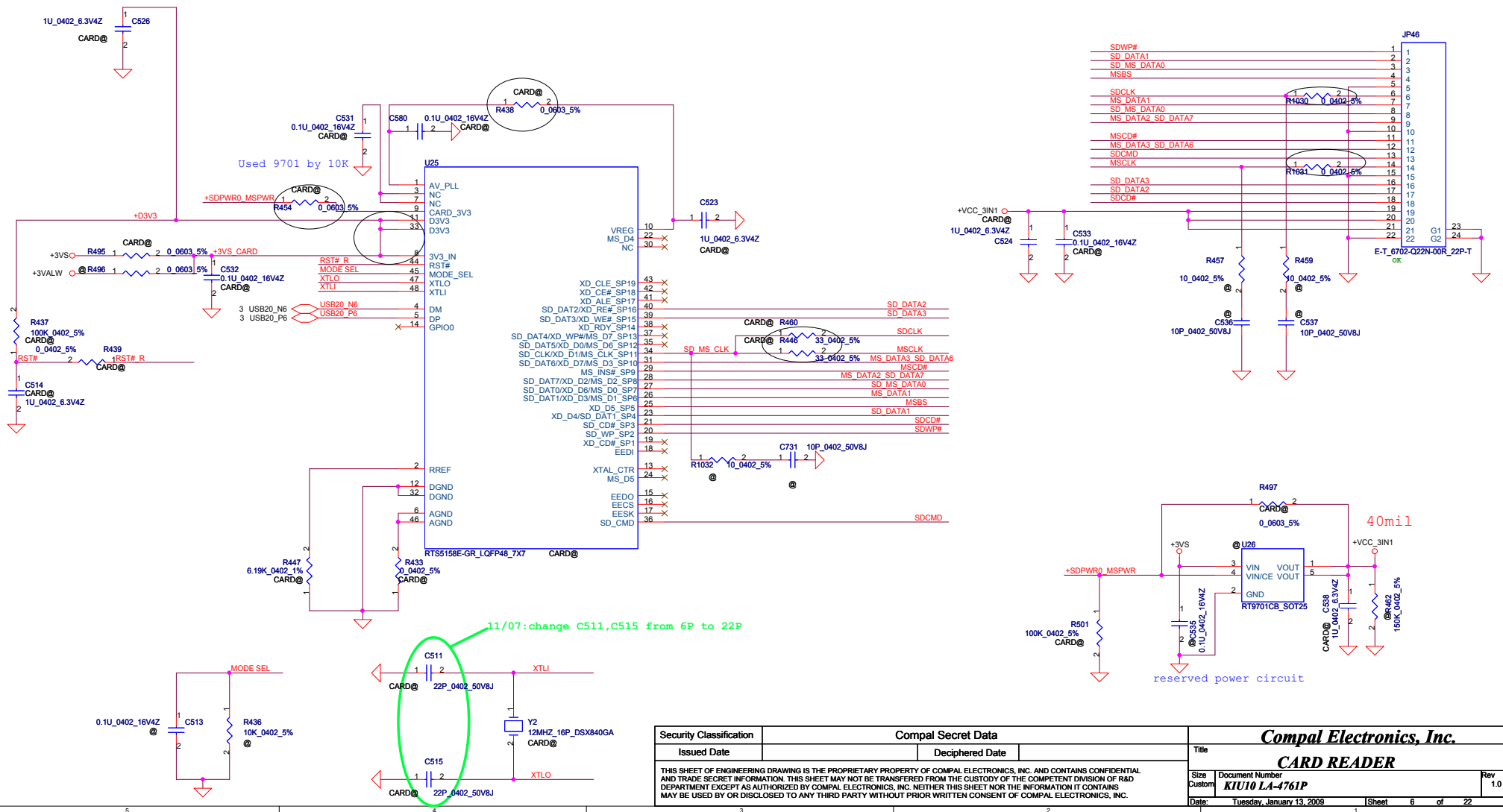


Sense Pin	Impedance	Codec Signals
SENSE A	39.2K	PORT-A (PIN 32, 33)
	20K	PORT-B (PIN 21, 22)
	10K	PORT-C (PIN 23, 24)
SENSE B	5.1K	PORT-D (PIN 48)
	39.2K	PORT-E (PIN 14, 15)
	20K	PORT-F (PIN 16, 17)
	10K	PORT-G (PIN 20)
	5.1K	PORT-H (PIN 47)

DGND To AGND Bypass

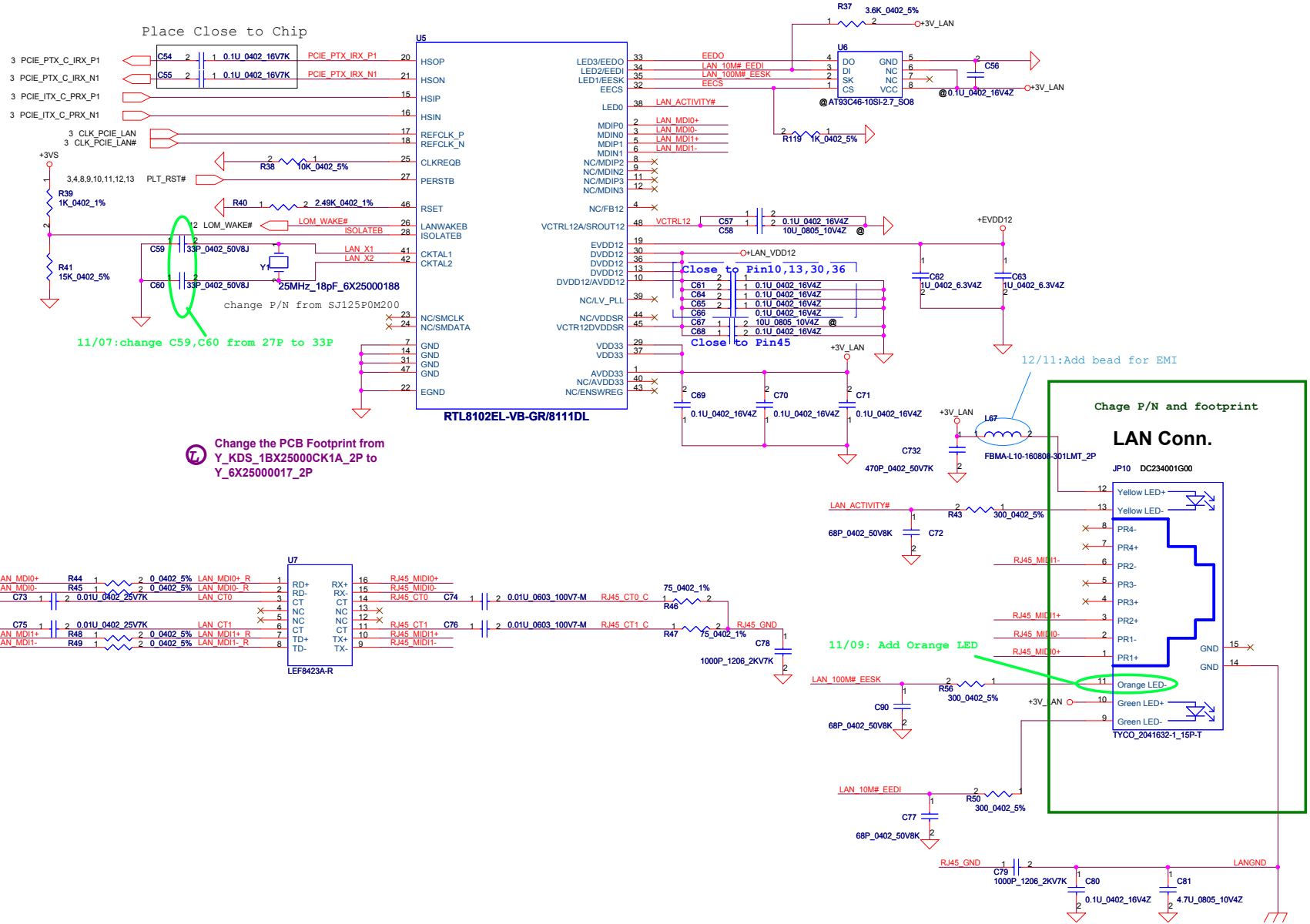


3 in 1 Card Reader



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Issued Date		Deciphered Date		CARD READER	
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Date		Sheet		of	
Tuesday, January 13, 2009		6		22	





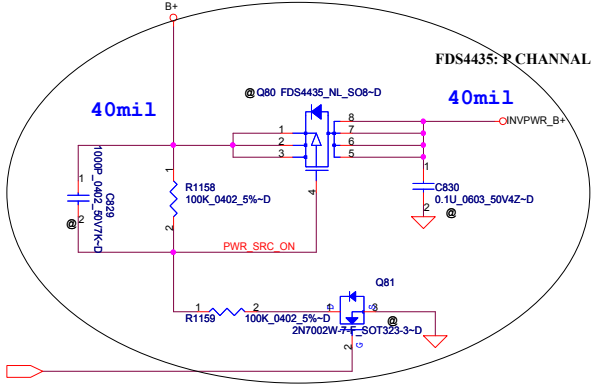
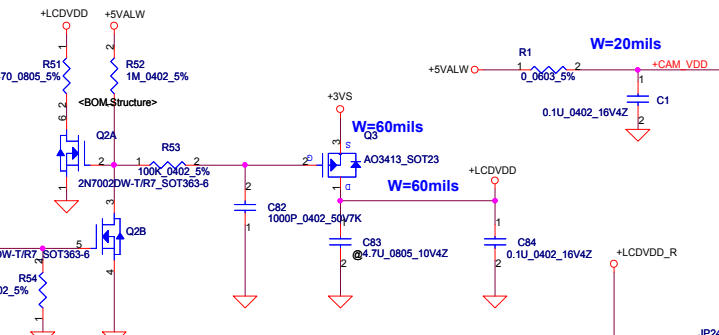
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Compal Electronics, Inc.

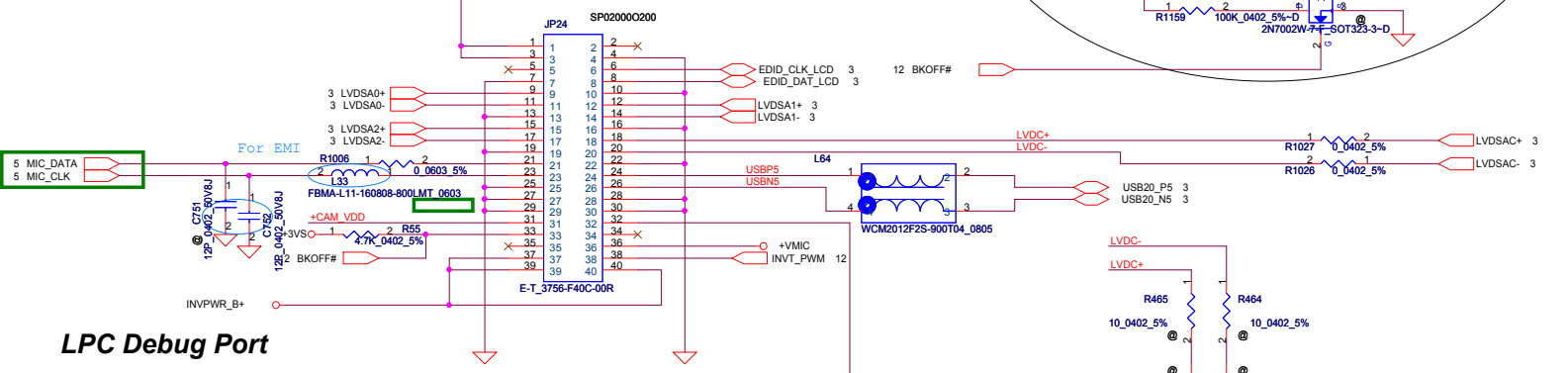
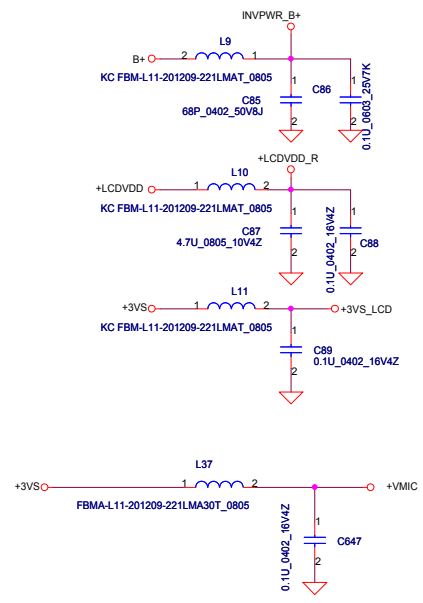
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Document Number: KIU10 LA-4761P
 Date: Tuesday, January 13, 2009
 Sheet: 7 of 22
 Rev: 1.0

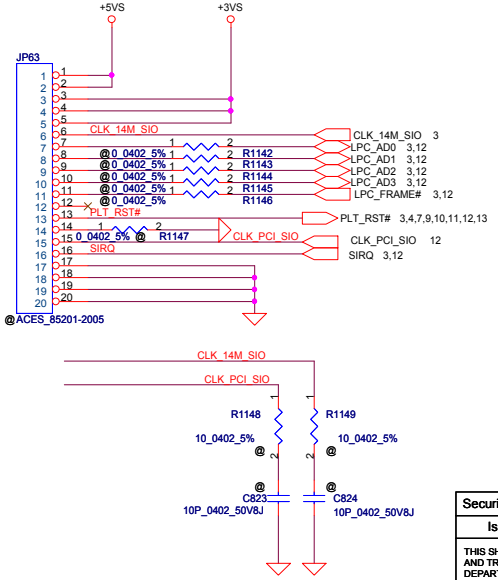
LCD POWER CIRCUIT



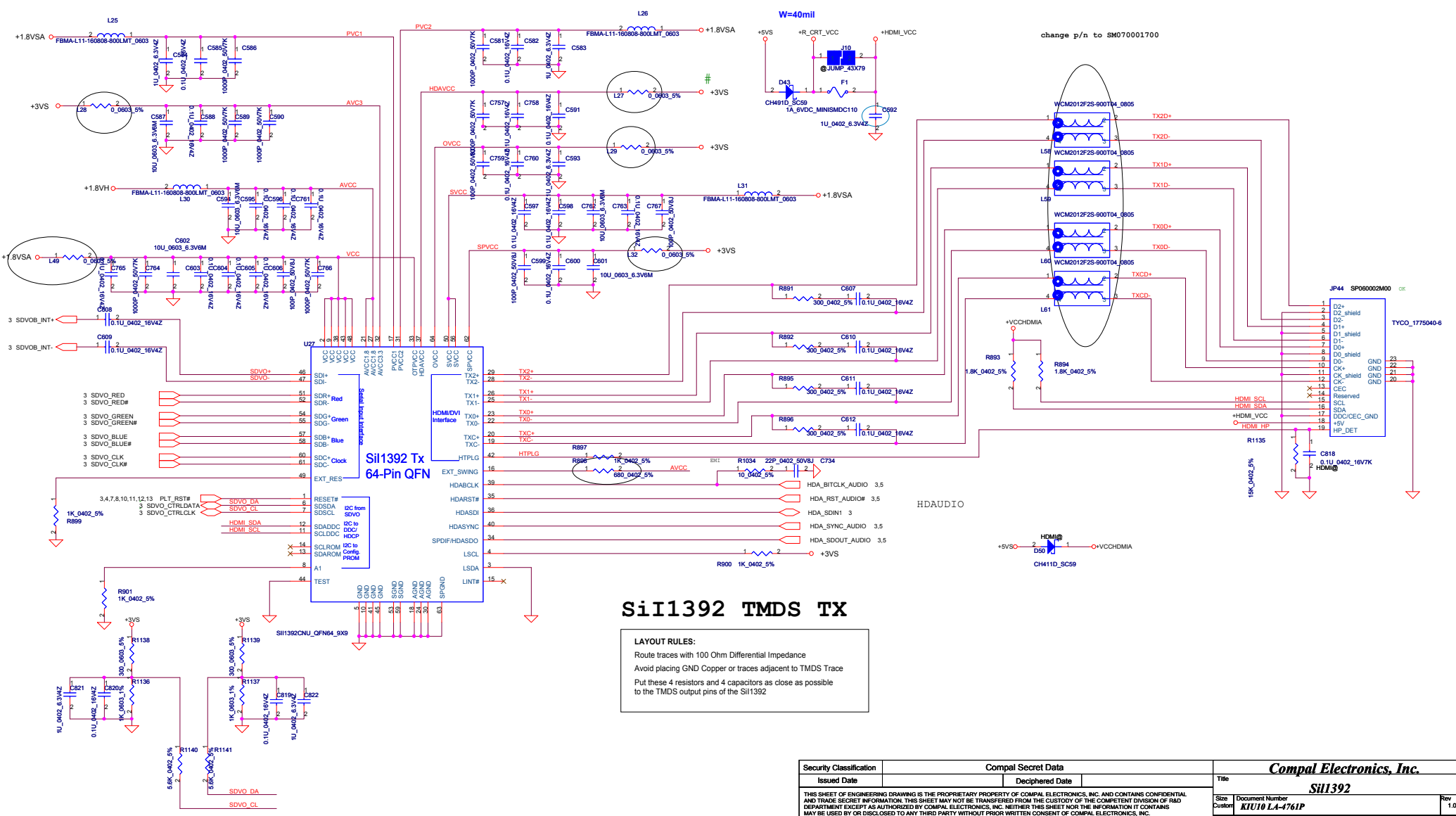
LCD/PANEL BD. Conn.



LPC Debug Port



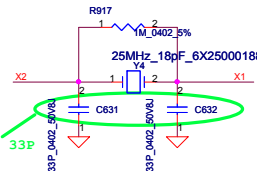
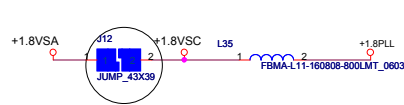
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	KIU10 LA-4761P				
Date:	Tuesday, January 13, 2009	Sheet	8	of	22



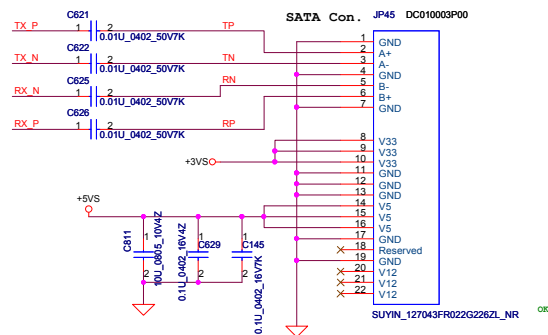
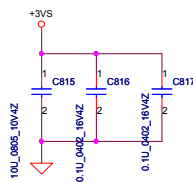
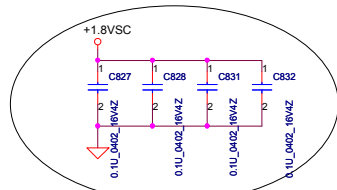
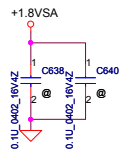
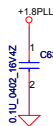
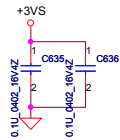
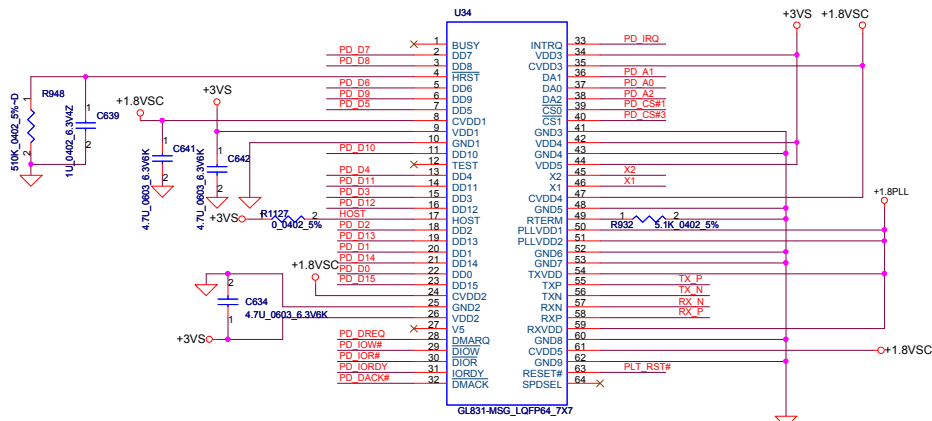
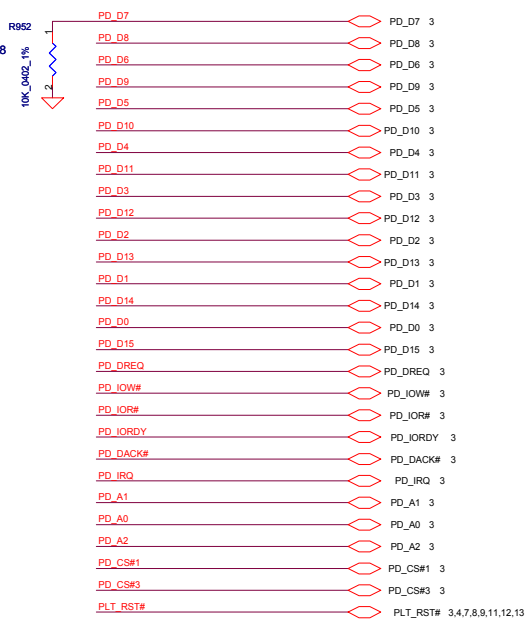
SiI1392 TMDs TX

LAYOUT RULES:
 Route traces with 100 Ohm Differential Impedance
 Avoid placing GND Copper or traces adjacent to TMDs Trace
 Put these 4 resistors and 4 capacitors as close as possible to the TMDs output pins of the SiI1392

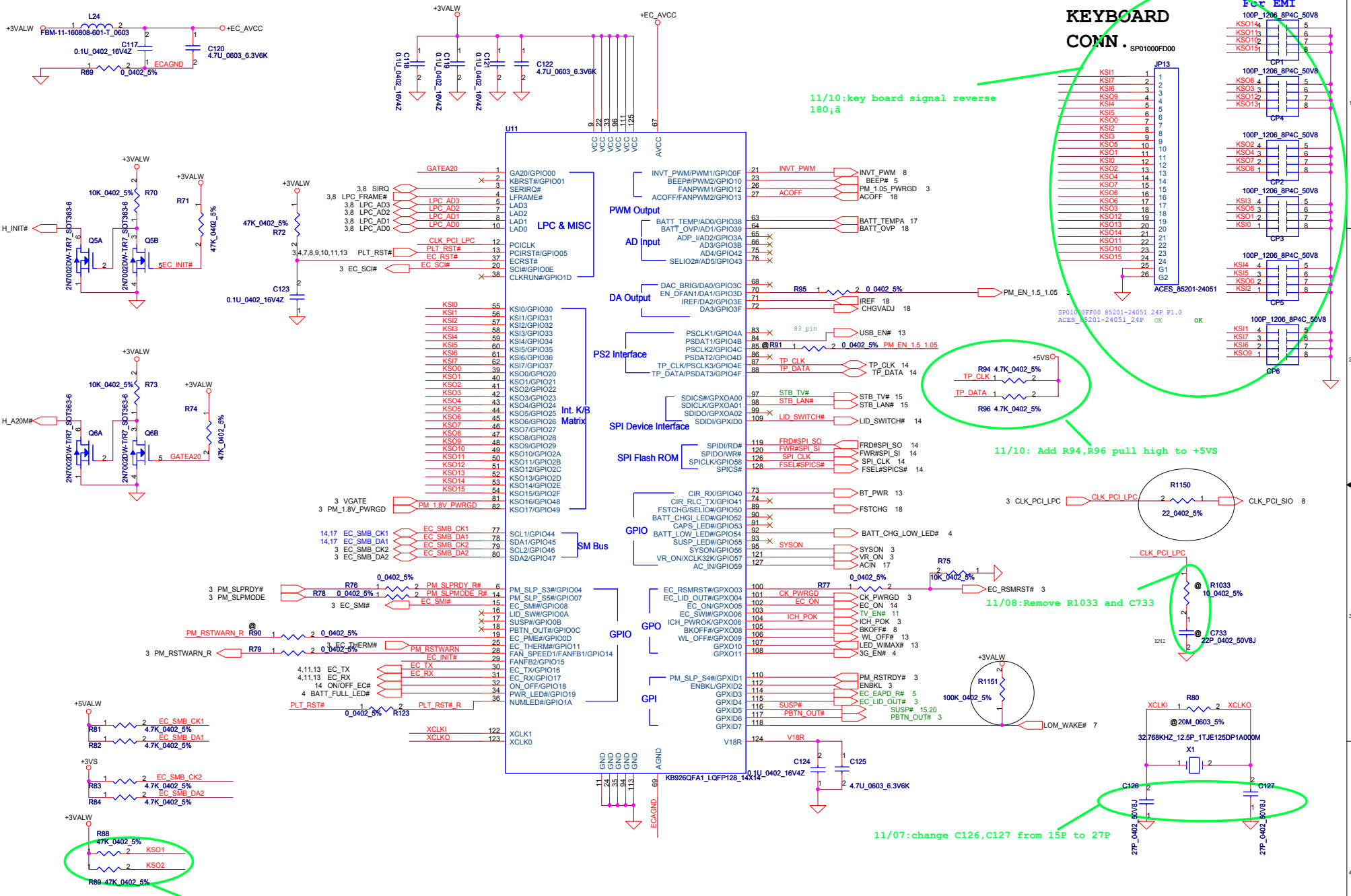
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			Rev 1.0
			Date: Tuesday, January 13, 2009 Sheet 9 of 22



11/07:change C631,C632 from 22P to 33P

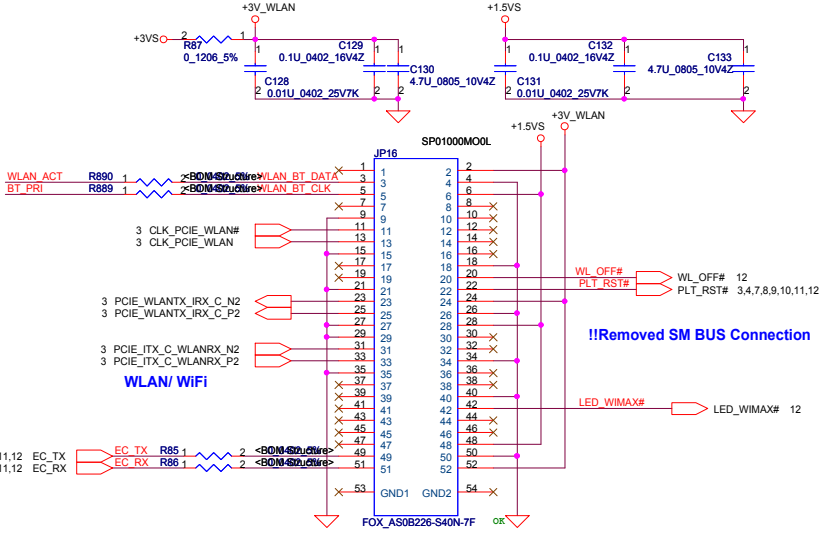


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				KJ10 LA-4761P	Rev 1.0
				Date: Tuesday, January 13, 2009	Sheet 10 of 22

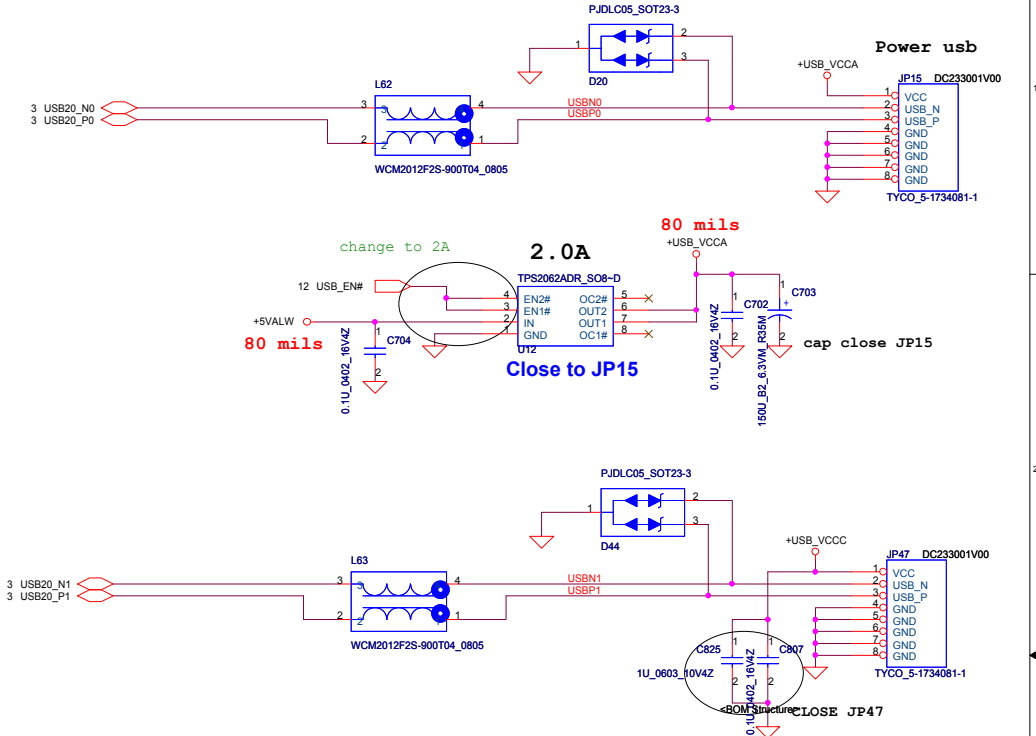


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				Date:	Tuesday, January 13, 2009
				Sheet	12 of 22

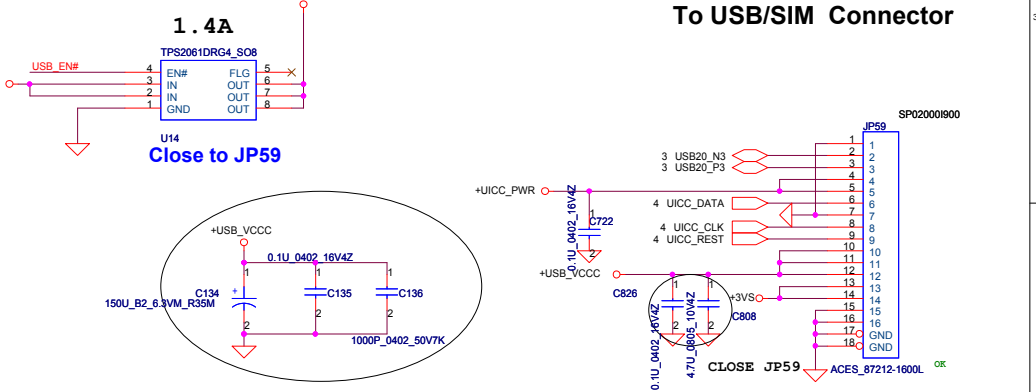
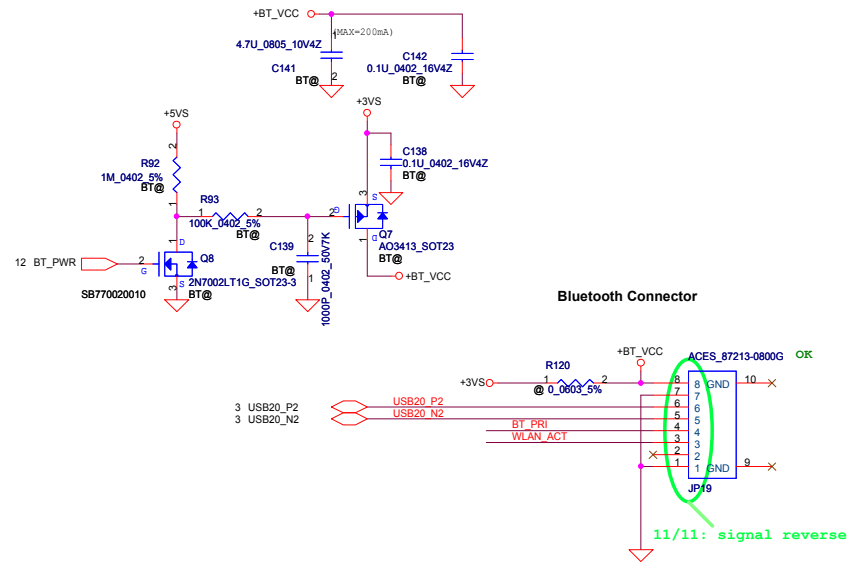
PCIe Mini Card for WLAN/WiMax



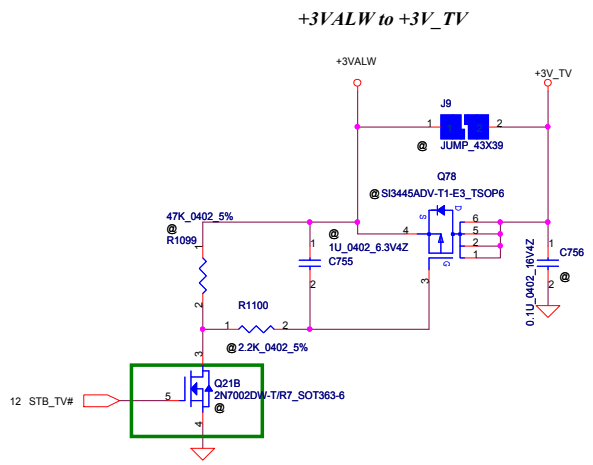
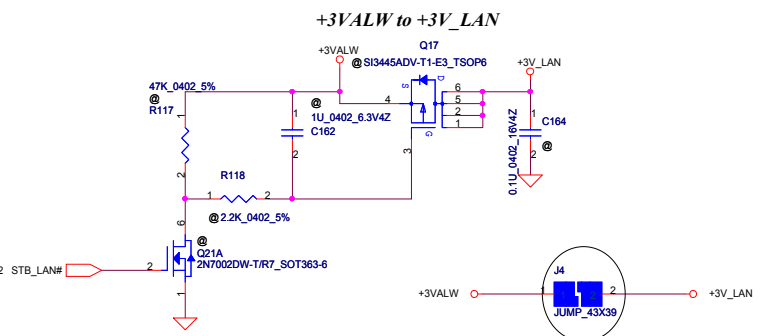
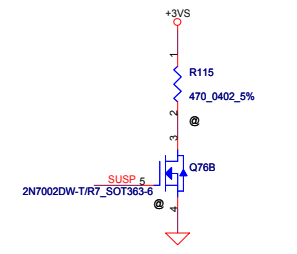
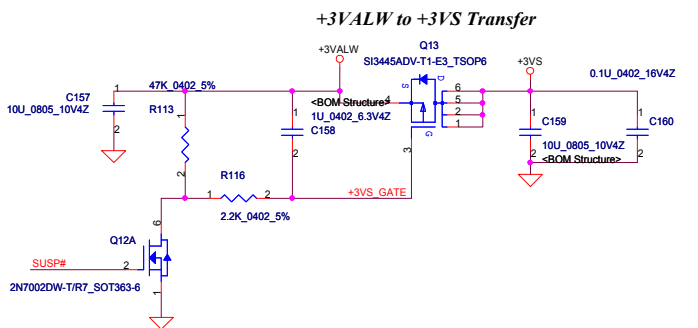
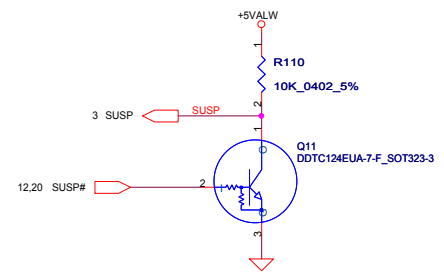
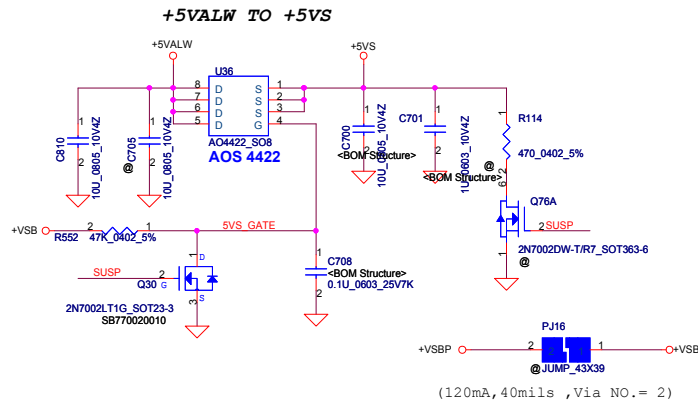
On Board USB Connector



BlueTooth Interface



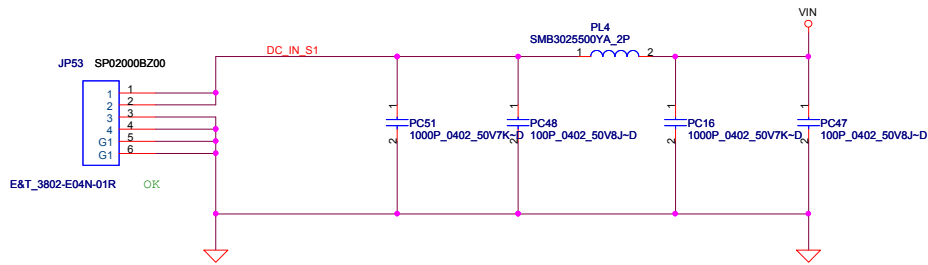
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	KIU10 LA-4761P				Rev 1.0



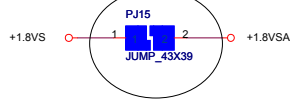
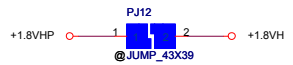
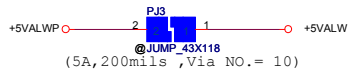
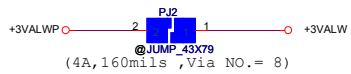
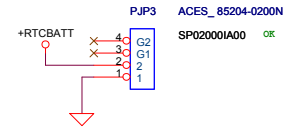
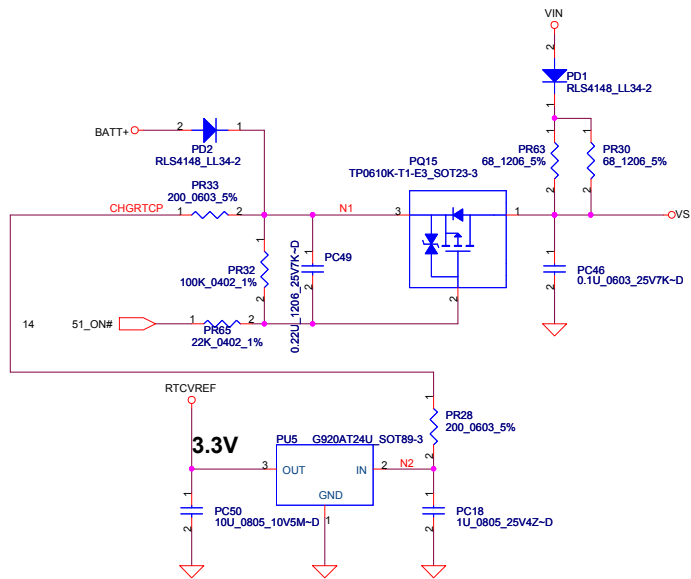
PRODUCT SUMMARY

Vgs	rDS (on)	ID (A)
-8	0.042 @ VGS = .4.5 V	+-.5,6
	0.060 @ VGS = .2.5 V	+-.4,7
	0.080 @ VGS = .1.8 V	+-.2,9

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Date:	Tuesday, January 13, 2009	Sheet	15 of 22

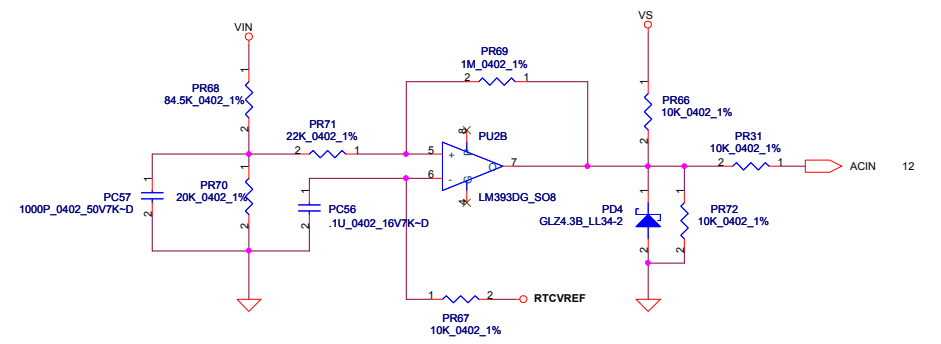
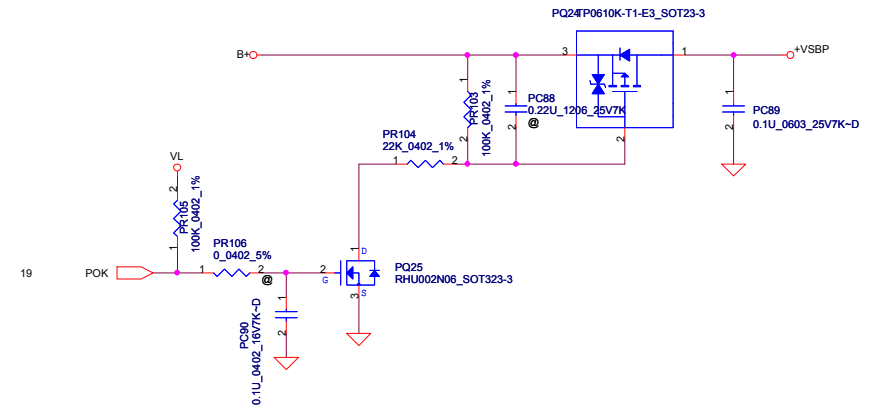
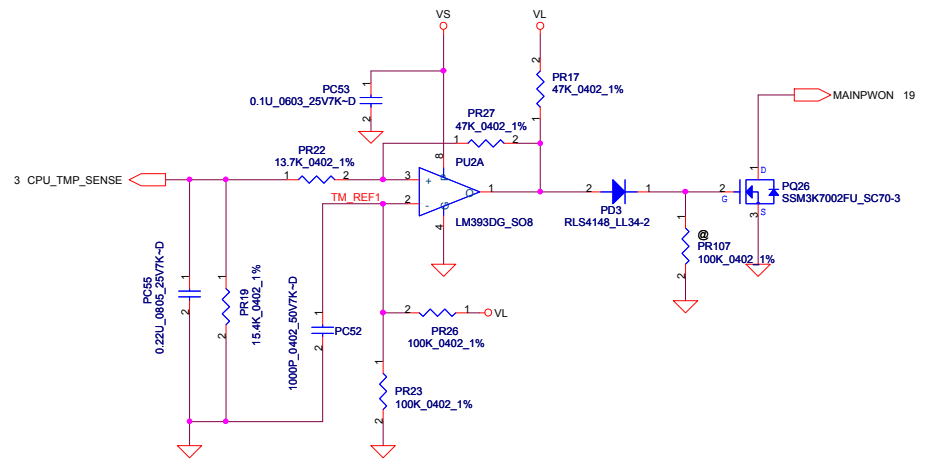
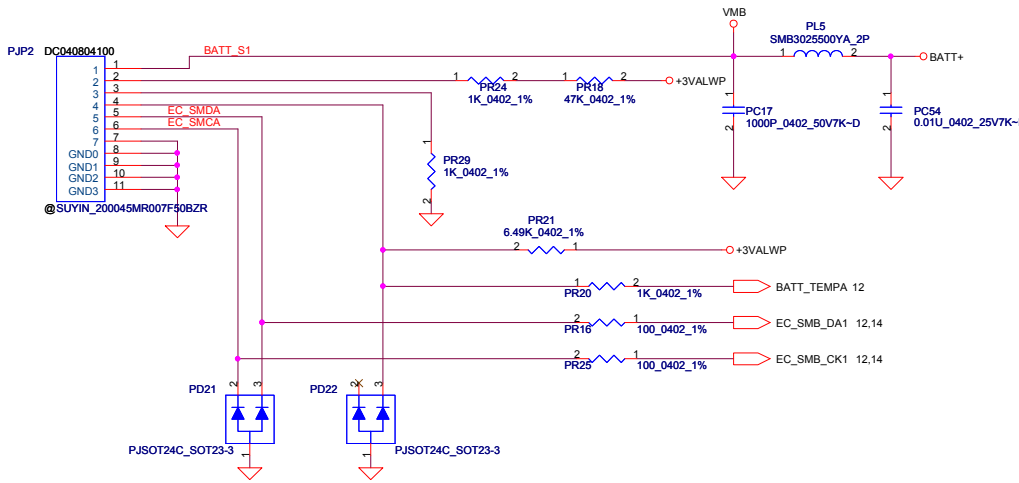


E&T_3802-E04N-01R OK



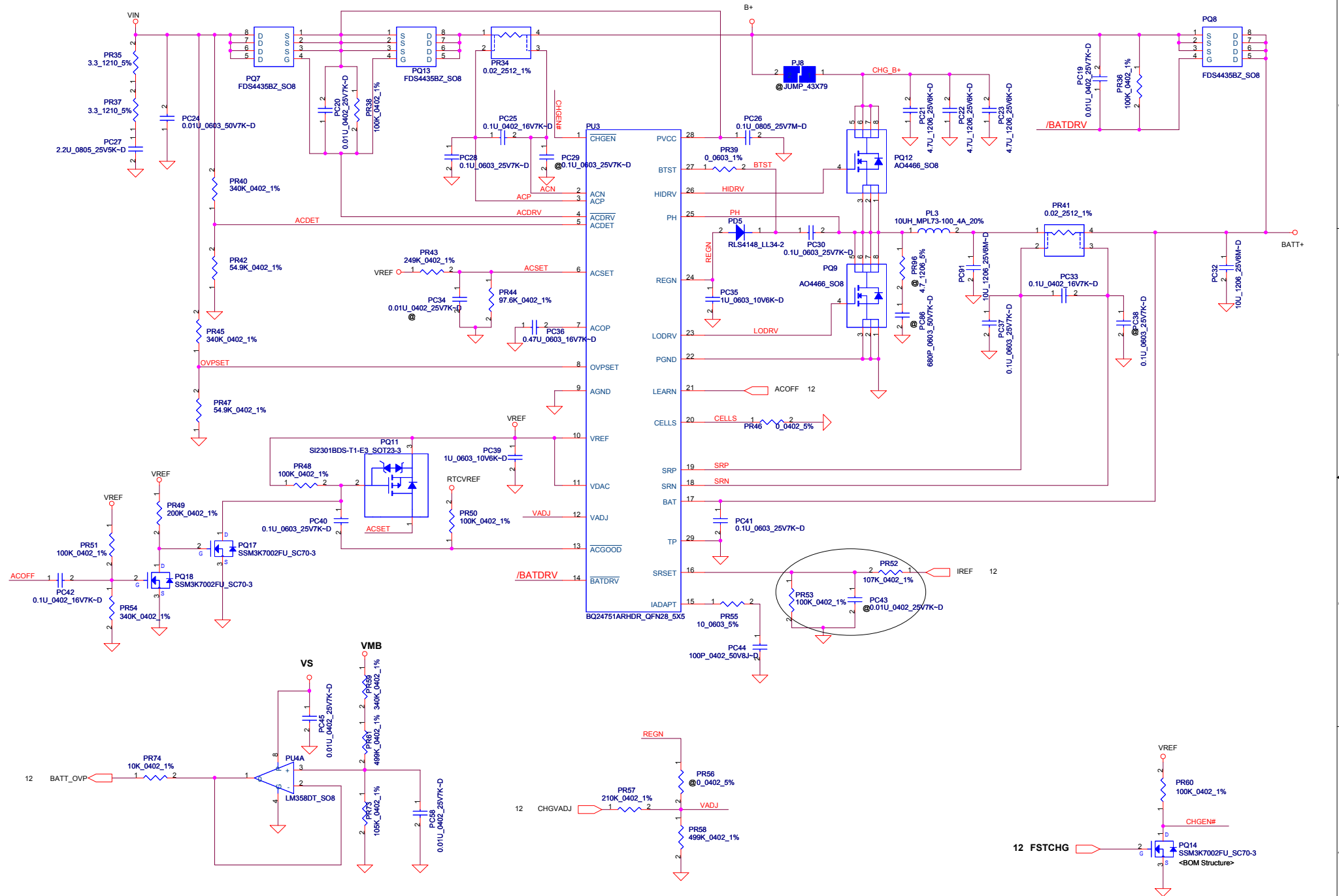
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Issued Date		Deciphered Date	Title DCIN/DECTOR	
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			Date: Tuesday, January 13, 2009	Rev 0.3
			Sheet 16	of 22

PH1 under CPU bottom side :
 CPU thermal protection at 92 degree C
 Recovery at 56 degree C

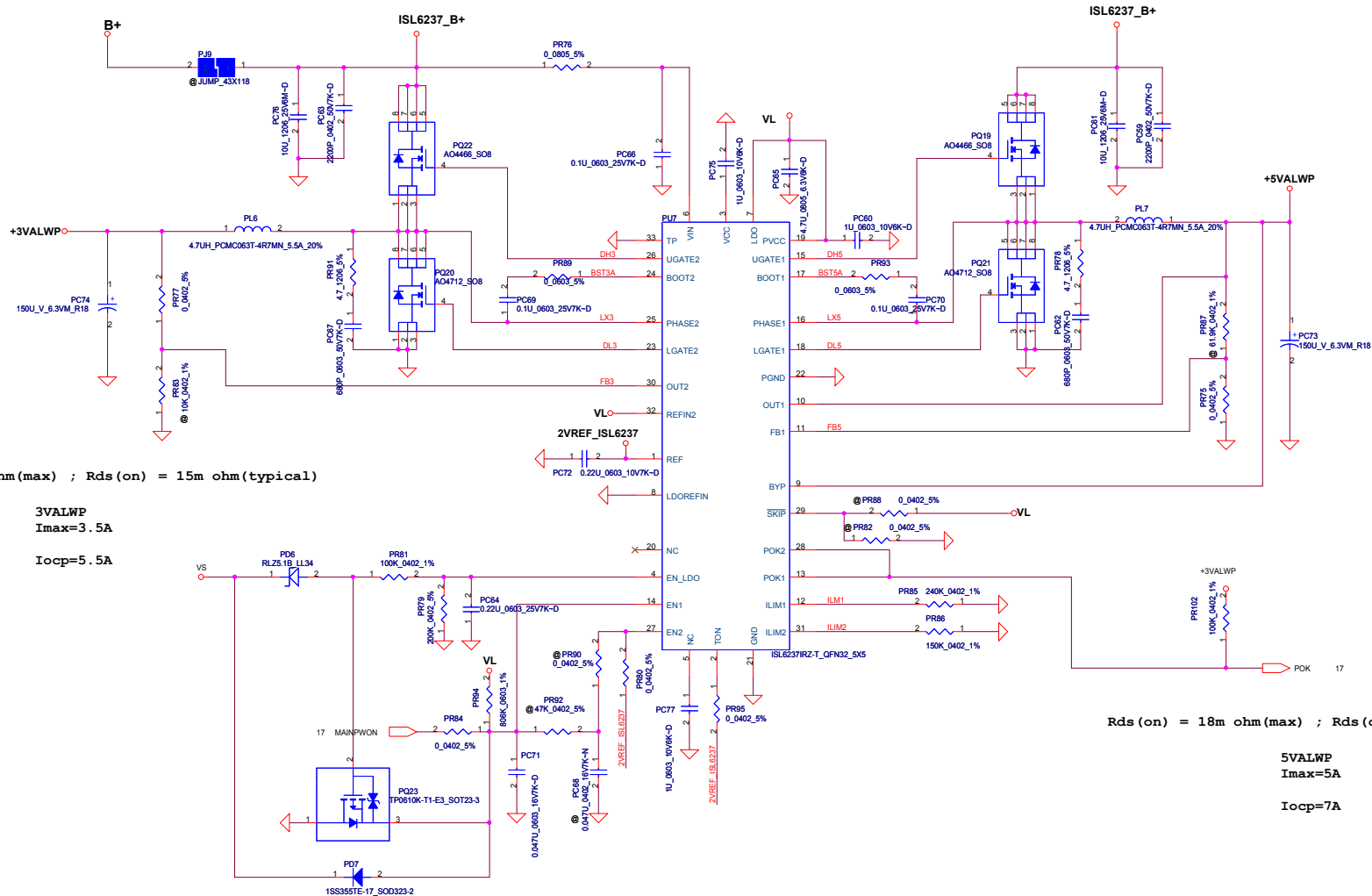


Vin Detector		
High	18.384	17.901
Low	17.728	16.976

Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date		Deciphered Date	Title BATTERY CONN / OTP	
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Size	Document Number KIU10 LA-4761P	Rev 1.0	Date: Tuesday, January 13, 2009	Sheet 17 of 22



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Date: Tuesday, January 13, 2009				Rev 0.3
Sheet 18 of 22				



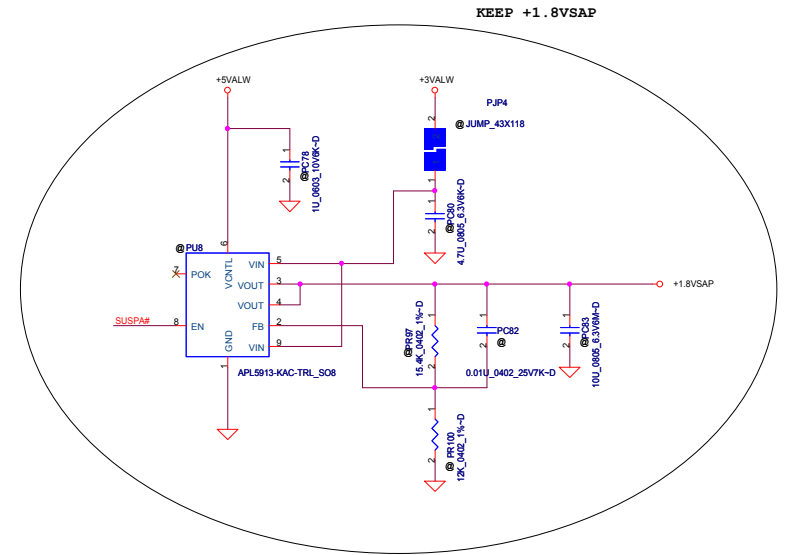
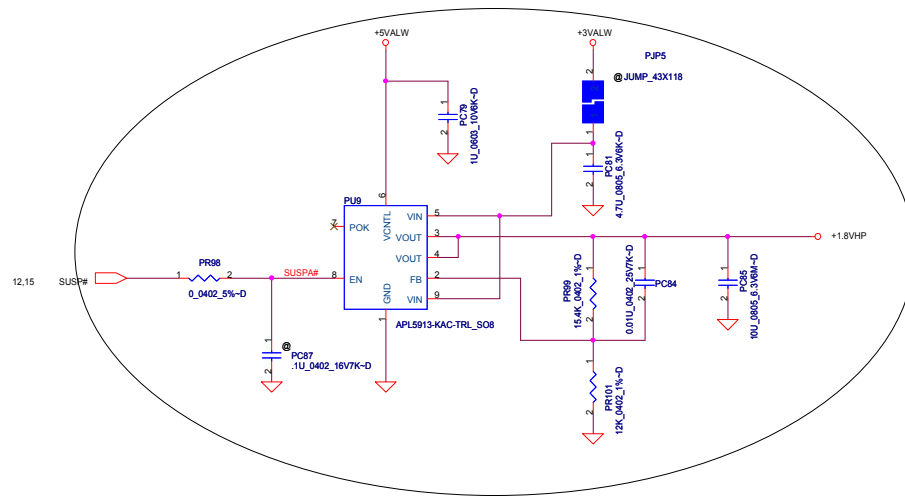
Rds(on) = 18m ohm(max) ; Rds(on) = 15m ohm(typical)

3VALWP
 Imax=3.5A
 Iocp=5.5A

Rds(on) = 18m ohm(max) ; Rds(on) = 15m ohm(typical)

5VALWP
 Imax=5A
 Iocp=7A

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				+3VALWP/+5VALWP	
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Size	Document Number			Rev	
C	KIU10 LA-4761P			0.3	
Date:	Tuesday, January 13, 2009	Sheet	20	of	22

NO	DATE	PAGE	MODIFICATION LIST	PURPOSE
1	0907	03	del C729,C730,C727,C728	MODIFY HDMI CLK.
2	0907	03	J5,J6,J11 del bom structure	default
3	0907	06	RTS158E's pin11 and Pin9 should be isolate,define pin23,pin26	vendor suggestion
4	0907	05	modify R1113,R1114,ADD L65 L66 D51,	vendor suggestion
5	0918	03	add L6C debug port	vendor require
6	0918	15	modify Q30 P/N as same Q8	for same P/N
7	0918	12	del R458,C539,R1008,add R1150,R1151,R1152	Correct BOM
8	0918	06	add bom structure # R1032,C731	Correct BOM
9	0918	14	add bom structure # U39,R1128,R1129	Correct BOM
10	0918	18	adjust PR52,PR53,PC43	Power team require
11	0918	20	add bom structure # page 20	Power team require
12	0918	06	R1030,R1031 change to 0 ohm,R460,R466 change to 33 ohm,R454,R438 change to 0603 package,	correct schematic
13	0918	06	Add one 0.1uF (C525)and 1uF (C826,0603) on JP59 pin 10 for +USB_VCC	correct schematic
14	0918	06	change p/n to SM070001700	correct schematic
15	0918	15	del J4 bom structure	correct schematic
16	0918	16	del PJ15 bom structure	correct schematic
17	0919	06	u25 short pin8 and pin11/33 together	correct schematic
18	0919	13	u12 pin3 connector to pin 4	correct schematic
19	0922	05	add bom structure # C804	vendor require
20	0922	13	CHANGE C134,C135,C138 FROM +USB_VCCA TO +USB_VCCC	noise issue when s3 resume
21	0923	16	merge +1.8VDA ,+1.8VM	FOR PORT 3 USB VOLTAGE
22	0924	10	add +1.8Vsc Voltage	correct schematic
23	0924	05	SET MIC2 BOM #	me require
24	0924	14	change JP20 pin defin	correct schematic
25	0924	08	add LED invert switch circuit	correct schematic
26	0924	03	Add R1161 on JP5 pin10 for LPC_FRAME#	correct schematic
27	0924	09	Change 0ohm location to L*	correct schematic
28	0924	09	C809 move to page by 3	correct schematic
29	0924	05	Del MIC1	correct schematic
30	0924	12	add R1150 for CLK_PCI_LPC	correct schematic
31	1107	04	change R573 from 120 ohm to 470 ohm	correct schematic
32	1107	06	change C511, C515 from 6P to 22P	LED is too light
33	1107	07	change C59, C60 from 27P to 33P	correct schematic
34	1107	10	change C631, C632 from 22P to 33P	correct schematic
35	1107	12	change C126, C127 from 15P to 27P	correct schematic
36	1108	03	change R1130 from 0 ohm to 22 ohm	correct schematic
37	1108	12	Remove R1033 and C733	correct schematic
38	1109	07	Add Orange LED	correct schematic
39	1109	12	Add pull high resistor to KS01 and KS02	correct schematic
40	1110	03	Add C834,C835 (12pF) for EMI	Correct schematic
41	1110	14	Add C836,C837,C838,C839,C840 (12pF) for EMI	For EMI
42	1110	12	Key board signal reverse	key board signal reverse
43	1110	12	ADD R94,R96 pull high resistor to +5VS	Correct schematic
44	1111	11	Add J13 to connect +3VS to +3V_TV	TV turner issue
45	1111	11	JP19 signal reverse	Correct schematic
46	1113	14	Remove R1067,R1068,R1069	Correct schematic
47	1113	08	Add C751,C752 for EMI	For EMI
48	1202	03	Remove R1009,R1132,C833	For Audio can not work issue
49	1202	03	Change R1152 from 10K ohm to 1.5M ohm	For Audio can not work issue
50	1202	14	reverse T/P CONN signal	Correct schematic
51	1209	04	Bridge DA1 and DA2 to +5V_ALM	Correct schematic
52	1209	04	Add and gate to P1T_RST# to 3G function	TV turner issue
53	1211	07	Add Bead for EMI on +3V_LAN to LAN connect	For EMI
54	1212	09	change C592 from 0.1u to 1u	Correct schematic
55	1212	11	change JP54 power from +3VS to +3V_TV	Correct schematic
56	1215	04	change R573 from 470 to 820	LED too light issue
57	1216	14	Add R105 (0 Ohm) to cut off FSEL#SPIC# to JP22&U18	Correct schematic
58	1217	08	Change R1007 (0ohm) to L33(FBMA-L11-160808-800LMT)	For EMI
59	1217	03	change U8 circuit design	For EMI
60	0102	03	Remove U8 circuit design and Add R1153	Correct schematic

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