

UM3B/UM6B SYSTEM BLOCK DIAGRAM

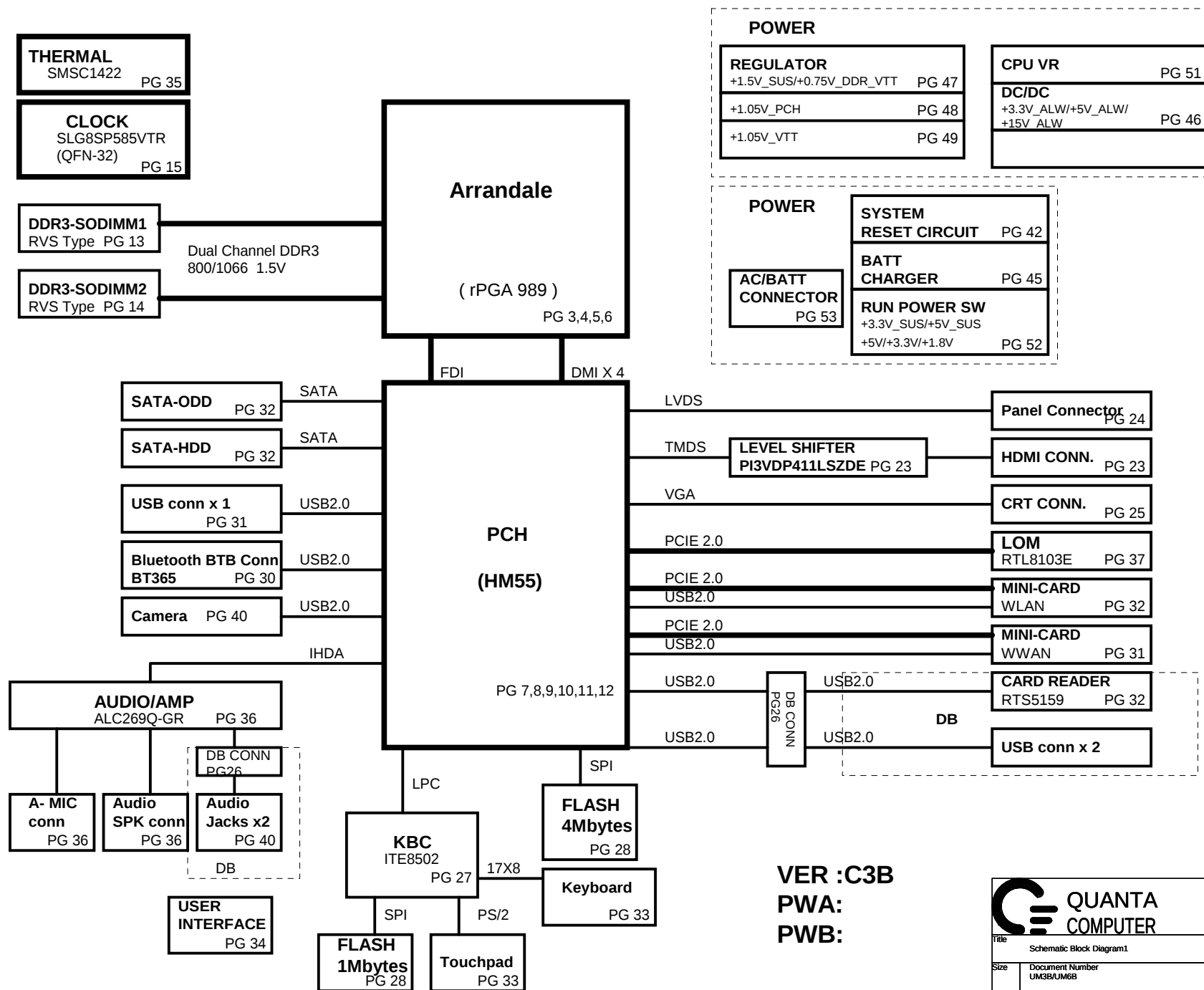


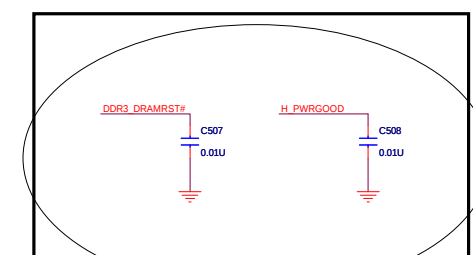
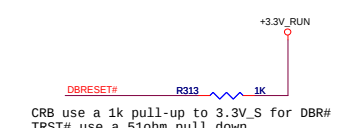
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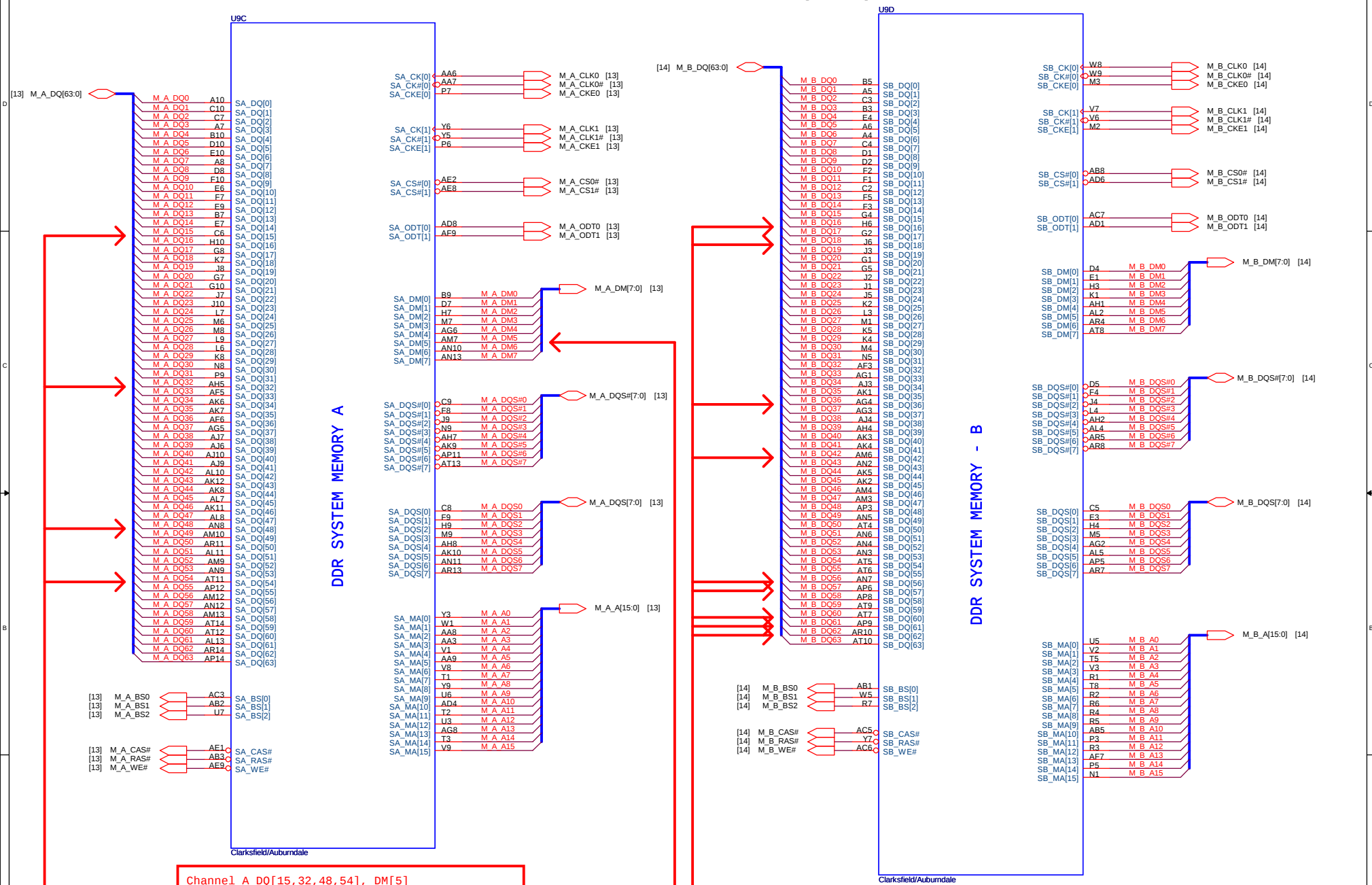
Power States

POWER PLANE	VOLTAGE	PAGE	DESCRIPTION	CONTROL SIGNAL	ACTIVE IN
+PWR_SRC	10V~+19V	24,30,45,46,47,48,49,50,51	MAIN POWER		S0~S5
+RTC_CELL	+3.0V~+3.3V	08,11,29,30	RTC		S0~S5
+5V_ALW2	+5V	37,46,52,53	LARGE POWER	MAIN POWER	S0~S5
+5V_ALW	+5V	13,33,44,46,47,48,49,50,51,52	LARGE POWER	ALW_ON	S0~S5
+3.3V_ALW	+3.3V	29,30,35,36,37,42,44,45,46,47,51,52,53	8051 POWER	3.3V_ALW_ON	S0~S5
+5V_SUS	+5V	11,33,34,37,51,52	SLP_S5# CTRLD POWER	SUS_ON	
+3.3V_SUS	+3.3V	07,08,09,10,11,13,14,19,24,28,29,37,41,42,44,48,49,50,52	SLP_S5# CTRLD POWER	SUS_ON	
+1.5V_SUS	+1.5V	03,05,13,14,47,50,52	SODIMM POWER	SUS_ON	
+0.75V_DDR_VTT	+0.75V	13,14,47,52	SODIMM POWER	RUN_ON	
+5V_RUN	+5V	11,18,24,25,35,36,38,39,40,51,52	SLP_S3# CTRLD POWER	RUN_ON	
+3.3V_RUN	+3.3V	3,7,8,9,10,11,13,14,15,17,24,25,26,28,29,30,31,32,33,35,37,38,39,40,41,42,46,51,52,60	SLP_S3# CTRLD POWER	RUN_ON	
+1.8V_RUN	+1.8V	05,11,44,52	SDVO POWER	RUN_ON	
+1.05V_VTT	+1.1V	03,05,10,11,49,60	CPU POWER	RUN_ON	
+1.5V_RUN	+1.5V	11,28,31,32,52	Express Card/Min Card	RUN_ON	
+5V_HDD	+5V	35	HDD Power	HDDC_EN	
+1.05V_PCH	+1.05V	08,09,11,15,48	PCH POWER	RUN_ON	
+VCC_CORE	+0.7V~+1.77V	05,51	CPU CORE POWER	IMVP_VR_ON	
+LCDVCC	+3.3V	24	LCD Power	LCDVCC_TST_EN & ENVDD	
+5V_MOD	+5V	35	MOD Power	MODC_EN	

GND PLANE	PAGE	DESCRIPTION
 GND	ALL	

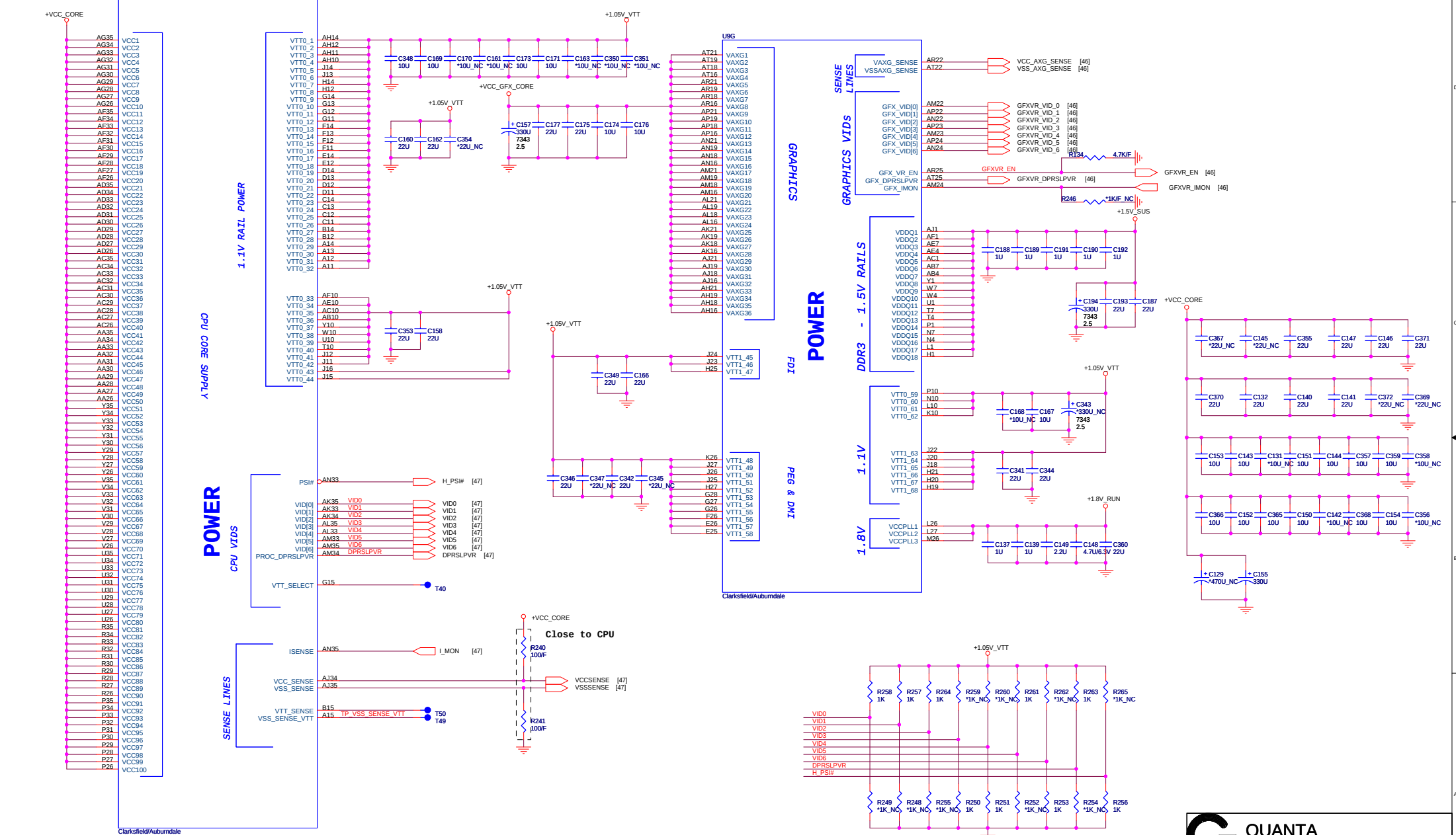


AUBURNDALE/CLARKSFIELD PROCESSOR (DDR3)



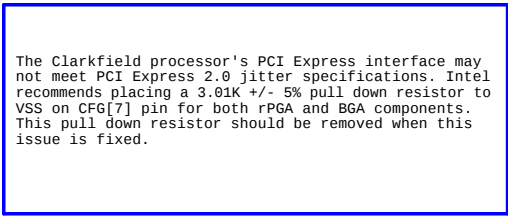
CPU Core Power

AUBURNDALE/CLARKSFIELD PROCESSOR (POWER)

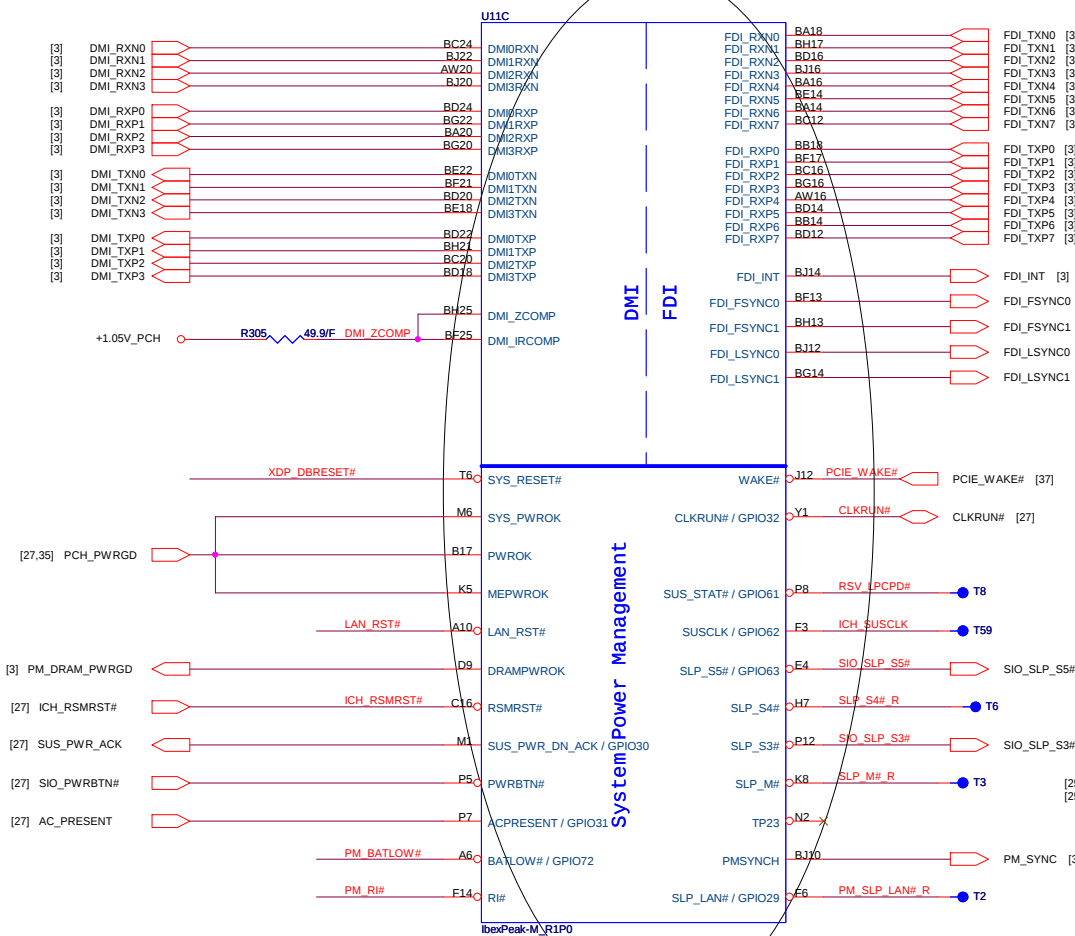


AUBURNDALE/CLARKSFIELD PROCESSOR (POWER)

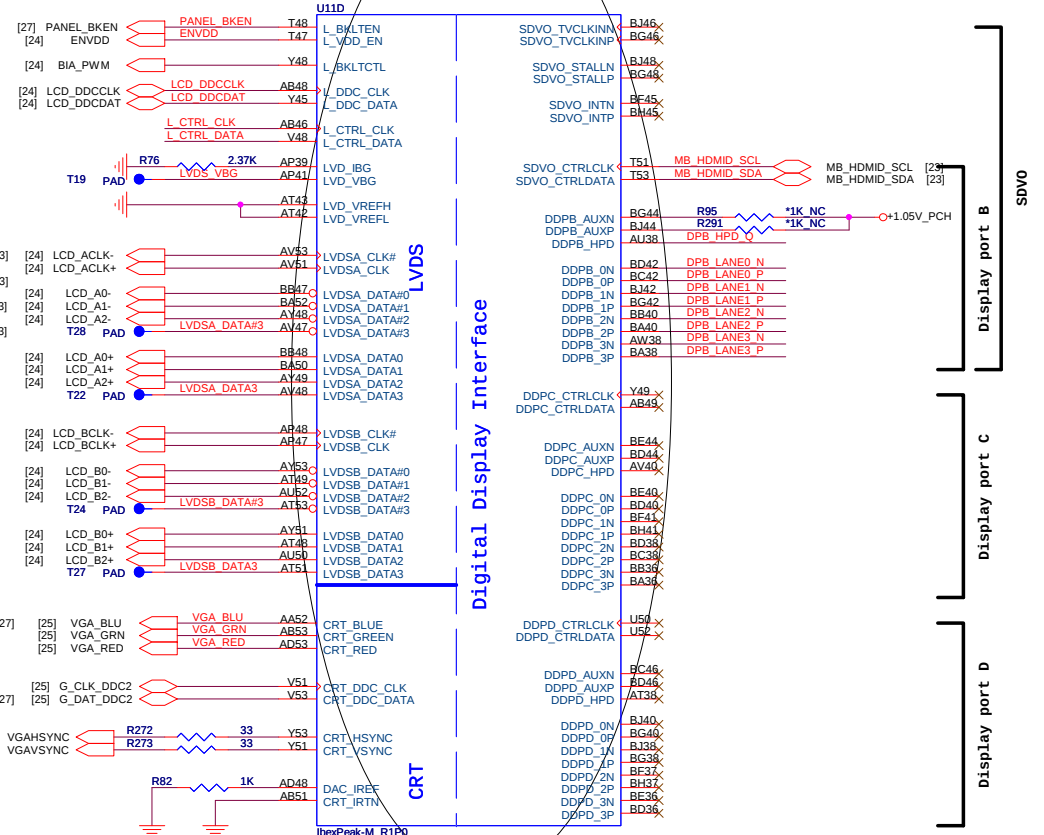
	2	1
AUBURNDALE/CLARKSFIELD PROCESSOR(RESERVED, CFG)		



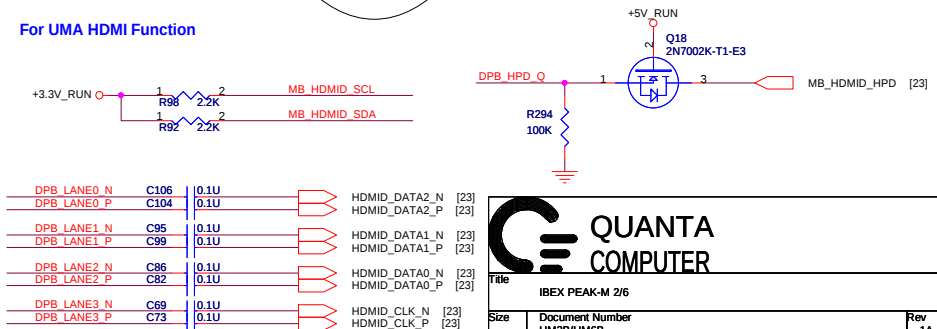
IBEX PEAK-M (DMI, FDI, GPIO)



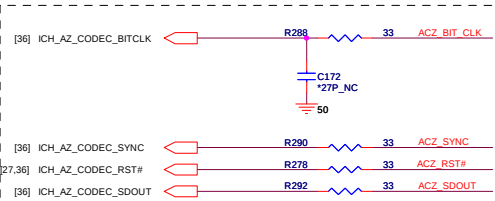
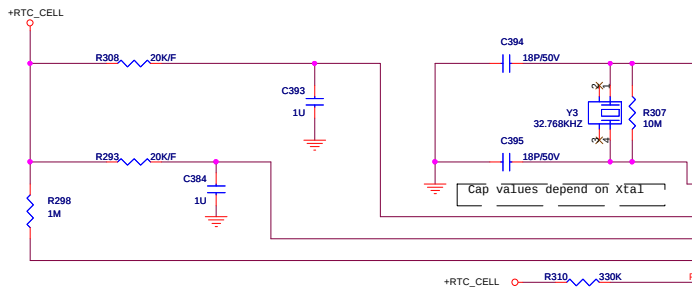
IBEX PEAK-M (LVDS, DDI)



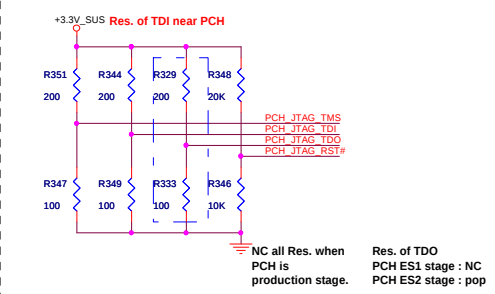
For UMA HDMI Function



IBEX PEAK-M (HDA, JTAG, SATA)



Place all series terms close to PCH except for SDIN input lines, which should be close to source. Placement of R773, R775, R776 & R777 should equal distance to the T split trace point. Basically, keep the same distance from T for all series termination resistors.

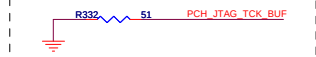


[INVTRMEN] Internal Voltage Regulator Enable. This signal enables the internal 1.05 V regulators. This signal must be always pulled-up to VccRTC.

Flash Descriptor Security Override

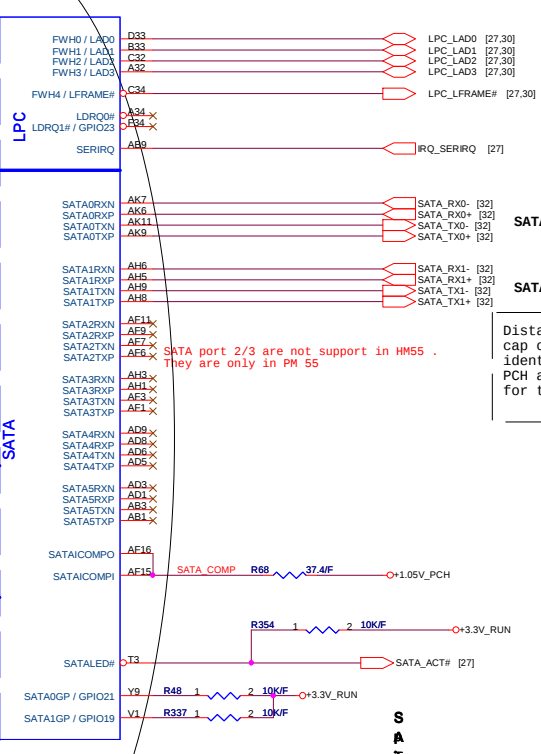
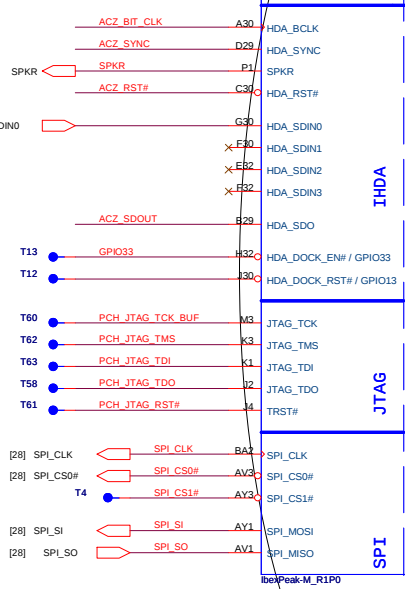
GPI033	Low = Enabled High = Disabled
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Note : GPI033 is a signal used for Flash Descriptor Security Override/ME Debug Mode. This signal should be only asserted low through an external pull-down in manufacturing or debug environments ONLY.



Note : Only pop when PCH is production stage & need "JTAG boundary Scan". Remember to depop XDP side Res.

JTAG Test Pads are need to put on the same side of mother board.

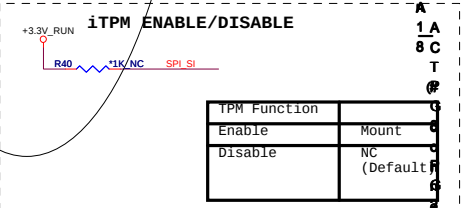


SATA port 2/3 are not support in HM55 . They are only in PM 55

SATA HDD

SATA ODD

Distance between the PCH and cap on the "P" signal should be identical distance between the PCH and cap on the "N" signal for the same pair.



TPM Function	
Enable	Mount
Disable	NC (Default)

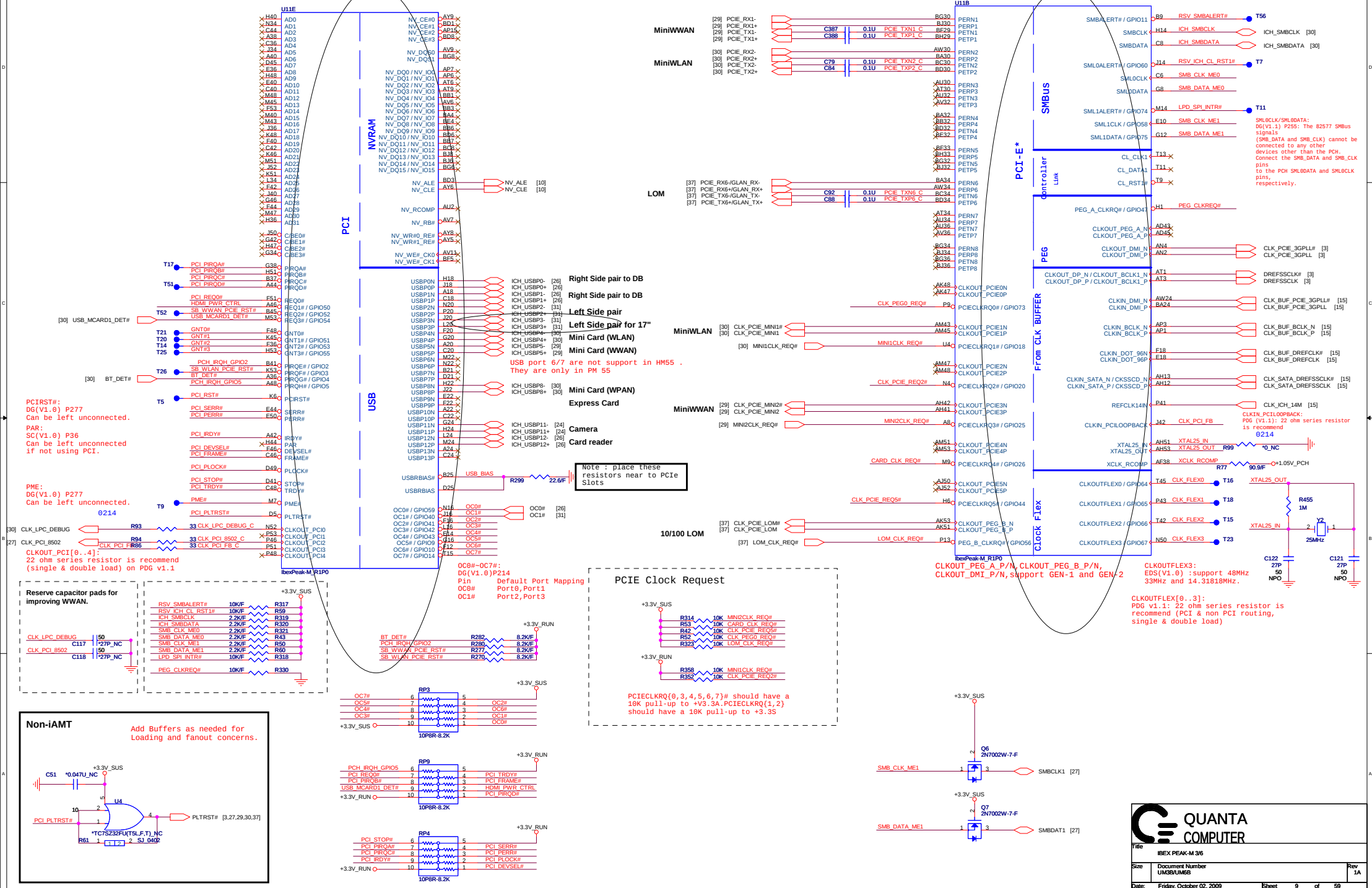
QUANTA COMPUTER

IBEX PEAK-M 1/6

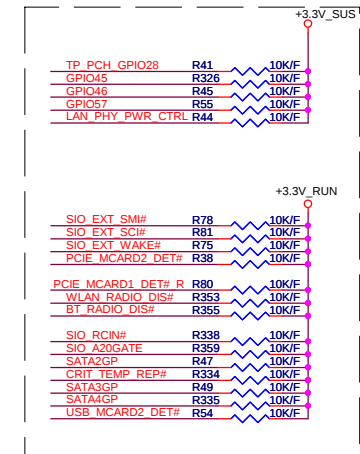
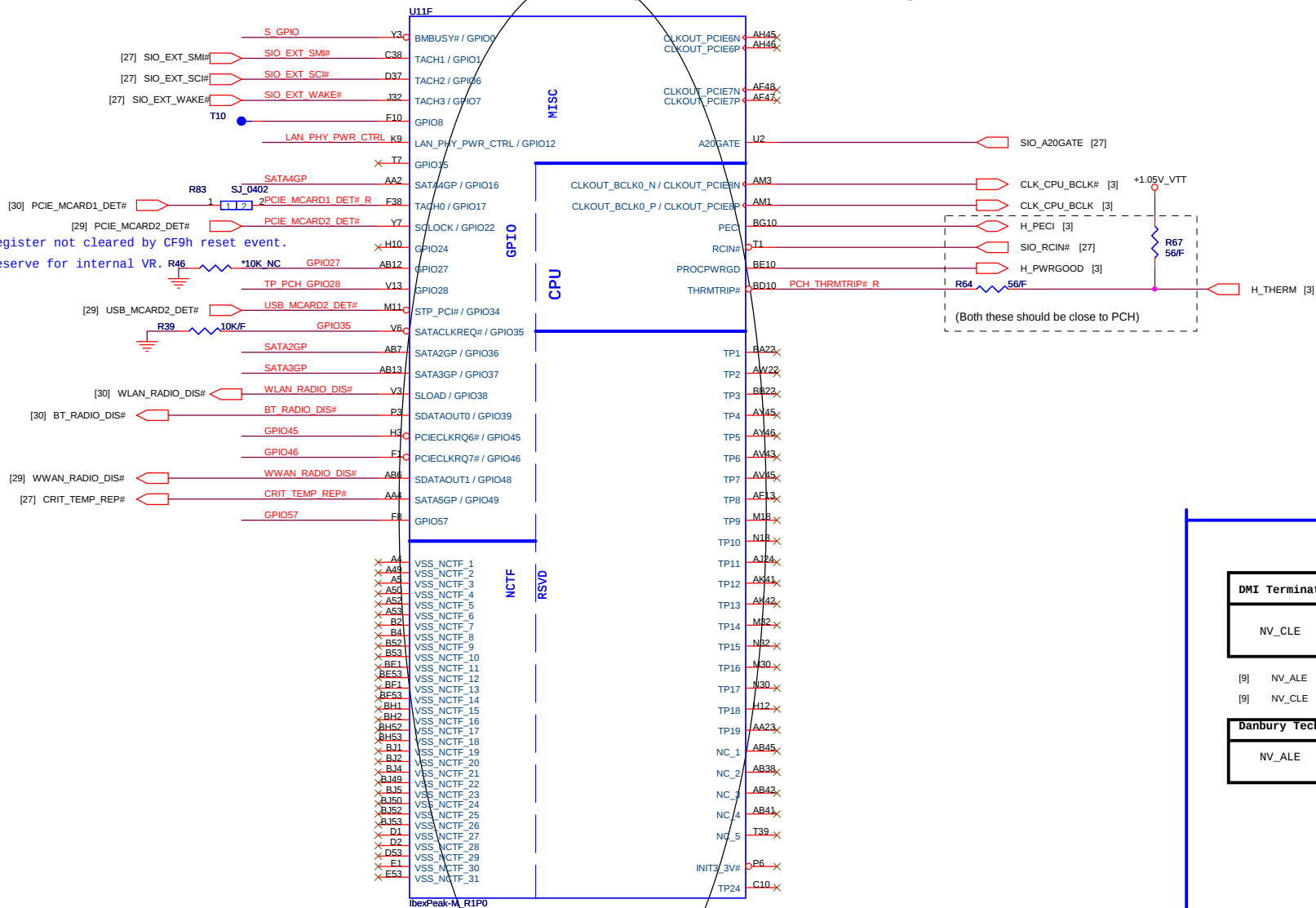
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IBEX PEAK-M (PCI-E, SMBUS, CLK)



~~IBEX PEAK-M (GPIO, VSS_NCTF, RSVD)~~



DMI Termination Voltage	
NV_CLE	Set to Vcc when LOW Set to Vcc/2 when HIGH

Danbury Technology Enabled	
NV_ALE	High = Enable Low = Disable

Schematic diagram of the WWAN Radio Disabling circuit. The circuit shows two input signals, **S GPIO** and **WWAN_RADIO_DIS#**, each connected to a resistor (R336 and R36 respectively) which is then connected to a 10K pull-up resistor leading to a **+3.3V_RUN** supply.

BMBUSY#:
If not used, require a weak pull-up (8.2- K Ω to 10 k Ω) to Vcc3_3.
CRB(v1.6)P28: it has 1K PU and 100 ohm on this net for validation purpose.

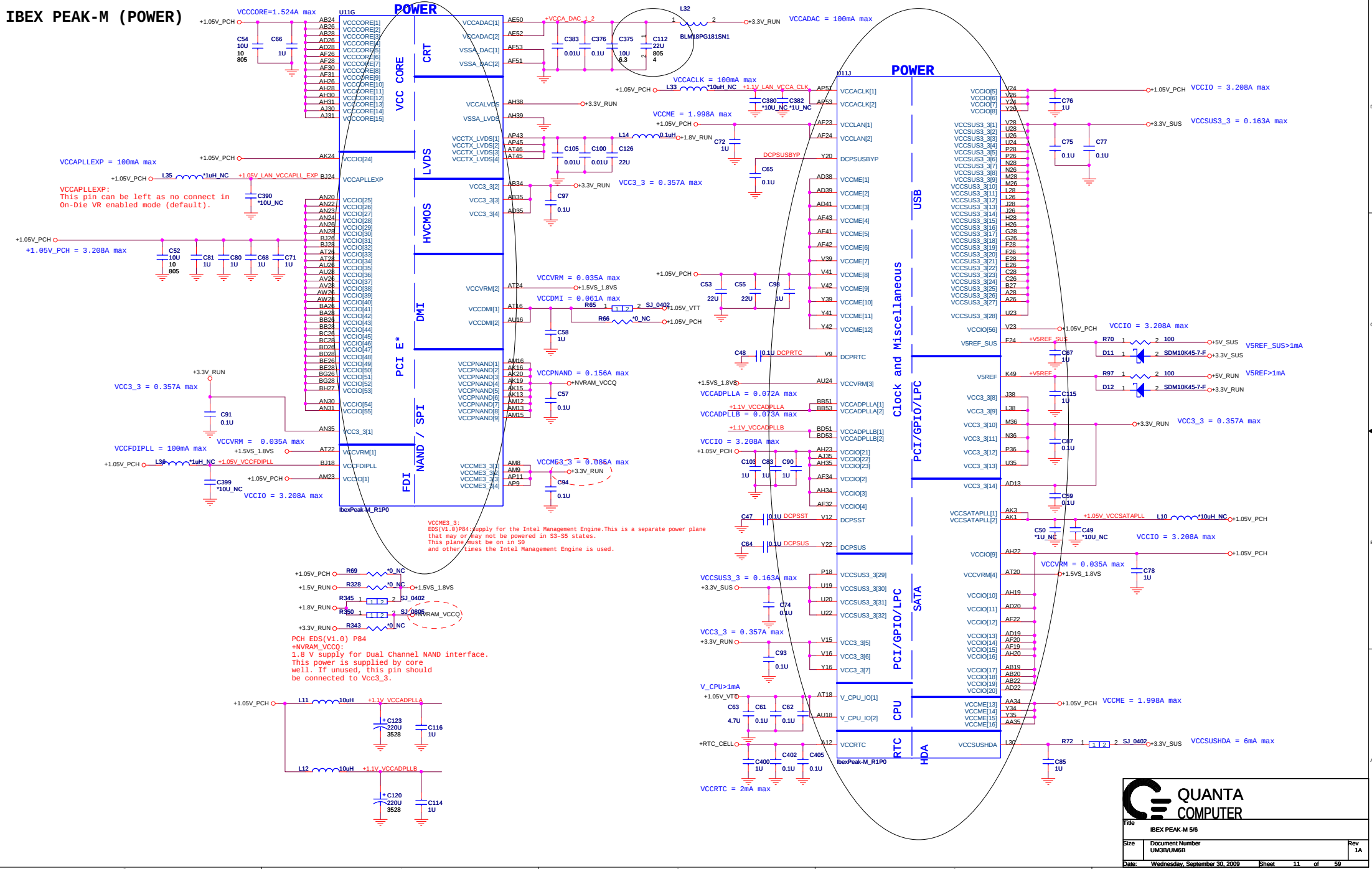
BMBUSY#:(Intel feedback)
Follow CRB checklist, 1K is
for intel BIOS validation purpose.

WWAN_RADIO_DIS#	1-X High = Strong (Default)
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IBEX PEAK-M (POWER)

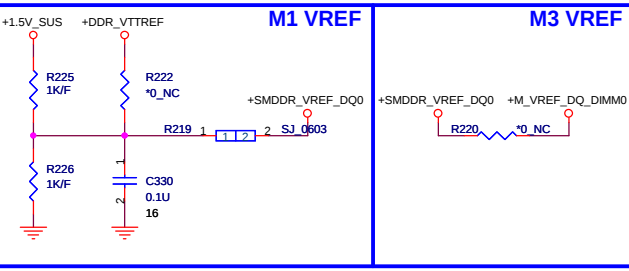
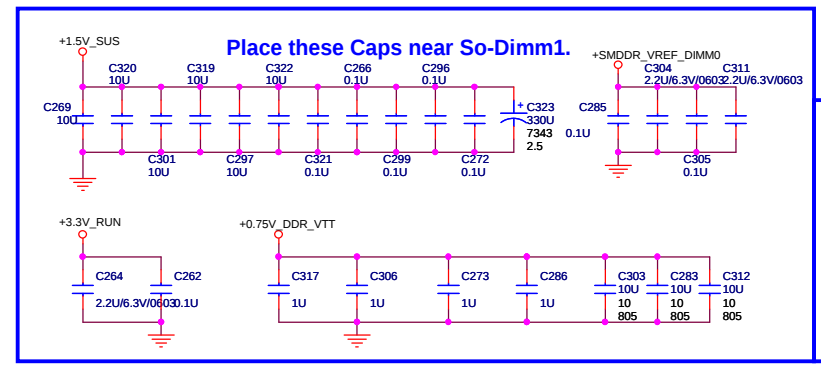
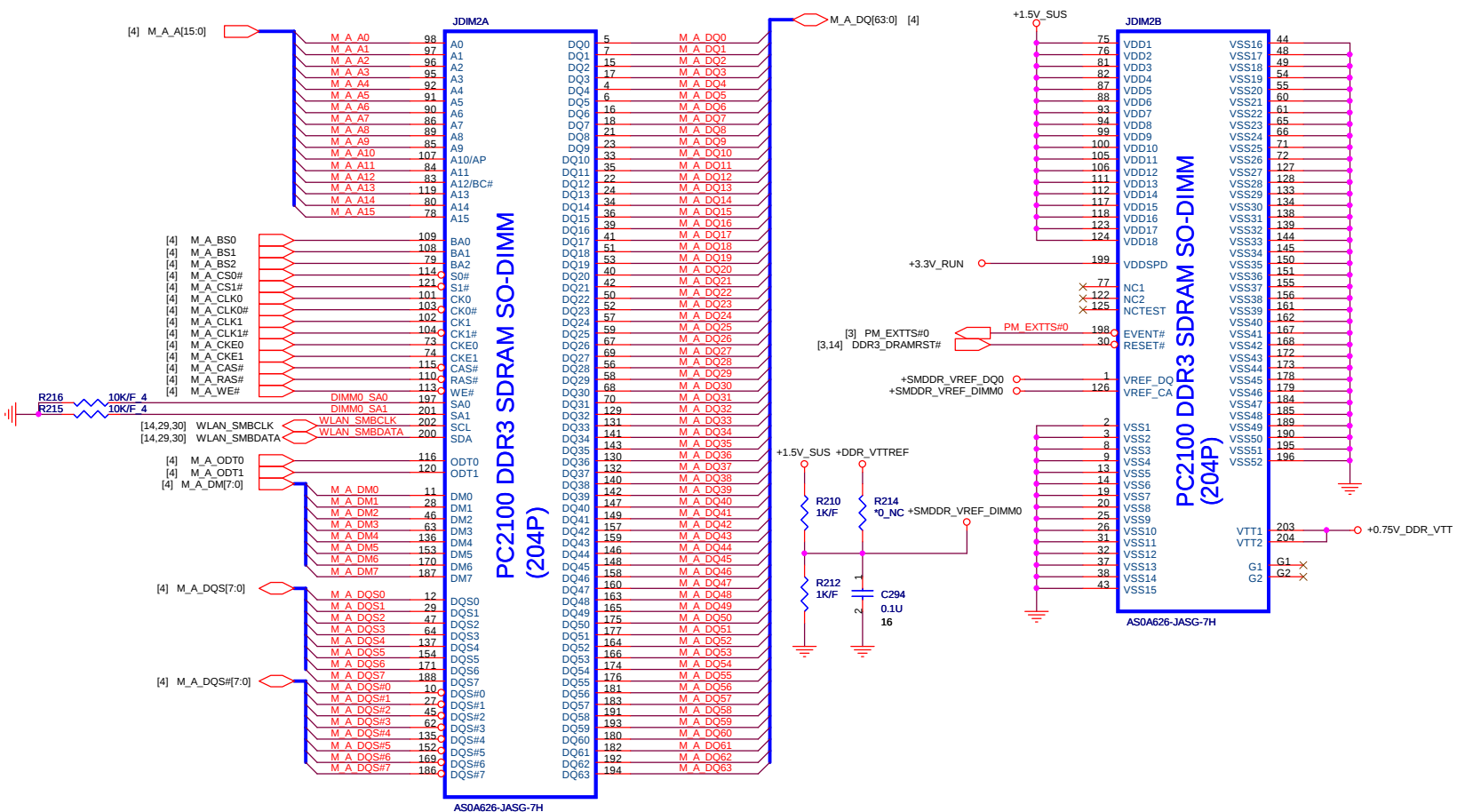


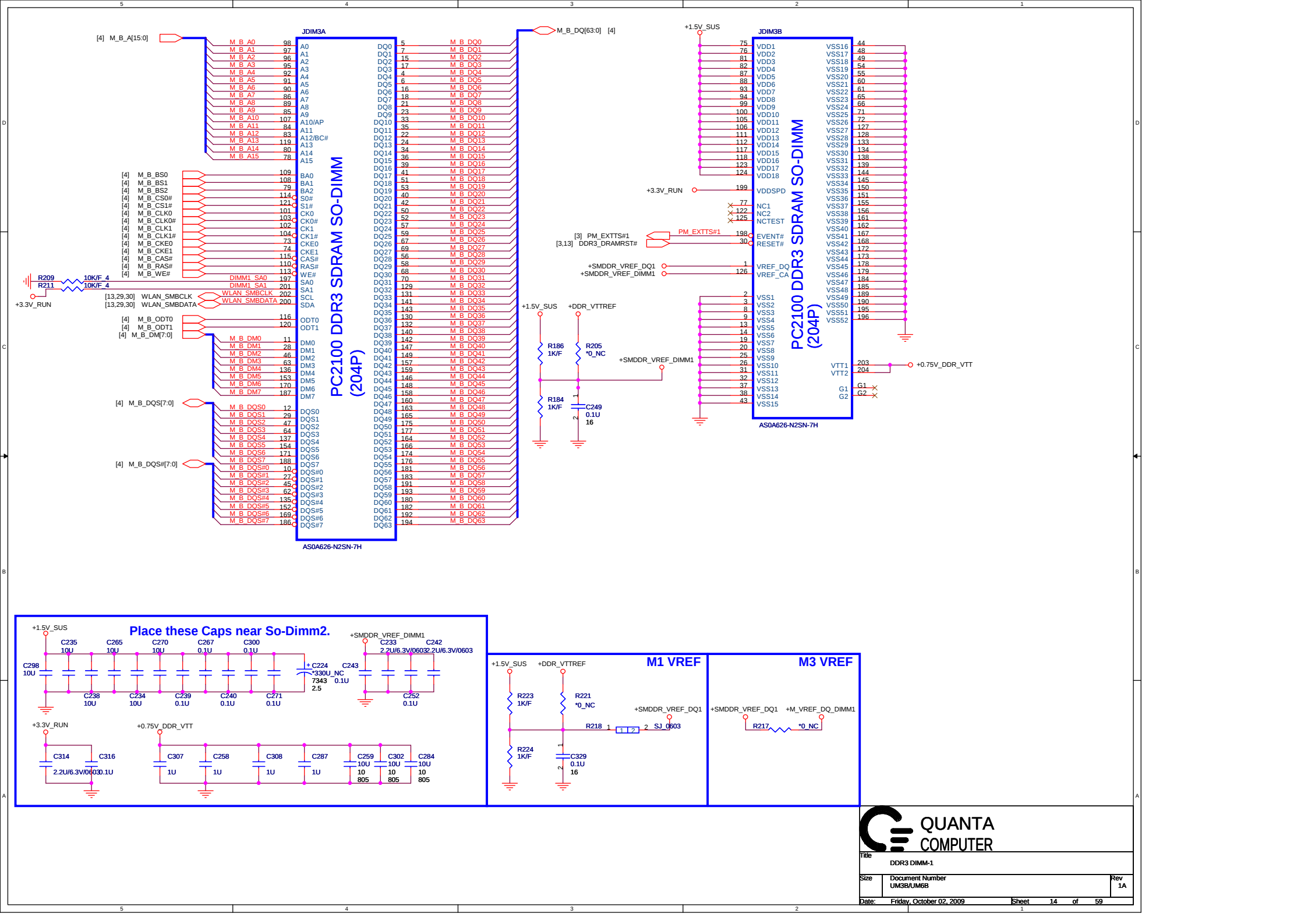
Title			
IBEX PEAK-M 5/6			
Size	Document Number	Rev	
	UM3B/UM6B	1A	
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IBEX PEAK-M (GND)

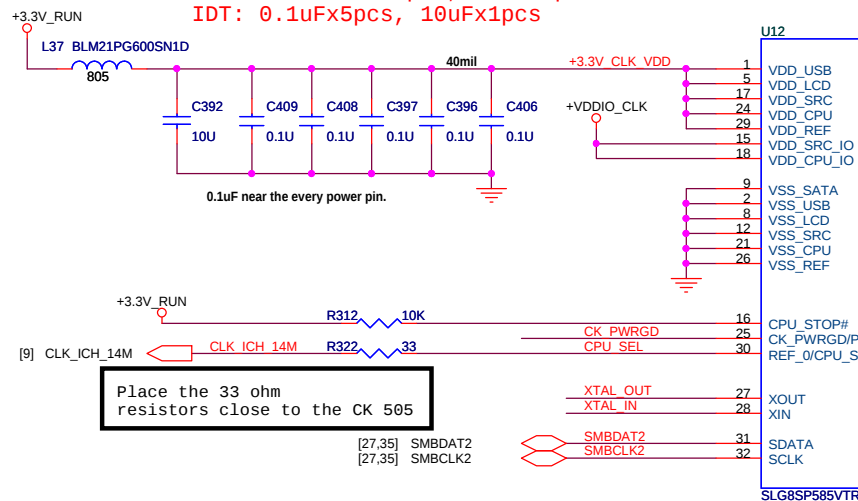
AB16	VSS[0]		
AA19	VSS[1]	VSS[80]	AK30
AA20	VSS[2]	VSS[81]	AK32
AA22	VSS[3]	VSS[82]	AK34
AA15	VSS[4]	VSS[83]	AK36
AA24	VSS[5]	VSS[84]	AK38
AA26	VSS[6]	VSS[85]	AK40
AA28	VSS[7]	VSS[86]	AK42
AA30	VSS[8]	VSS[87]	AK44
AA32	VSS[9]	VSS[88]	AK46
AB17	VSS[10]	VSS[89]	AK48
AB18	VSS[11]	VSS[90]	AK50
AB19	VSS[12]	VSS[91]	AK52
AB20	VSS[13]	VSS[92]	AK54
AB21	VSS[14]	VSS[93]	AK56
AB22	VSS[15]	VSS[94]	AK58
AB23	VSS[16]	VSS[95]	AK60
AB24	VSS[17]	VSS[96]	AK62
AB25	VSS[18]	VSS[97]	AK64
AB26	VSS[19]	VSS[98]	AK66
AB27	VSS[20]	VSS[99]	AK68
AB28	VSS[21]	VSS[100]	AK70
AB29	VSS[22]	VSS[101]	AK72
AB30	VSS[23]	VSS[102]	AK74
AB31	VSS[24]	VSS[103]	AK76
AB32	VSS[25]	VSS[104]	AK78
AB33	VSS[26]	VSS[105]	AK80
AB34	VSS[27]	VSS[106]	AK82
AB35	VSS[28]	VSS[107]	AK84
AB36	VSS[29]	VSS[108]	AK86
AB37	VSS[30]	VSS[109]	AK88
AB38	VSS[31]	VSS[110]	AK90
AB39	VSS[32]	VSS[111]	AK92
AB40	VSS[33]	VSS[112]	AK94
AB41	VSS[34]	VSS[113]	AK96
AB42	VSS[35]	VSS[114]	AK98
AB43	VSS[36]	VSS[115]	AK100
AB44	VSS[37]	VSS[116]	AK102
AB45	VSS[38]	VSS[117]	AK104
AB46	VSS[39]	VSS[118]	AK106
AB47	VSS[40]	VSS[119]	AK108
AB48	VSS[41]	VSS[120]	AK110
AB49	VSS[42]	VSS[121]	AK112
AB50	VSS[43]	VSS[122]	AK114
AB51	VSS[44]	VSS[123]	AK116
AB52	VSS[45]	VSS[124]	AK118
AB53	VSS[46]	VSS[125]	AK120
AB54	VSS[47]	VSS[126]	AK122
AB55	VSS[48]	VSS[127]	AK124
AB56	VSS[49]	VSS[128]	AK126
AB57	VSS[50]	VSS[129]	AK128
AB58	VSS[51]	VSS[130]	AK130
AB59	VSS[52]	VSS[131]	AK132
AB60	VSS[53]	VSS[132]	AK134
AB61	VSS[54]	VSS[133]	AK136
AB62	VSS[55]	VSS[134]	AK138
AB63	VSS[56]	VSS[135]	AK140
AB64	VSS[57]	VSS[136]	AK142
AB65	VSS[58]	VSS[137]	AK144
AB66	VSS[59]	VSS[138]	AK146
AB67	VSS[60]	VSS[139]	AK148
AB68	VSS[61]	VSS[140]	AK150
AB69	VSS[62]	VSS[141]	AK152
AB70	VSS[63]	VSS[142]	AK154
AB71	VSS[64]	VSS[143]	AK156
AB72	VSS[65]	VSS[144]	AK158
AB73	VSS[66]	VSS[145]	AK160
AB74	VSS[67]	VSS[146]	AK162
AB75	VSS[68]	VSS[147]	AK164
AB76	VSS[69]	VSS[148]	AK166
AB77	VSS[70]	VSS[149]	AK168
AB78	VSS[71]	VSS[150]	AK170
AB79	VSS[72]	VSS[151]	AK172
AB80	VSS[73]	VSS[152]	AK174
AB81	VSS[74]	VSS[153]	AK176
AB82	VSS[75]	VSS[154]	AK178
AB83	VSS[76]	VSS[155]	AK180
AB84	VSS[77]	VSS[156]	AK182
AB85	VSS[78]	VSS[157]	AK184
AB86	VSS[79]	VSS[158]	AK186

U11	VSS[159]	VSS[259]	H49
B11	VSS[160]	VSS[260]	H5
B15	VSS[161]	VSS[261]	J24
B17	VSS[162]	VSS[262]	K11
B19	VSS[163]	VSS[263]	K43
B21	VSS[164]	VSS[264]	K47
B23	VSS[165]	VSS[265]	L14
B25	VSS[166]	VSS[266]	L18
B27	VSS[167]	VSS[267]	L2
B29	VSS[168]	VSS[268]	L22
B31	VSS[169]	VSS[269]	L26
B33	VSS[170]	VSS[270]	L36
B35	VSS[171]	VSS[271]	L40
B37	VSS[172]	VSS[272]	L52
B39	VSS[173]	VSS[273]	M12
B41	VSS[174]	VSS[274]	M16
B43	VSS[175]	VSS[275]	M20
B45	VSS[176]	VSS[276]	M24
B47	VSS[177]	VSS[277]	M28
B49	VSS[178]	VSS[278]	M32
B51	VSS[179]	VSS[279]	M36
B53	VSS[180]	VSS[280]	M40
B55	VSS[181]	VSS[281]	M44
B57	VSS[182]	VSS[282]	M48
B59	VSS[183]	VSS[283]	M52
B61	VSS[184]	VSS[284]	N24
B63	VSS[185]	VSS[285]	P11
B65	VSS[186]	VSS[286]	P15
B67	VSS[187]	VSS[287]	P22
B69	VSS[188]	VSS[288]	P26
B71	VSS[189]	VSS[289]	P32
B73	VSS[190]	VSS[290]	P36
B75	VSS[191]	VSS[291]	P42
B77	VSS[192]	VSS[292]	P46
B79	VSS[193]	VSS[293]	P52
B81	VSS[194]	VSS[294]	P56
B83	VSS[195]	VSS[295]	R2
B85	VSS[196]	VSS[296]	R52
B87	VSS[197]	VSS[297]	T12
B89	VSS[198]	VSS[298]	T16
B91	VSS[199]	VSS[299]	T20
B93	VSS[200]	VSS[300]	T5
B95	VSS[201]	VSS[301]	T8
B97	VSS[202]	VSS[302]	U30
B99	VSS[203]	VSS[303]	U32
B101	VSS[204]	VSS[304]	U36
B103	VSS[205]	VSS[305]	U40
B105	VSS[206]	VSS[306]	U44
B107	VSS[207]	VSS[307]	U48
B109	VSS[208]	VSS[308]	V11
B111	VSS[209]	VSS[309]	V16
B113	VSS[210]	VSS[310]	V19
B115	VSS[211]	VSS[311]	V20
B117	VSS[212]	VSS[312]	V22
B119	VSS[213]	VSS[313]	V30
B121	VSS[214]	VSS[314]	V31
B123	VSS[215]	VSS[315]	V32
B125	VSS[216]	VSS[316]	V34
B127	VSS[217]	VSS[317]	V35
B129	VSS[218]	VSS[318]	V38
B131	VSS[219]	VSS[319]	V42
B133	VSS[220]	VSS[320]	V45
B135	VSS[221]	VSS[321]	V46
B137	VSS[222]	VSS[322]	V47
B139	VSS[223]	VSS[323]	V49
B141	VSS[224]	VSS[324]	V5
B143	VSS[225]	VSS[325]	V7
B145	VSS[226]	VSS[326]	V8
B147	VSS[227]	VSS[327]	W2
B149	VSS[228]	VSS[328]	W52
B151	VSS[229]	VSS[329]	Y11
B153	VSS[230]	VSS[330]	Y12
B155	VSS[231]	VSS[331]	Y15
B157	VSS[232]	VSS[332]	Y18
B159	VSS[233]	VSS[333]	Y22
B161	VSS[234]	VSS[334]	Y28
B163	VSS[235]	VSS[335]	Y31
B165	VSS[236]	VSS[336]	Y32
B167	VSS[237]	VSS[337]	Y38
B169	VSS[238]	VSS[338]	Y43
B171	VSS[239]	VSS[339]	Y46
B173	VSS[240]	VSS[340]	Y49
B175	VSS[241]	VSS[341]	Y5
B177	VSS[242]	VSS[342]	Y8
B179	VSS[243]	VSS[343]	Y9
B181	VSS[244]	VSS[344]	Y24
B183	VSS[245]	VSS[345]	Y43
B185	VSS[246]	VSS[346]	Y49
B187	VSS[247]	VSS[347]	Y52
B189	VSS[248]	VSS[348]	Y54
B191	VSS[249]	VSS[349]	Y57
B193	VSS[250]	VSS[350]	Y62
B195	VSS[251]	VSS[351]	Y65
B197	VSS[252]	VSS[352]	Y68
B199	VSS[253]	VSS[353]	Y71
B201	VSS[254]	VSS[354]	Y74
B203	VSS[255]	VSS[355]	Y77
B205	VSS[256]	VSS[356]	Y80
B207	VSS[257]	VSS[357]	Y83
B209	VSS[258]	VSS[358]	Y86

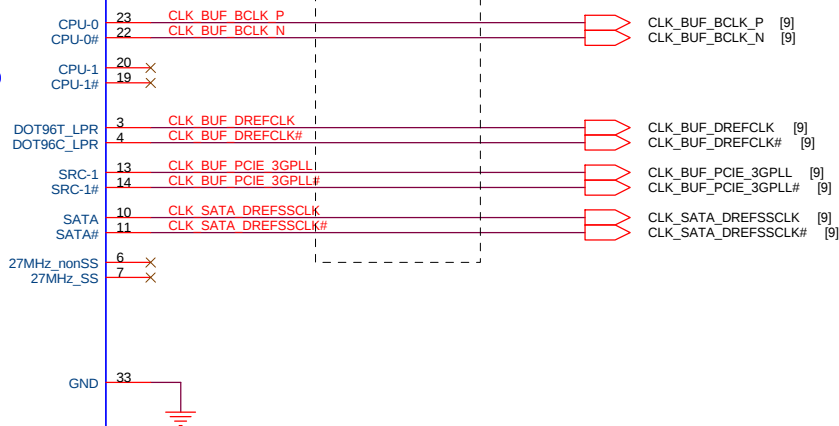




Realtek: 0.1uF x 6pcs, 22uF x 1pcs
IDT: 0.1uF x 5pcs, 10uF x 1pcs

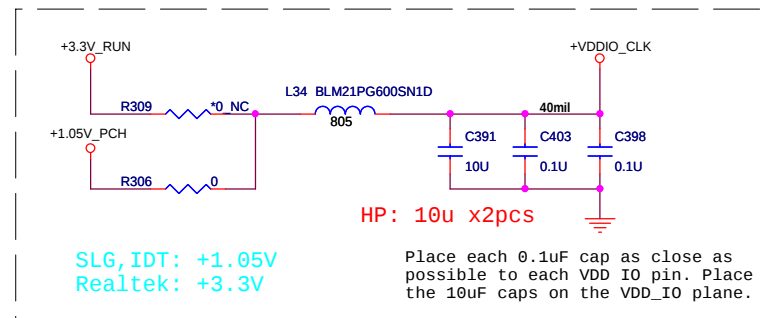


Place within 0.5" of CLKGEN



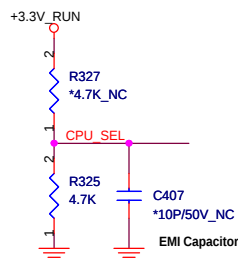
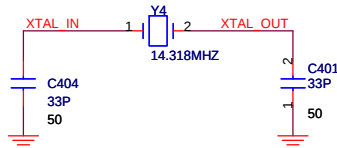
Place the 33 ohm resistors close to the CK 505

Realtek: 0.1uF x 3pcs, 22uF x 1pcs
IDT: 0.1uF x 2pcs, 10uF x 1pcs



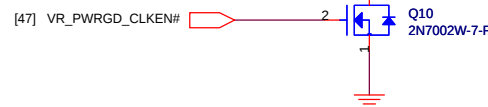
+VDDIO_CLK:
SLG date sheet (V0.2) P15: Min 1.05V, Max 3.465V.
Realtek date sheet (V1.2) P11: Min 1.05V, Max 3.3V.
IDT date sheet (V0.7) P10: Min 0.9975V, Max 3.465V.

Add capacitor pads for improving WWAN.



PIN	CPU_0	CPU_1
0 (default)	133MHz	133MHz
1 (0.7V-1.5V)	100MHz	100MHz

CPU_SEL:
SLG date sheet (V0.2) P15:
High Voltage: Min 0.7V, Max 1.5V.
Low Voltage: Min Vss-0.3V, Max 0.35V.
Realtek date sheet (V1.2) P11:
High Voltage: Min 0.7V, Max 1.5V.
Low Voltage: Min Vss-0.3V, Max 0.35V.
IDT date sheet (V0.7) P10:
High Voltage: Min 0.7V, Max 1.5V.
Low Voltage: Min Vss-0.3V, Max 0.35V.



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 QUANTA COMPUTER		
Title VGA-M92-XT (PCIe)		
Size	Document Number UM3B/UM6B	Rev 1A
Date: Wednesday, September 30, 2009		
Sheet 16 of 59		

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		QUANTA COMPUTER
VGA-MB2-XT (PCIe)		
Size	Document Number	Rev
	UM3BU/UM6B	1A
Date: Wednesday, September 30, 2009 Sheet 17 of 59		

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NUMBER SAME AS DISCRETE**

 QUANTA COMPUTER		
Title VGA-M92-XT (PCIe)		
Size	Document Number UM38/UM6B	Rev 1A
Date: Wednesday, September 30, 2009 Sheet 19 of 59		

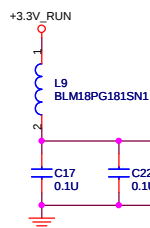
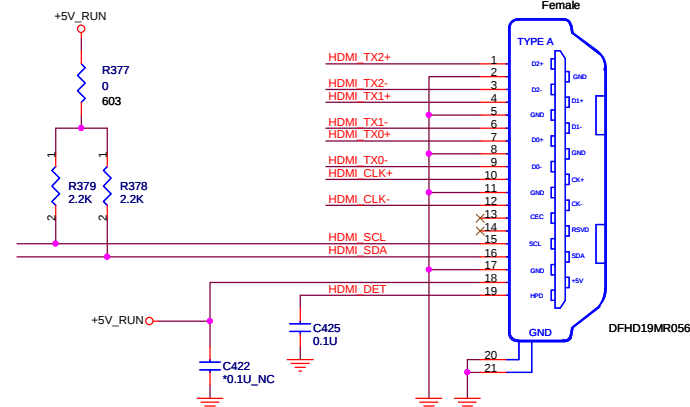
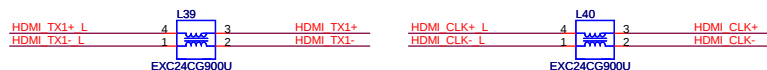
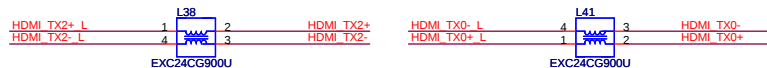
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NUMBER SAME AS DISCRETE**

 QUANTA COMPUTER			
Title VGA-M92-XT (PCIe)			
Size	Document Number UM3B/UM6B		Rev 1A
Date	Wednesday, September 30, 2009		Sheet 21 of 59

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NUMBER SAME AS DISCRETE**

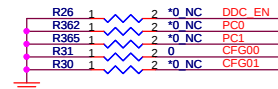
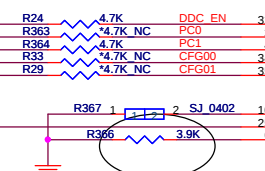
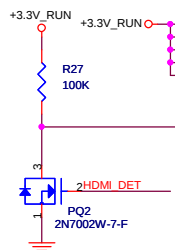
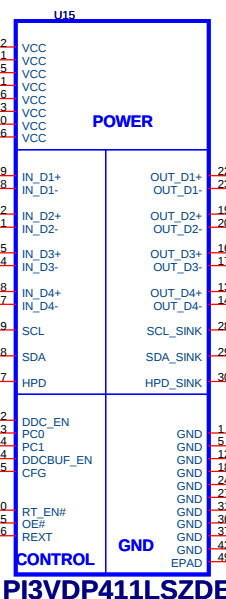
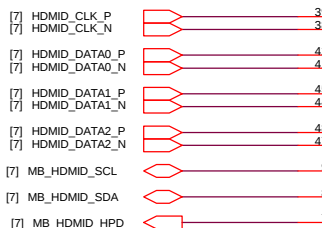
 QUANTA COMPUTER		
Title VGA-M92-XT (PCIe)		
Size	Document Number UM3B/UM6B	Rev 1A
Date: Wednesday, September 30, 2009 Sheet 22 of 59		



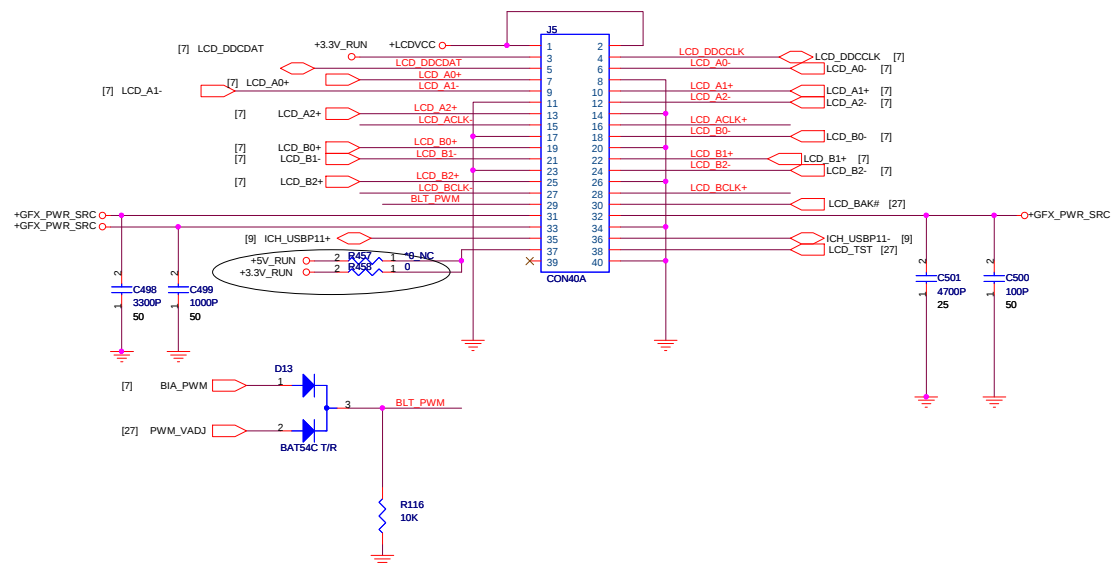
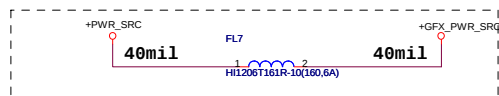
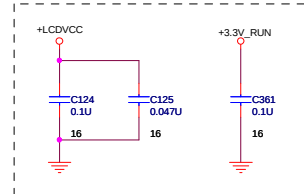
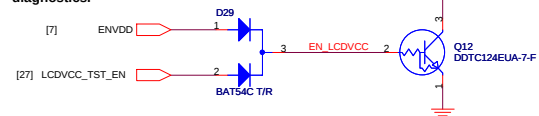
EQUALIZATION SETTING
PC1:PC0=0:0 8dB
PC1:PC0=0:1 4dB Recommended
PC1:PC0=1:0 12dB
PC1:PC0=1:1 0dB

SCL2/SDA2 Low-level input/output Voltage
CFG01:CFG00=0:0 VIL:<0.4V VOL:0.6V (Default)
CGF01:CGF00=0:1 VIL:<0.36V VOL:0.55V
CGF01:CGF00=1:0 VIL:<0.44V VOL:0.65V
CGF01:CGF00=1:1 VIL:<0.36V VOL:0.6V

HDMI_PWR_CTRL
0 is Enable
1 is Disable

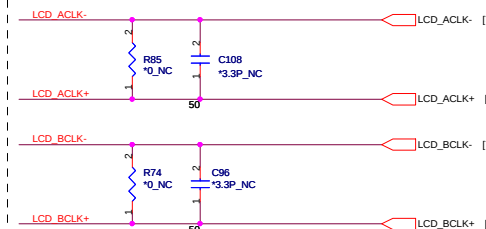


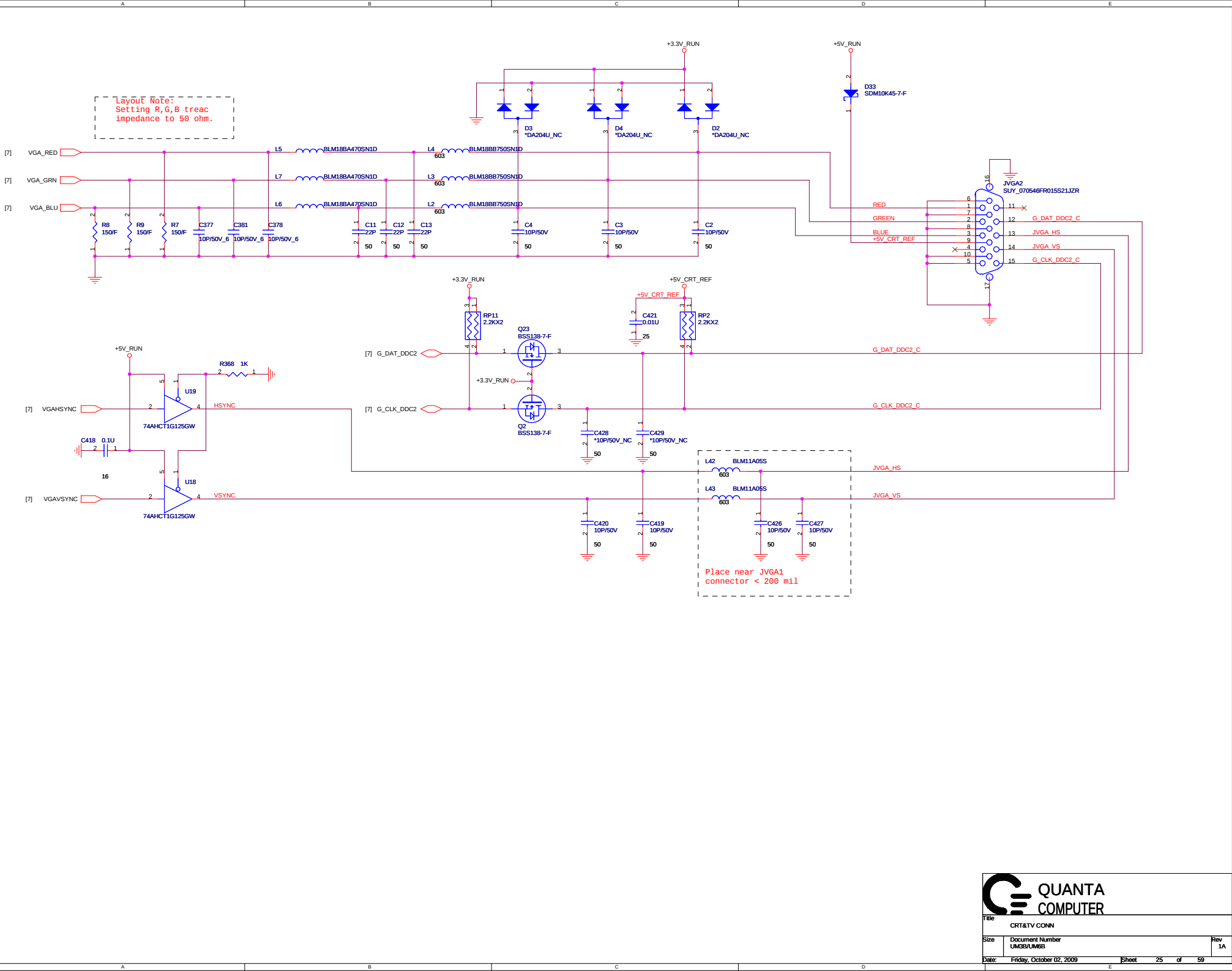
Support the new imbedded diagnostics.

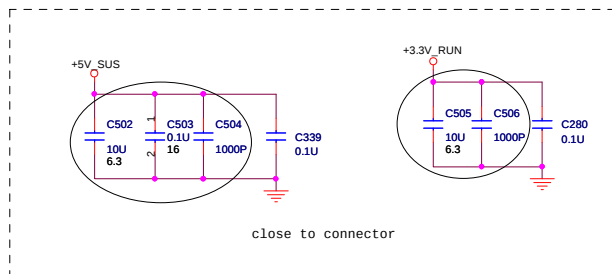
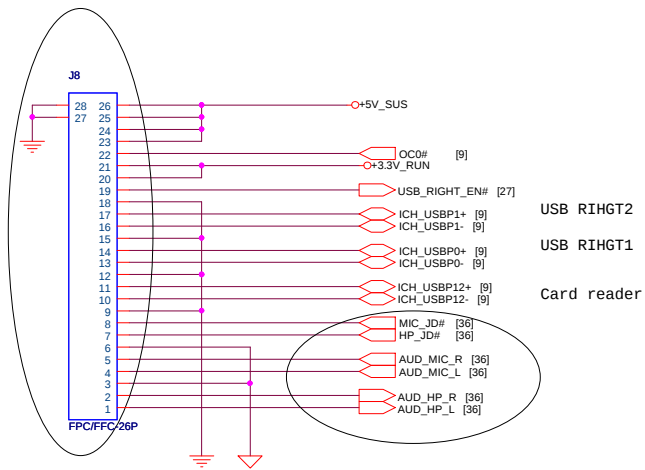


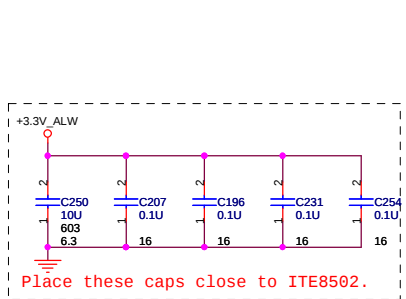
Shunt capacitors on LVDS for improving WWAN.

LCD B0-	C109	1	2	*3.3P NC	50	LCD B0+
LCD B1-	C107	1	2	*3.3P NC	50	LCD B1+
LCD B2-	C101	1	2	*3.3P NC	50	LCD B2+
LCD A0-	C119	1	2	*3.3P NC	50	LCD A0+
LCD A1-	C113	1	2	*3.3P NC	50	LCD A1+
LCD A2-	C111	1	2	*3.3P NC	50	LCD A2+



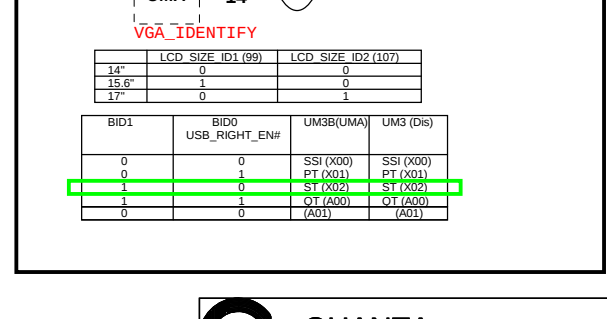
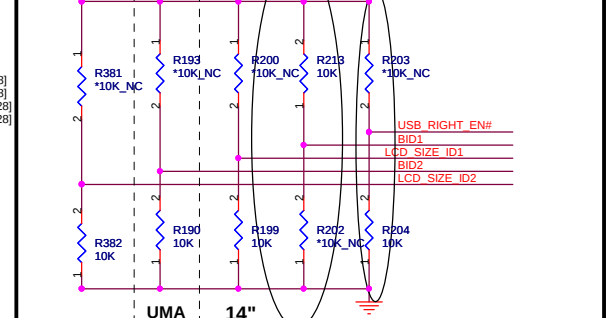
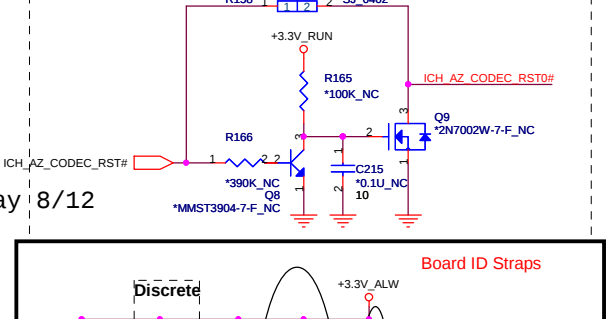
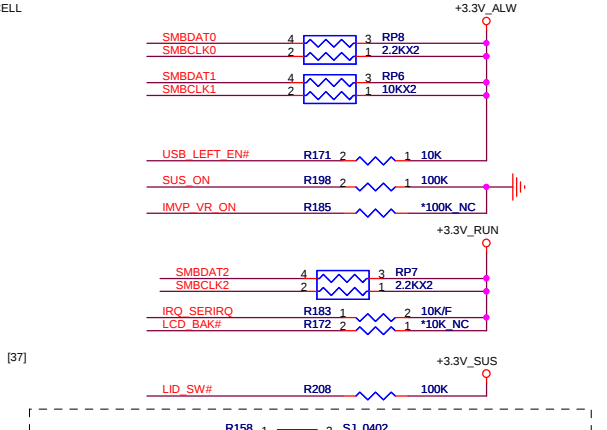
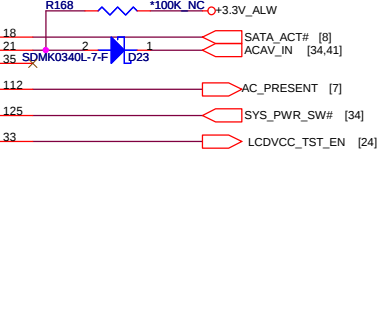
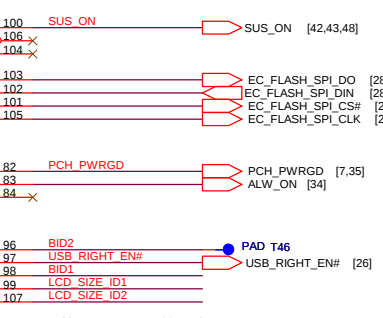
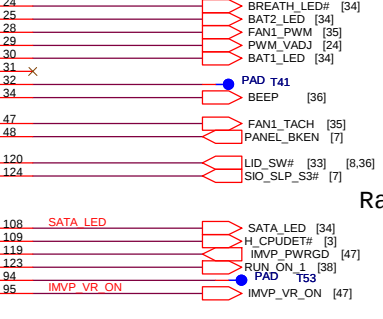
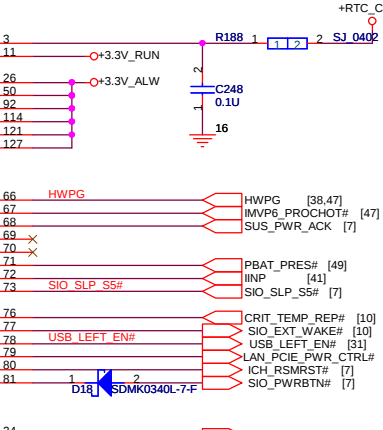
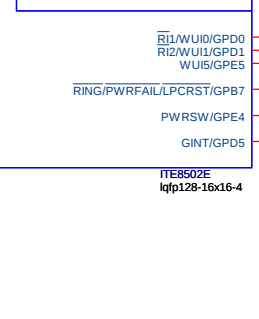
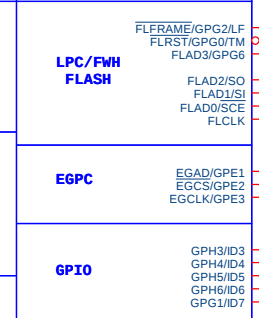
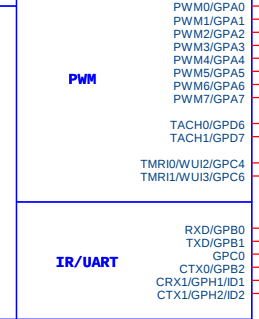
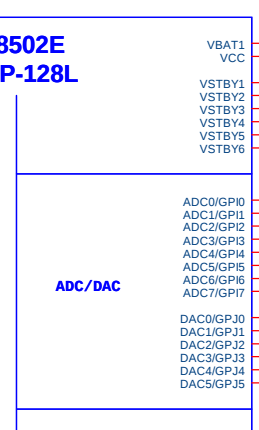
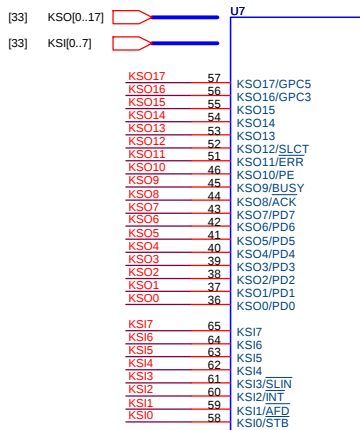
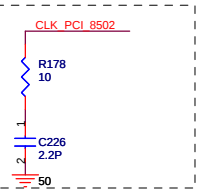
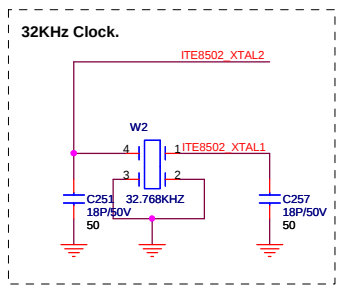




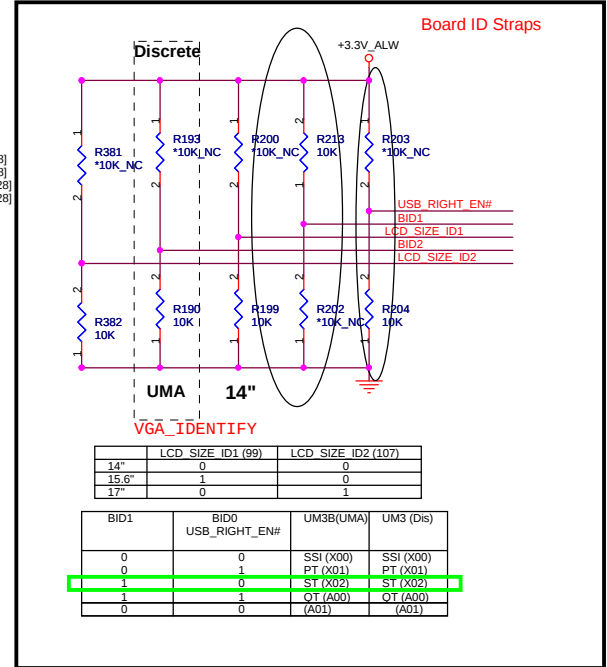


CLK, LCD and Thermal

Charge and BAT



Ray 8/12





QUANTA

COMPUTER

Title

I/O Controller IT8502

Size

Document Number
UM3B/UM6B

Date

Friday, October 02, 2009

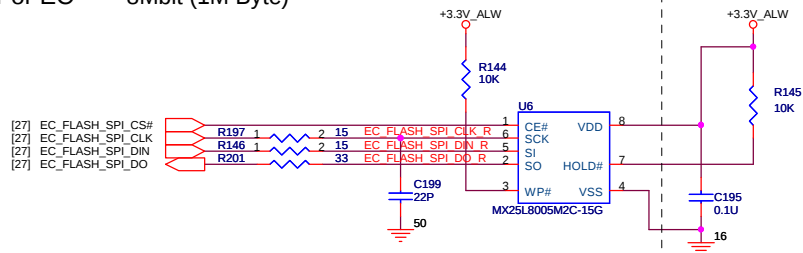
Sheet

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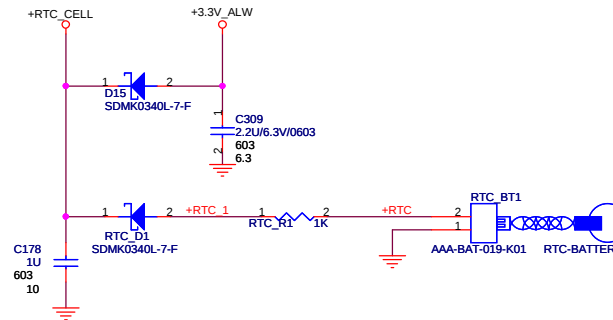
Rev

1A

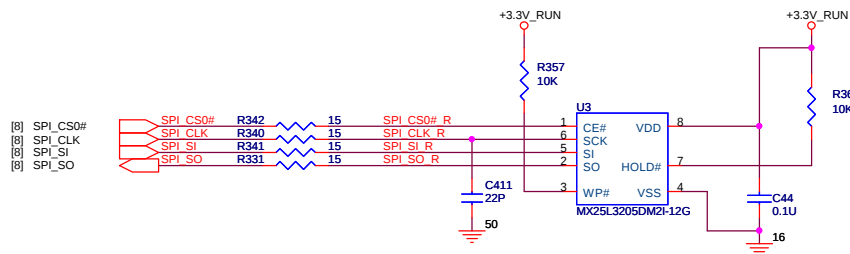
For EC 8Mbit (1M Byte)



RTC BATTERY

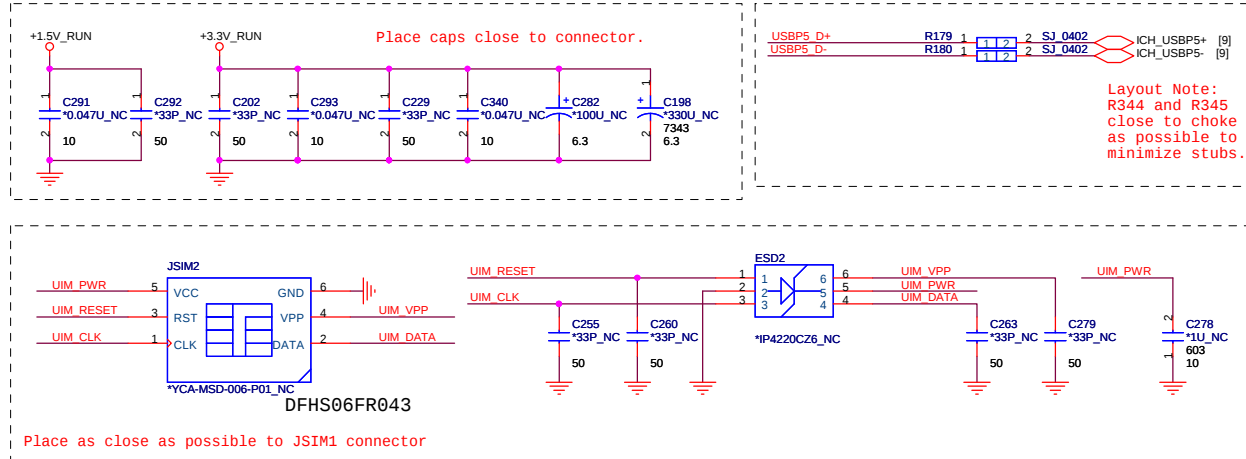
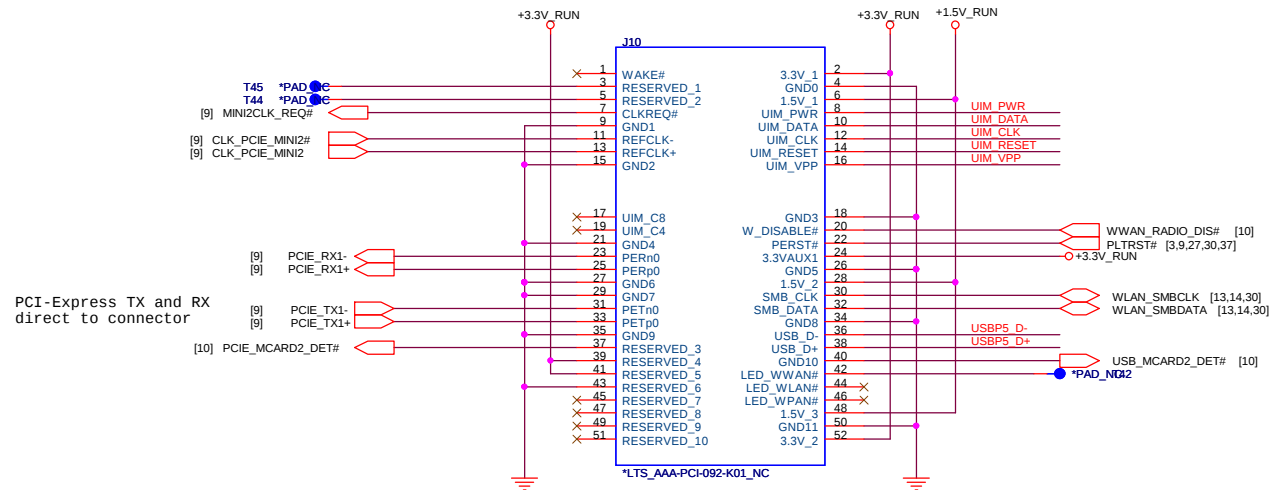


For PCH 32Mbit (4M Byte)



Title		
Ultra I/O Controller ECE5028		
Size	Document Number UMGB/UM6B	Rev 1A
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MiniCard WWAN connector



Title	MINI-PCI
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Size

	Document Number
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UM3B/UM6B

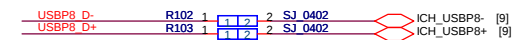
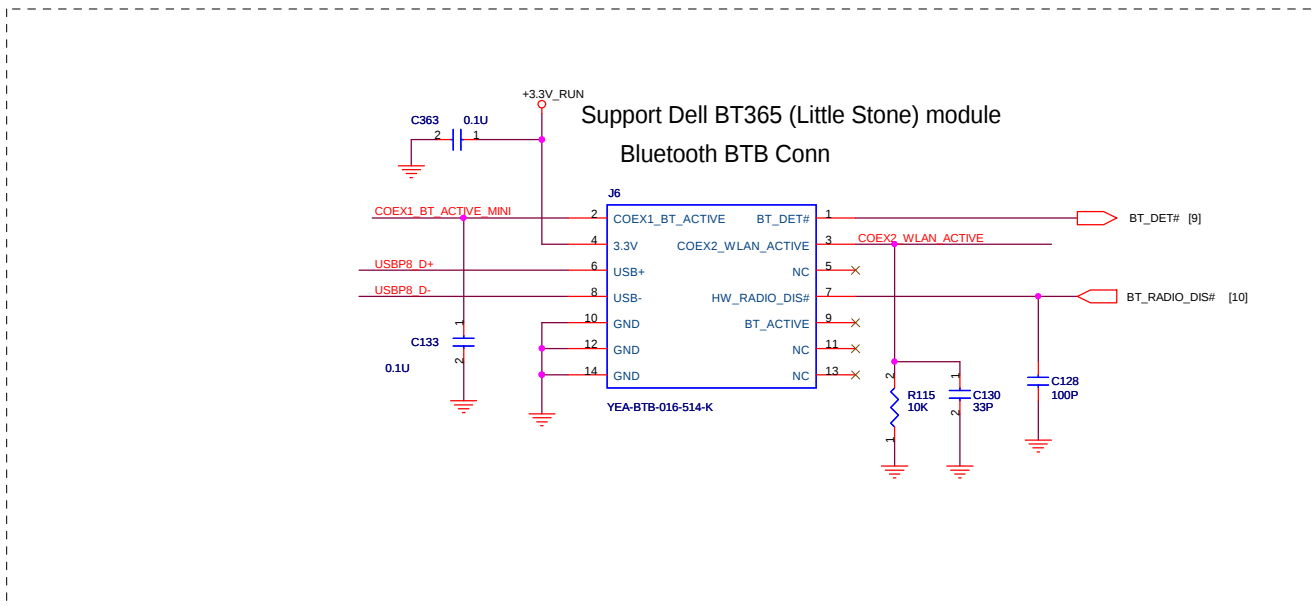
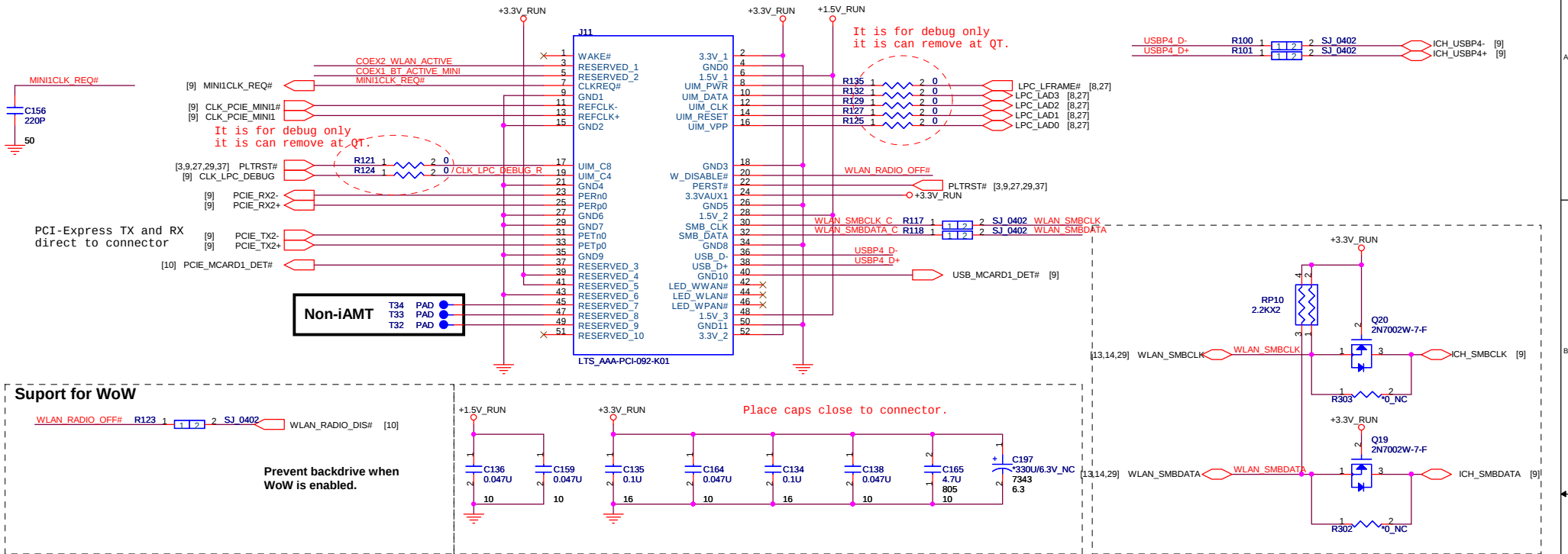
Date: Friday, October 02, 2009

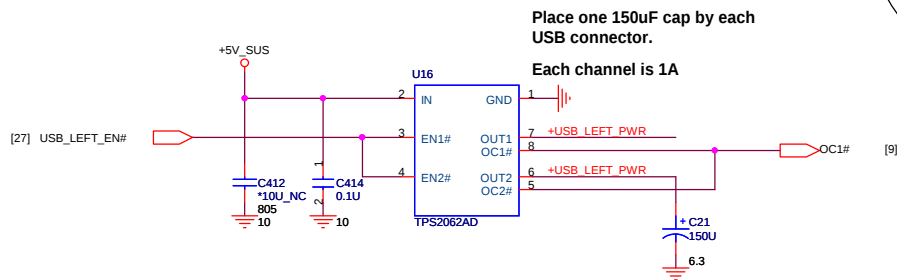
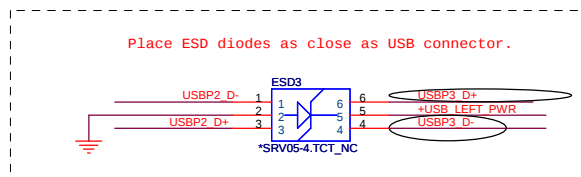
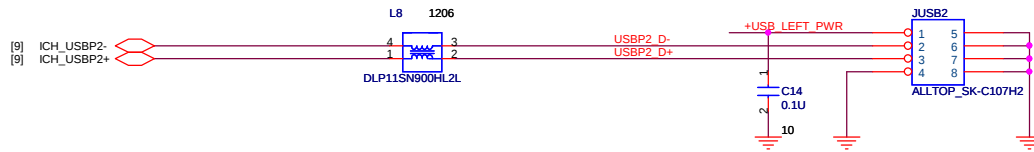
Sheet

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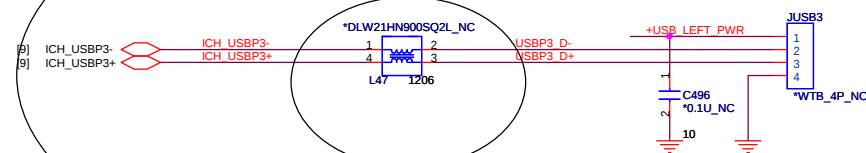
MiniCard WLAN connector





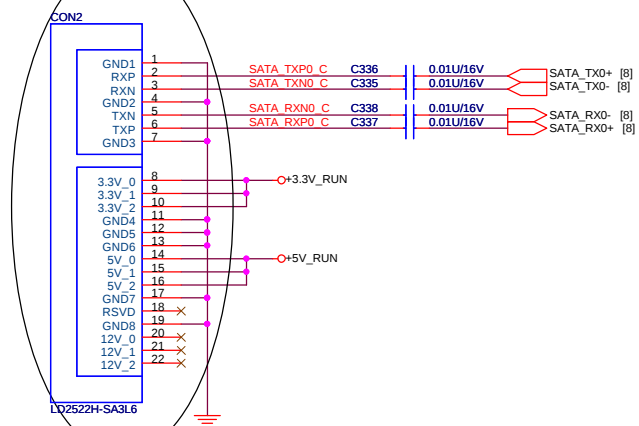
REV FOR 17"

Add L47 ,C496 , JUSB3 for UM5

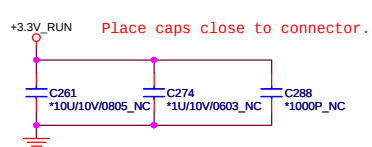


EDISON 8/10

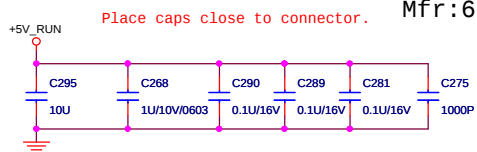
HDD Connector.



UM5與UM3/6不同，只差在高度，footprint沒變

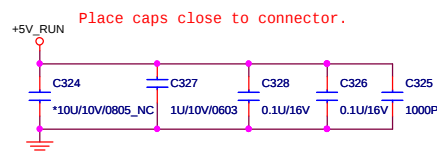
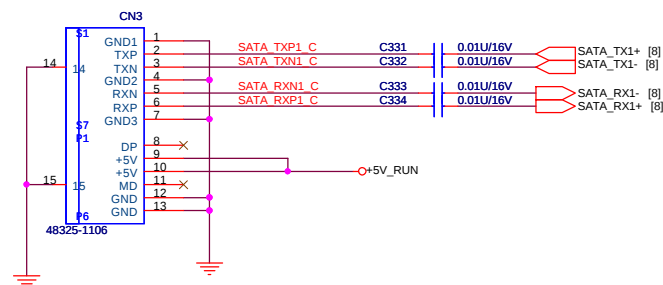


UM5/UM5B
PN:DFHS22FR137
Mfr:67492-1224

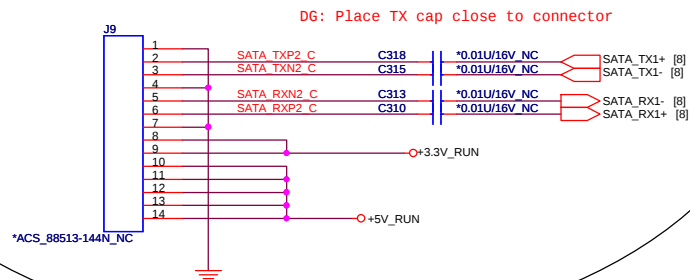


UM3/UM3B/UM6/UM6B
PN:
Mfr:67492-1730

ODD Connector



REV FOR 15.6"



DG: Place TX cap close to connector

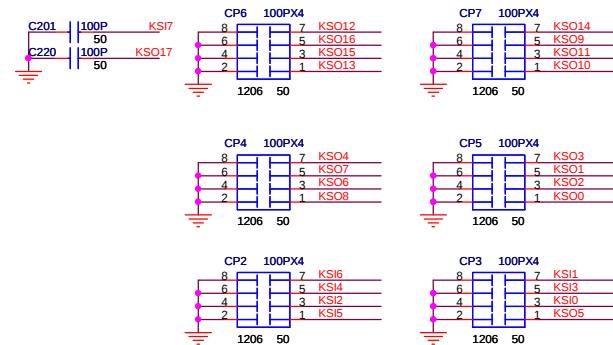
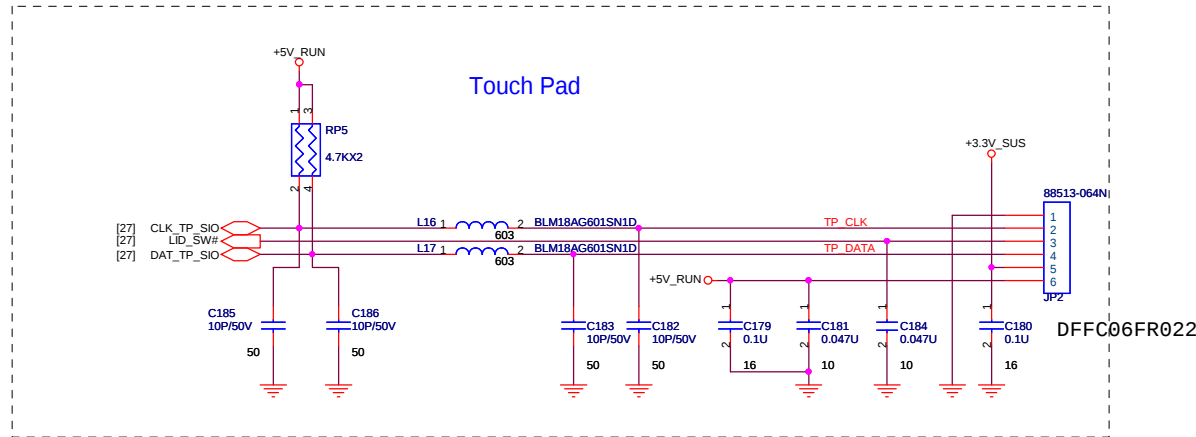
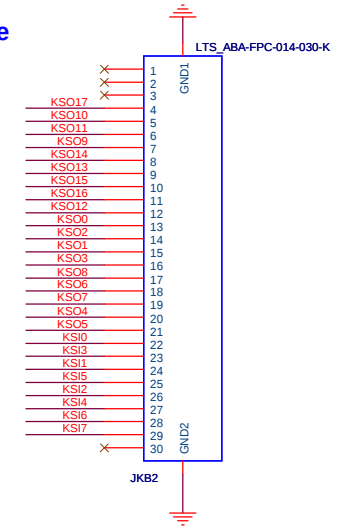


Title			SATA (HDD&CD_ROM)
Size	Document Number	Rev	
	UM3B/UM6B	1A	
Date:	Friday, October 02, 2009	Sheet	32 of 59

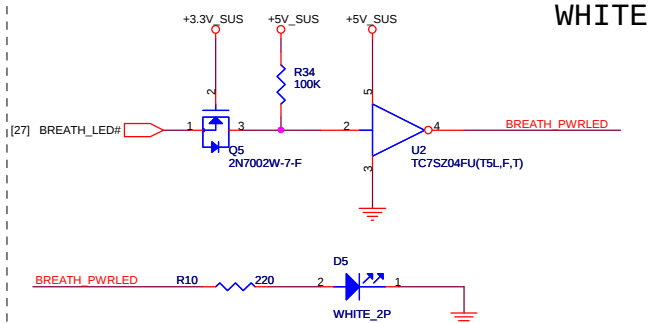
KEYBOARD CONNECTOR

Top side

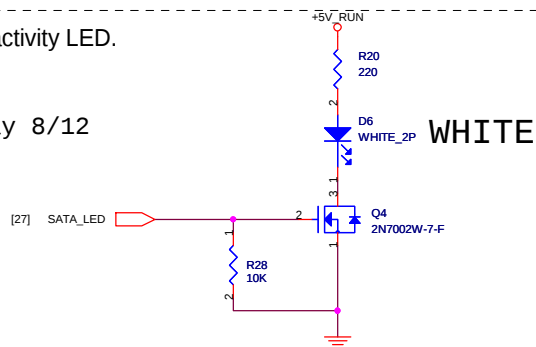
[27] KSO[0..17]
[27] KSI[0..7]



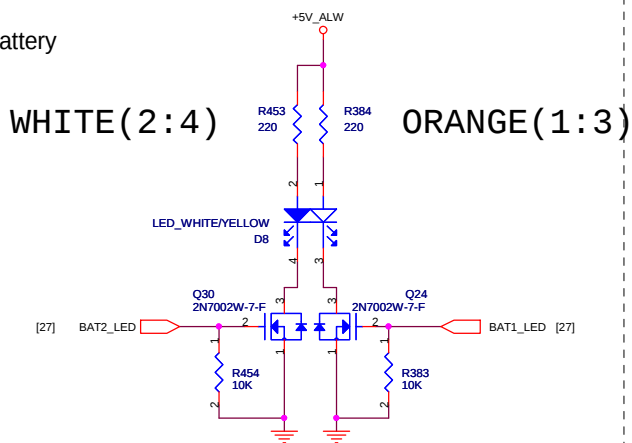
Power



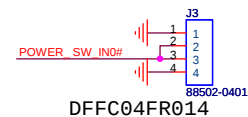
HDD activity LED.



Battery

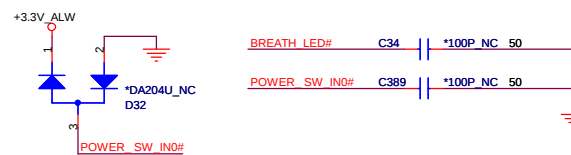
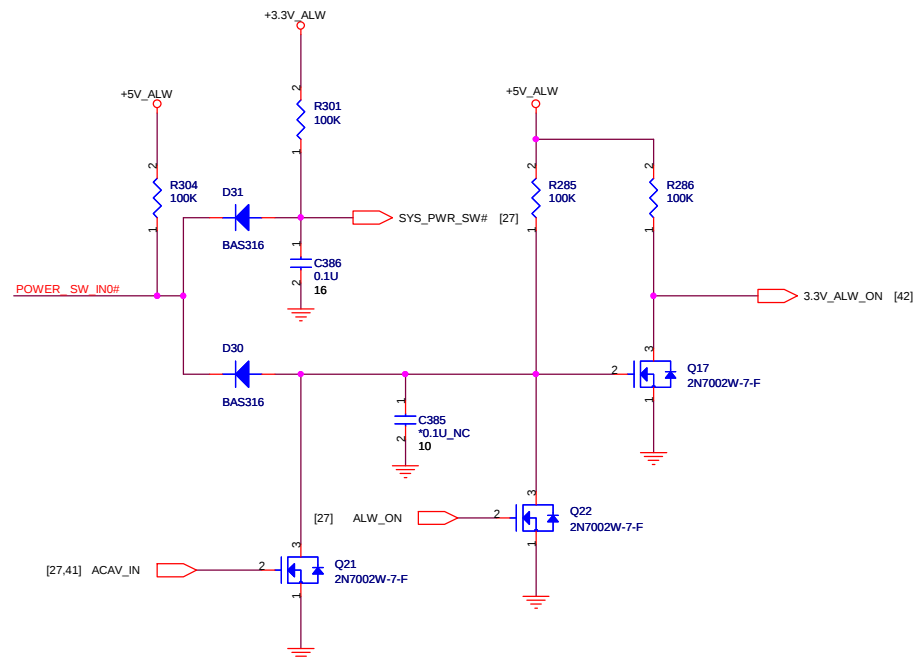


Power button Cable



PIN2,3 connect to POWER_ SW_IN0#

3VALW ON POWER LOGIC



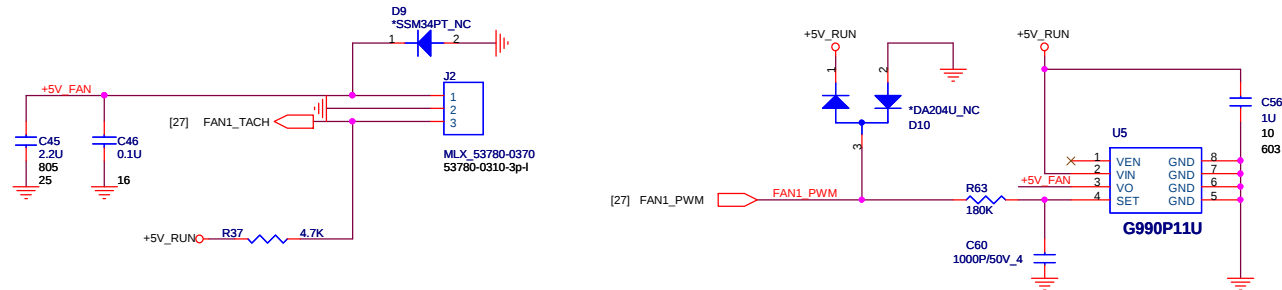
Title	SWITCH, KEYBOARD & LED&Touch Screen Module
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Size	Document Number UM3B/UM6B
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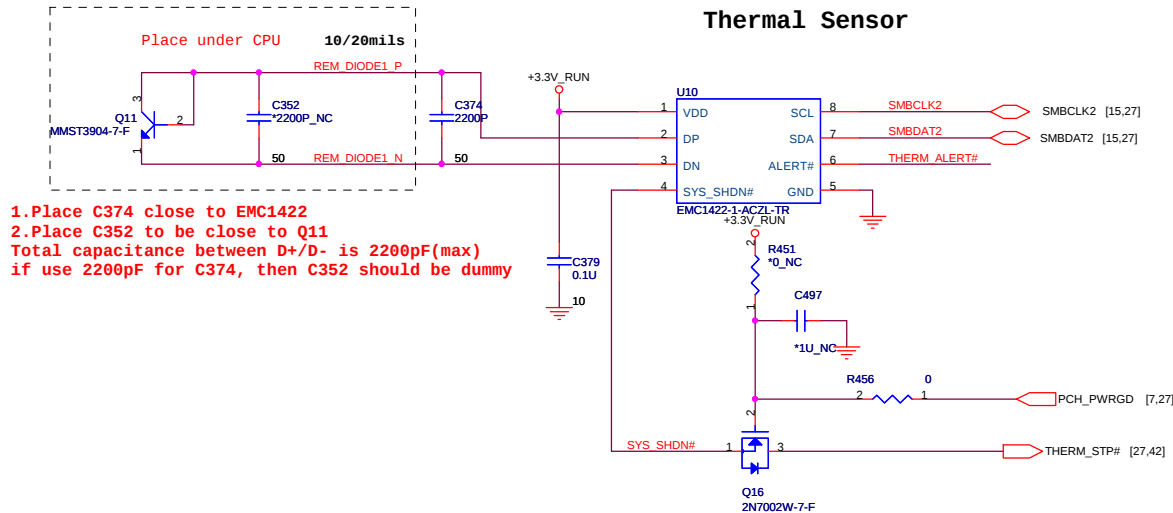
Rev	1A
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FAN CONTROL

6/23 COPY FROM RM6

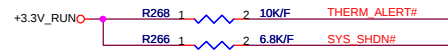


Thermal Sensor

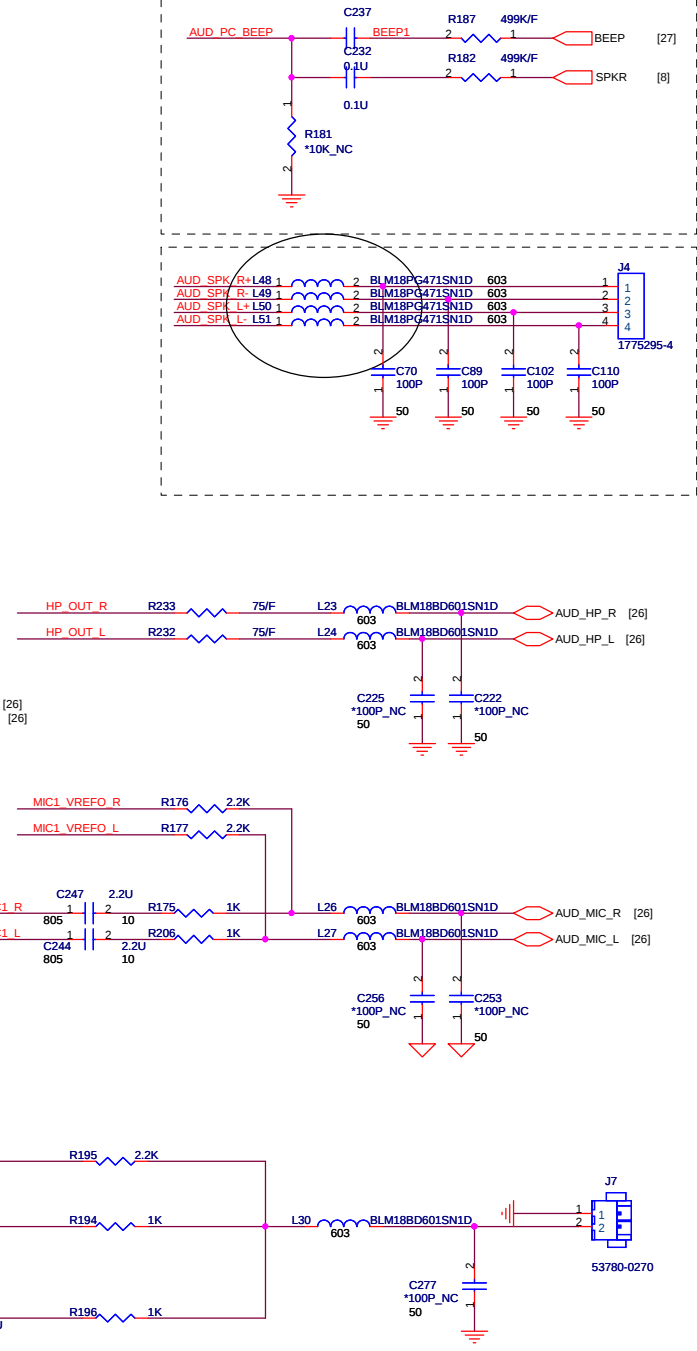
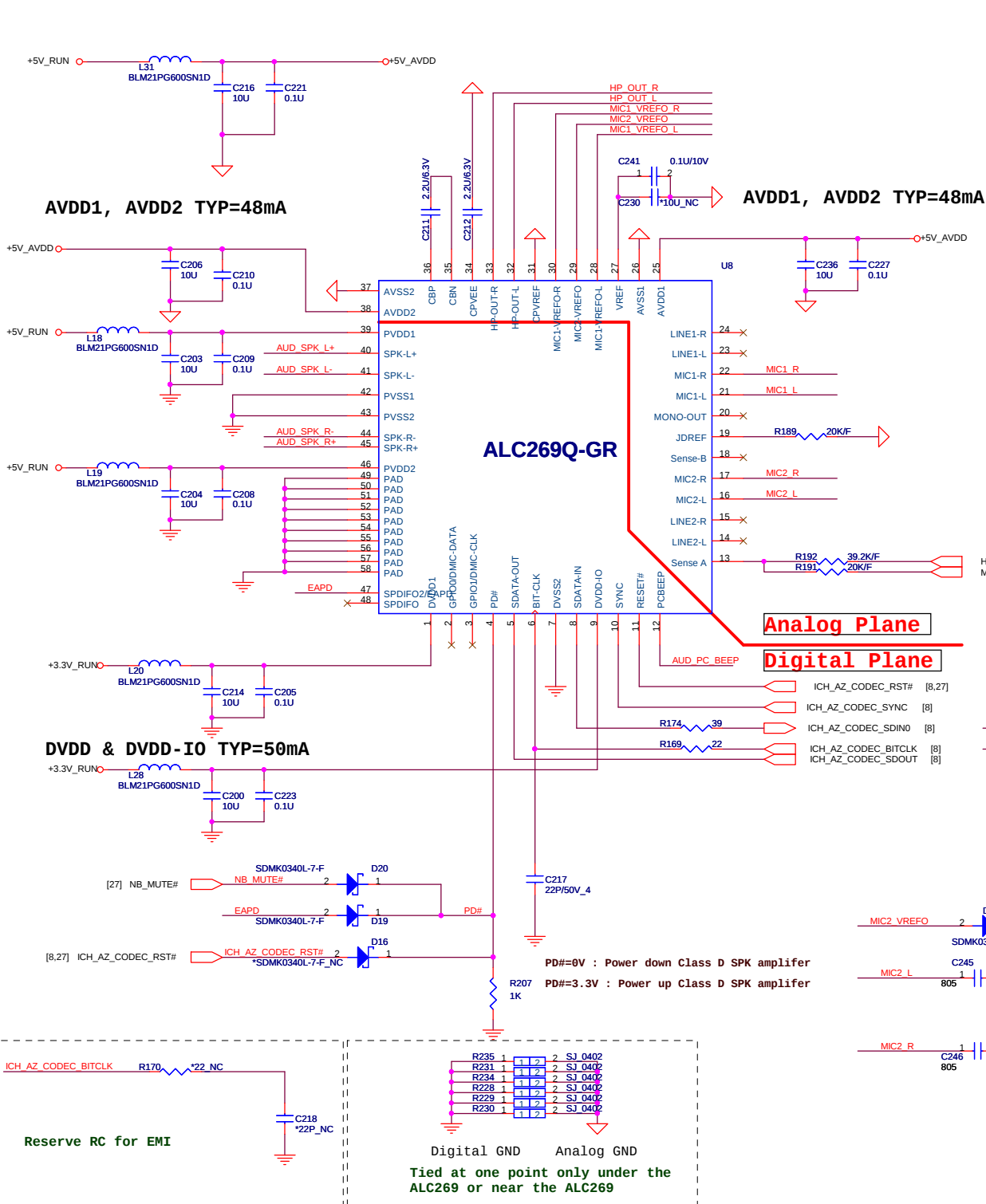


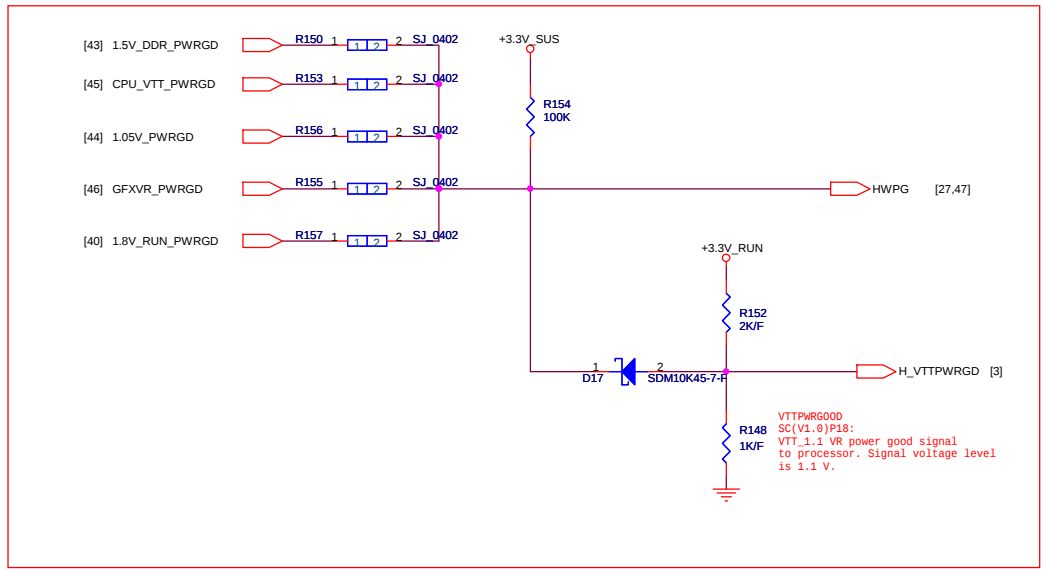
- 1.Place C374 close to EMC1422
 - 2.Place C352 to be close to Q11
- Total capacitance between D+/D- is 2200pF(max)
if use 2200pF for C374, then C352 should be dummy

OTP 85 degree C



Title		
FAN & THERMAL		
Size	Document Number	Rev
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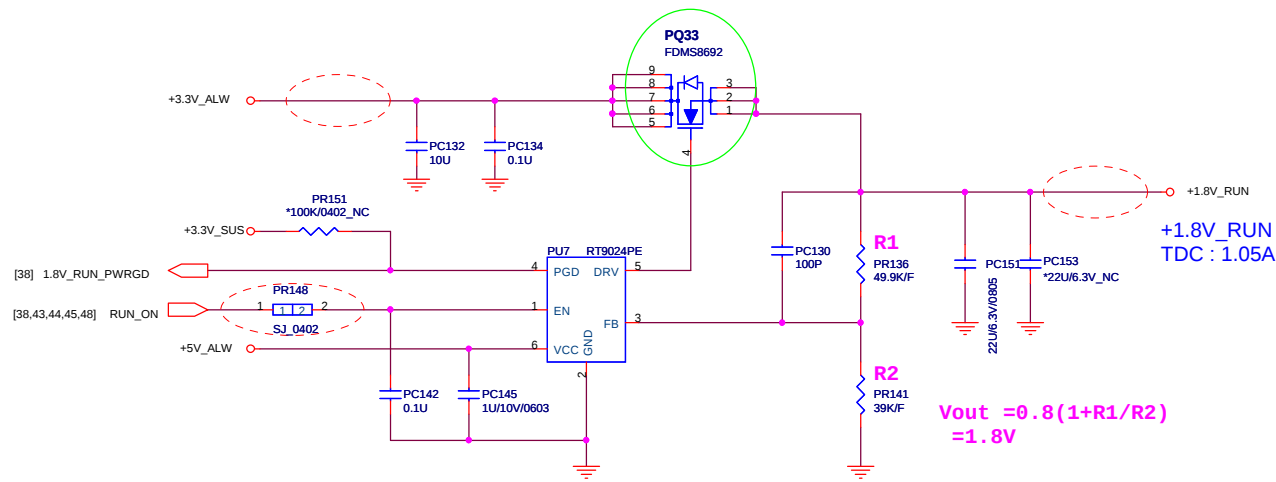
[27] RUN_ON_1

R32 1 1 2 2 SJ_0402

RUN_ON [40,43,44,45,48]

	1	2	3	4	5
A					
B					
C					
D					

 QUANTA COMPUTER		
Title Battery Selector		
Size	Document Number UMGB/UMGB	Rev 1A
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+1.8V_RUN for CPU and PCH 1.8V

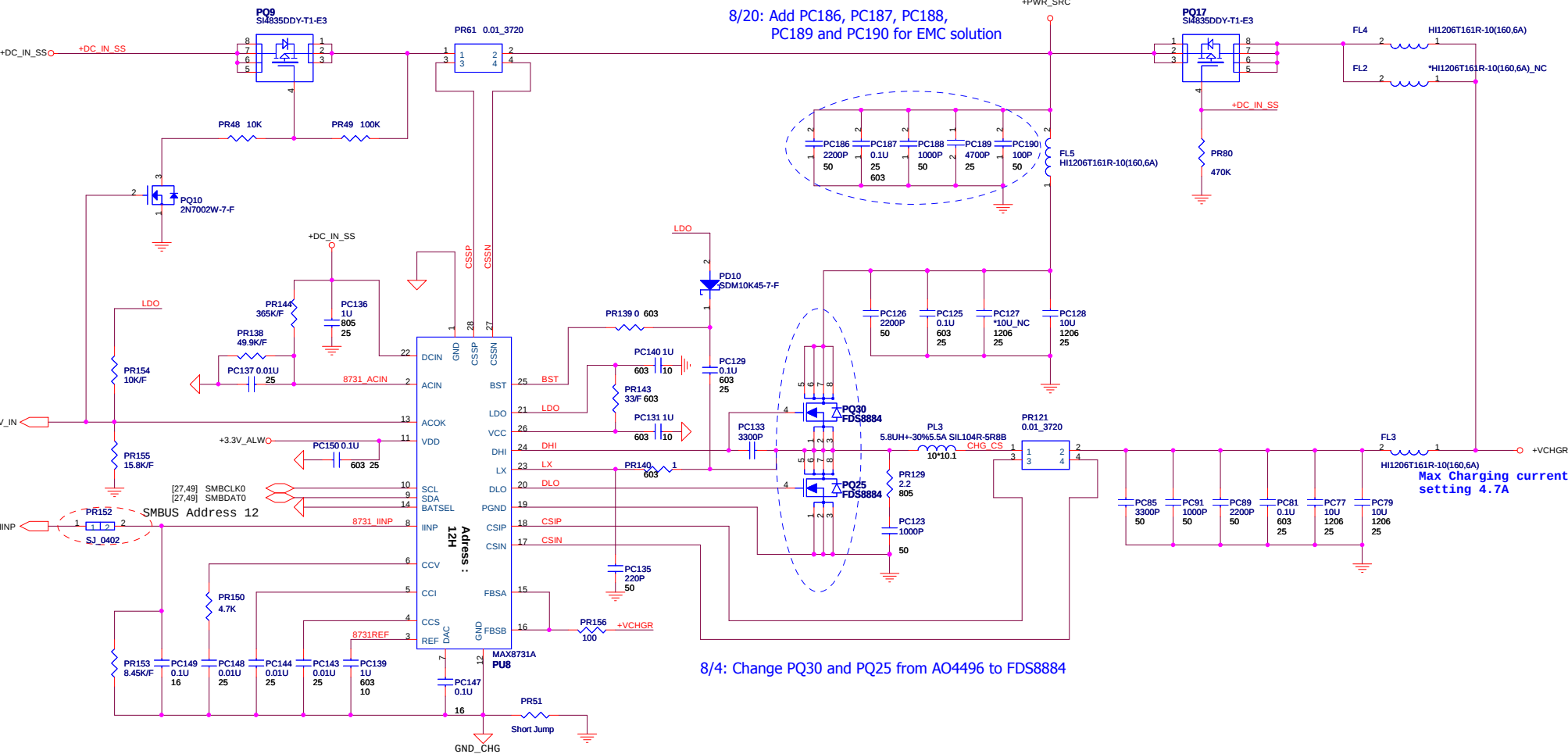
09/08: remove PJP11 and PJP18, change PR148 from 0 ohm to shot jump

Continuous current : 13A
Rds(on) : 18mohm

Continuous current : 13A
Rds(on) : 18mohm

8/20: Add PC186, PC187, PC188,
PC189 and PC190 for EMC solution

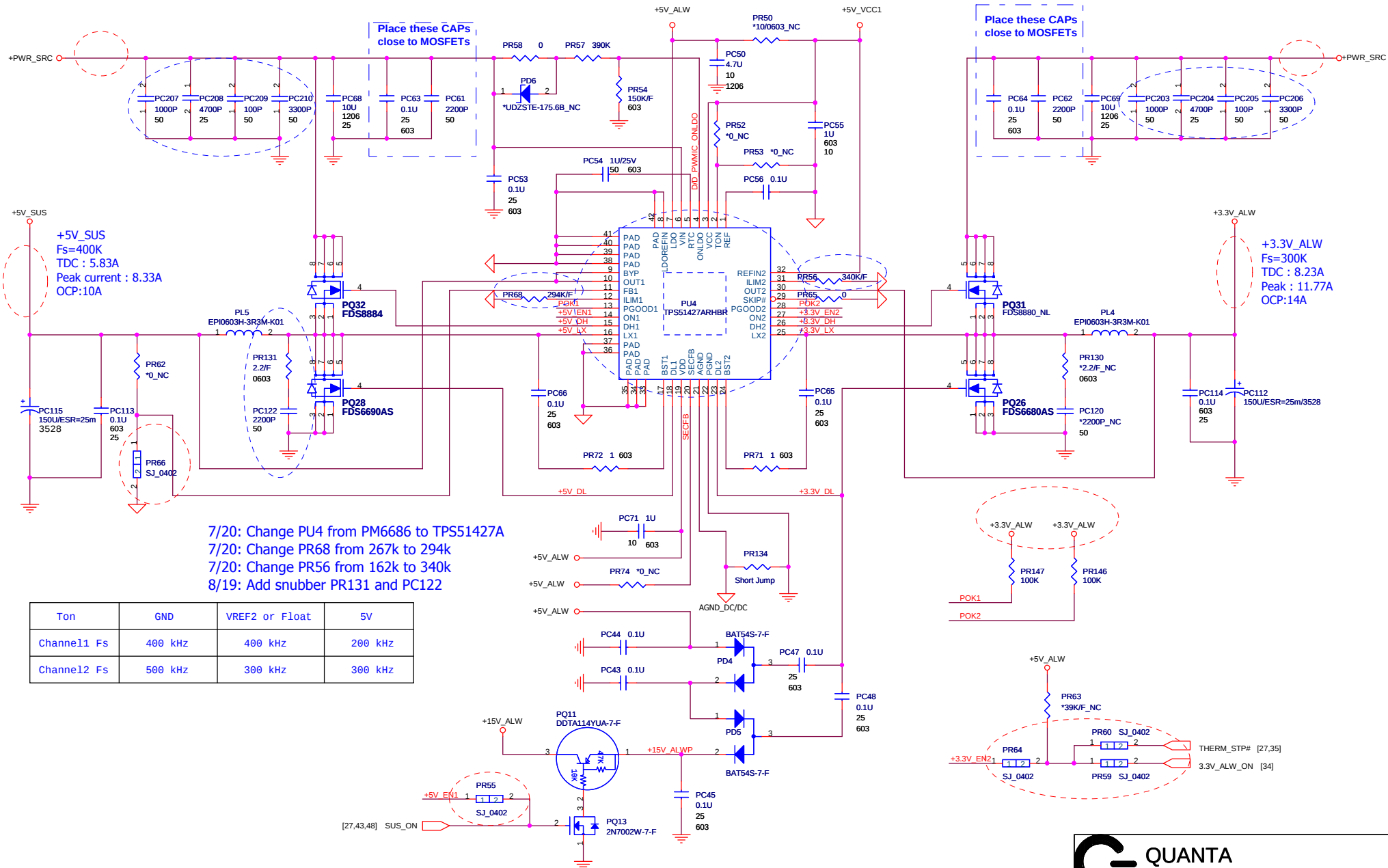
8/4: Change PQ30 and PQ25 from AO4496 to FDS8884



09/08: change PR152 from 0 ohm to shot jump

09/08: change PR59, PR60, PR64, PR66, PR55 from 0 ohm to shot jump
 09/08: remove PJP8, PJP9, PJP12 and PJP15
 8/20: Add PC207, PC208, PC209 and PC210
 for EMC solution

8/20: Add PC203, PC204, PC205 and PC206
 for EMC solution

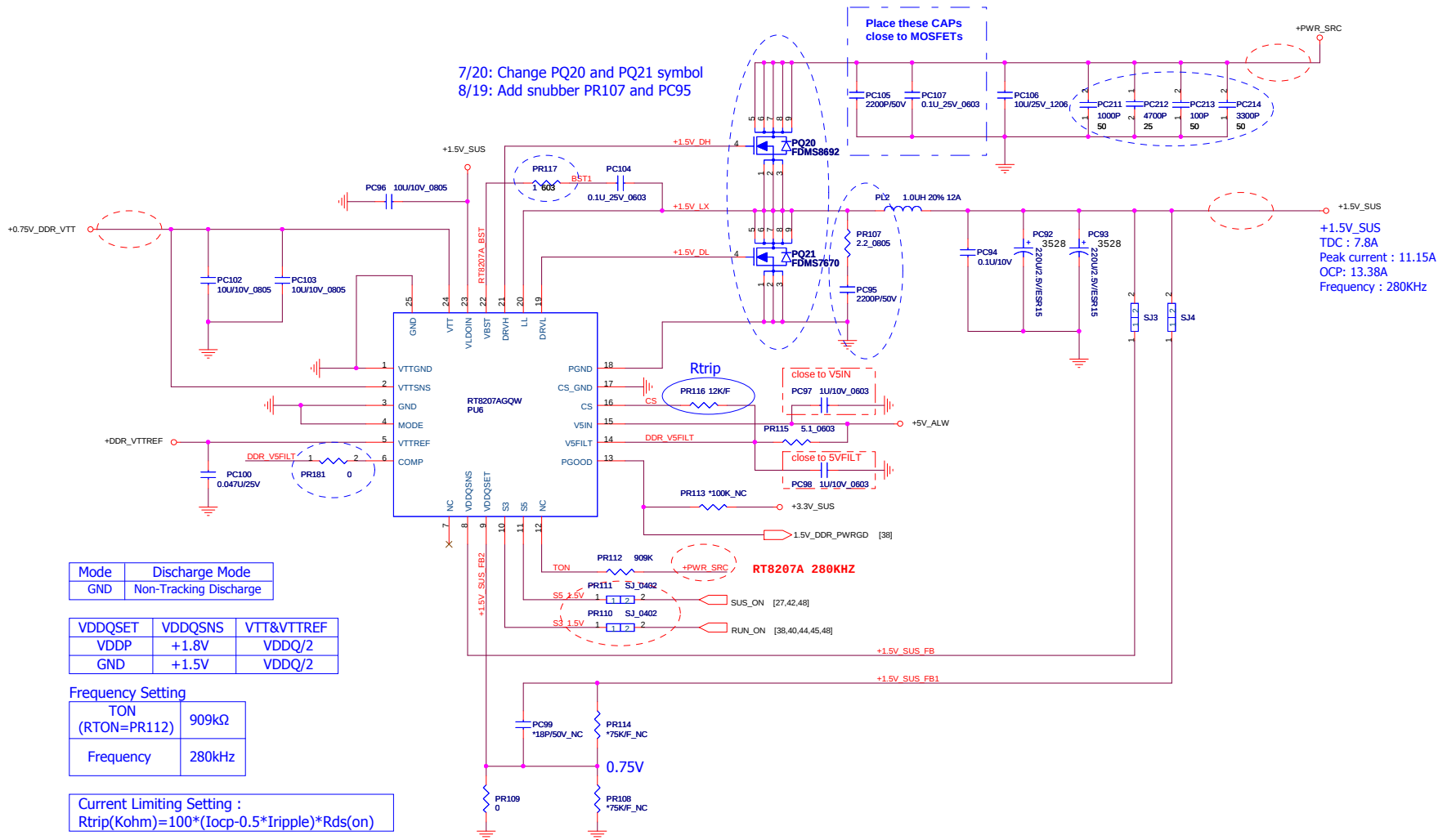


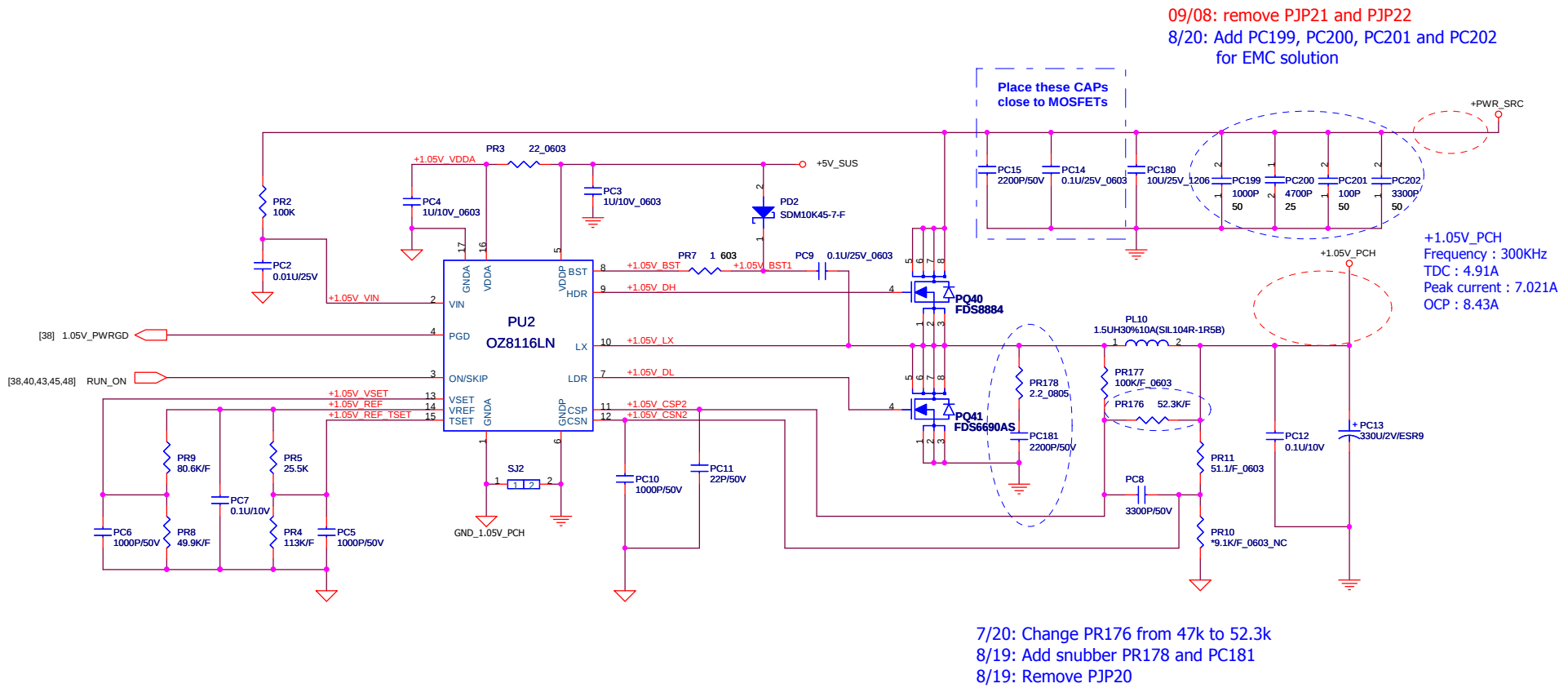
Ton	GND	VREF2 or Float	5V
Channel1 Fs	400 kHz	400 kHz	200 kHz
Channel2 Fs	500 kHz	300 kHz	300 kHz

09/08: change PR110 and PR111 from 0 ohm to shot jump
09/08: remove PJP4, PJP5, PJP6 and PJP7

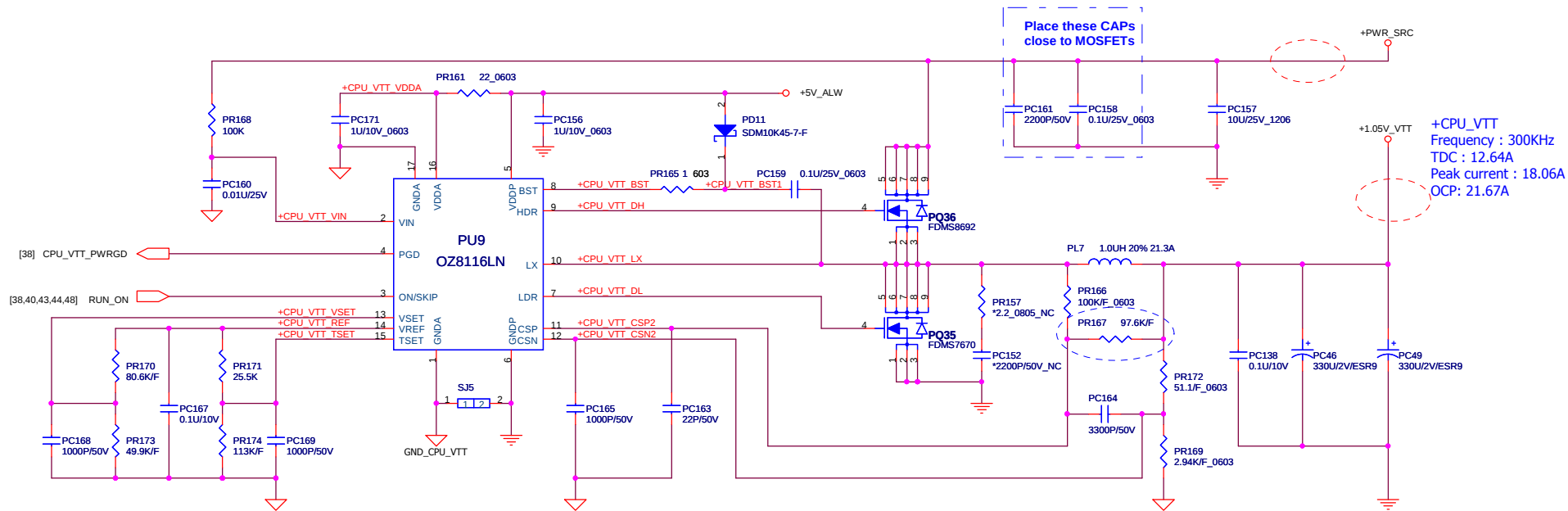
8/20: Add PC211, PC212, PC213 and PC214
for EMC solution

7/20: Change PQ20 and PQ21 symbol
8/19: Add snubber PR107 and PC95





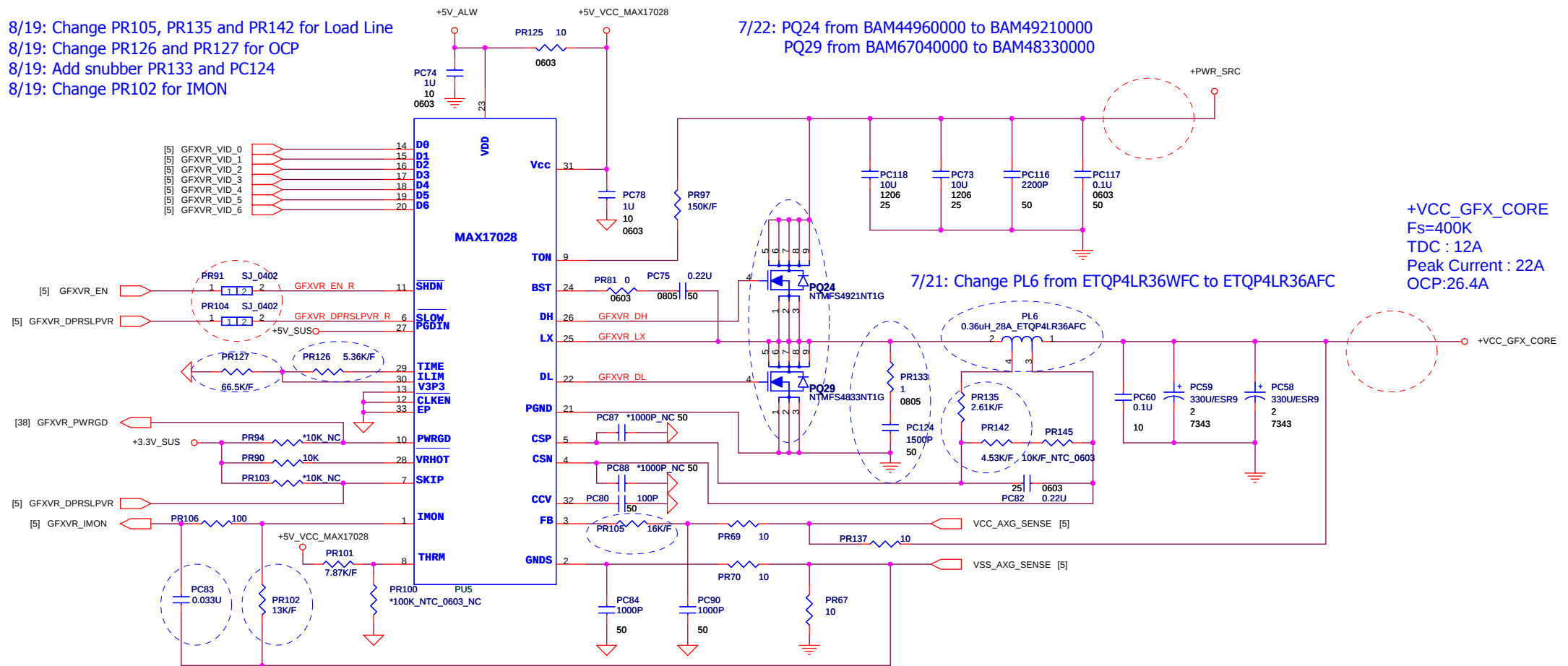
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Size	Document Number	Rev
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Title		
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Size	Document Number	Rev
Custom	UMGB/UM6B	1A
Date:	Friday, October 02, 2009	Sheet 45 of 59

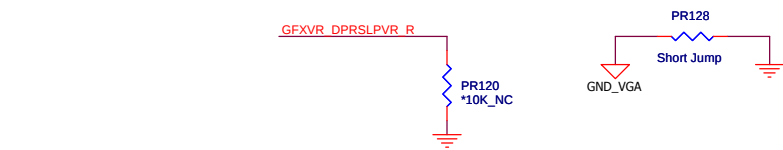
8/19: Change PR105, PR135 and PR142 for Load Line
 8/19: Change PR126 and PR127 for OCP
 8/19: Add snubber PR133 and PC124
 8/19: Change PR102 for IMON

7/22: PQ24 from BAM44960000 to BAM49210000
 PQ29 from BAM67040000 to BAM48330000

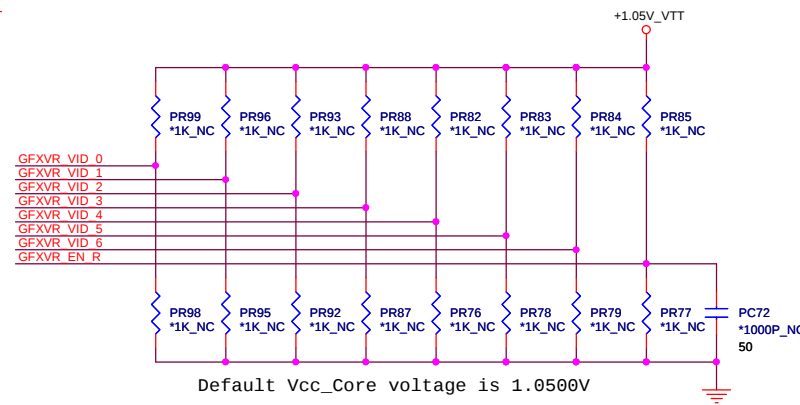


+VCC_GFX_CORE
 Fs=400K
 TDC : 12A
 Peak Current : 22A
 OCP:26.4A

7/21: Change PL6 from ETQP4LR36WFC to ETQP4LR36AFC

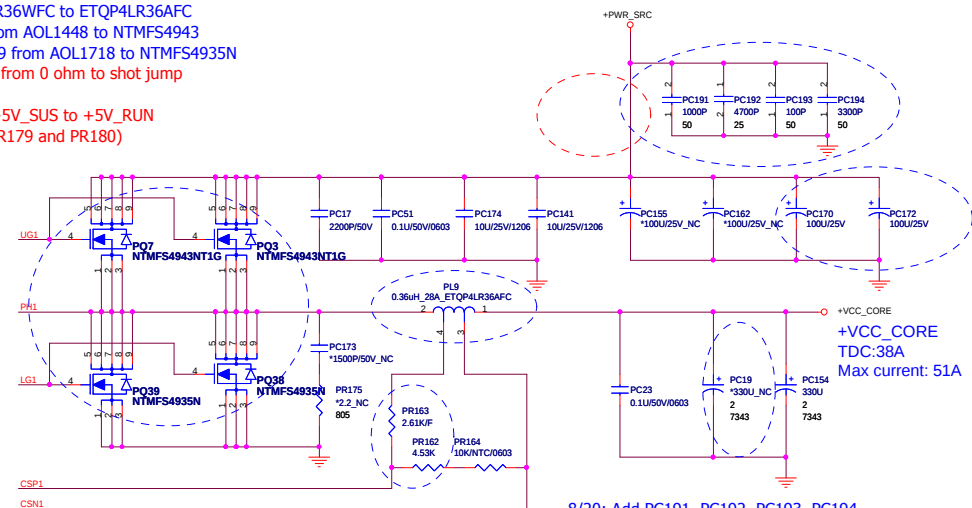
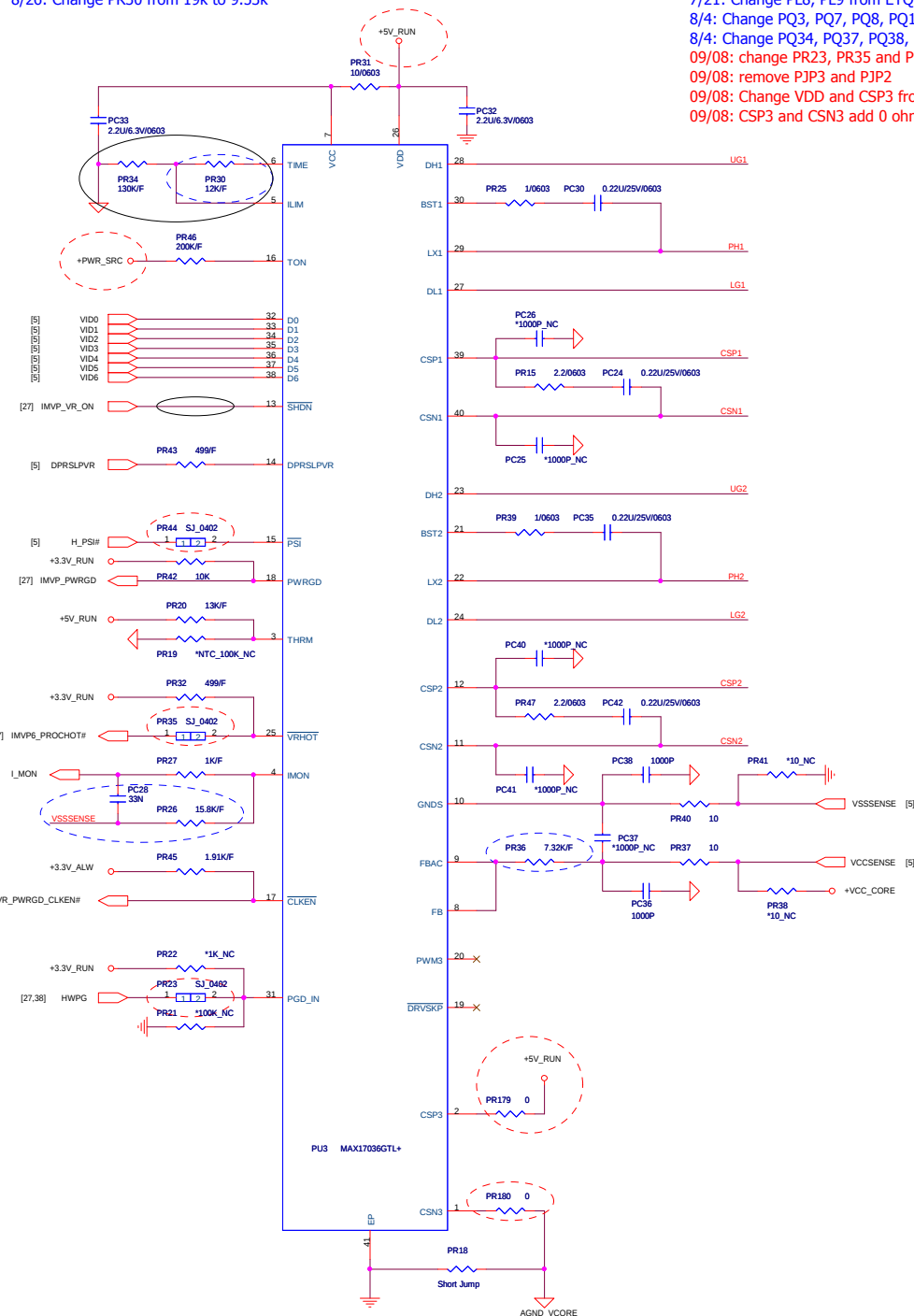


8/26: Change PC83 and PR102 for IMON
 09/08: change PR91 and PR104 from 0 ohm to shot jump
 09/08: remove PJP10, PJP13 and PJP16

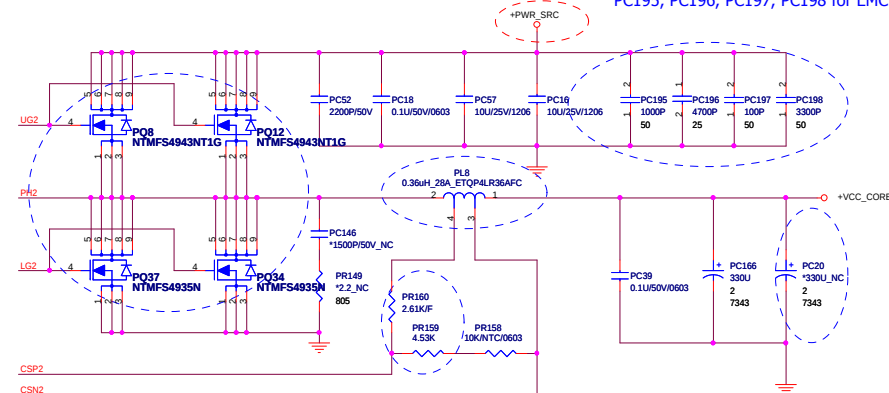


8/26: Change PR30 from 19k to 9.53k

7/21: Change PL8, PL9 from ETQP4LR36WFC to ETQP4LR36AFC
8/4: Change PQ3, PQ7, PQ8, PQ12 from AOL1448 to NTMFS4943
8/4: Change PQ34, PQ37, PQ38, PQ39 from AOL1718 to NTMFS4935N
09/08: change PR23, PR35 and PR44 from 0 ohm to shot jump
09/08: remove PJP3 and PJP2
09/08: Change VDD and CSP3 from +5V_SUS to +5V_RUN
09/08: CSP3 and CSN3 add 0 ohm (PR179 and PR180)



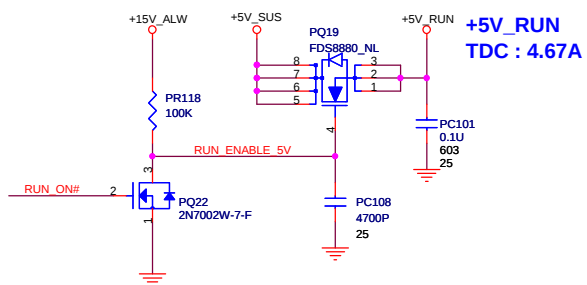
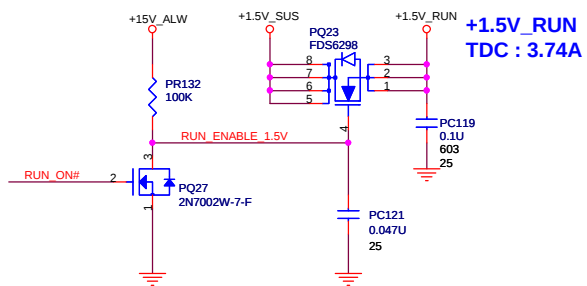
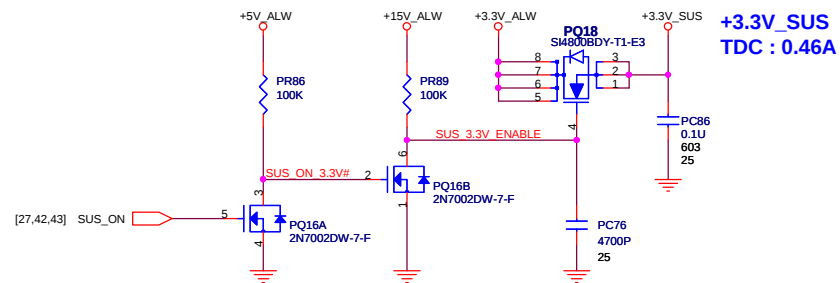
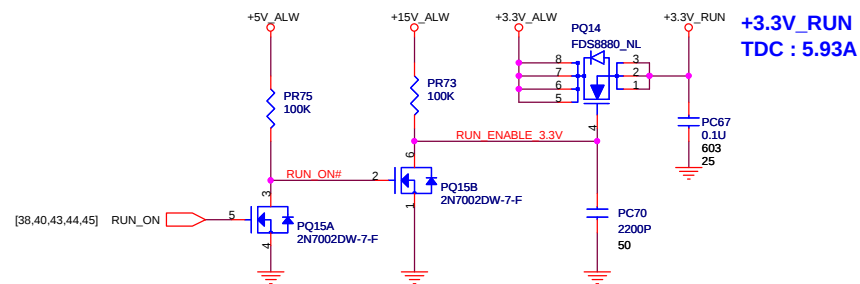
8/20: Add PC191, PC192, PC193, PC194, PC195, PC196, PC197, PC198 for EMC solution



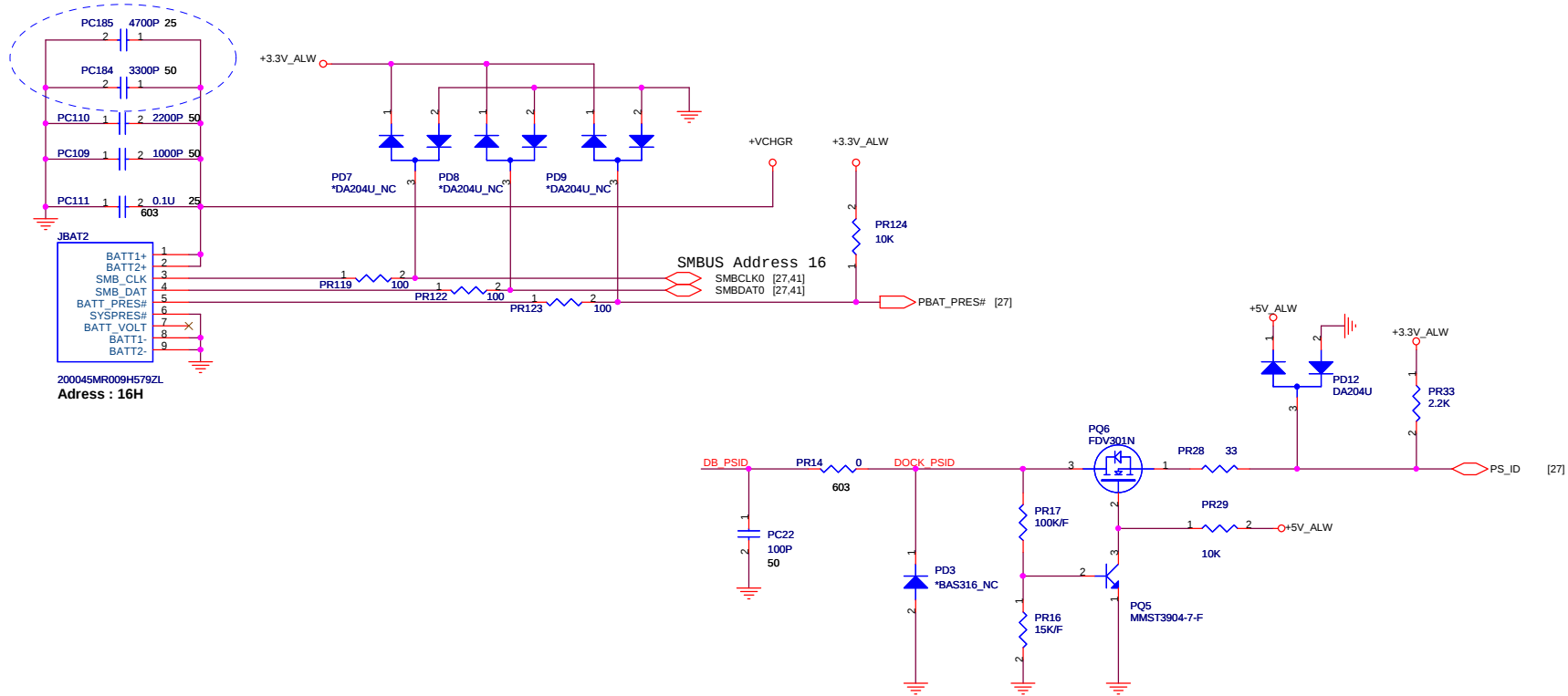
8/13: NC output cap PC19 and PC20

8/13: Change for Load Line and IMON regulator
change PR26 from 9.53k to 15.8k
change PR36 from 6.8k to 7.32k
change PR159, PR162 from 3.4k to 4.53k
change PR160, PR163 from 1.8k to 2.61k
change PC28 from 0.1uF to 33nF

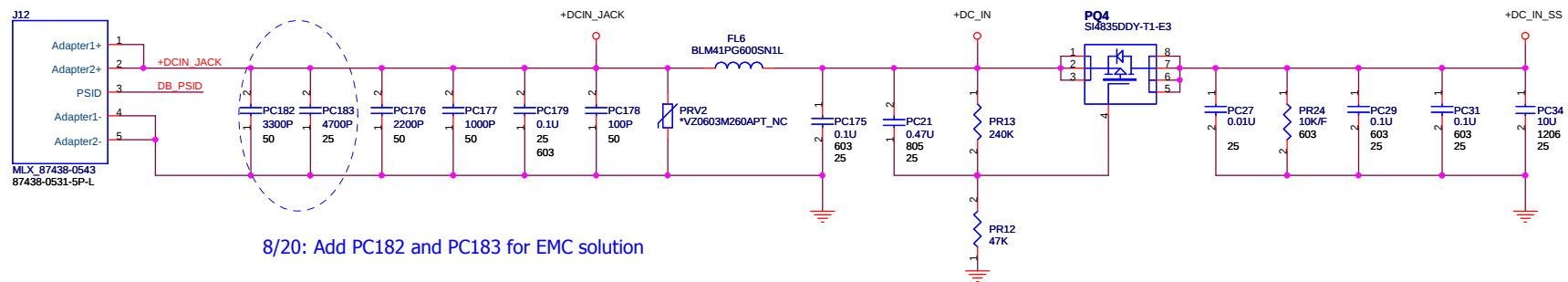
Title CPU core (MAX17036)			
Size	Document Number UM38/UM6B		Rev
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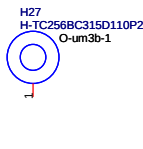
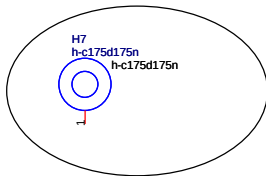
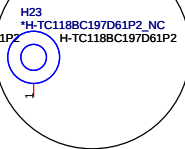
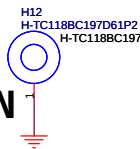
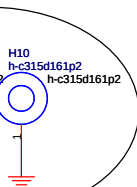
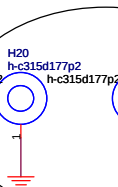
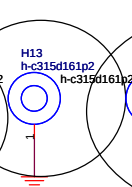
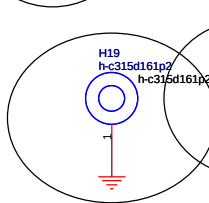
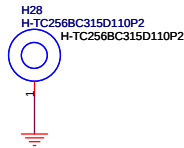
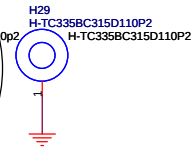
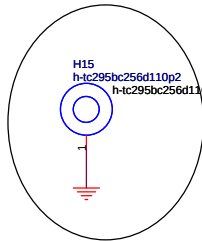
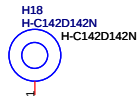
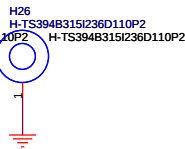
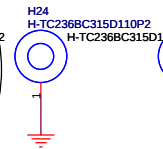
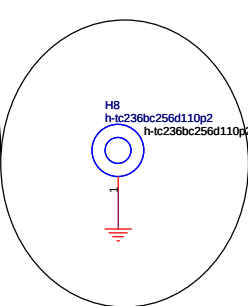
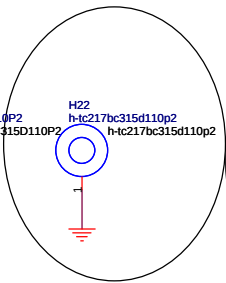
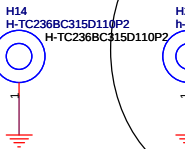
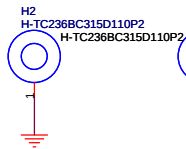
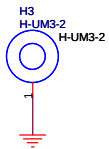
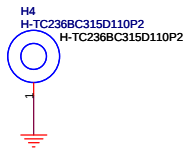
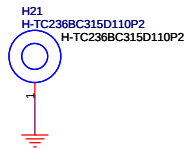
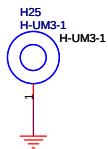
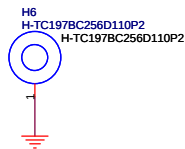


8/20: Add PC184 and PC185 for EMC solution



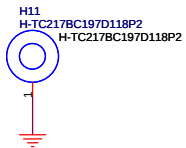
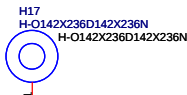
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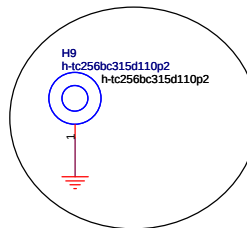
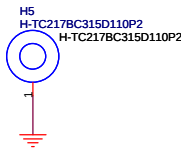


WLAN

BOT
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BT TOP
QPN: FBCW4003010

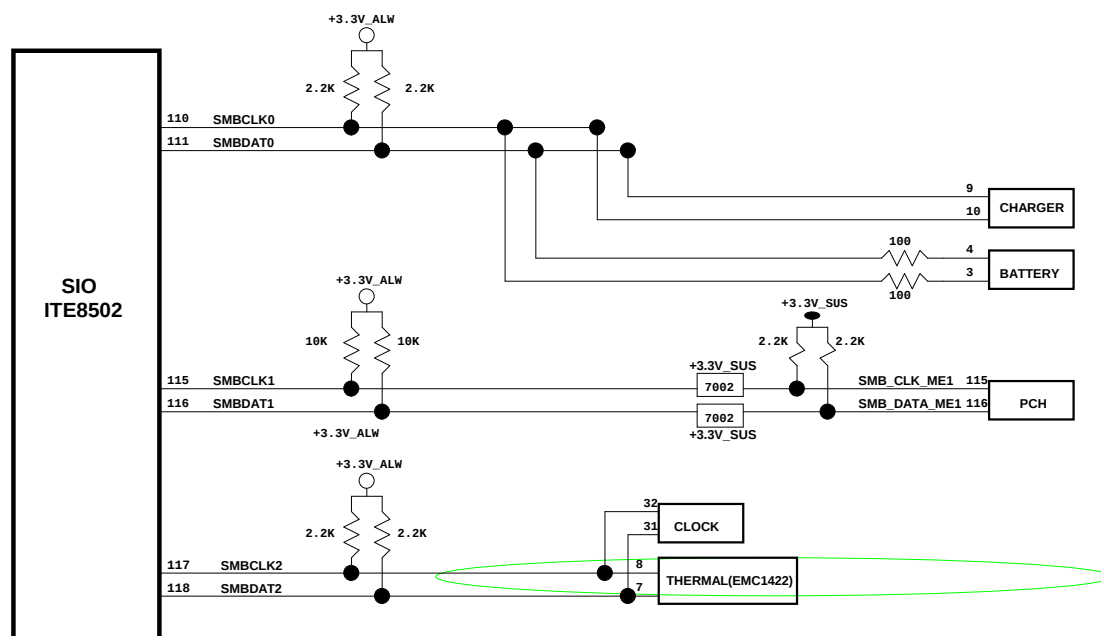
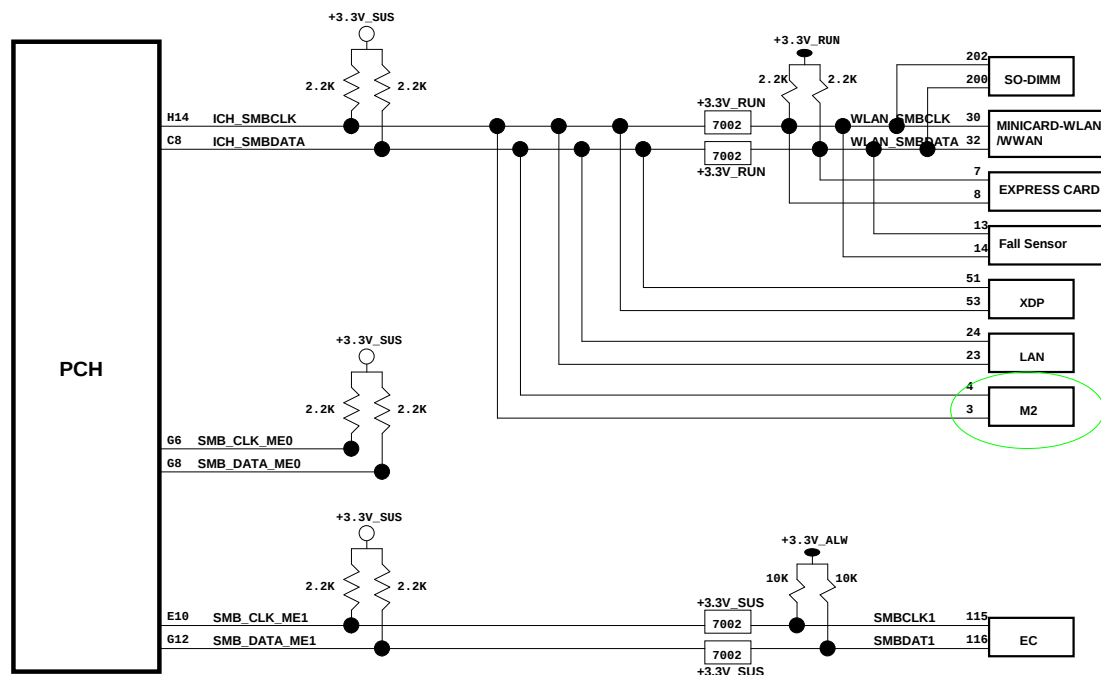


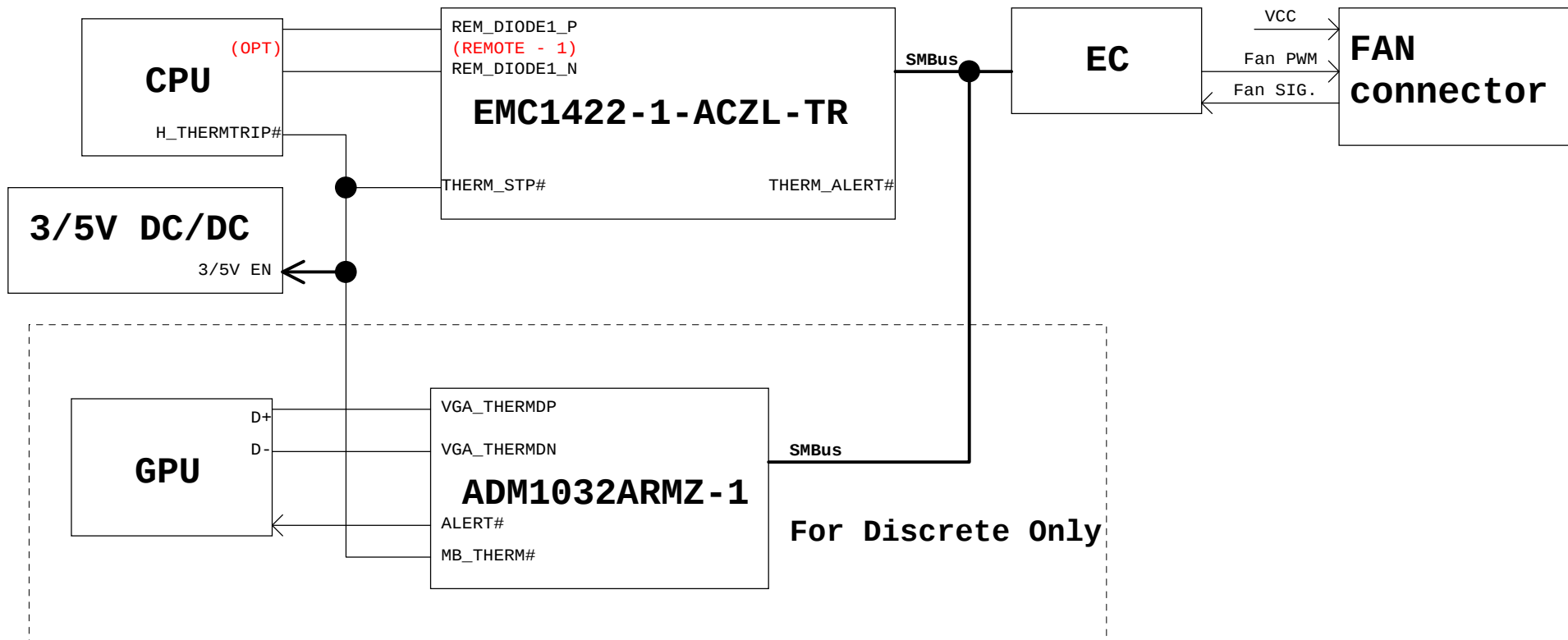
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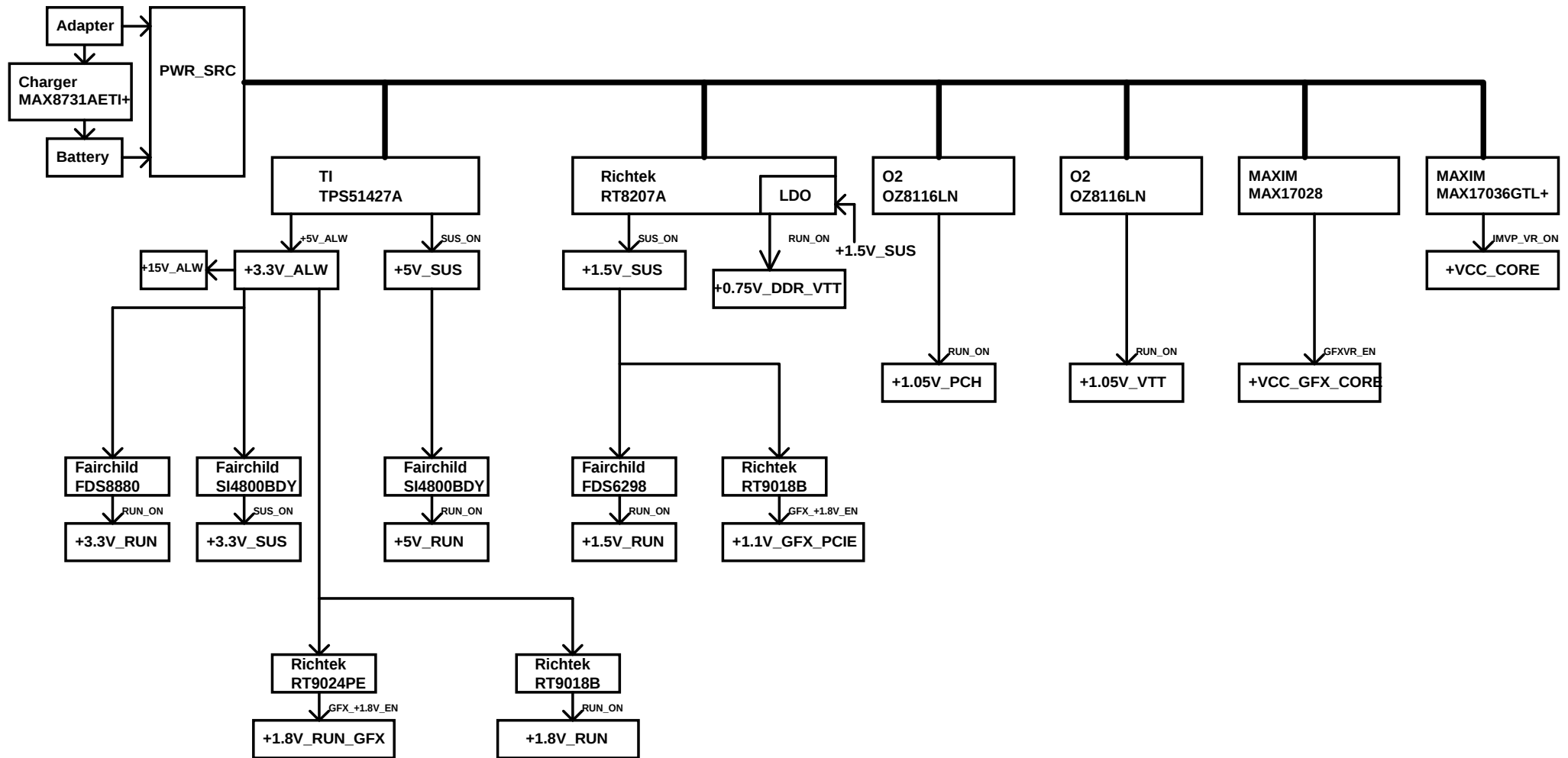


QUANTA
COMPUTER

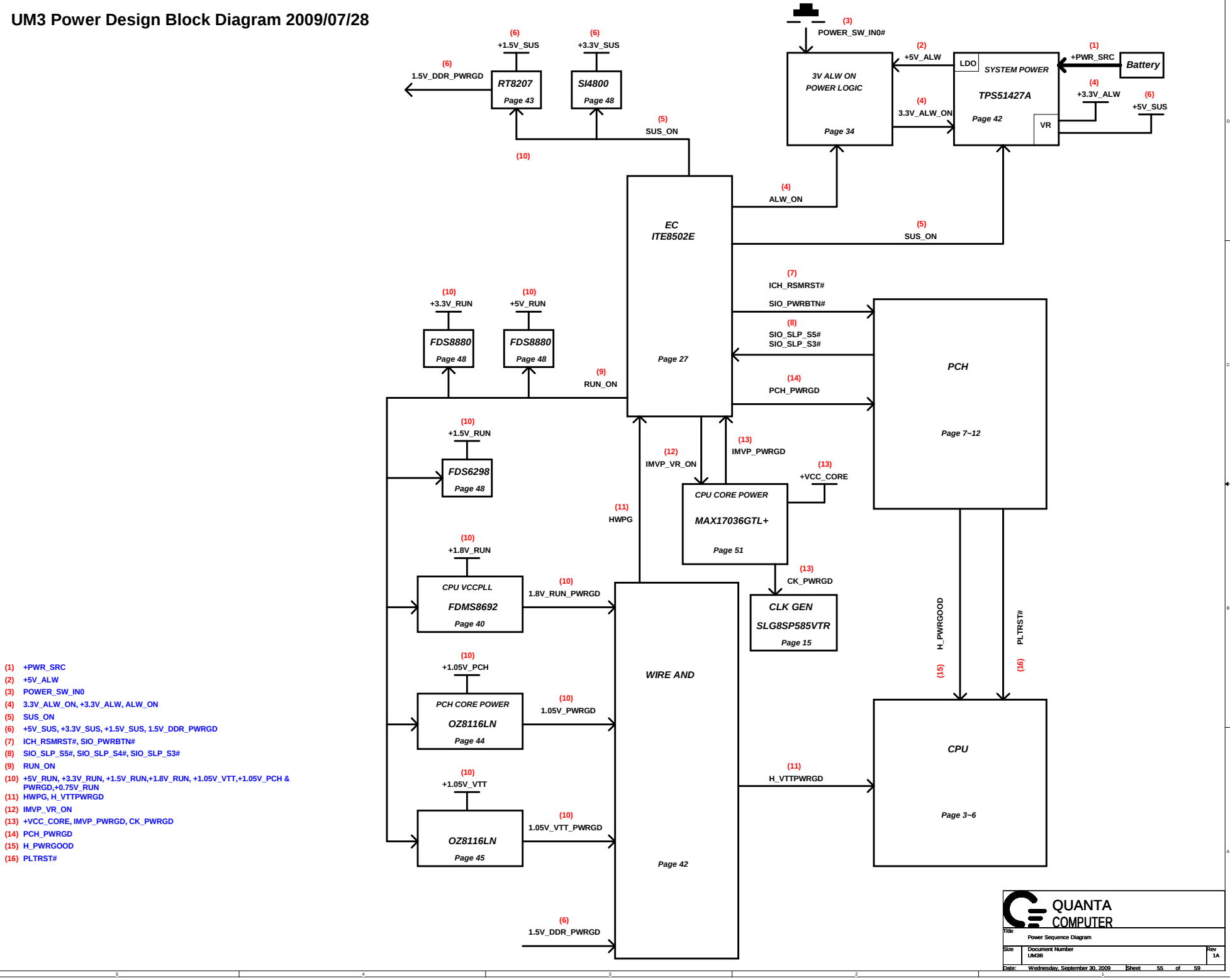
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Size	Document Number	Rev
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UM3 Power Design Block Diagram 2009/07/28



1	2	3	4	5	6	7	8
A							
B							
C							
D							
1	2	3	4	5	6	7	8

		QUANTA COMPUTER	
Title SMBUS BLOCK			
Size	Document Number UM3B/UM6B		Rev 1A
Date:	Wednesday, September 30, 2009	Sheet	56 of 59