

Compal Confidential

KIU00 LA-4501P M/B Schematics Document

2008-08-20

REV:1.0

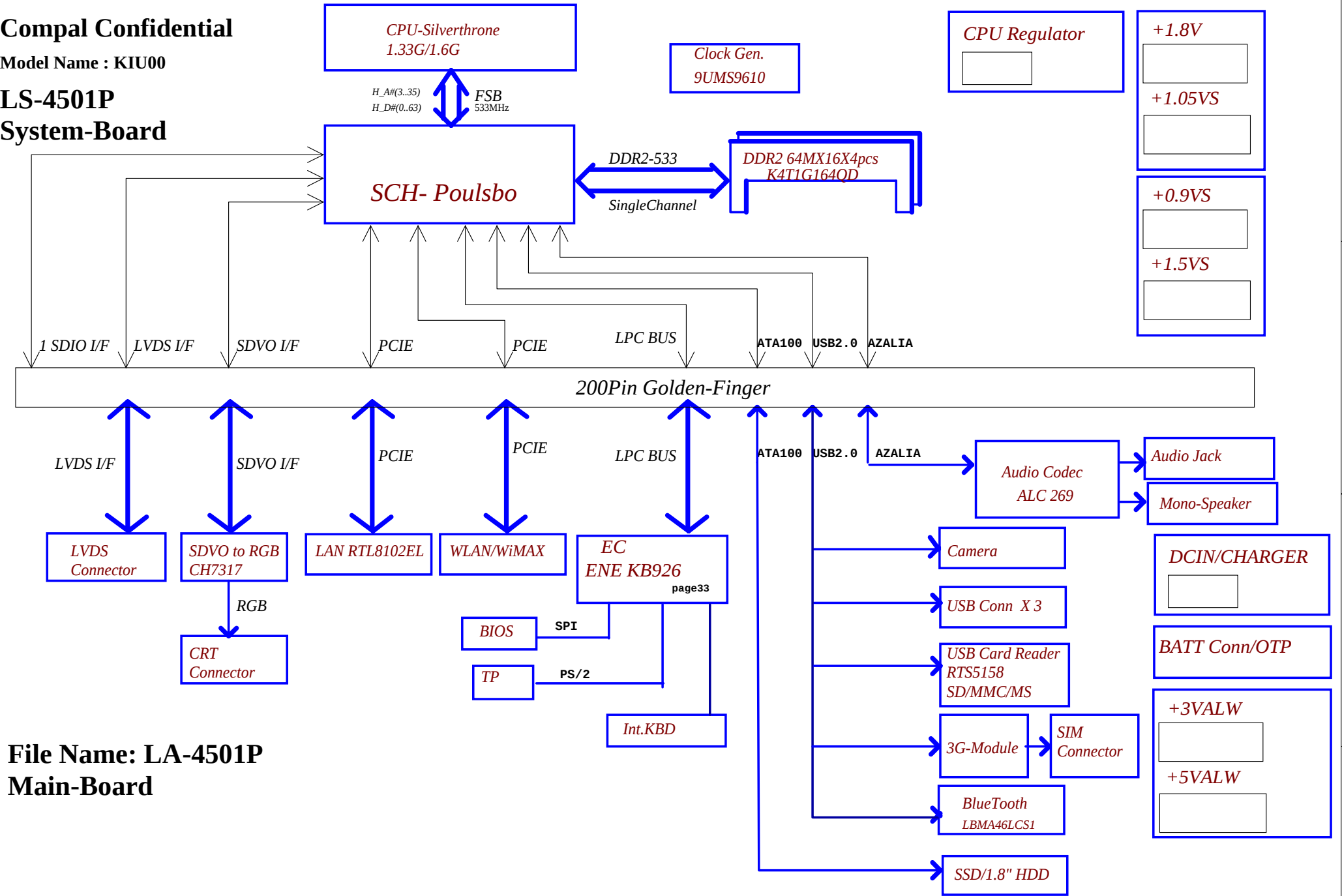
Security Classification		Compal Secret Data		Title	
Issued Date	2008/05/30	Deciphered Date	2011/05/30	Cover Sheet	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Document Number Yao-Ming(Menlow)
				Date:	Rev 1.0
				Sheet 1 of 18	

Compal Confidential

Model Name : KIU00

LS-4501P

System-Board

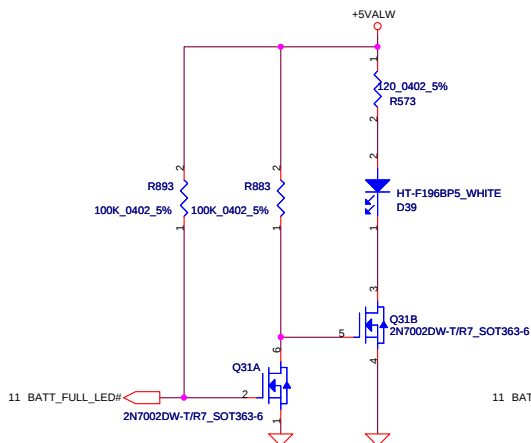


File Name: LA-4501P

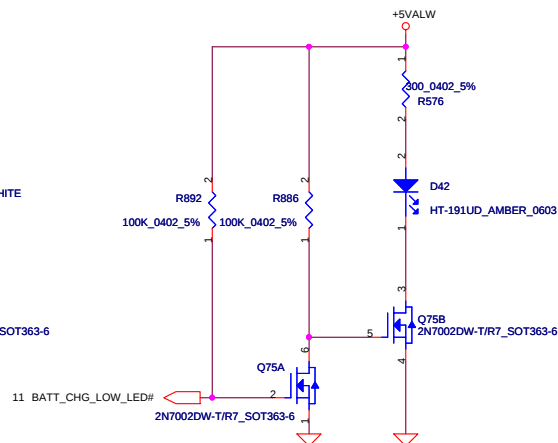
Main-Board

Security Classification		Compal Secret Data		Title	
Issued Date	2008/05/30	Deciphered Date	2011/05/30	Compal Electronics, Inc.	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custort	Yao-Ming(Menlow)
				Date:	Wednesday, August 20, 2008
				Sheet	2 of 18
				Rev	1.0

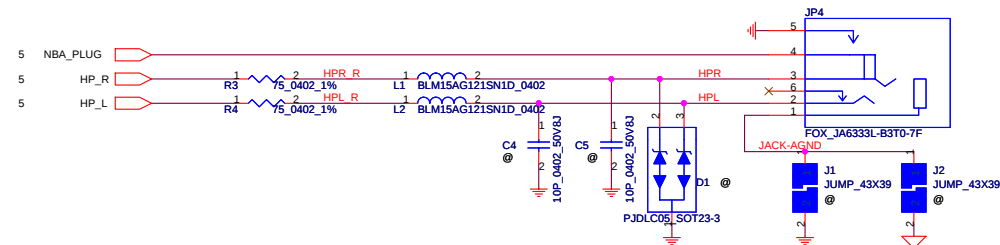
FULL LED



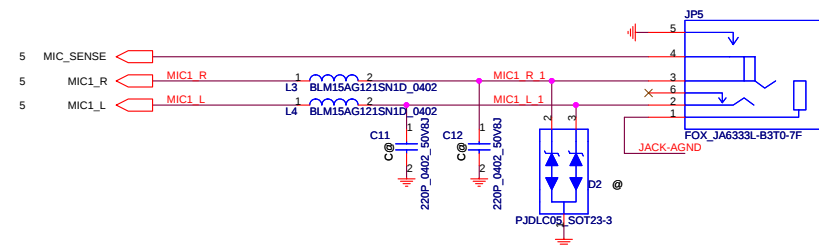
BATT CHARGE



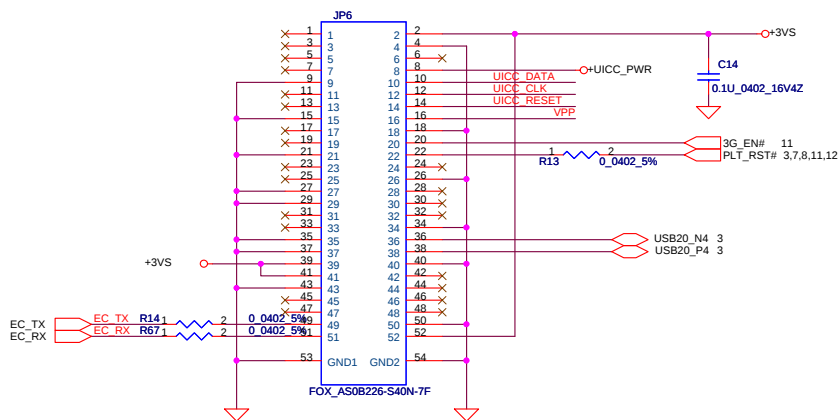
HeadPhone JACK



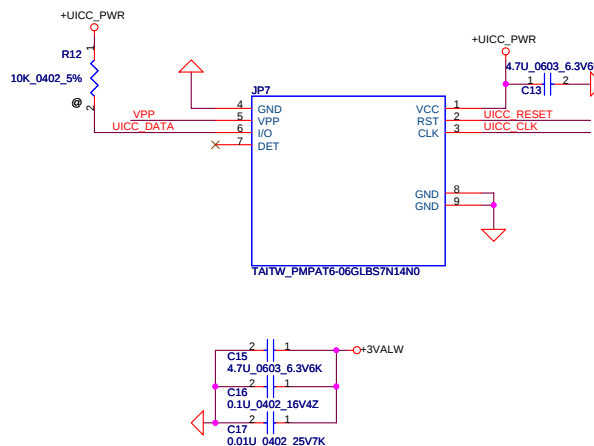
Ext.MIC/LINE IN JACK



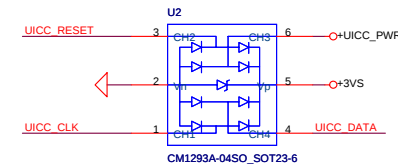
PCIe Mini Card for 3G



SIM Connector

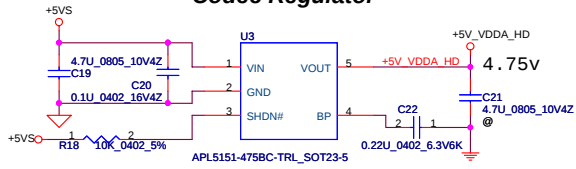


Cap close to JP7

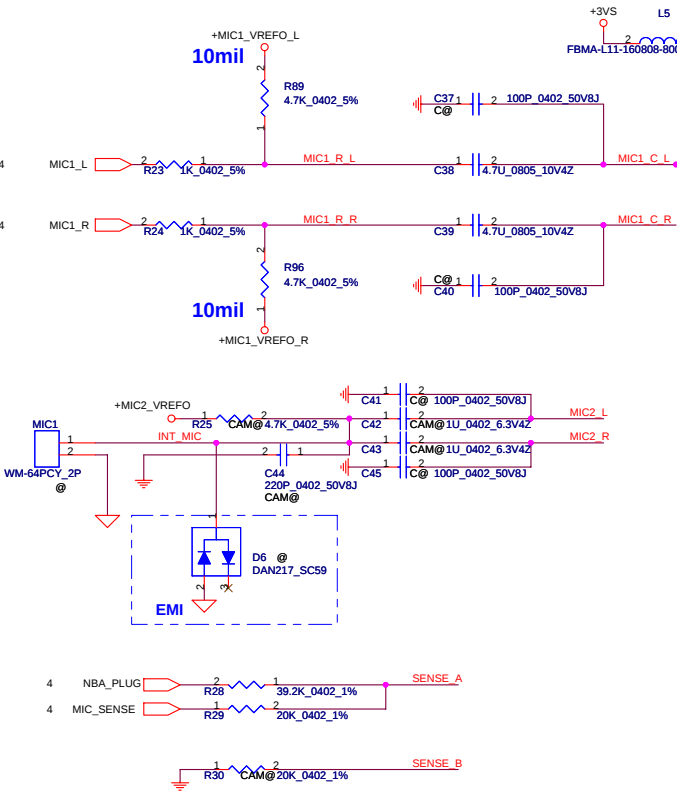


Security Classification				Compal Secret Data		Title	
Issued Date	2008/05/30	Deciphered Date	2011/05/30			Size	Document Number
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.						Yao-Ming(Menlow)	Rev 1.0
						Date: Wednesday, August 20, 2008	Sheet 4 of 18

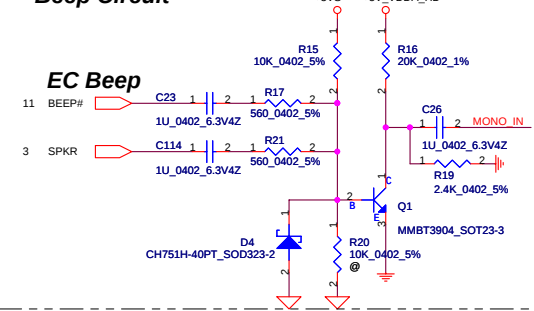
Codec Regulator



INT MIC

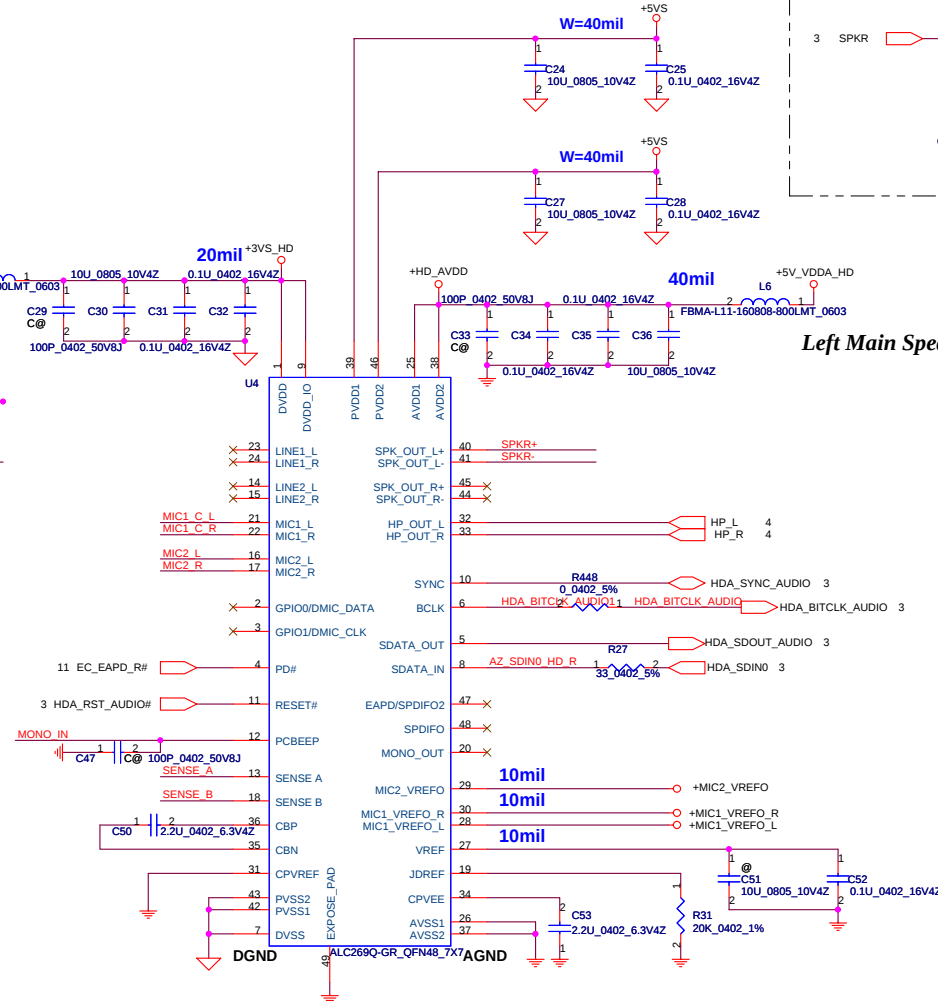
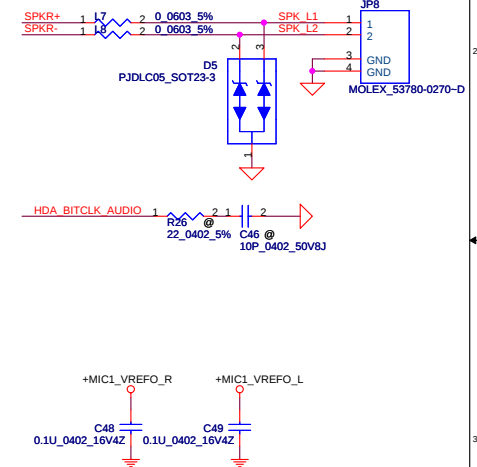


Beep Circuit

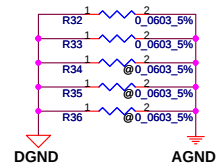


Left Main Speaker Connector

Speaker Conn.



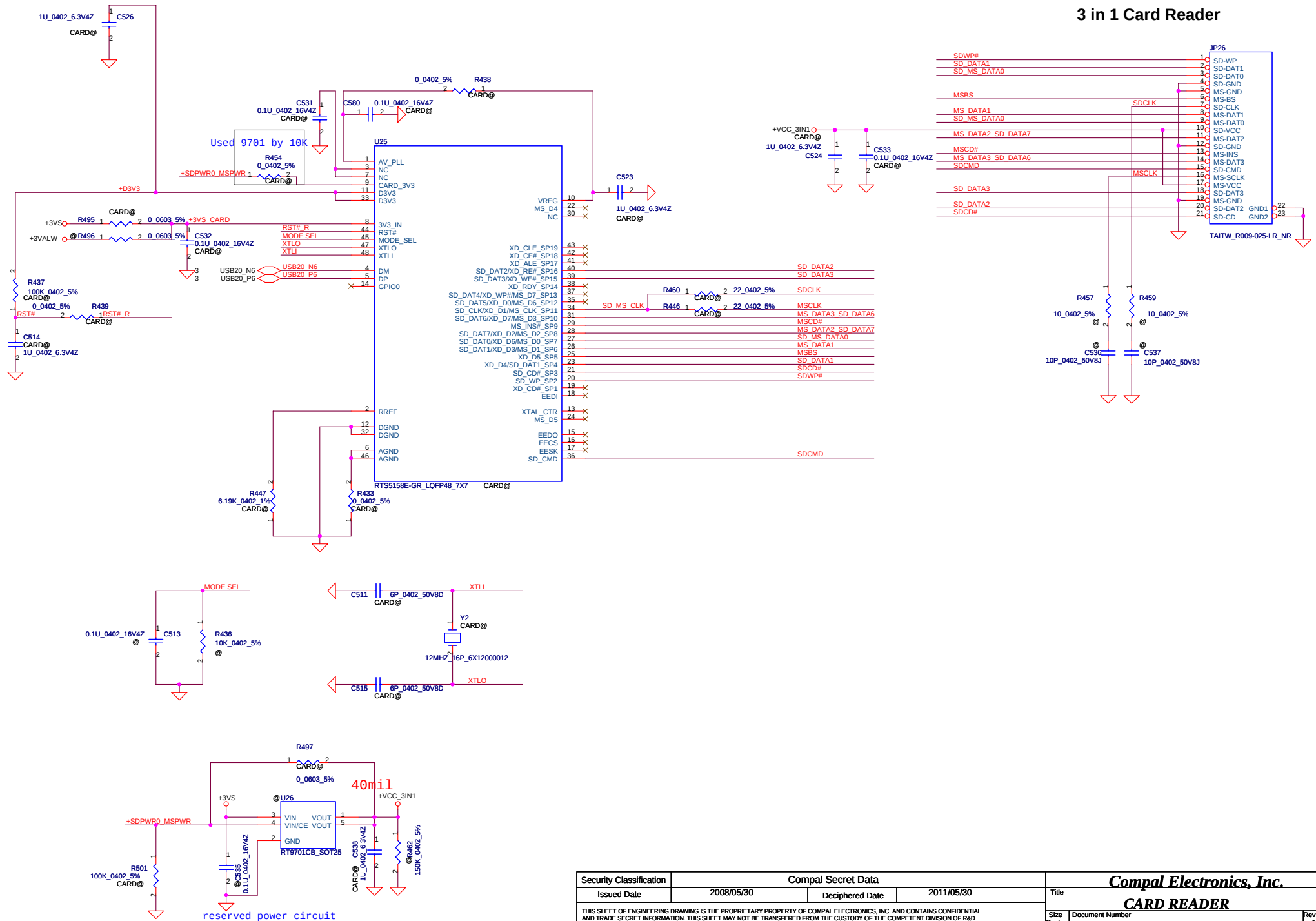
DGND To AGND Bypass



Sense Pin	Impedance	Codec Signals
SENSE A	39.2K	PORT-A (PIN 32, 33)
	20K	PORT-B (PIN 21, 22)
	10K	PORT-C (PIN 23, 24)
	5.1K	PORT-D (PIN 48)
SENSE B	39.2K	PORT-E (PIN 14, 15)
	20K	PORT-F (PIN 16, 17)
	10K	PORT-G (PIN 20)
	5.1K	PORT-H (PIN 47)

Security Classification		Compal Secret Data		Compal Electronics, Inc.			
Issued Date		2008/05/30	Deciphered Date	2011/05/30	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					HDA-ALC269Q-GR		
					Size	Document Number	Rev 1.0
					Custmr	Yao-Ming(Menlow)	
					Date:	Wednesday, August 20, 2008	

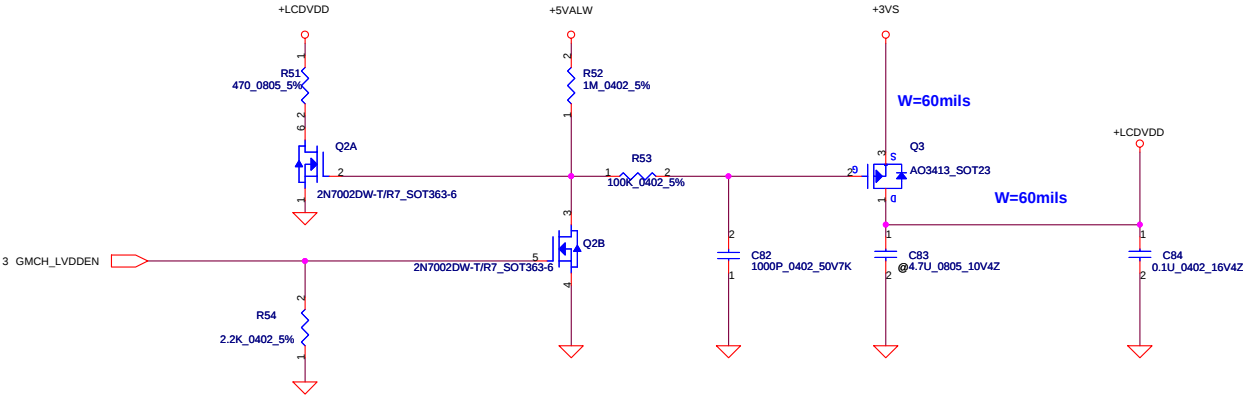
3 in 1 Card Reader



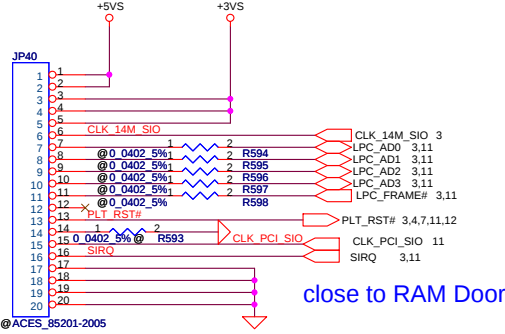
Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				2008/05/30				Title			
				Deciphered Date				2011/05/30			
								Size			
								Document Number			
								Yao-Ming(Menlow)			
								Date			
								Wednesday, August 20, 2008			
								Sheet 6 of 18			
								Rev 1.0			

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

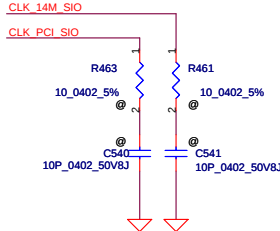
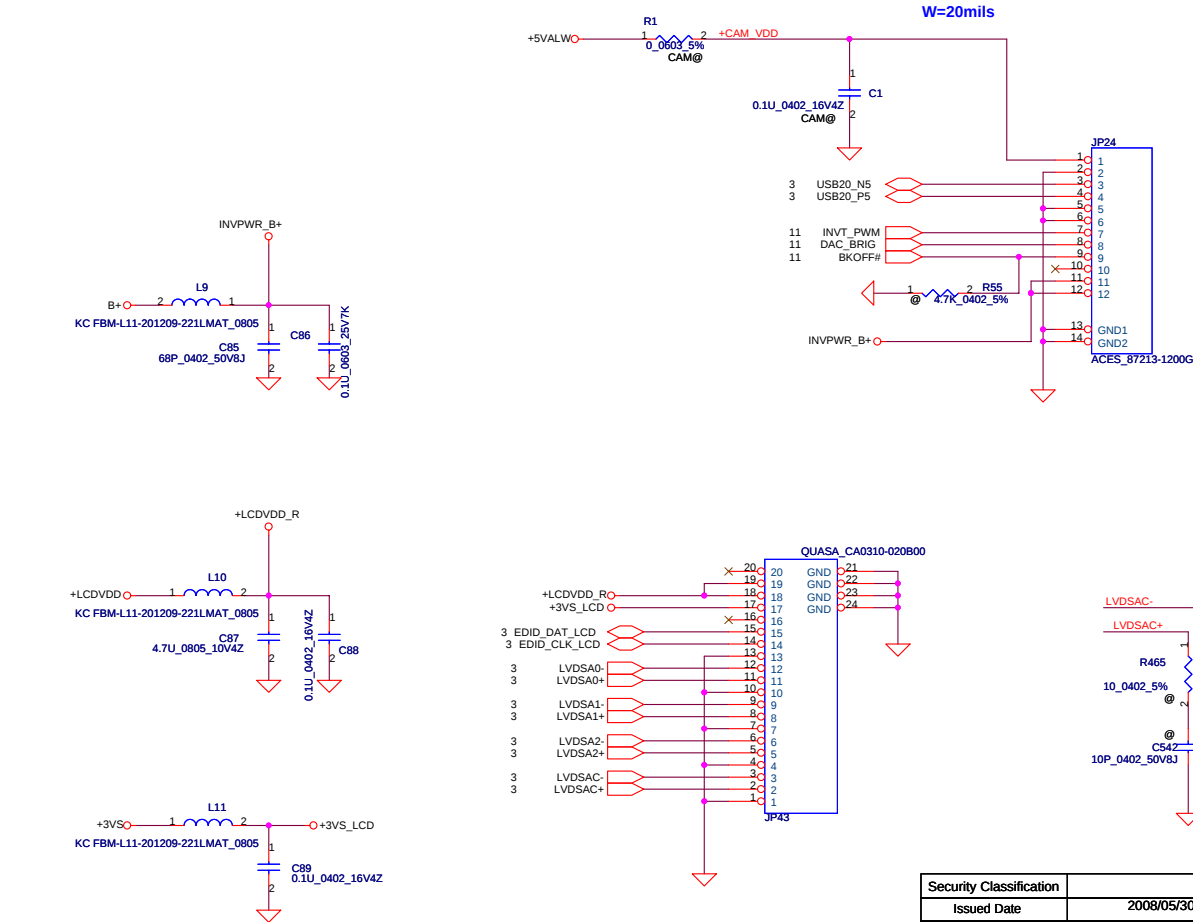
LCD POWER CIRCUIT



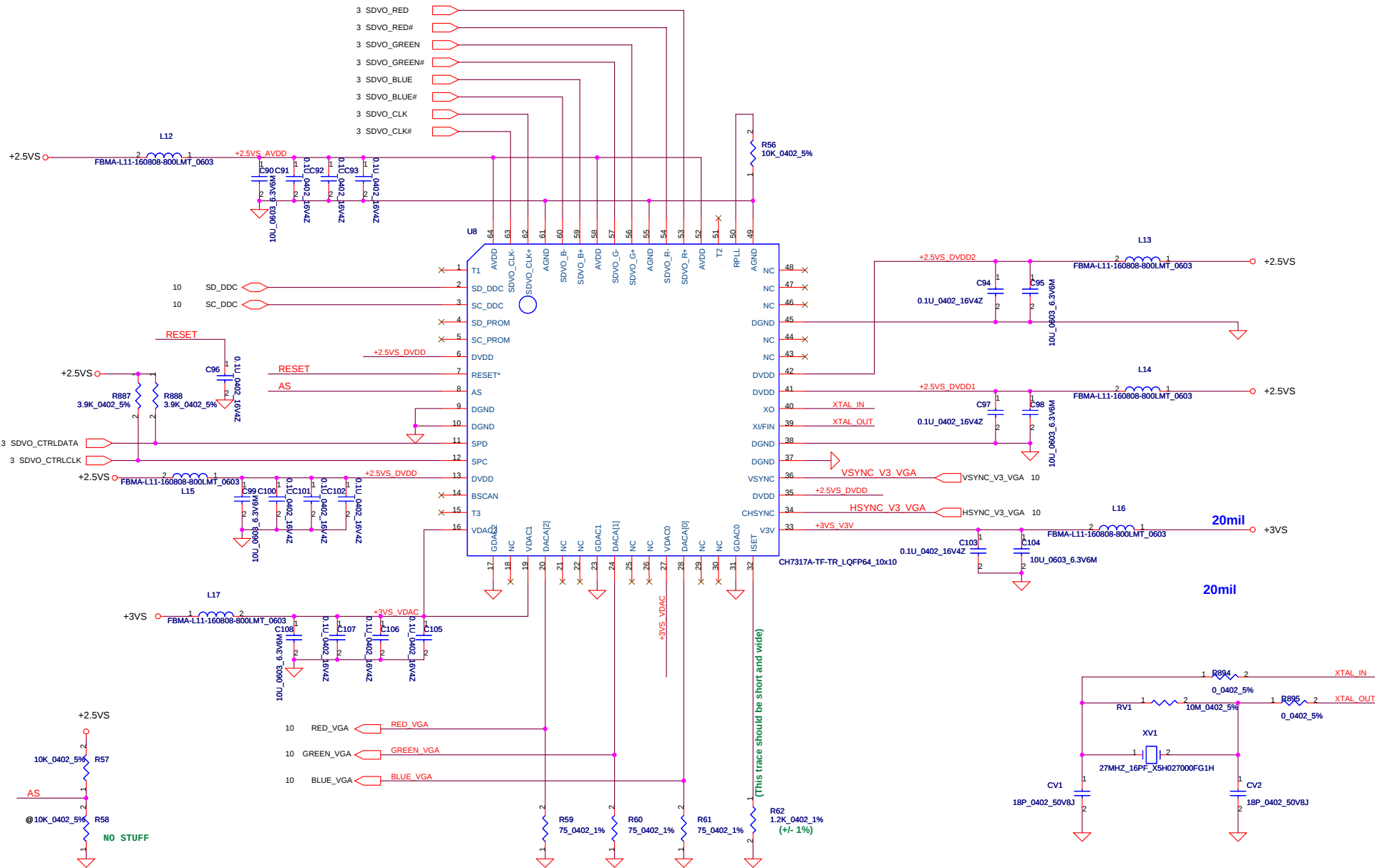
LPC Debug Port



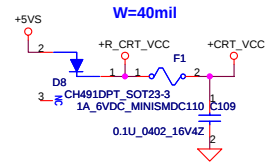
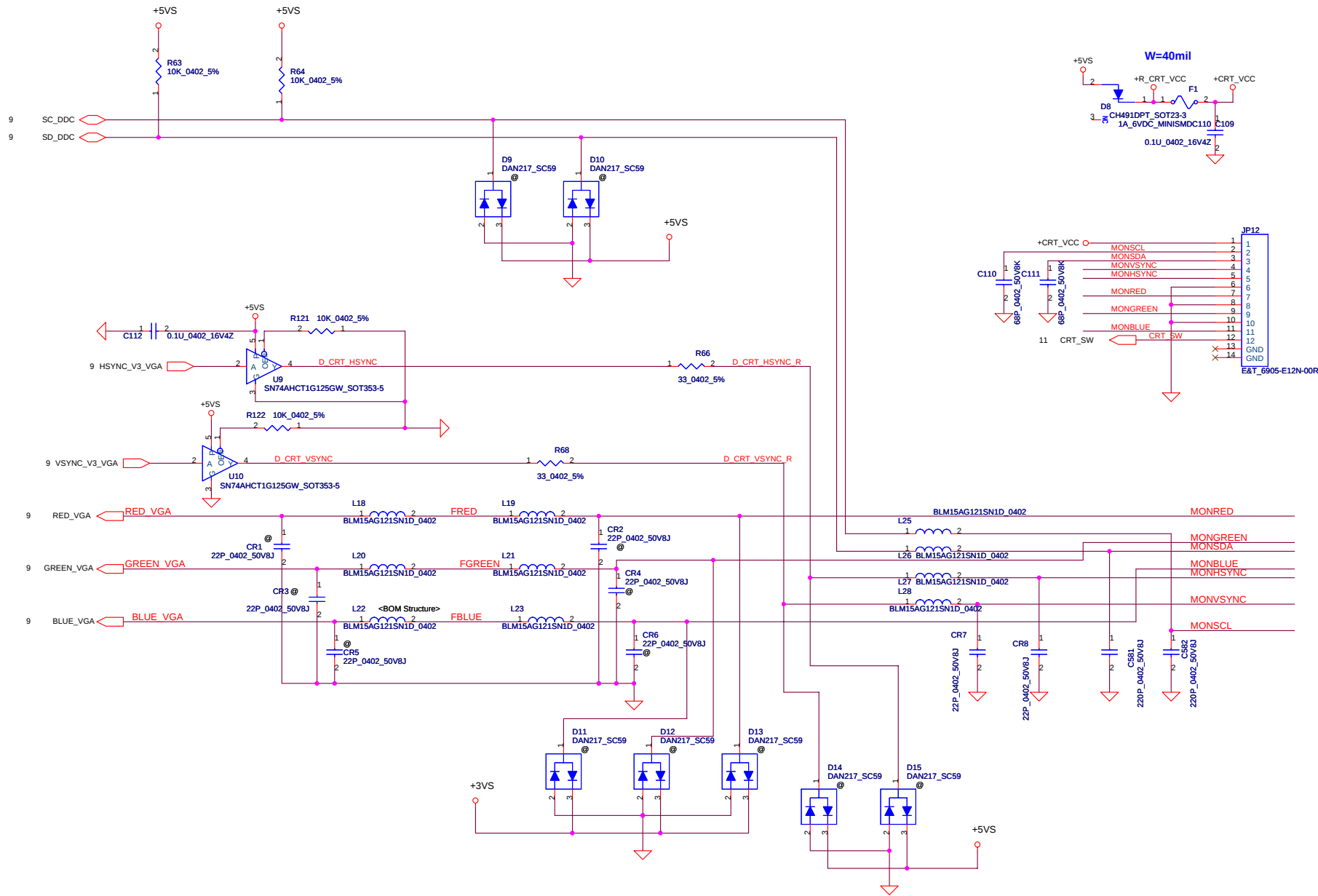
LCD/PANEL BD. Conn.



Security Classification		Compal Secret Data		Title	
Issued Date	2008/05/30	Deciphered Date	2011/05/30	Compal Electronics, Inc.	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Document Number
				Yao-Ming(Menlow)	Rev 1.0
				Date: Wednesday, August 20, 2008	Sheet 8 of 18



Security Classification		Compal Secret Data		Title	
Issued Date	2008/05/30	Deciphered Date	2011/05/30	CH7317	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Custom	1.0
				Document Number	
				Yao-Ming(Menlow)	
				Date:	Wednesday, August 20, 2008
				Sheet	9 of 18



Security Classification		Compal Secret Data		Title	
Issued Date	2008/05/30	Deciphered Date	2011/05/30	VGA BYPASS	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Document Number
				Date	Wednesday, August 20, 2008
				Sheet	10 of 18
				Rev	1.0

Compal Electronics, Inc.

VGA BYPASS

Document Number

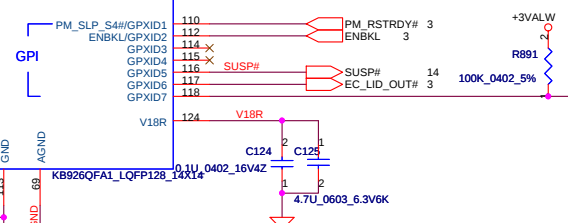
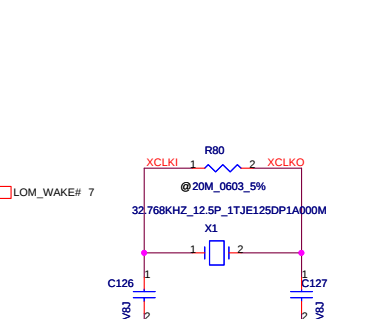
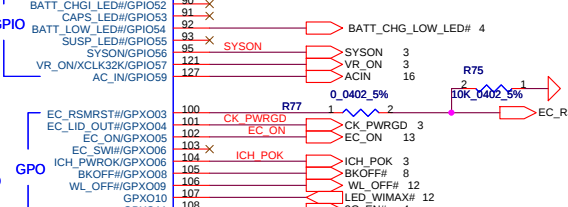
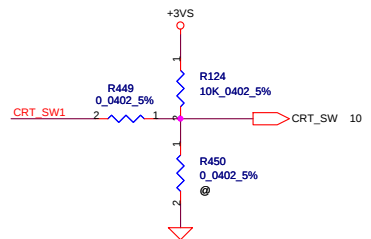
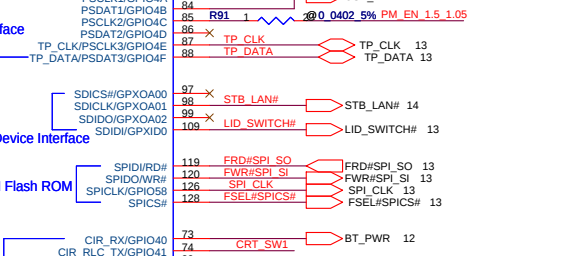
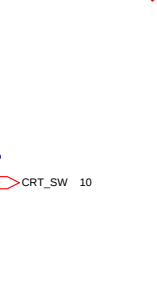
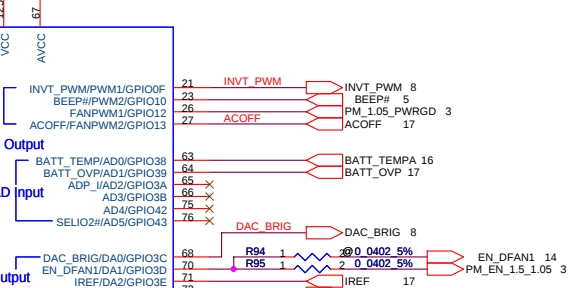
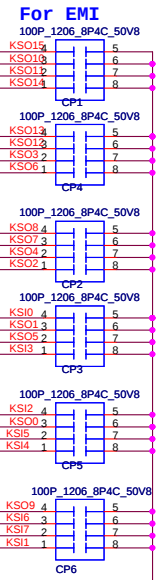
Yao-Ming(Menlow)

Date: Wednesday, August 20, 2008

Sheet 10 of 18

Rev

1.0



Security Classification		Compal Secret Data		Compal Electronics, Inc. ENE-KB926/KB Conn	
Issued Date	2008/05/30	Deciphered Date	2011/05/30	Title	ENE-KB926/KB Conn Yao-Ming(Menlow)
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Document Number Yao-Ming(Menlow)	
				Date: Wednesday, August 20, 2008	Rev 1.0
				Sheet 11 of 18	

The diagram illustrates the internal circuitry of the ACES 89911-5204 module. Key components and connections include:

- Power Connections:**
 - +3V_WLAN:** Connected to pin 1 of the header and through resistor R87 (0.1206 5%) to pin 2.
 - +1.5V_S:** Connected to pin 1 of the header and through capacitor C129 (0.1U_0402_16V4Z) to pin 2.
 - +3V_WLAN:** Connected to pin 1 of the header and through capacitor C132 (0.1U_0402_16V4Z) to pin 2.
- Signal Connections:**
 - WLAN:** Pins 3 and 4 are connected to R89 (0.0402 5%) and R88 (0.0402 5%) respectively.
 - BT:** Pins 5 and 6 are connected to R89 (0.0402 5%) and R88 (0.0402 5%) respectively.
 - PCIe:** Pins 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 57, 58, 59, 60, 61, 62, 63, 64, 65, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79, 80, 81, 82, 83, 84, 85, 86, 87, 88, 89, 90, 91, 92, 93, 94, 95, 96, 97, 98, 99, 100, 101, 102, 103, 104, 105, 106, 107, 108, 109, 110, 111, 112, 113, 114, 115, 116, 117, 118, 119, 120, 121, 122, 123, 124, 125, 126, 127, 128, 129, 130, 131, 132, 133, 134, 135, 136, 137, 138, 139, 140, 141, 142, 143, 144, 145, 146, 147, 148, 149, 150, 151, 152, 153, 154, 155, 156, 157, 158, 159, 160, 161, 162, 163, 164, 165, 166, 167, 168, 169, 170, 171, 172, 173, 174, 175, 176, 177, 178, 179, 180, 181, 182, 183, 184, 185, 186, 187, 188, 189, 190, 191, 192, 193, 194, 195, 196, 197, 198, 199, 200, 201, 202, 203, 204, 205, 206, 207, 208, 209, 210, 211, 212, 213, 214, 215, 216, 217, 218, 219, 220, 221, 222, 223, 224, 225, 226, 227, 228, 229, 230, 231, 232, 233, 234, 235, 236, 237, 238, 239, 240, 241, 242, 243, 244, 245, 246, 247, 248, 249, 250, 251, 252, 253, 254, 255, 256, 257, 258, 259, 260, 261, 262, 263, 264, 265, 266, 267, 268, 269, 270, 271, 272, 273, 274, 275, 276, 277, 278, 279, 280, 281, 282, 283, 284, 285, 286, 287, 288, 289, 290, 291, 292, 293, 294, 295, 296, 297, 298, 299, 300, 301, 302, 303, 304, 305, 306, 307, 308, 309, 310, 311, 312, 313, 314, 315, 316, 317, 318, 319, 320, 321, 322, 323, 324, 325, 326, 327, 328, 329, 330, 331, 332, 333, 334, 335, 336, 337, 338, 339, 340, 341, 342, 343, 344, 345, 346, 347, 348, 349, 350, 351, 352, 353, 354, 355, 356, 357, 358, 359, 360, 361, 362, 363, 364, 365, 366, 367, 368, 369, 370, 371, 372, 373, 374, 375, 376, 377, 378, 379, 380, 381, 382, 383, 384, 385, 386, 387, 388, 389, 390, 391, 392, 393, 394, 395, 396, 397, 398, 399, 400, 401, 402, 403, 404, 405, 406, 407, 408, 409, 410, 411, 412, 413, 414, 415, 416, 417, 418, 419, 420, 421, 422, 423, 424, 425, 426, 427, 428, 429, 430, 431, 432, 433, 434, 435, 436, 437, 438, 439, 440, 441, 442, 443, 444, 445, 446, 447, 448, 449, 450, 451, 452, 453, 454, 455, 456, 457, 458, 459, 460, 461, 462, 463, 464, 465, 466, 467, 468, 469, 470, 471, 472, 473, 474, 475, 476, 477, 478, 479, 480, 481, 482, 483, 484, 485, 486, 487, 488, 489, 490, 491, 492, 493, 494, 495, 496, 497, 498, 499, 500, 501, 502, 503, 504, 505, 506, 507, 508, 509, 510, 511, 512, 513, 514, 515, 516, 517, 518, 519, 520, 521, 522, 523, 524, 525, 526, 527, 528, 529, 530, 531, 532, 533, 534, 535, 536, 537, 538, 539, 540, 541, 542, 543, 544, 545, 546, 547, 548, 549, 550, 551, 552, 553, 554, 555, 556, 557, 558, 559, 560, 561, 562, 563, 564, 565, 566, 567, 568, 569, 570, 571, 572, 573, 574, 575, 576, 577, 578, 579, 580, 581, 582, 583, 584, 585, 586, 587, 588, 589, 590, 591, 592, 593, 594, 595, 596, 597, 598, 599, 600, 601, 602, 603, 604, 605, 606, 607, 608, 609, 610, 611, 612, 613, 614, 615, 616, 617, 618, 619, 620, 621, 622, 623, 624, 625, 626, 627, 628, 629, 630, 631, 632, 633, 634, 635, 636, 637, 638, 639, 640, 641, 642, 643, 644, 645, 646, 647, 648, 649, 650, 651, 652, 653, 654, 655, 656, 657, 658, 659, 660, 661, 662, 663, 664, 665, 666, 667, 668, 669, 670, 671, 672, 673, 674, 675, 676, 677, 678, 679, 680, 681, 682, 683, 684, 685, 686, 687, 688, 689, 690, 691, 692, 693, 694, 695, 696, 697, 698, 699, 700, 701, 702, 703, 704, 705, 706, 707, 708, 709, 710, 711, 712, 713, 714, 715, 716, 717, 718, 719, 720, 721, 722, 723, 724, 725, 726, 727, 728, 729, 730, 731, 732, 733, 734, 735, 736, 737, 738, 739, 740, 741, 742, 743, 744, 745,

JP18

Pin	Signal	Pin	Signal
1	PLT_RST#	21	PD_D15
2		22	PD_DREQ
3		23	PD_IOW#
4		24	PD_IOR#
5	PD_D7	25	PD_IORDY
6	PD_D8	26	PD_DACK#
7	PD_D6	27	PD_IRQ
8	PD_D9	28	PD_A1
9	PD_D5	29	PD_A0
10	PD_D10	30	PD_A2
11	PD_D4	31	PD_CS#1
12	PD_D11	32	PD_CS#3
13	PD_D3	33	
14	PD_D12	34	
15	PD_D2	35	
16	PD_D13	36	
17	PD_D1	37	
18	PD_D14	38	
19	PD_D0	39	
20	PD_D15	40	

ACES_87151-40051

1U_0402_5.3V4Z

100U_0603_5.3V6M

0.1U_0402_16V7K

+3VS

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/05/30	Deciphered Date	2011/05/30	Title	WLAN/BT/PATA/USB B
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Document Number	1.0
				Yao-Ming(Menlow)	
Date:	Wednesday, August 20, 2008	Sheet	12	of	18

ON/OFFBTN#

JP42

1 NC1 3 X

2 NC2 4 X

E&T_3802-E02N-01R

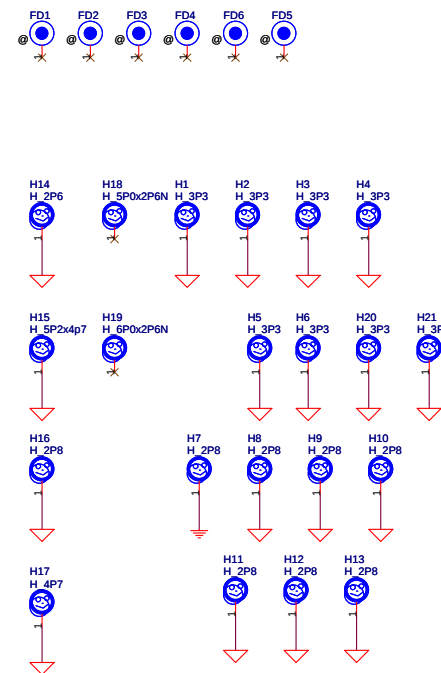
The schematic diagram illustrates the ON/OFF control circuit for the TJC533-V-T/R_6P. The circuit is divided into two sections: TOP Side and Bottom Side. On the TOP Side, a switch SW3 is connected to the ON/OFF_BTN# input. The switch controls the gate of a MOSFET Q9 (2N7002LT1G_SOT23-3) through a gate resistor R100 (10K_0402_5%). The MOSFET's source is connected to ground, and its drain is connected to the ON/OFF_EC# output (pin 11) and the 51_ON# output (pin 15). A diode D18 (DAN202UT106_SC70-3) is connected in parallel with the MOSFET's drain, with its anode to the output and its cathode to the +3V_ALW supply. A resistor R99 (100K_0402_5%) is connected between the +3V_ALW supply and the output. On the Bottom Side, a diode D19 (RLZ20A_LL34) is connected in parallel with the MOSFET's drain, with its anode to the output and its cathode to ground. A capacitor C149 (1000P_0402_50V7K) is connected between the output and ground. The circuit is connected to a +3V_ALW supply and ground.

Figure 10 shows the pin connections for the E&T_2941-G08N-00E-D. The diagram illustrates the connections for the FSEL#SPICS#, SPI_CLK, FWR#SPI_SI, and FRD#SPI_SO signals. The connections are as follows:

- Pin 11 (FSEL#SPICS#) connects to R101 (0.0402 5%) and INT_SPI_CS#.
- Pin 11 (SPI_CLK) connects to R102 (0.0402 5%) and SPI_CLK_R.
- Pin 11 (FWR#SPI_SI) connects to R103 (0.0402 5%) and SPI_SI.
- Pin 11 (FRD#SPI_SO) connects to R104 (0.0402 5%) and SPI_SO.

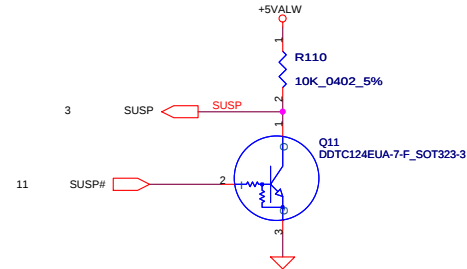
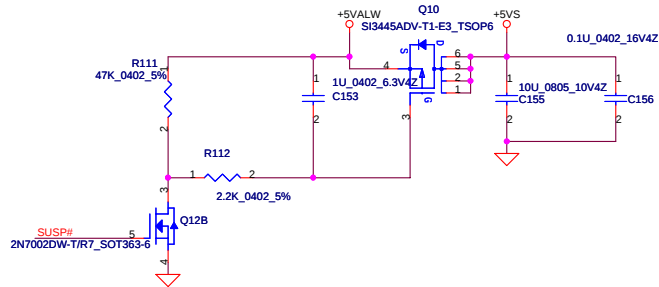
The diagram also shows a 100K_0402_5% resistor (R106) connected between the FSEL#SPICS# and SPI_CLK lines. The JP22 connector is shown with pins 1 through 8, and the component is labeled E&T_2941-G08N-00E-D.

The schematic diagram illustrates the internal components of the SA00001MP00 module. At the top, two power pins, +3VALW and +3VALV, are shown. The +3VALW pin is connected to a 3V3 supply and a 10k resistor (R105) leading to pin 1 of the AT24C16BN-SH-T SOIC 8P EEPROM (U17). The +3VALV pin is connected to a 3V3 supply and a 10k resistor (R107) leading to pin 1 of the SA00001MP00 sensor. The SA00001MP00 sensor is connected to the AT24C16BN-SH-T SOIC 8P EEPROM via I2C pins SCL (pin 5) and SDA (pin 6). The sensor's address pins A0 (pin 1), A1 (pin 2), and GND (pin 4) are also shown. The sensor's output is connected to the R106 and R108 resistors, which are connected to the +3VALW and +3VALV pins respectively. The sensor's output is also connected to the R109 and R110 resistors, which are connected to the +3VALW and +3VALV pins respectively.

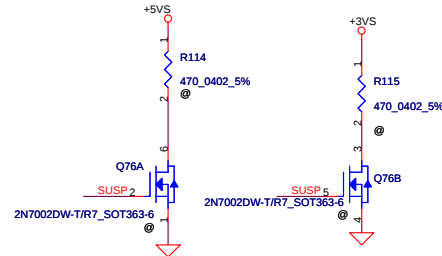
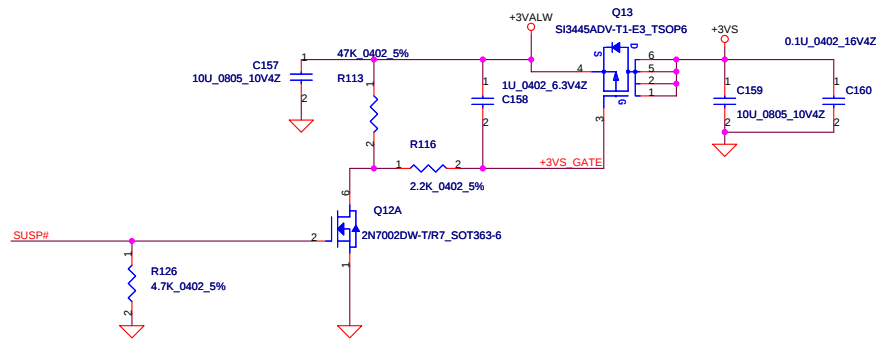


Security Classification		Compal Secret Data		Compal Electronics, Inc.					
Issued Date		2008/05/30	Deciphered Date				2011/05/30		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.						Title	SW/ROM/TP/LID		
						Size Custom	Document Number Yao-Ming(Menlow)		Rev 1.0
						Date:	Wednesday, August 20, 2008		Sheet

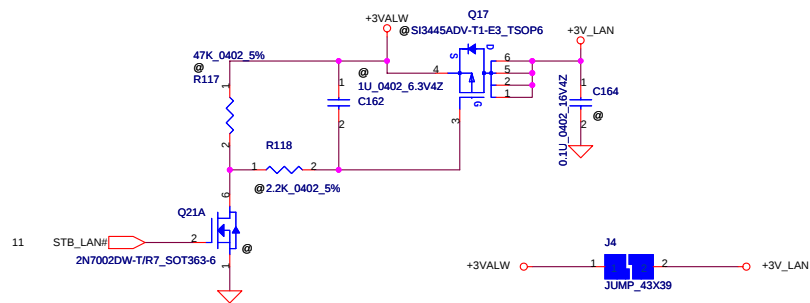
+5VALW to +5VS Transfer



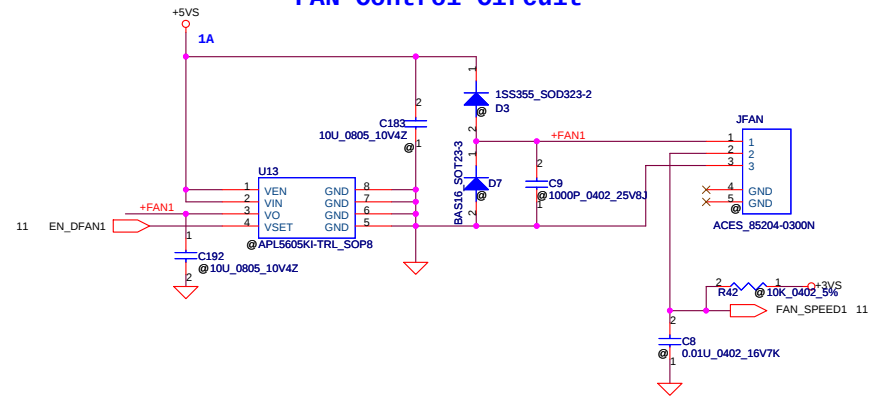
+3VALW to +3VS Transfer



+3VALW to +3V_LAN



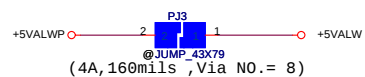
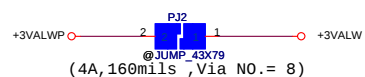
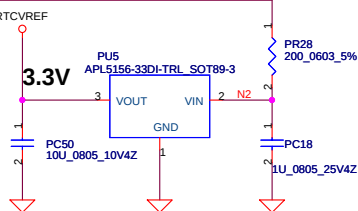
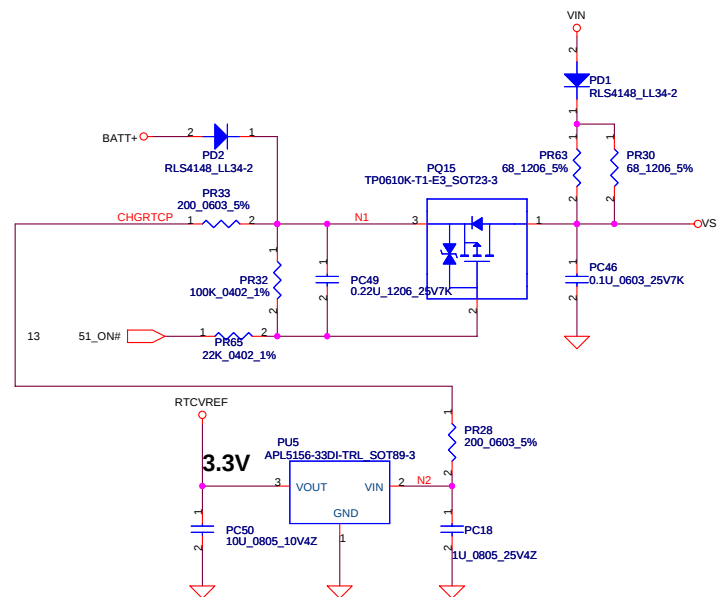
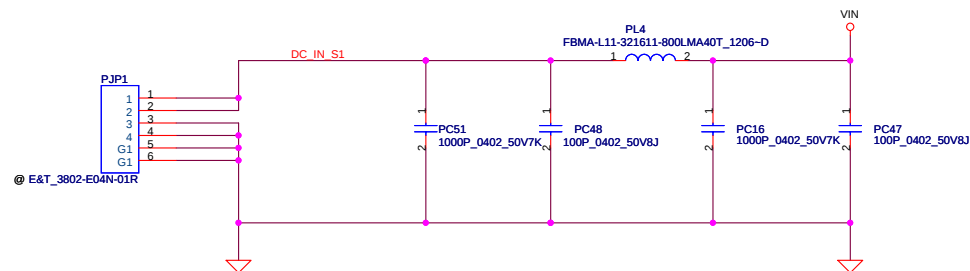
FAN Control Circuit



PRODUCT SUMMARY

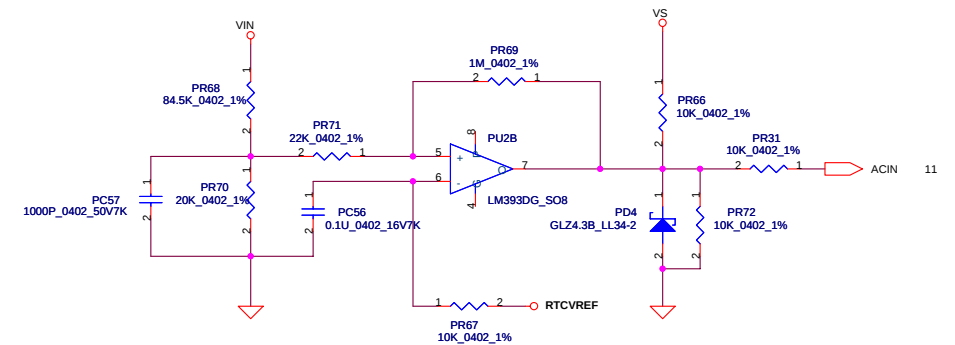
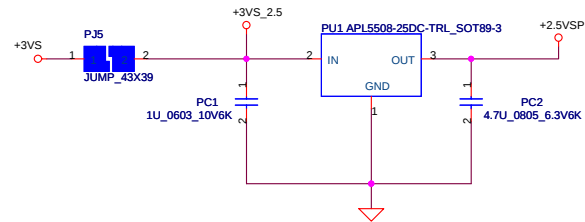
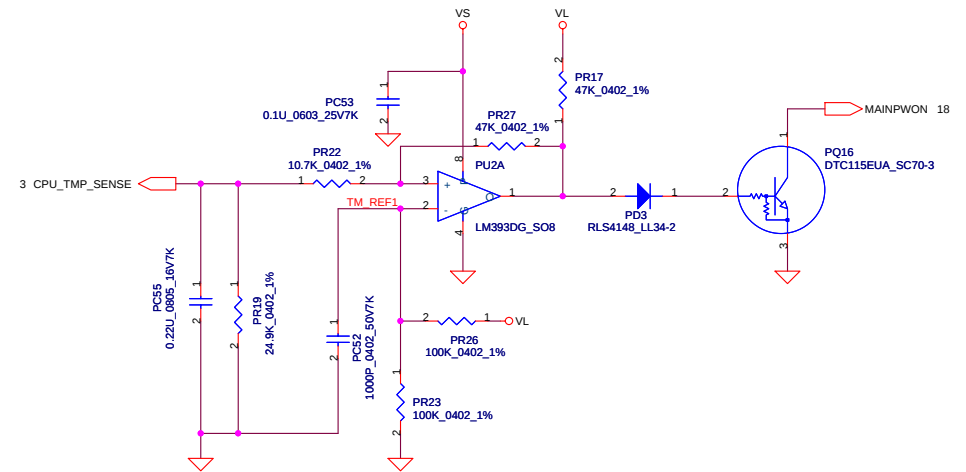
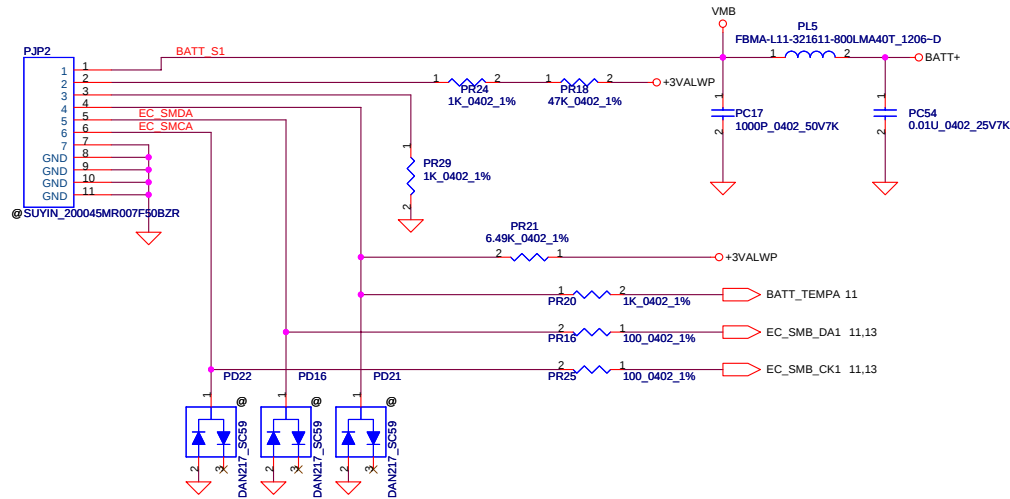
Vgs	rDS(on)	ID (A)
-8	0.042 @ VGS = .4.5 V	+ -5.6
	0.060 @ VGS = .2.5 V	+ -4.7
	0.080 @ VGS = .1.8 V	+ -2.9

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2008/05/30	Deciphered Date	2011/05/30	DC/DC Interface		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custort	Yao-Ming(Menlow)	1.0
				Date:	Wednesday, August 20, 2008	Sheet 14 of 18



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/05/30	Deciphered Date	2011/05/30	Title	DCIN/DECTOR
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Date	Wednesday, August 20, 2008
				Sheet	15 of 18
				Rev	1.0

PH1 under CPU botten side :
CPU thermal protection at 78 degree C
Recovery at 42 degree C

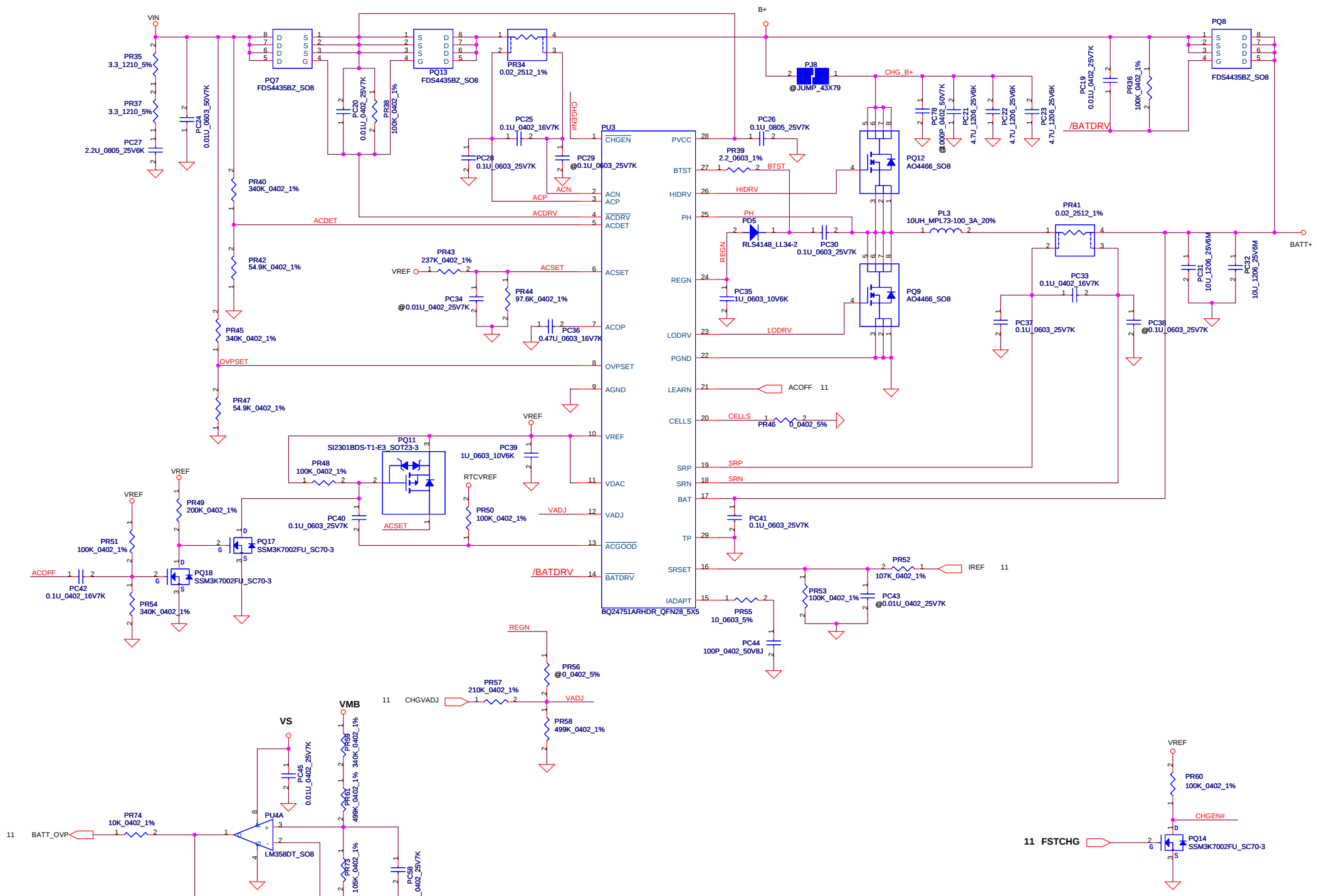


Vin Detector

High 18.384 17.901 17.430
Low 17.728 17.257 16.976

Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				2008/05/30				Title			
				Deciphered Date				BATTERY CONN / OTP			
				2011/05/30				Size			
								Document Number			
								Rev			
								1.0			
								Date: Wednesday, August 20, 2008			
								Sheet 16 of 18			

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/05/30	Deciphered Date	2011/05/30	Title	CHARGER
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Date:	Wednesday, August 20, 2008
				Sheet	17 of 18
				Rev	1.0

