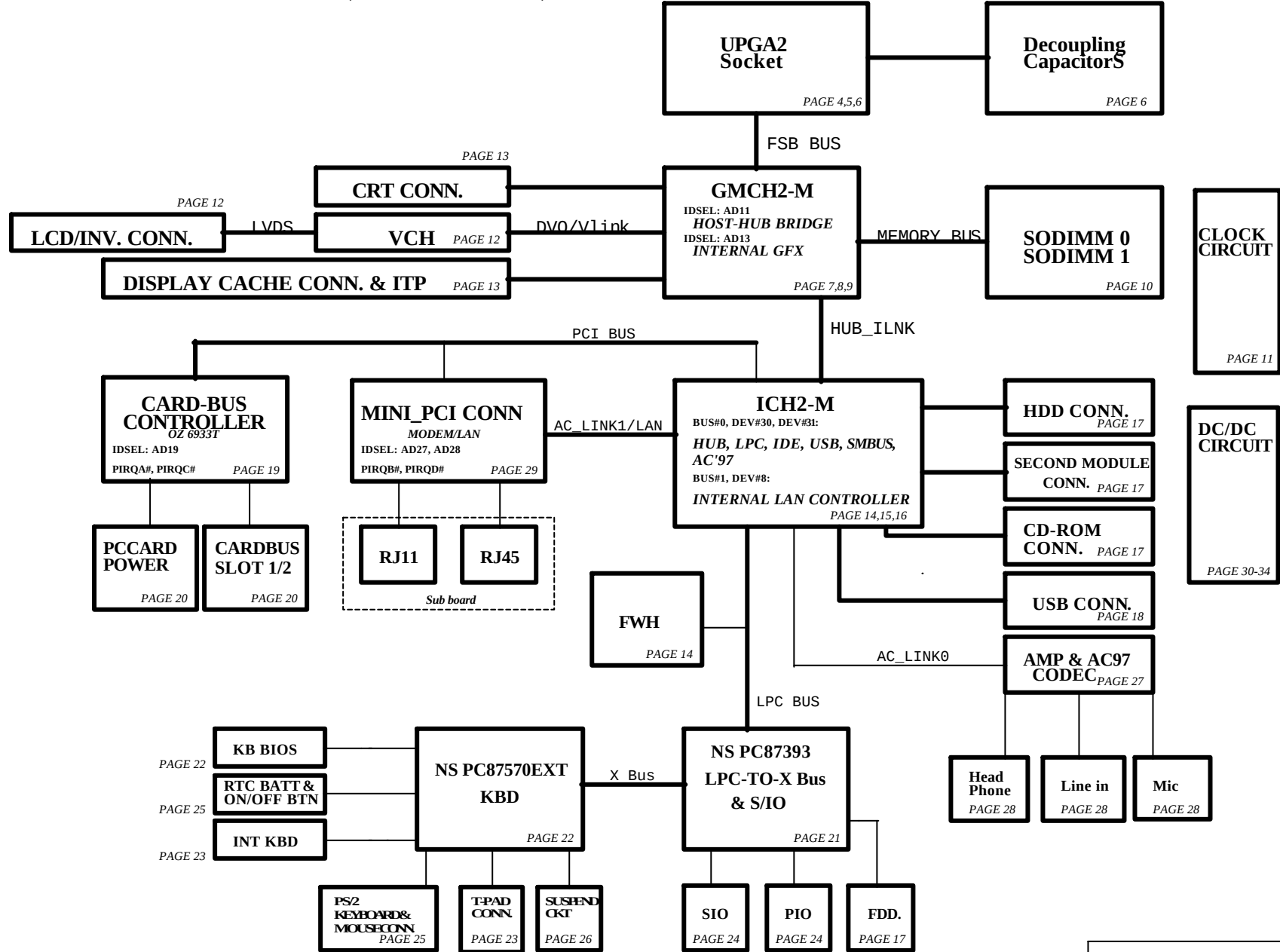


COMPAL CONFIDENTIAL
MODEL NAME : 888L2(SOLANO2-M)
Date: 01/11/01
Version: 2.0

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION . THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.		
Title	Cover Sheet	
Size	Document Number	Rev
	888L2 Main Board	2.0
Date:	Thursday, January 11, 2001	Sheet 1 of 42

MODEL NAME : 888L2 (SOLANO2-M)



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
Title			
System Block Diagram			
Size		Document Number	Rev
		888L2 Main Board	20
Date:		Thursday, January 11, 2001	Sheet 2 of 42

Revision History			
#	Date	Description	Version
1	2000/7/21	First Release <i>(EVT-SST)</i>	0.1
2	2000/9/20	Second Release <i>(DVT1-PT1)</i>	0.2A
3	2000/11/3	Third Release <i>(DVT2-PT2)</i>	0.2C1
4	2000/12/27	Fouth Release <i>(ST)</i>	1B
5	2001/01/11	fifth release <i>(QT)</i>	2.0

Chip information

GMCH2-M FW82815EM

Version	S-spec.	Q-spec.
A0		QA38
A1	SL4MP	QA75

ICH2-M FW82801BAM

Version	S-spec.	Q-spec.
A0		Q908, Q909, Q910, Q911, Q912
B0	SL45HQ	Q967, Q968
B1	SL4HN	QA36, QA37
B2		QA56, QA57

VCH FW82807AA

Version	S-spec.	Q-spec.
A0		Q989
A1		QA76
A2		QB41ES

EVT *(SST)*

GMCH2-M	QA38ES (A0)
ICH2-M	Q967ES (B0)
VCH	Q989ES (A0)

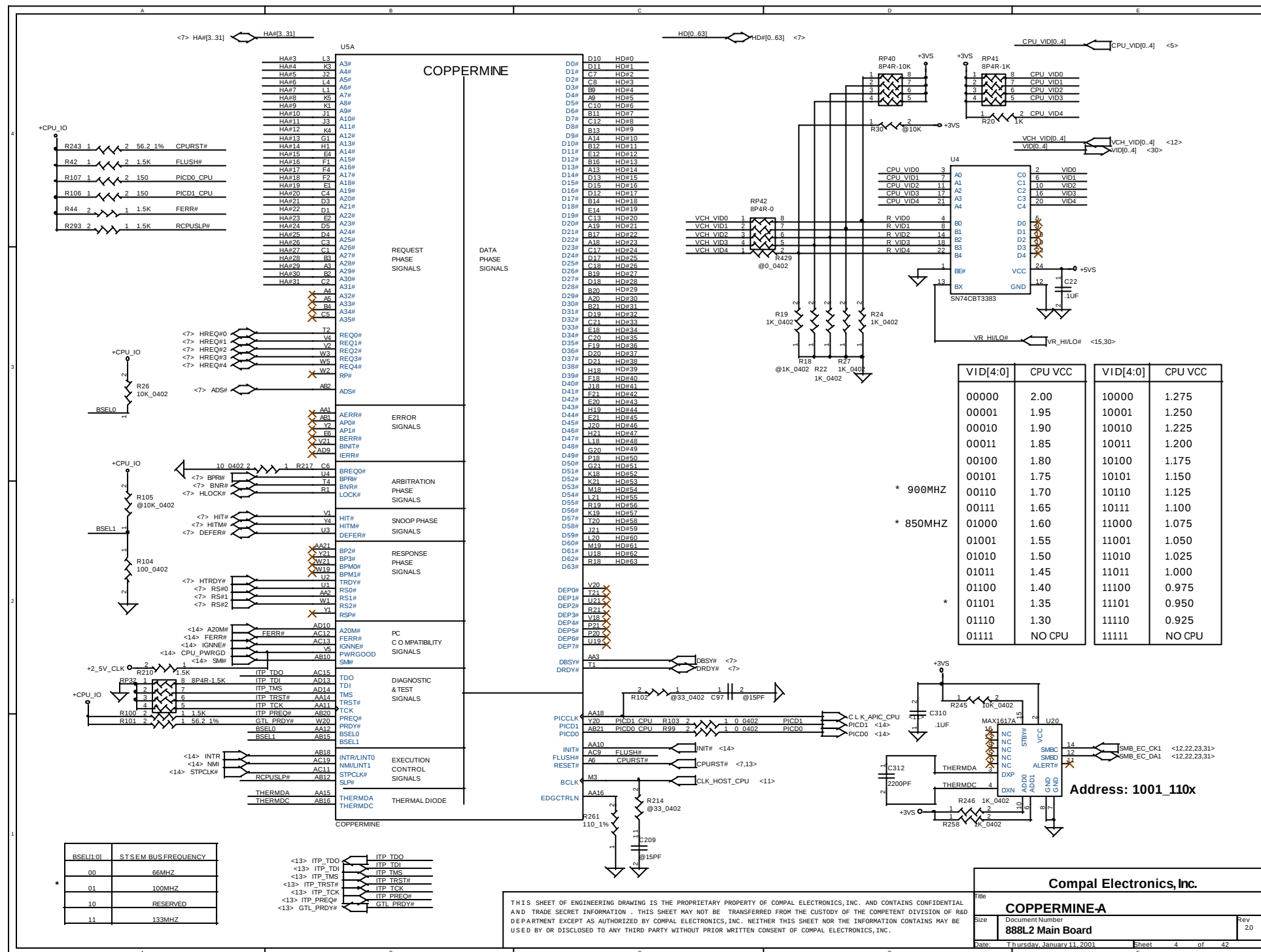
<i>DVT1 (PT1)</i>	<i>DVT2 (PT2)</i>
GMCH2-M	QA75ES (A1)
ICH2-M	QA57ES (B2)
VCH	Q076ES (A1)

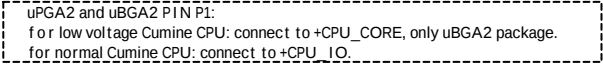
ST

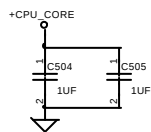
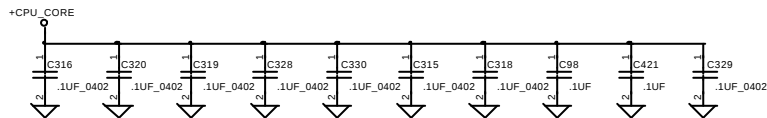
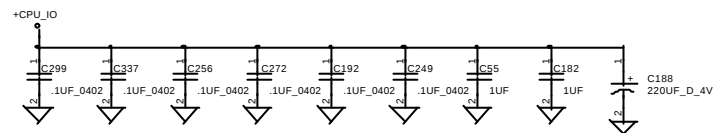
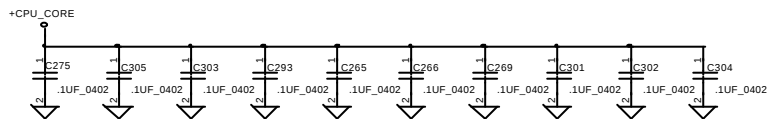
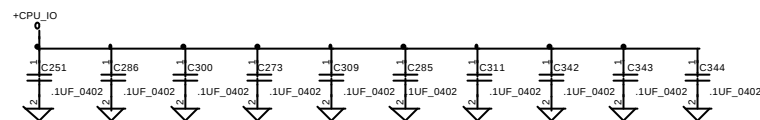
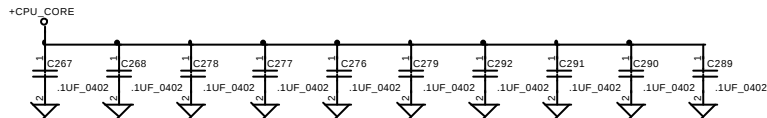
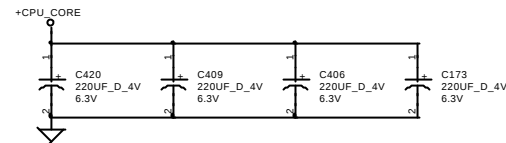
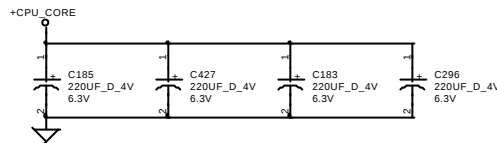
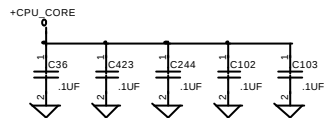
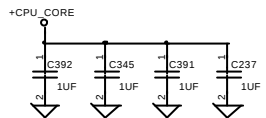
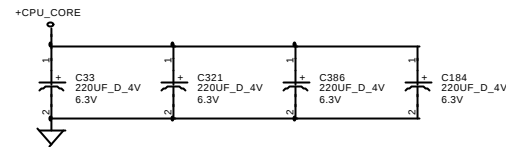
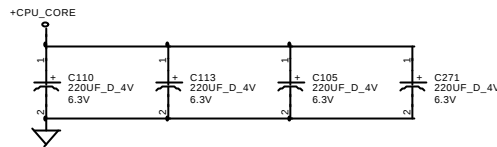
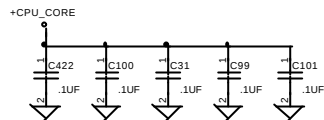
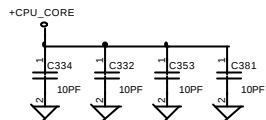
GMCH2-M	A1
ICH2-M	B2
VCH	A2

QT same as ST

Compal Electronics, Inc.			
Revision History			
Size	Document Number		Rev
	888L2 Main Board		2.0
Date:	Thursday, January 11, 2001	Sheet	3 of 42

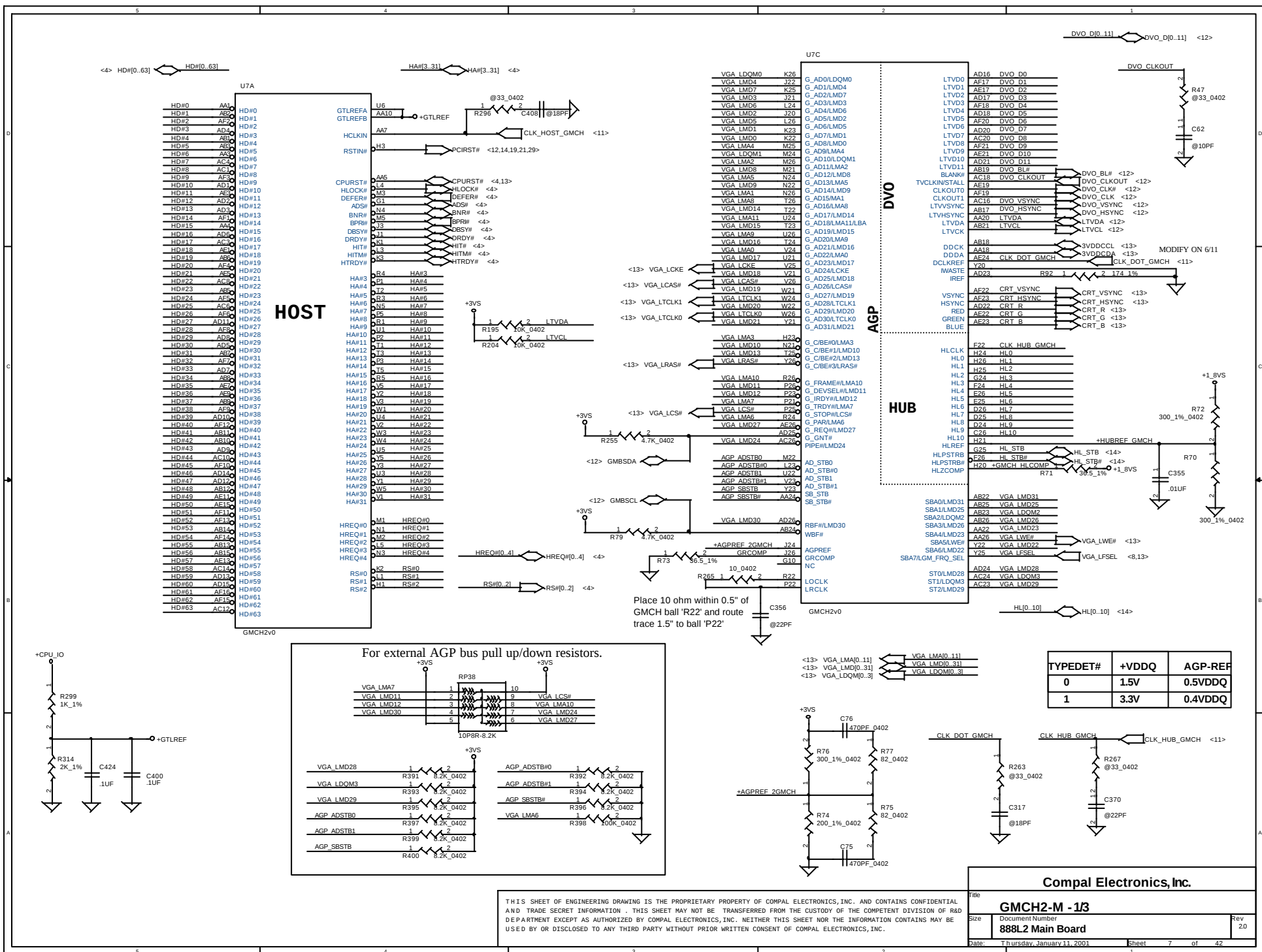






THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

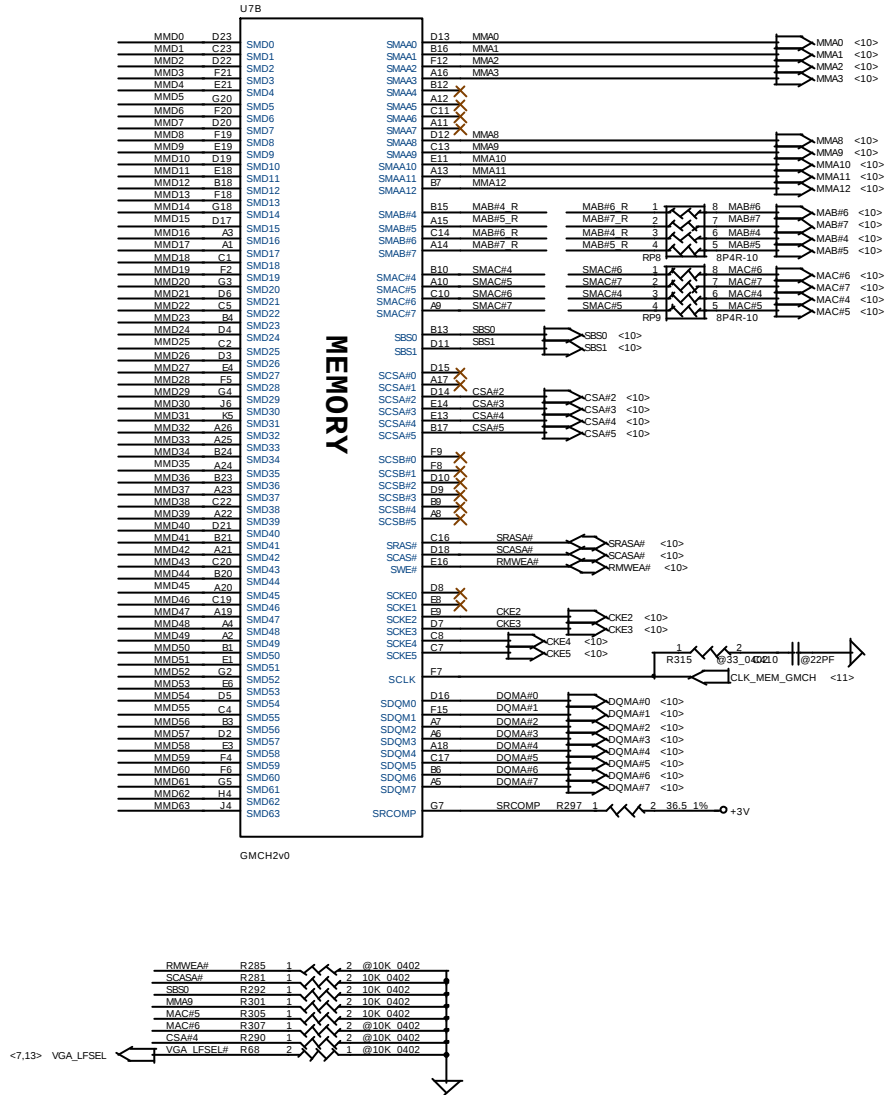
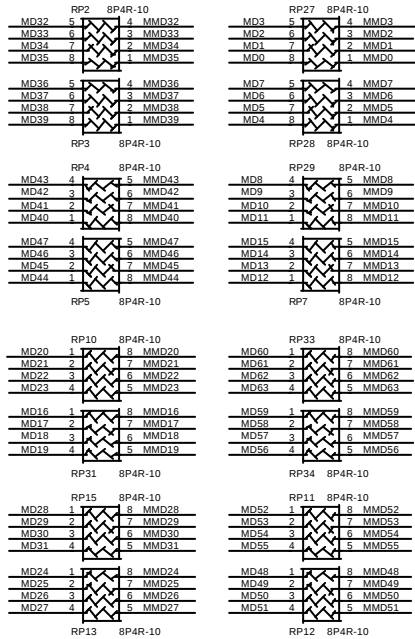
Compal Electronics, Inc.			
File			
CPU BYPASS			
Size	Document Number		Rev
	888L2 Main Board		20
Date:	Thursday, January 11, 2001		Sheet 6 of 42



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

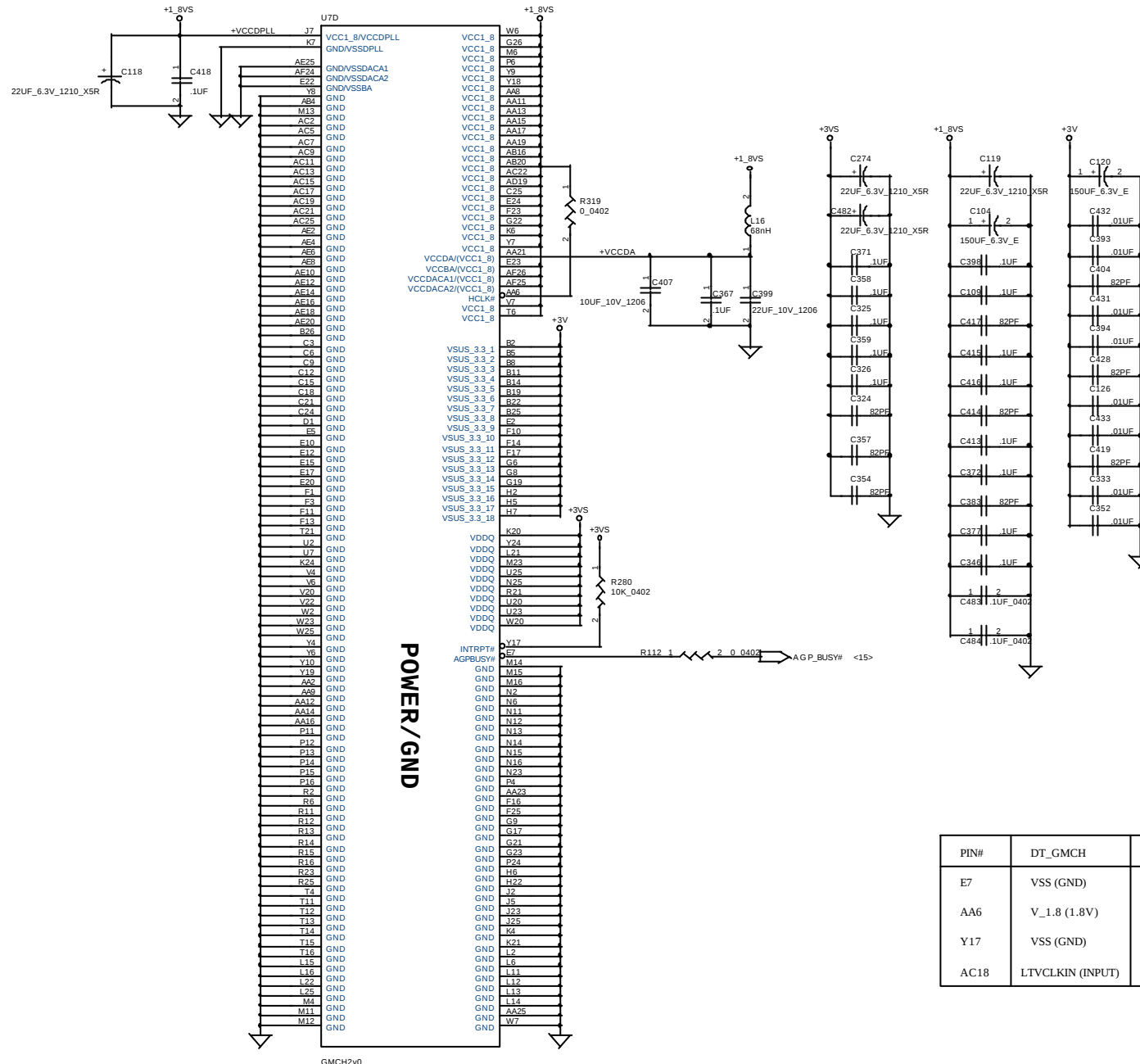
Power-Up Strap Options

Pin Name	Strap Description	Configuration	Interface	Internal
SCAS#	Host Freq.	"H" : 133MHz (Default) "L" : 100MHz	System Memory	PULL_UP
SWE#	Host Freq.	"H" : 100MHz (Default) "L" : 66MHz	System Memory	PULL_UP
SMAA11	IOQ Depth	"H" : 4 (Default) "L" : 1	System Memory	PULL_UP
SMAA10	ALL Z	"H" : Normal "L" : All Z	System Memory	PULL_UP
SMAA9	FSB P-MOS Kicker Enable	"H" : Enabled (Default) "L" : Disabled (Cumine)	System Memory	PULL_UP
SMAC6#	Enable VCH Serial Programming Mode	"H" : Enabled (Default) "L" : Disabled	System Memory	PULL_UP
SMAC5#	Enable Quick Start Support	"L" : Enabled (Default) (Quick Start Mode) "H" : Disabled (Stop Grant Mode)	System Memory	PULL_UP
VGA_LFSEL#	Local Memory Freq. Select	"H" : 133MHz (Default) "L" : 100MHz	AGP/LM	i815/i815-m



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

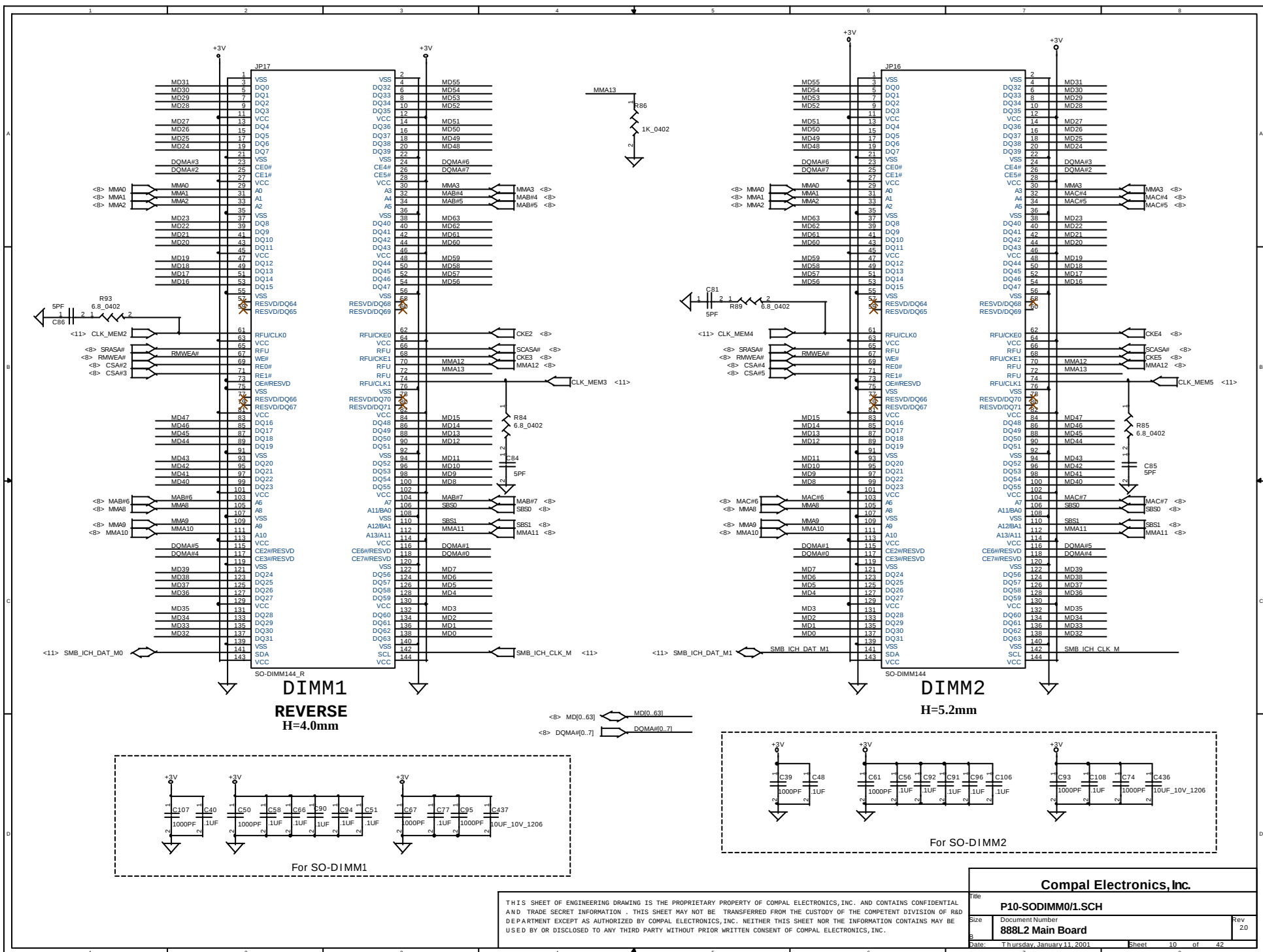
Compal Electronics, Inc.		
File	GMCH2-M - 23	
Size	Document Number	Rev
	888L2 Main Board	2.0
Date:	Thursday, January 11, 2001	Sheet 8 of 42

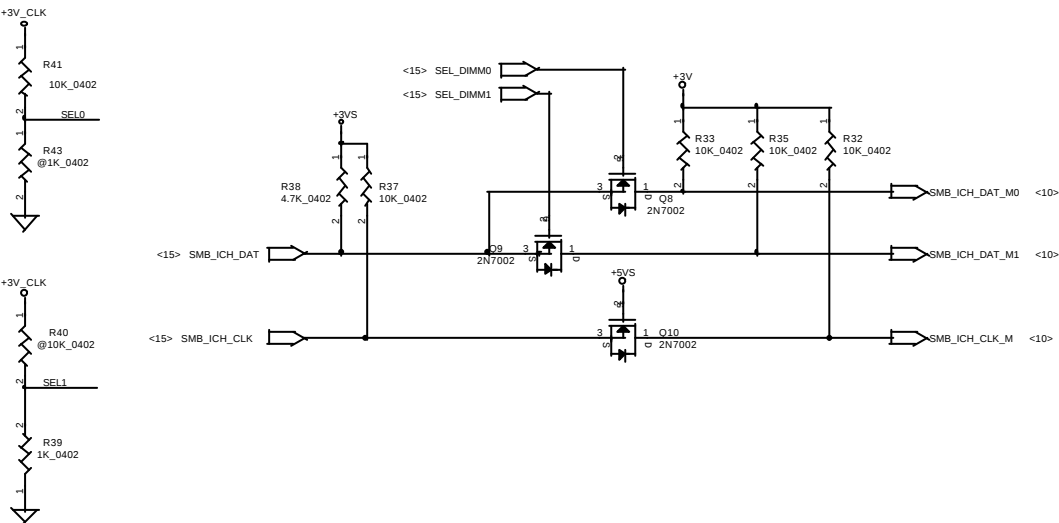
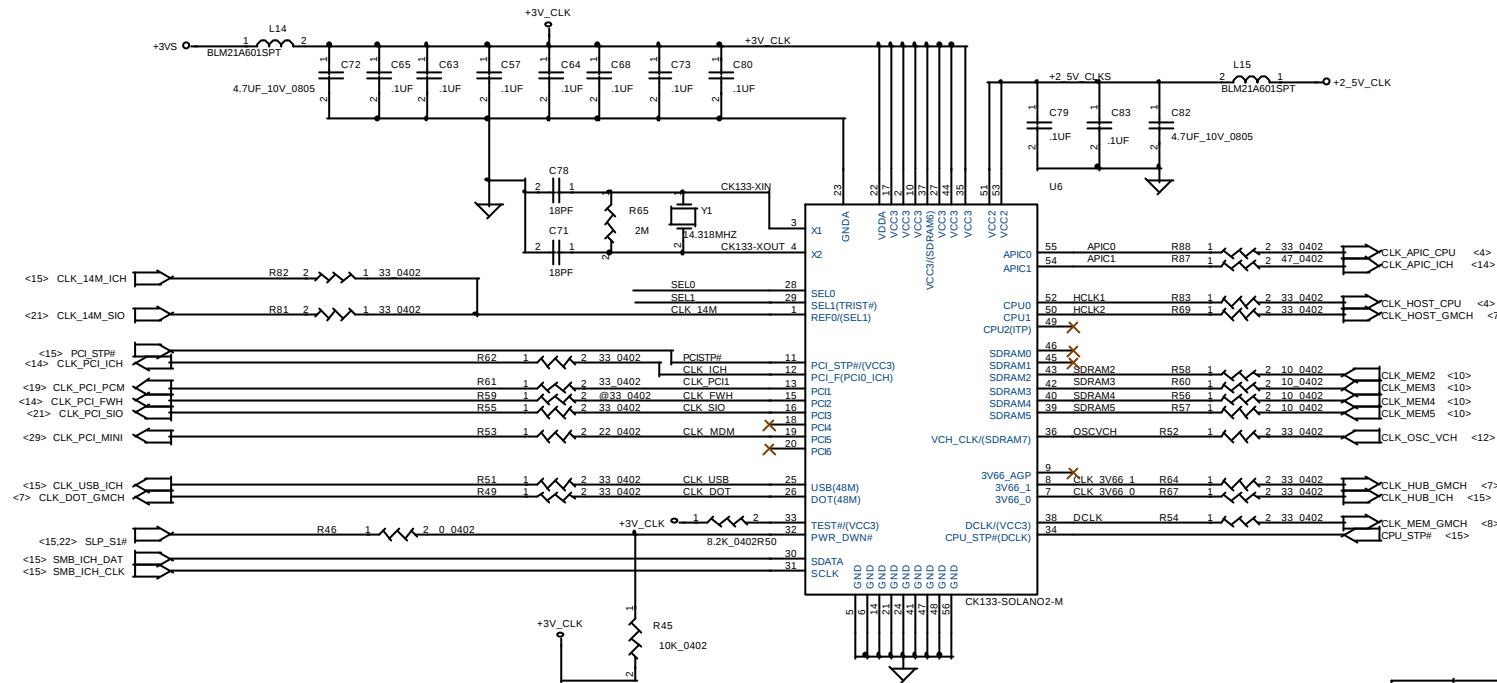


PIN#	DT_GMCH	GMCH2-M
E7	VSS (GND)	AGPBUSY# (OUTPUT)
AA6	V_1.8 (1.8V)	RESERVED (1.8V)
Y17	VSS (GND)	INTRPT# (INPUT)
AC18	LTVCLKIN (INPUT)	LTVCLKIN/STALL (INPUT)

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.		
File		
GMCH2-M - 3/3		
Size	Document Number	Rev
	888L2 Main Board	20
Date:	Thursday, January 11, 2001	Sheet 9 of 42



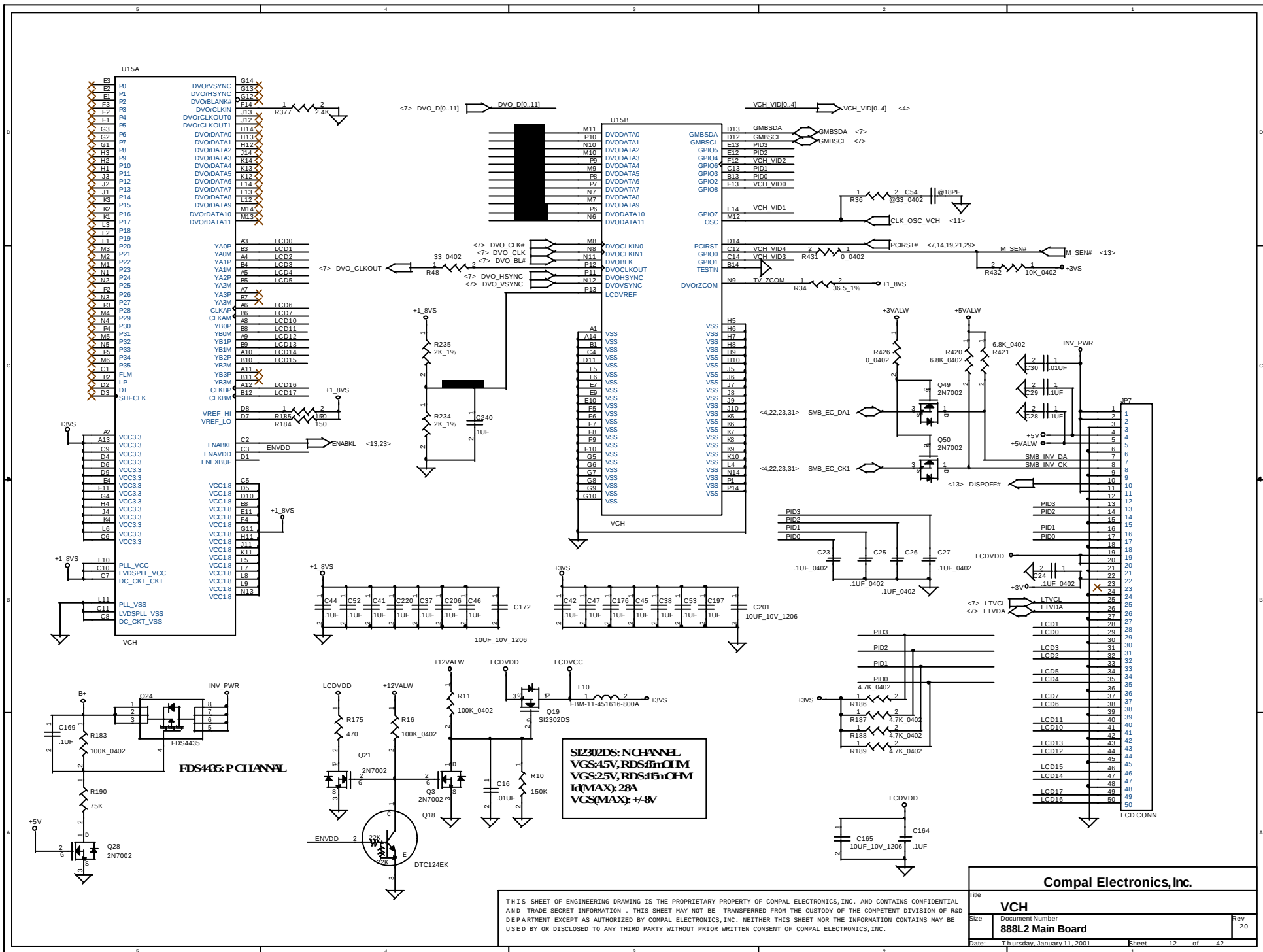


SEL1	SEL0	PSB	SDRAM
0	0	66	100
0	1	100	100
1	0	133	133
1	1	133	100

Pin #	CK-Solano		CK-Solano2-m	
	W218	C9815	W224	C9835
1	REF0/SEL1	REF/SEL1	REF	REF
11	VCC3	VCC3	PCI_STP#	PCI_STP#
29	Tristate#	Tristate#	SEL1	SEL1
33	VCC3	VCC3	TEST#	TEST#
34	DCLK	DCLK	CPU_STP#	CPU_STP#
36	SDRAM7	SDRAM7	VCH_CLK	VCH_CLK
37	SDRAM6	SDRAM6	VCC3	VCC3
38	VCC3	VCC3	DCLK	DCLK

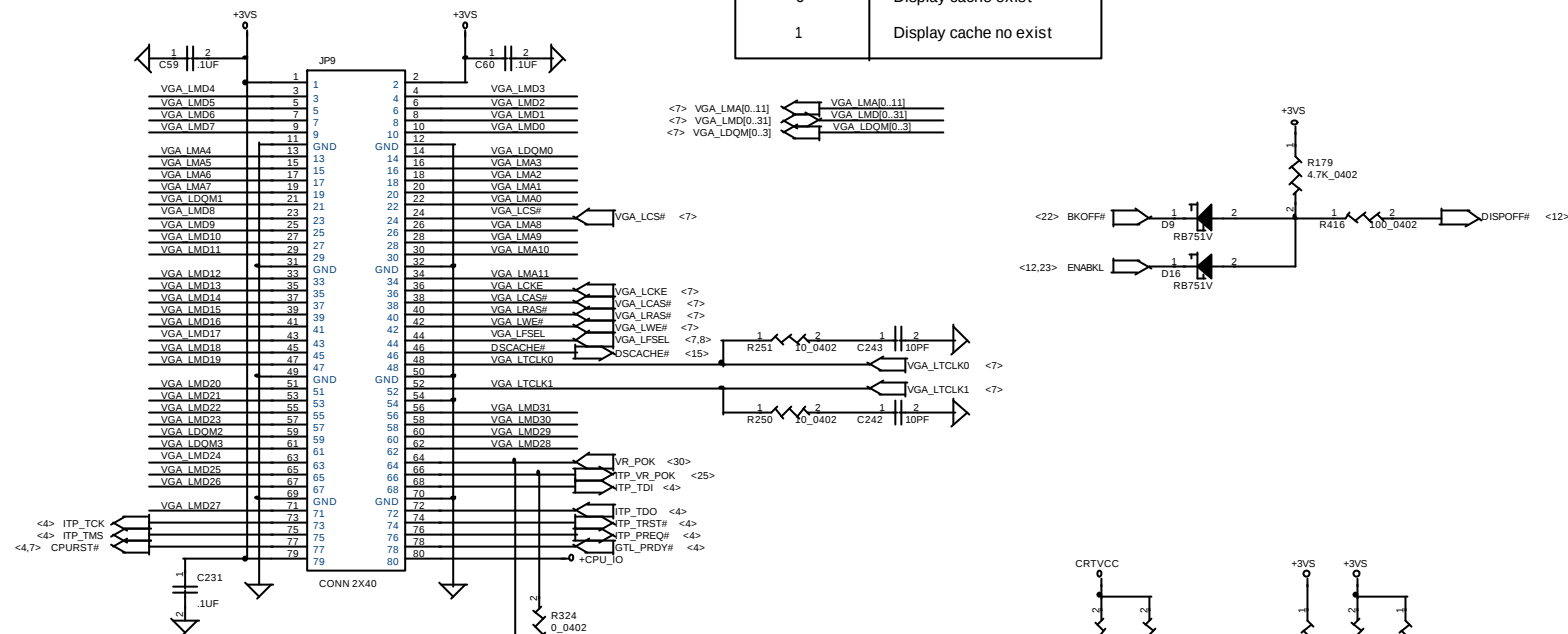
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINED MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
File	Clock Synthesizer		
Size	Document Number 888L2 Main Board		Rev 2.0
Date	Thursday, January 11, 2001		Sheet 11 of 42

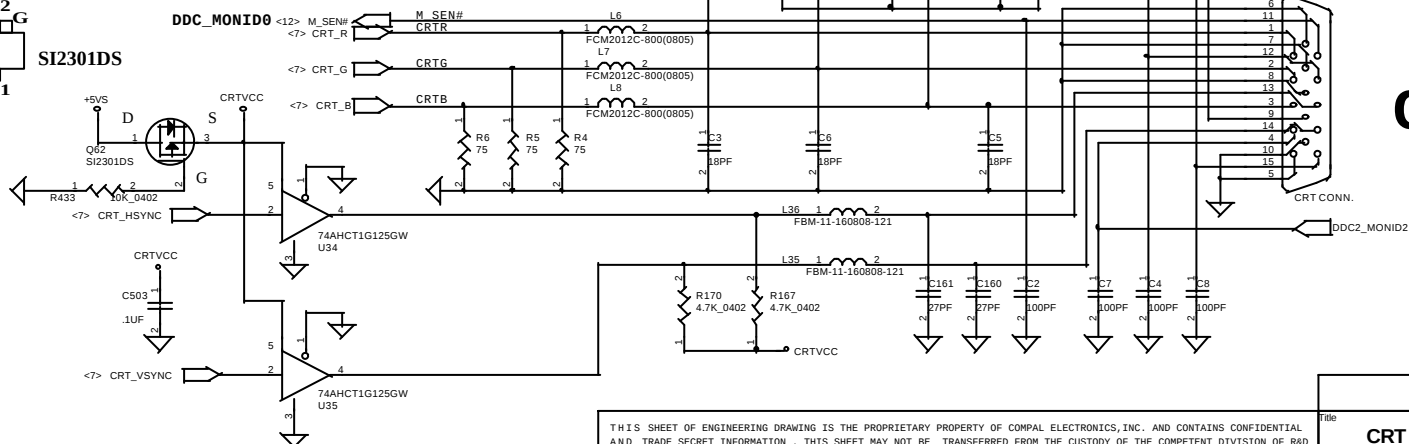
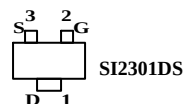


DISPLAY CACHE INTERFACE

DSCACHE#	Status
0	Display cache exist
1	Display cache no exist



SI2301DS: P CHANNEL
VGS: -4.5V, RDS: 130mΩ
VGS: -2.5V, RDS: 190mΩ
Id(MAX): 23A
VGS(MAX): +8V



CRT CONN.

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

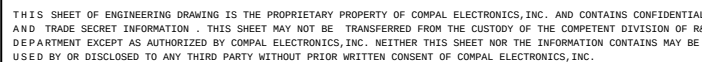
Compal Electronics, Inc.

CRT CONN

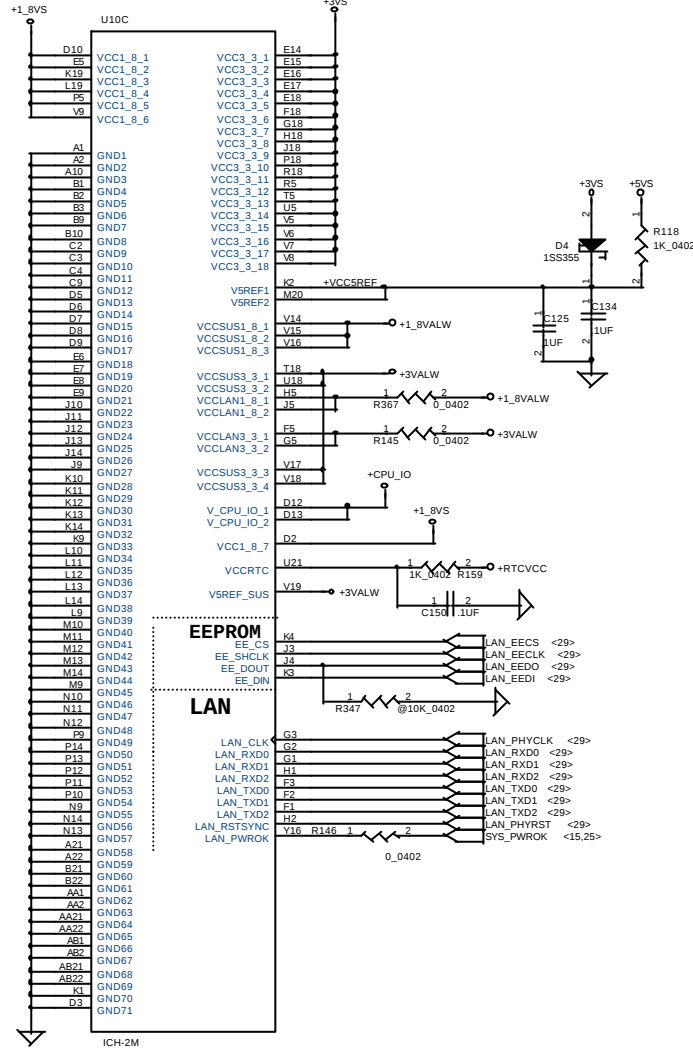
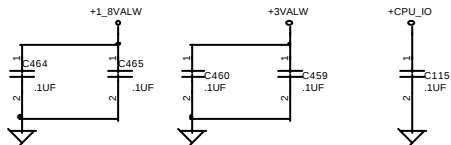
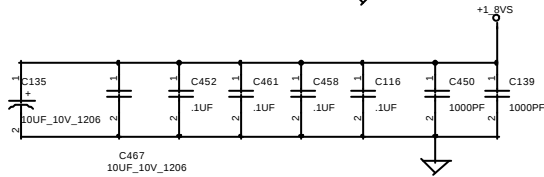
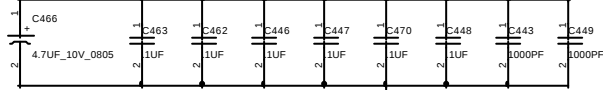
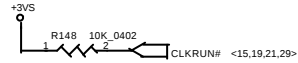
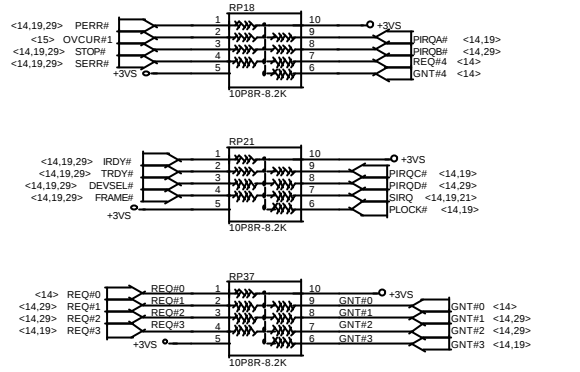
Document Number
888L2 Main Board

Rev

Date: Thursday, January 11, 2001 Sheet 13 of 4



PCI

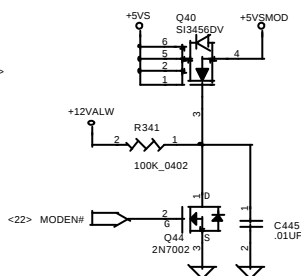
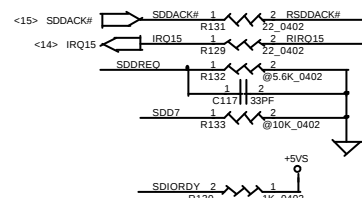
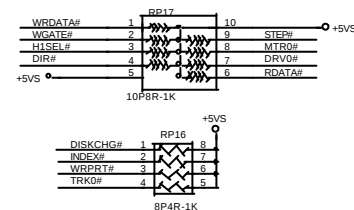
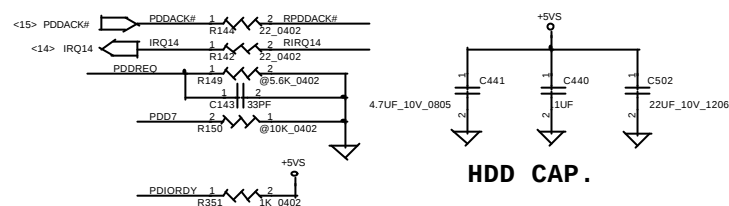
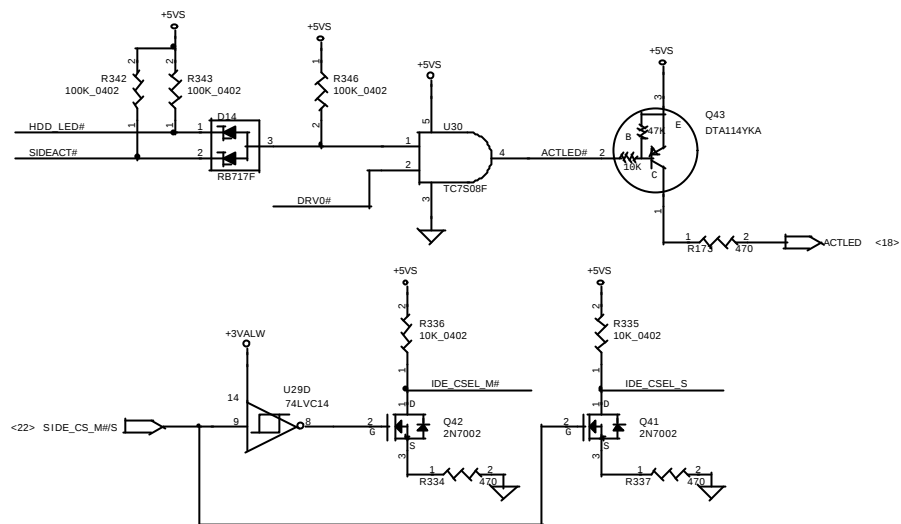
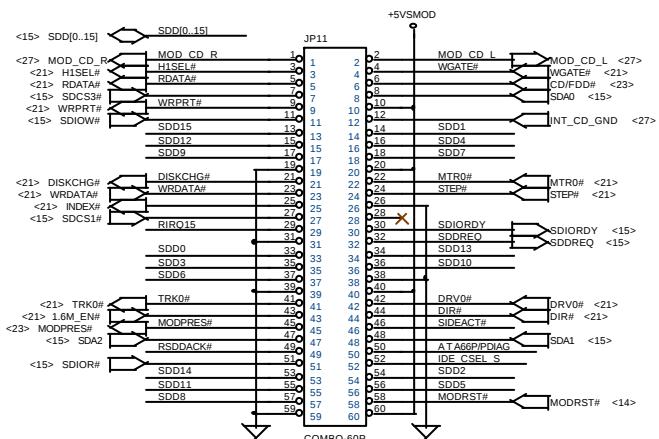
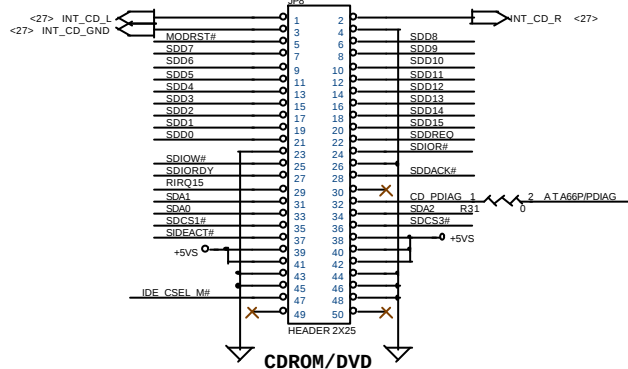
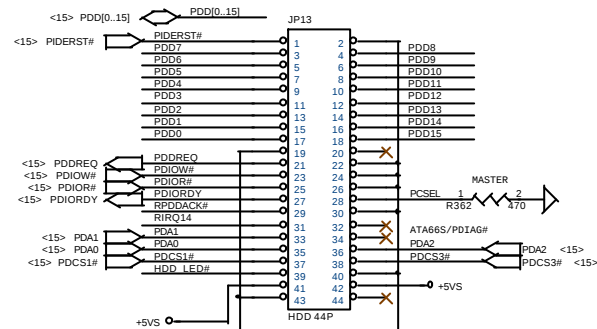


Compal Electronics, Inc.

File		
ICH2-m-C (FW82801BAM)		
Size		
Document Number		
888L2 Main Board		
Rev		
2.0		
Date:	Thursday, January 11, 2001	Sheet 16 of 42

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

IDE,CD-ROM & FDD Module CONN.



SI3456DV: NCHANNEL
VGS: 4.5V, RDS: 65mOHM
Id(MAX): 5.1A

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

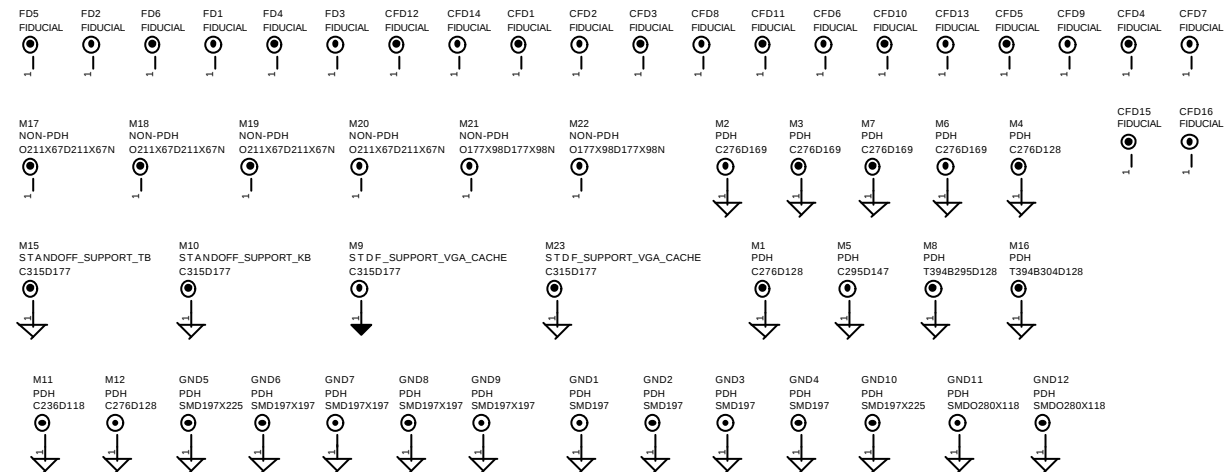
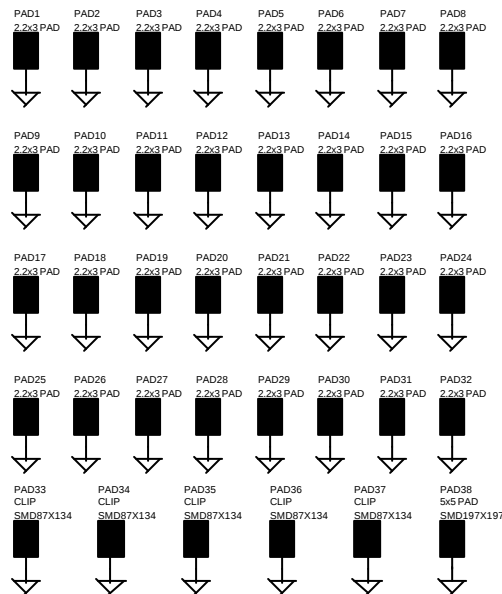
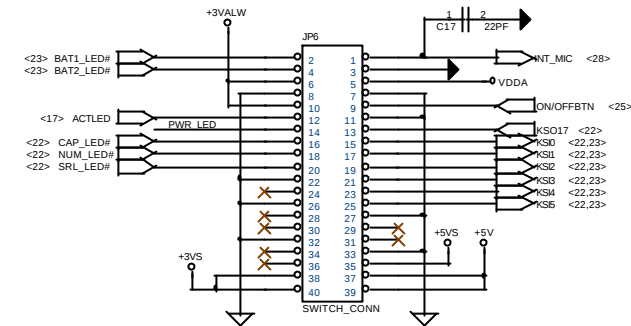
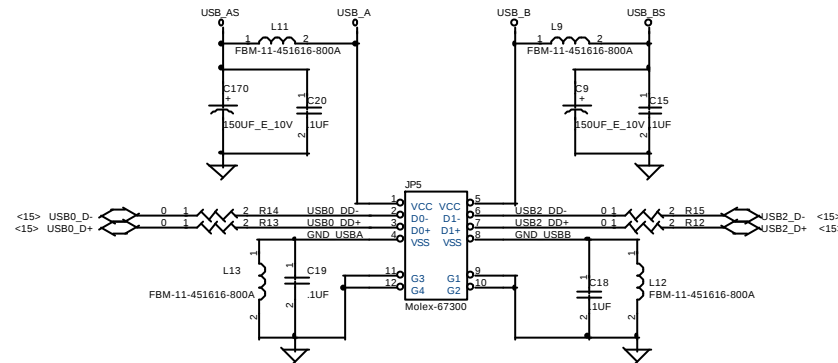
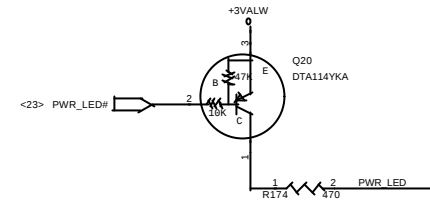
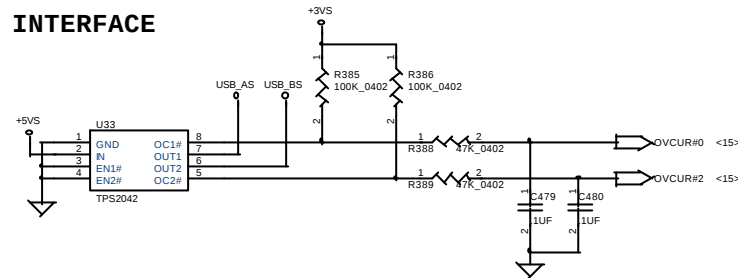
Compal Electronics, Inc.

IDE/FDD MODULE

Size	Document Number
	888L2 Main Board

Rev	2.0
-----	-----

USB&LED INTERFACE



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

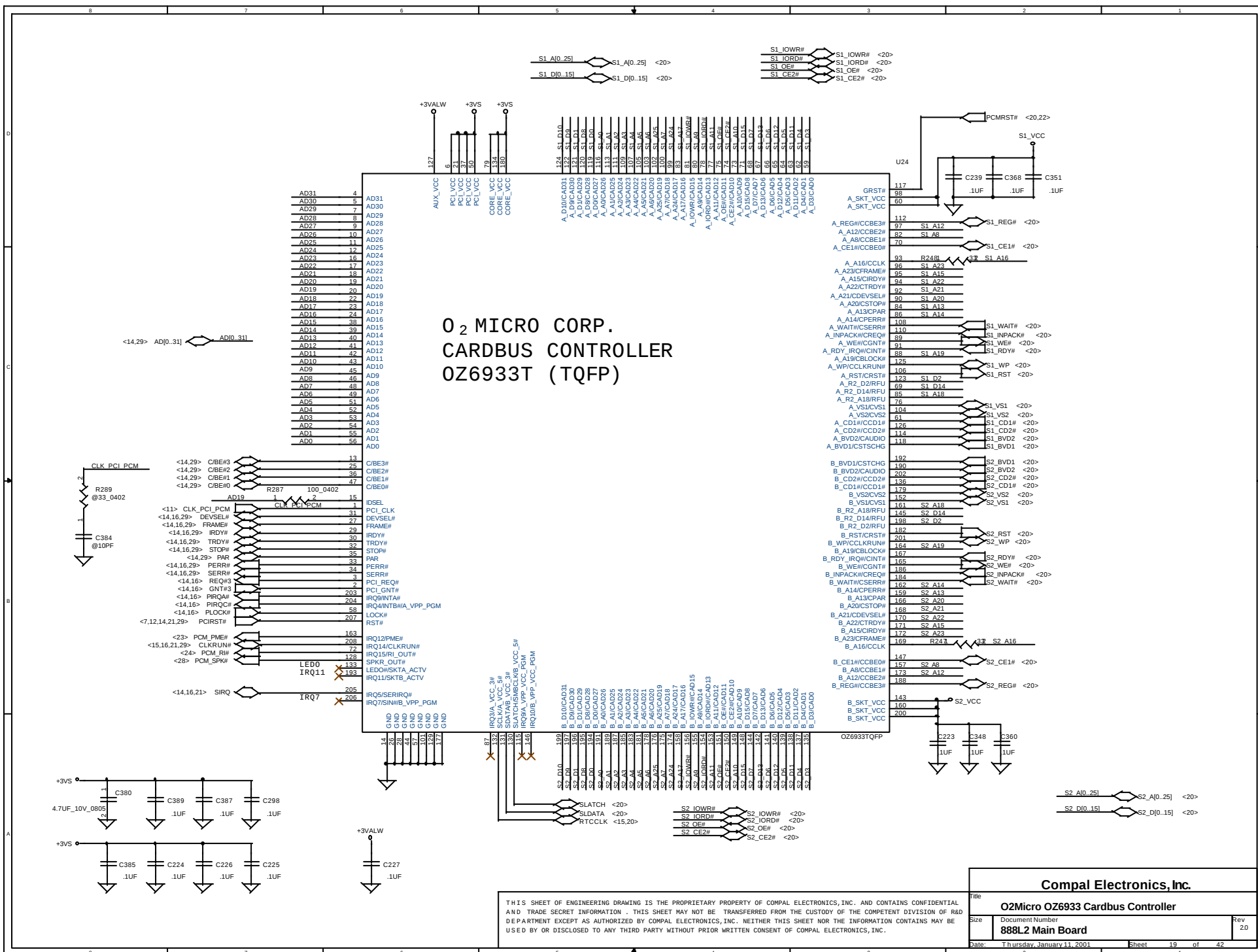
Compal Electronics, Inc.

USB & FRONT PANEL I/F

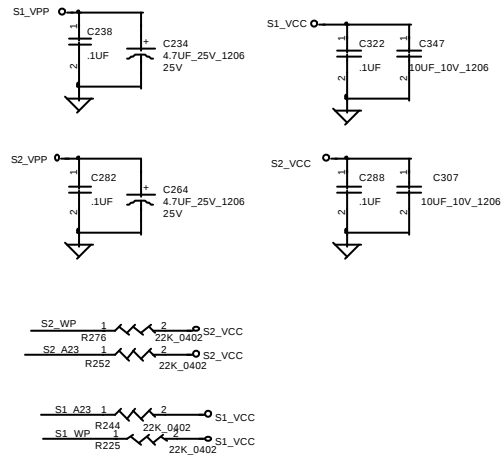
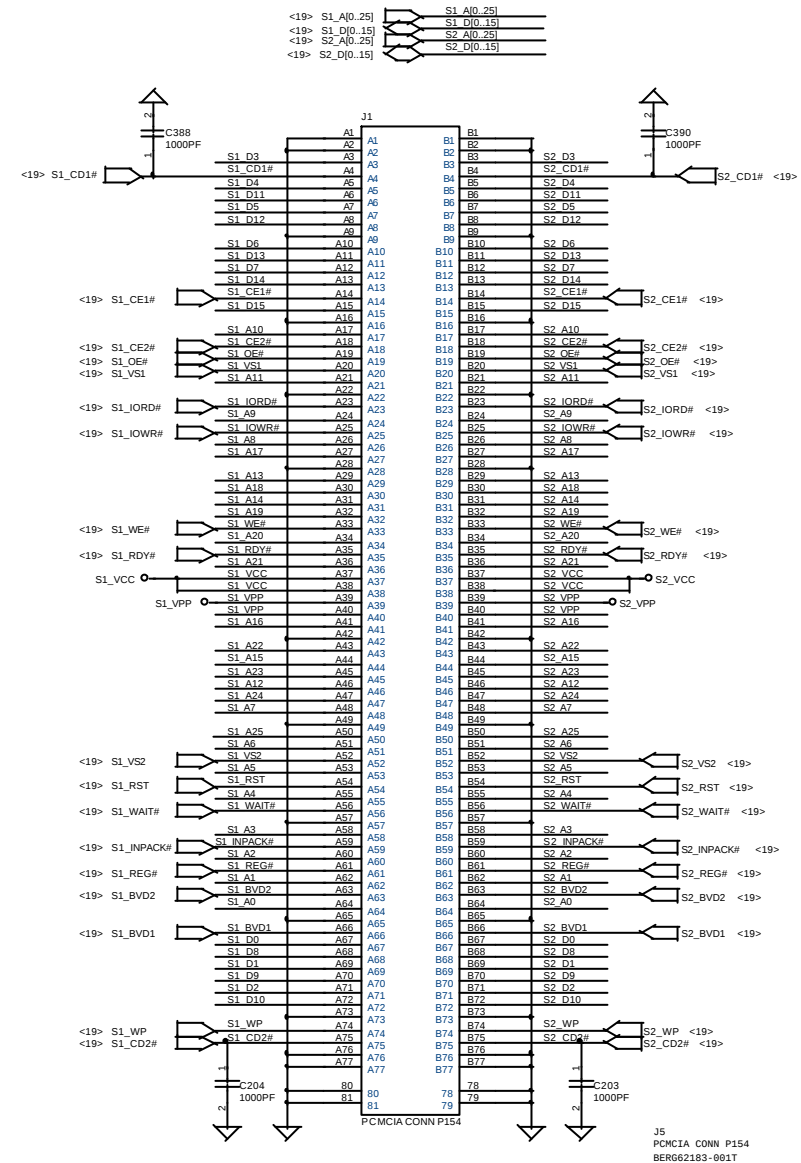
Document Number
888L2 Main Board

Rev	2.0
-----	-----

Date: Thursday, January 11, 2001 Sheet 18 of 42



CARDBUS SOCKET



Compal Electronics, Inc.					
Title FOXCONN Cardbus CONN .154PIN					
Size	Document Number 888L2 Main Board				Rev 2.0
Date:	Thursday, January 11, 2001		Sheet	20	of 42

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Pin 130 PU for Zero Latch

PC87570

NC : 1, 2, 43, 44, 45, 46, 87, 88, 89, 90, 91, 131, 132, 133, 134, 175, 176

Compal Electronics, Inc.

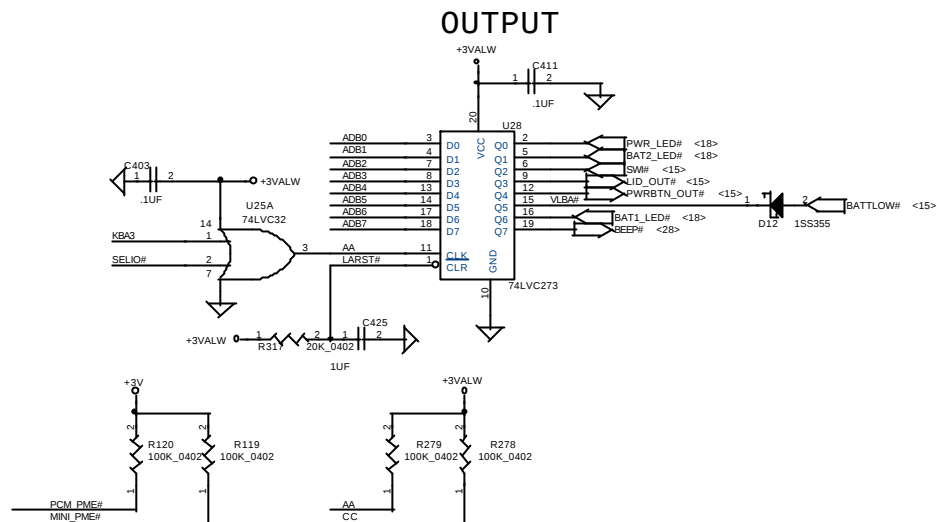
KBC/EC PC87570EXT

Document Number
888L2 Main Board

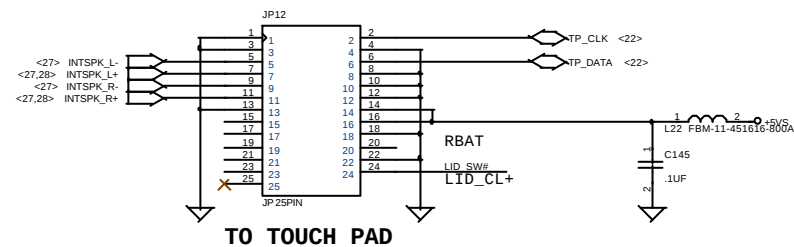
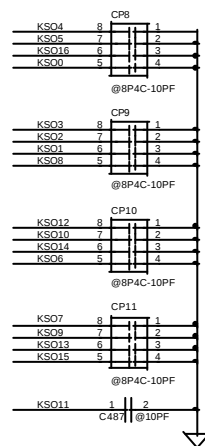
Date: Thursday, January 11, 2001

Sheet 22 of 42

Rev 20



```
24C104: 1011xxx R/W#
24C16: 1010xxx R/W#
```



Title	EC Extended I/O Port		
Size	Document Number	Rev	
	888L2 Main Board	2.0	
Date:	Thursday, January 11, 2001	Sheet	23 of 42

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

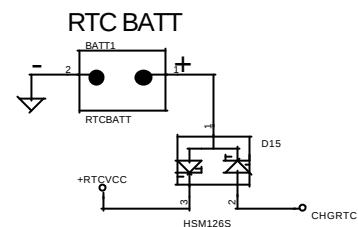
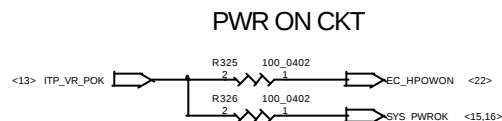
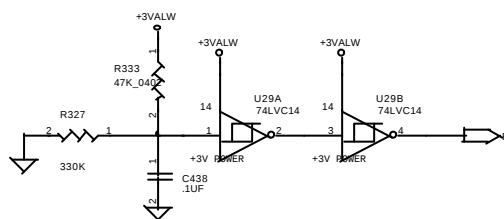
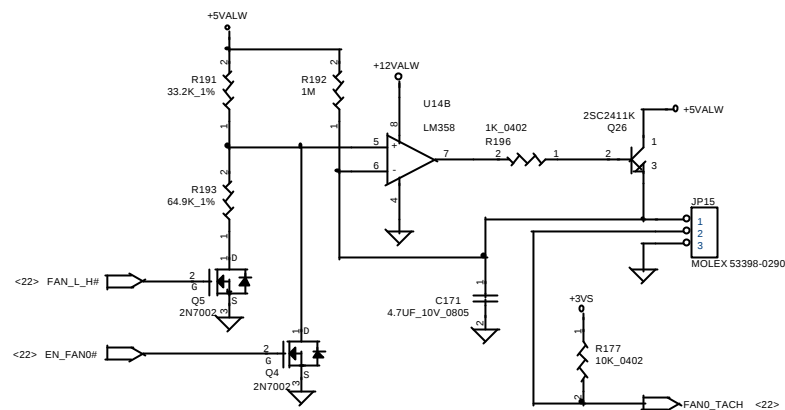
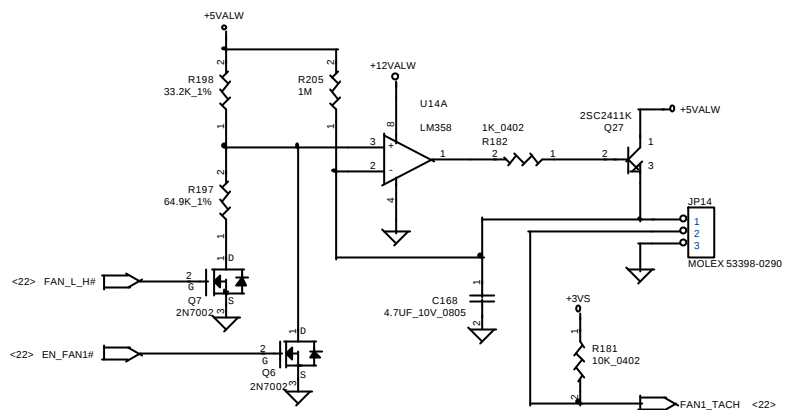
SERIAL & PARALLEL



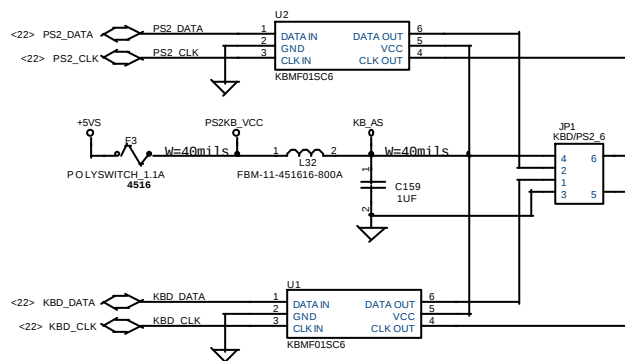
<21> LPD[0.7]



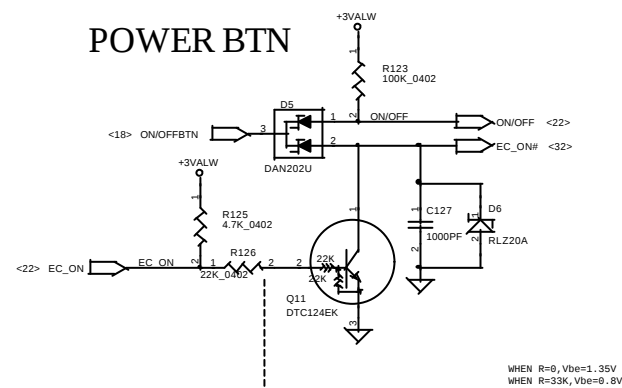
Title	Serial/parallel ports		
Size	Document Number	Rev	
	888L2 Main Board	2.0	
Date:	Thursday, January 11, 2001	Sheet	24 of 42



PS2 CONN.



POWER BTN

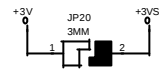
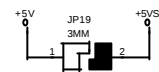
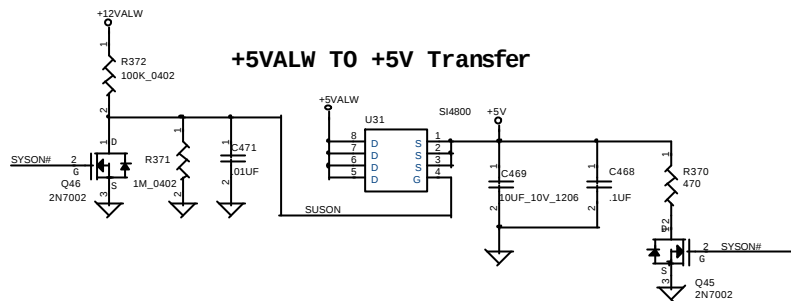


THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

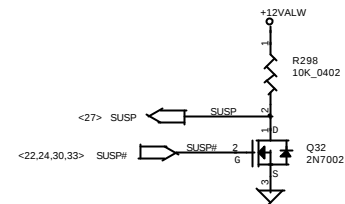
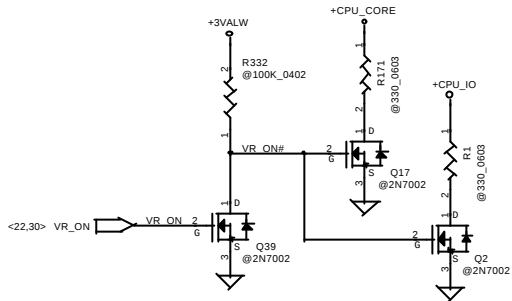
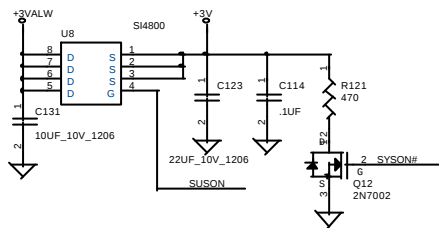
Compal Electronics, Inc.			
File	PSI2/RTC/RST CKT		
Size	Document Number		
	888L2 Main Board		Rev 2.0
Date:	Thursday, January 11, 2001	Sheet	25 of 42

RESET & SUSPEND CKT

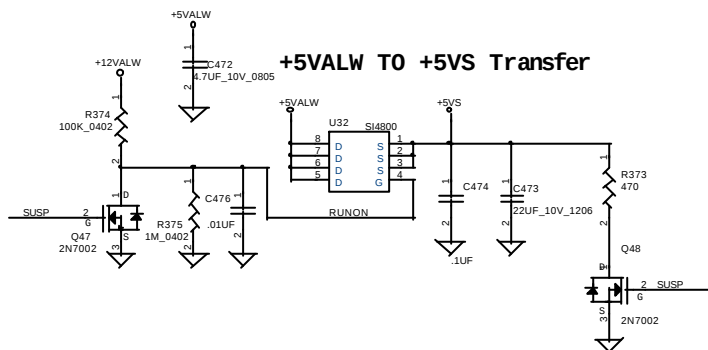
+5VALW TO +5V Transfer



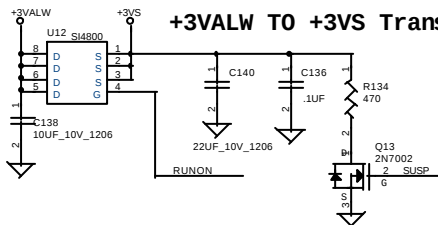
+3VALW TO +3V Transfer



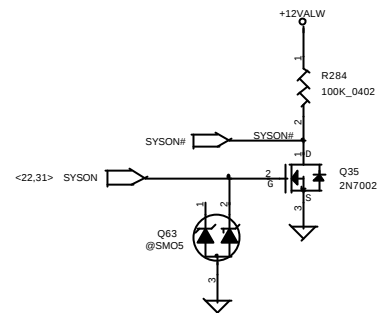
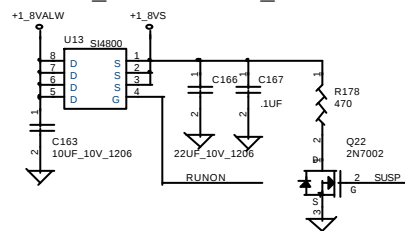
+5VALW TO +5VS Transfer



+3VALW TO +3VS Transfer



+1_8VALW TO +1_8VS Transfer



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.

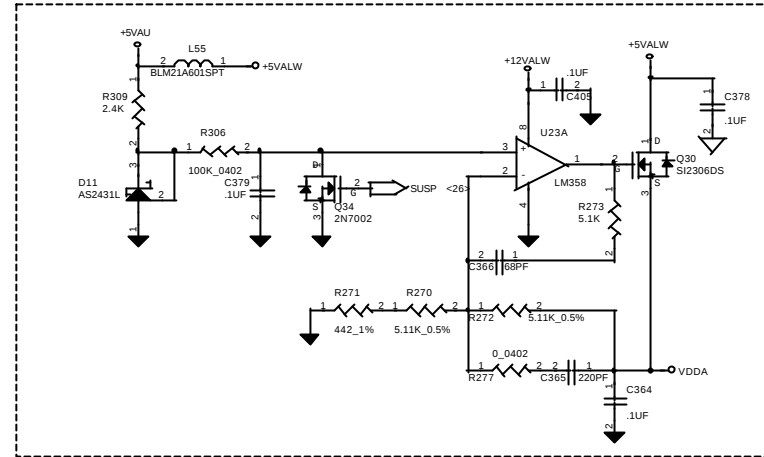
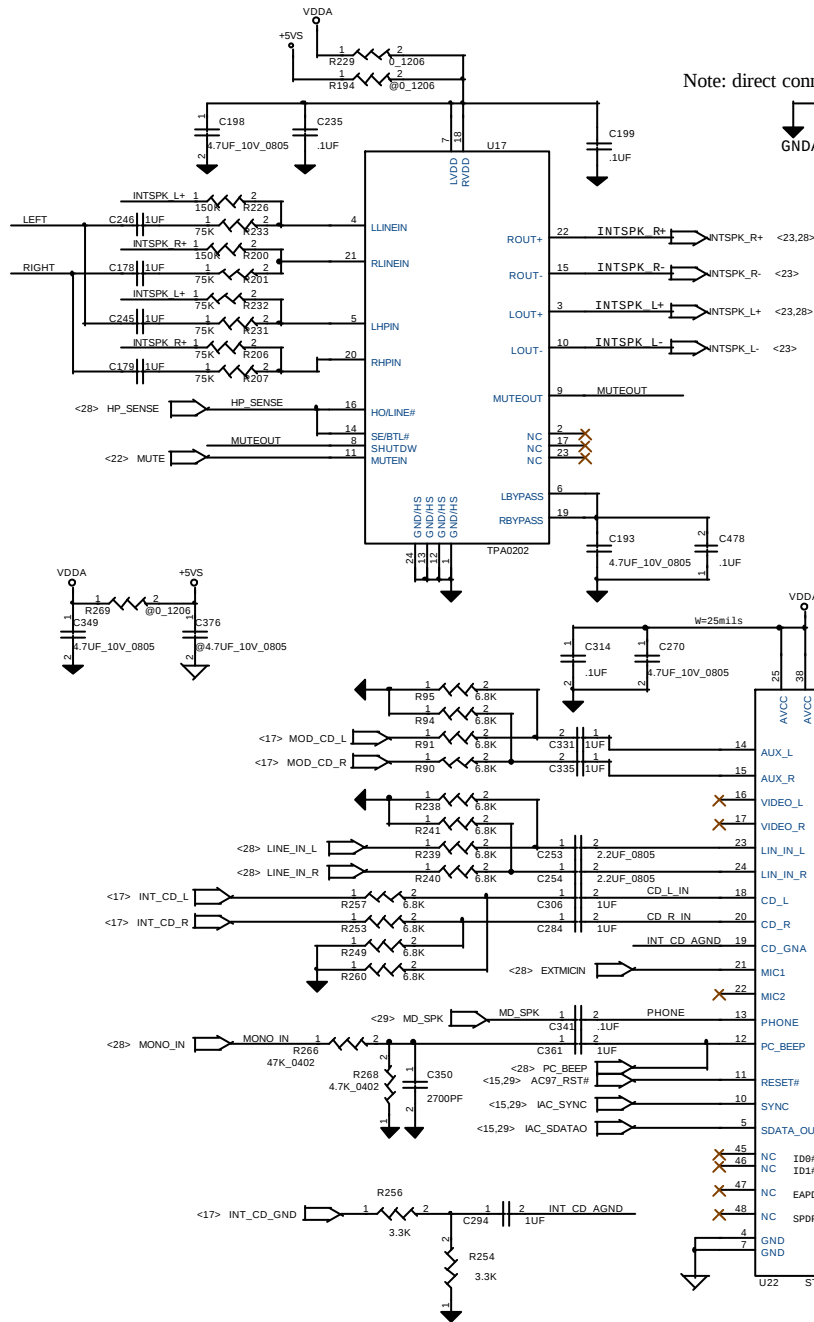
DC/DC INTERFACE

Document Number
888L2 Main Board

Rev
2.0

Date: Thursday, January 11, 2001 Sheet 26 of 42

Note: direct connect GNDA to GND.

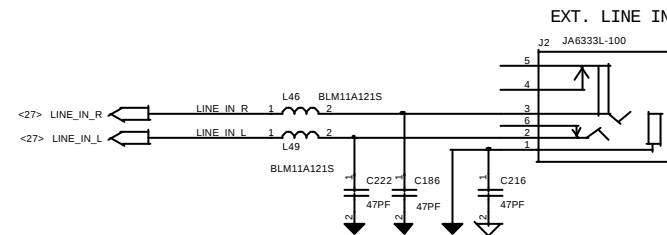
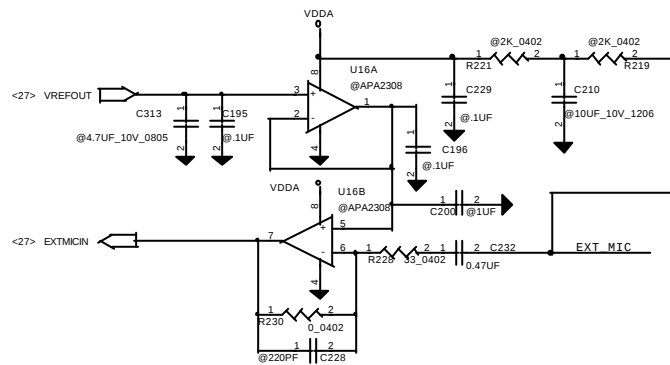
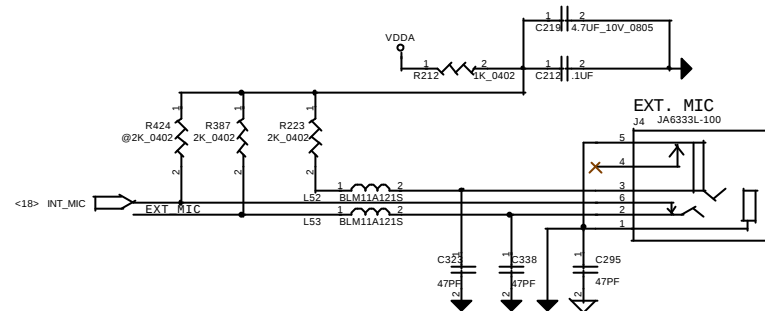
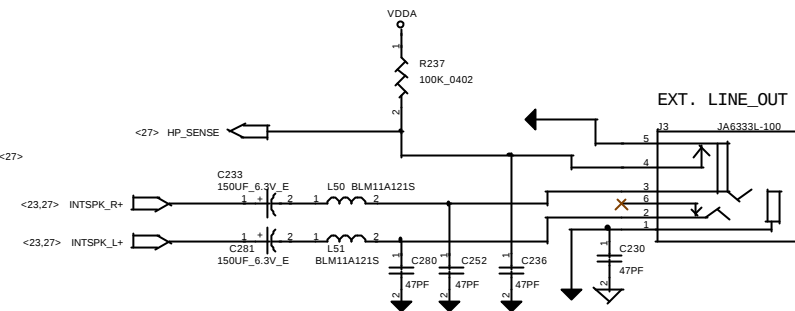
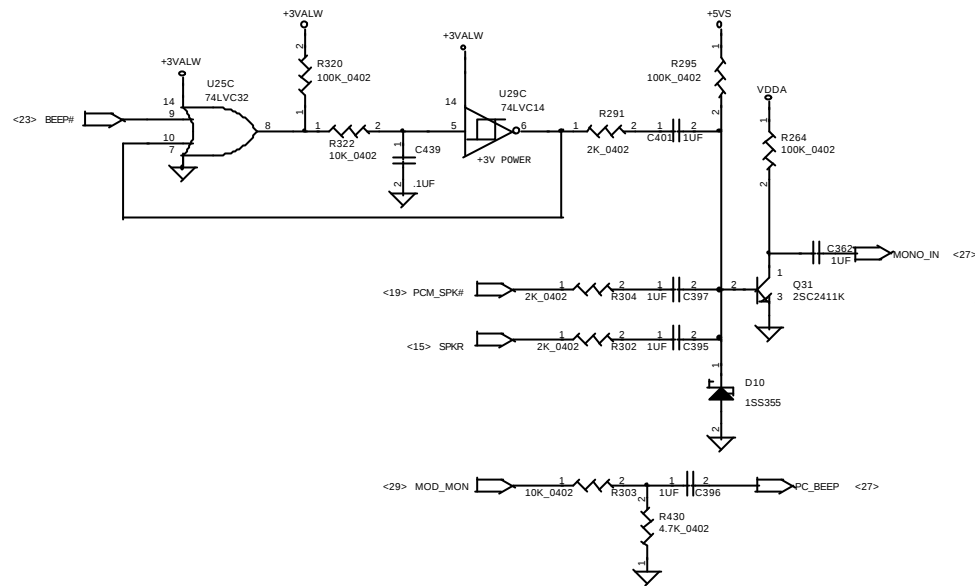


	STAC9700	AD1881A
C248	1UF	4.7UF 10V 0805
C260	1000PF	270PF
C259	1000PF	270PF
C501	Depop	1UF

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.		
File: AUDIO CODEC & AMP		
Size: Document Number	Rev 2.0	
888L2 Main Board		
Date: Thursday, January 11, 2001	Sheet: 27	of 42

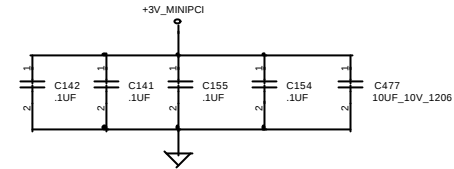
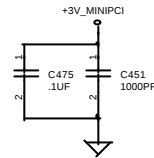
AUDIO JACK



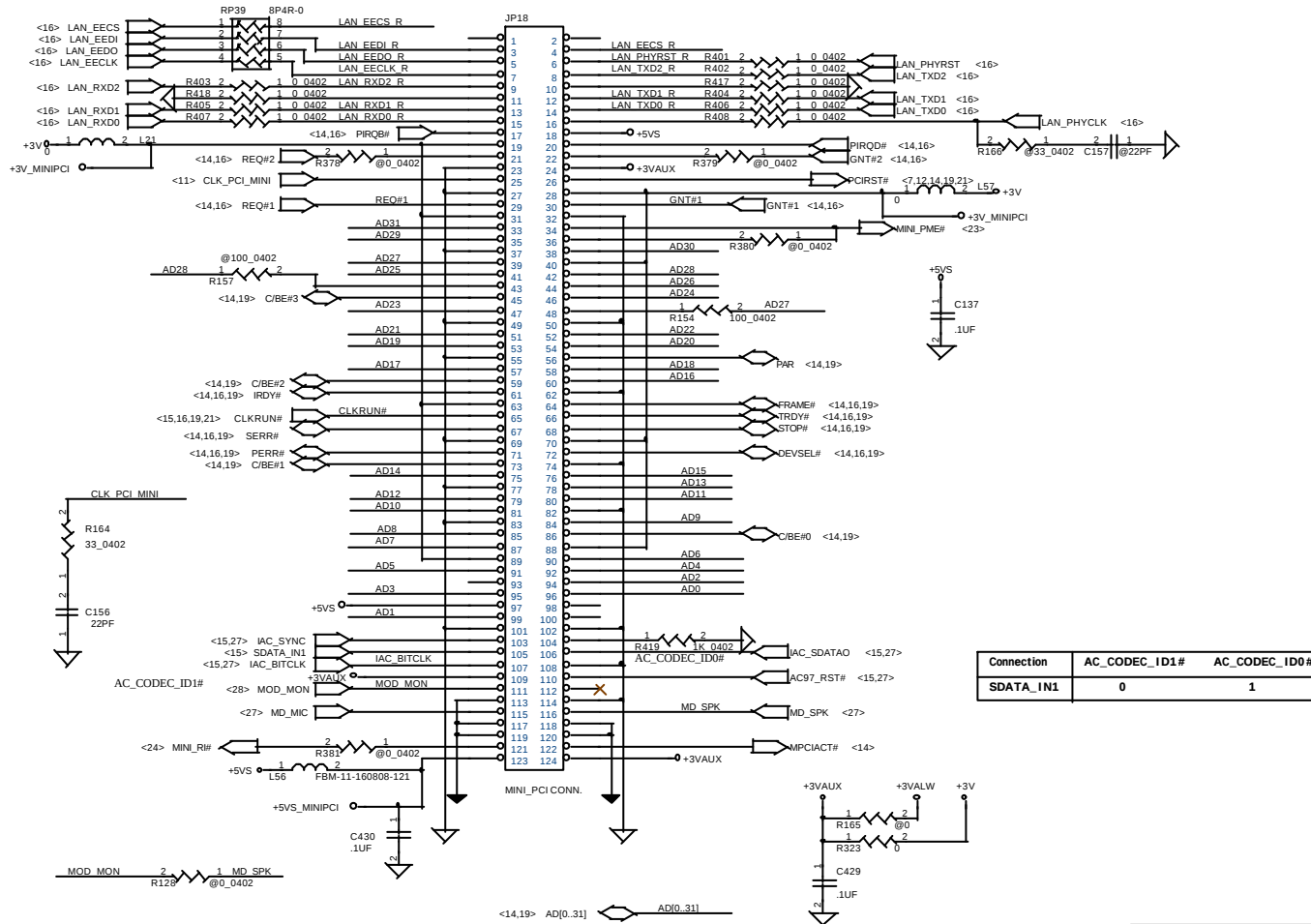
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
File	MONO_IN		
Size	Document Number		
	888L2 Main Board		Rev 2.0
Date:	Thursday, January 11, 2001	Sheet	28 of 42

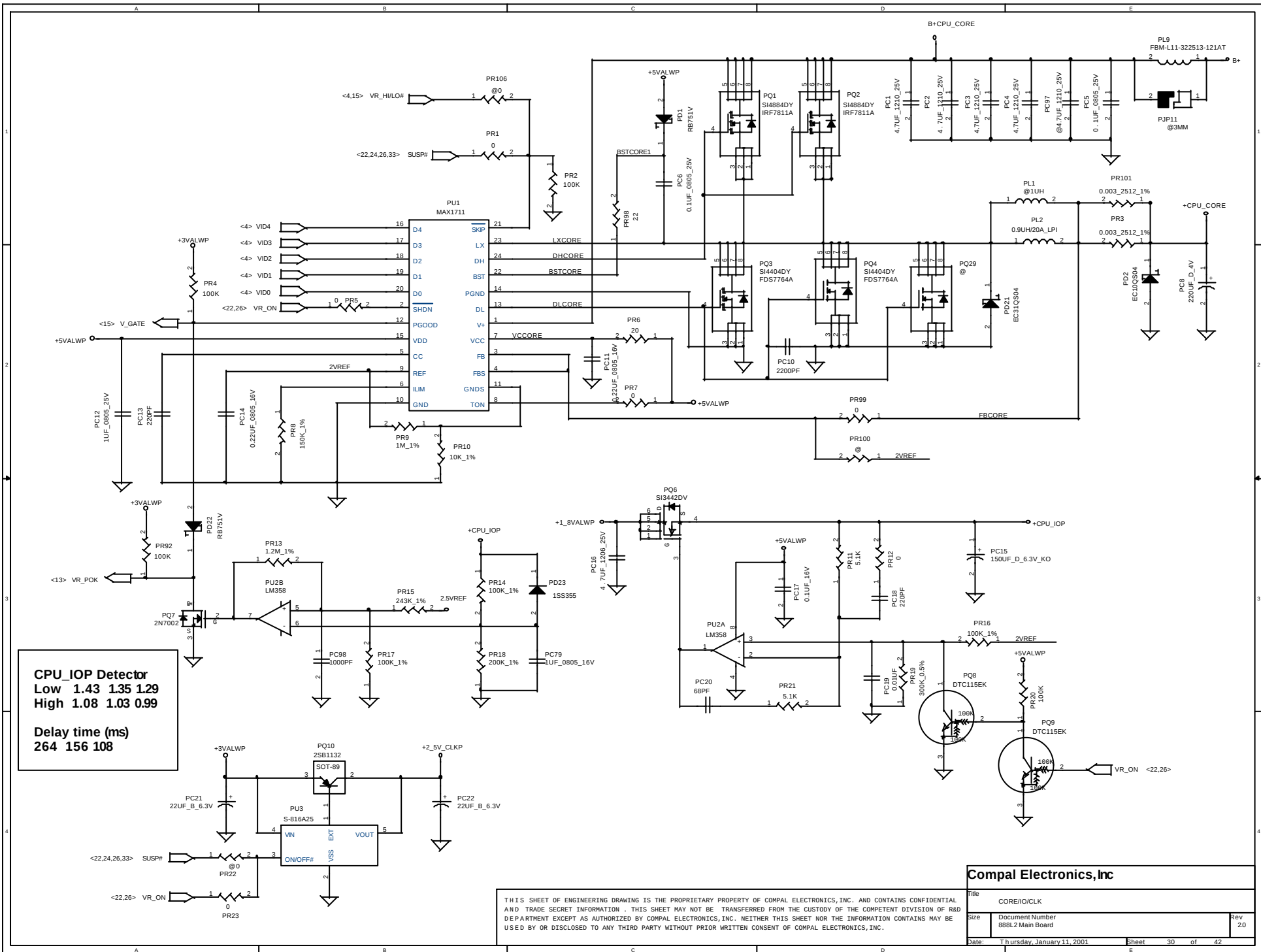
For Compal private Mini-PCI combo card - Mini-PCI type IIIc
pin 21. LAN REQ#
pin 22. LAN GNT#
pin 36. LAN PME#
pin 43. LAN IDSEL

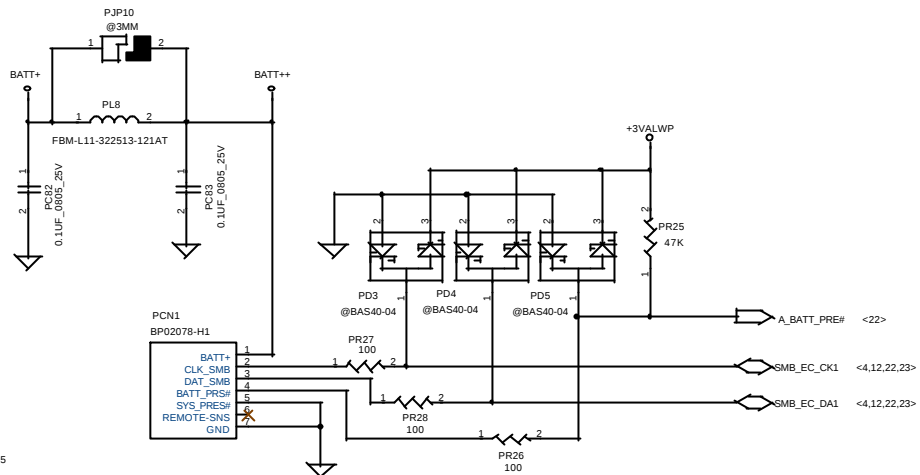
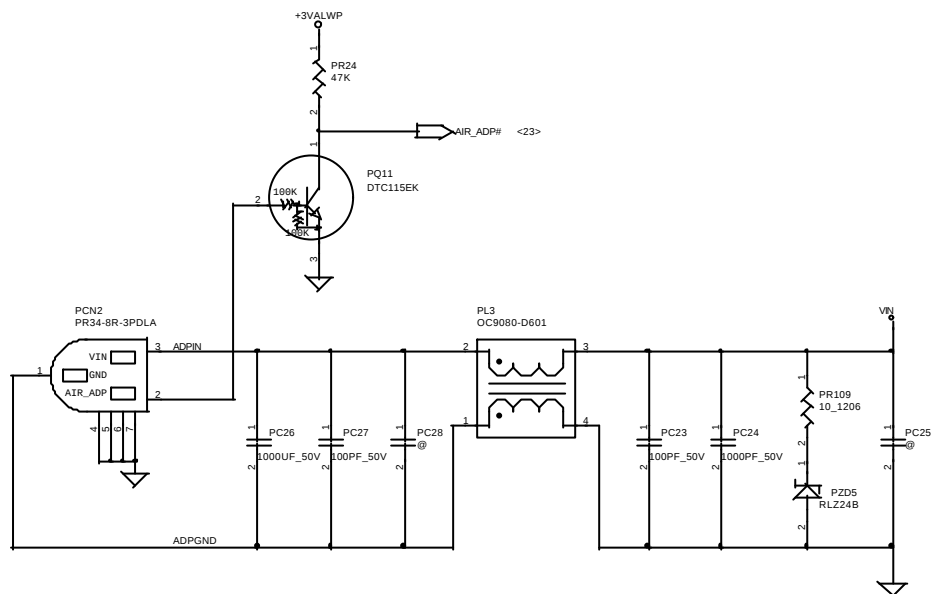


MINI_PCI CONNECTOR



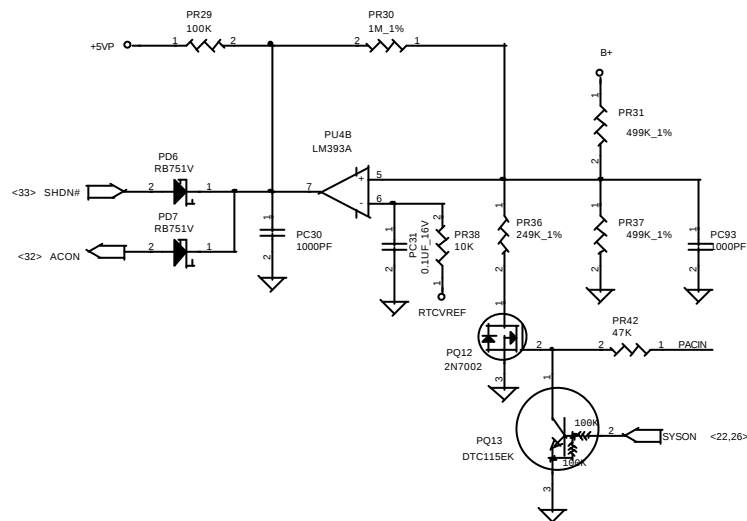
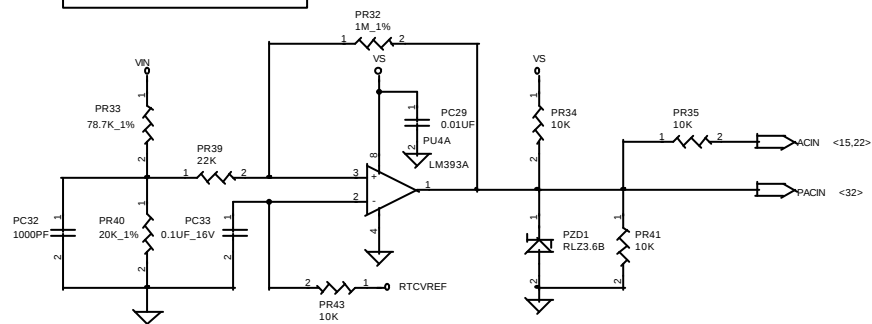
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.





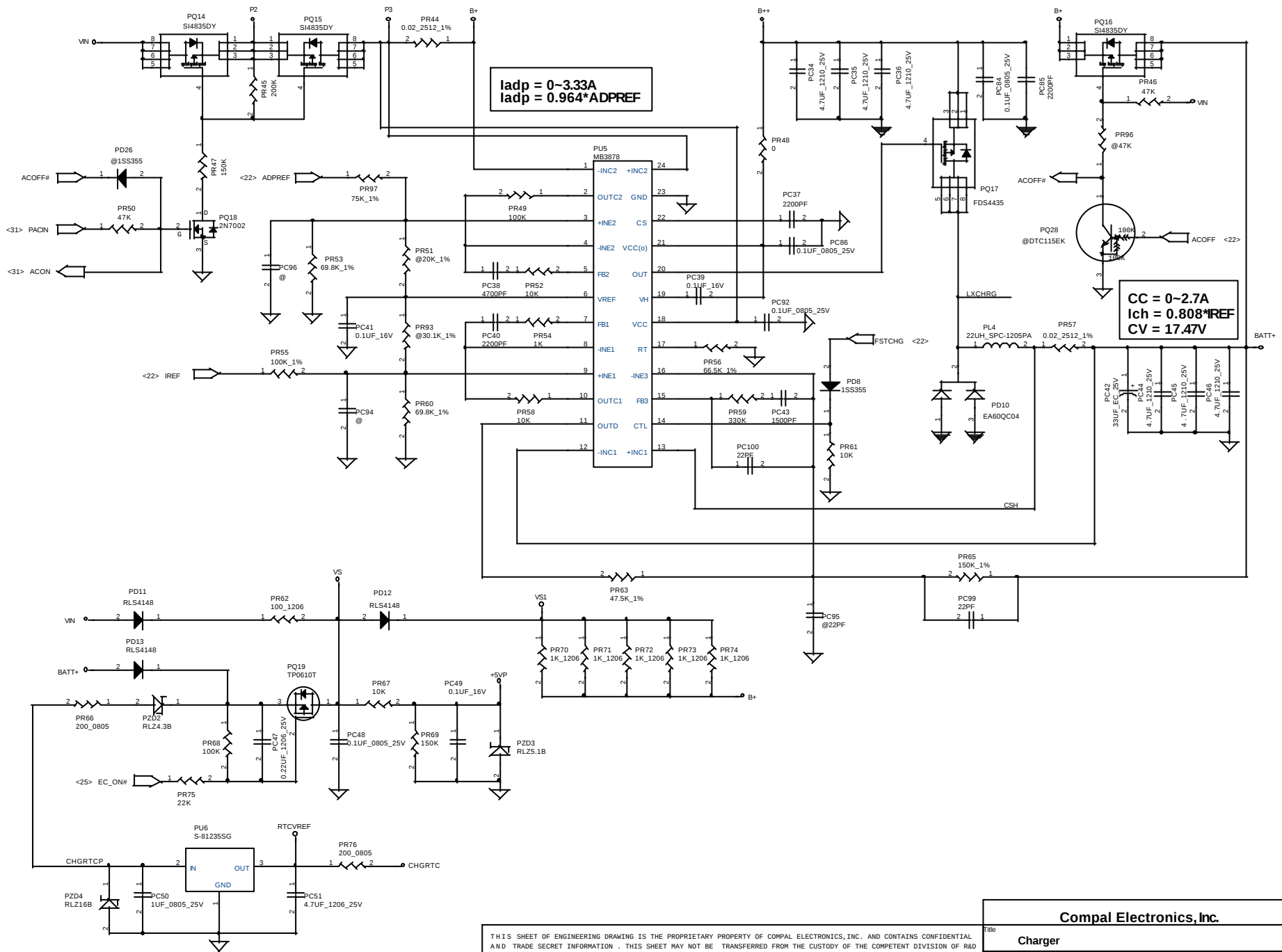
Precharge detector while AC adaptor
High 16.5 15.8 15.1
Low 13.9 13.2 12.5

Vin Detector
High 18.7 17.9 17.1
Low 18.0 17.3 16.5



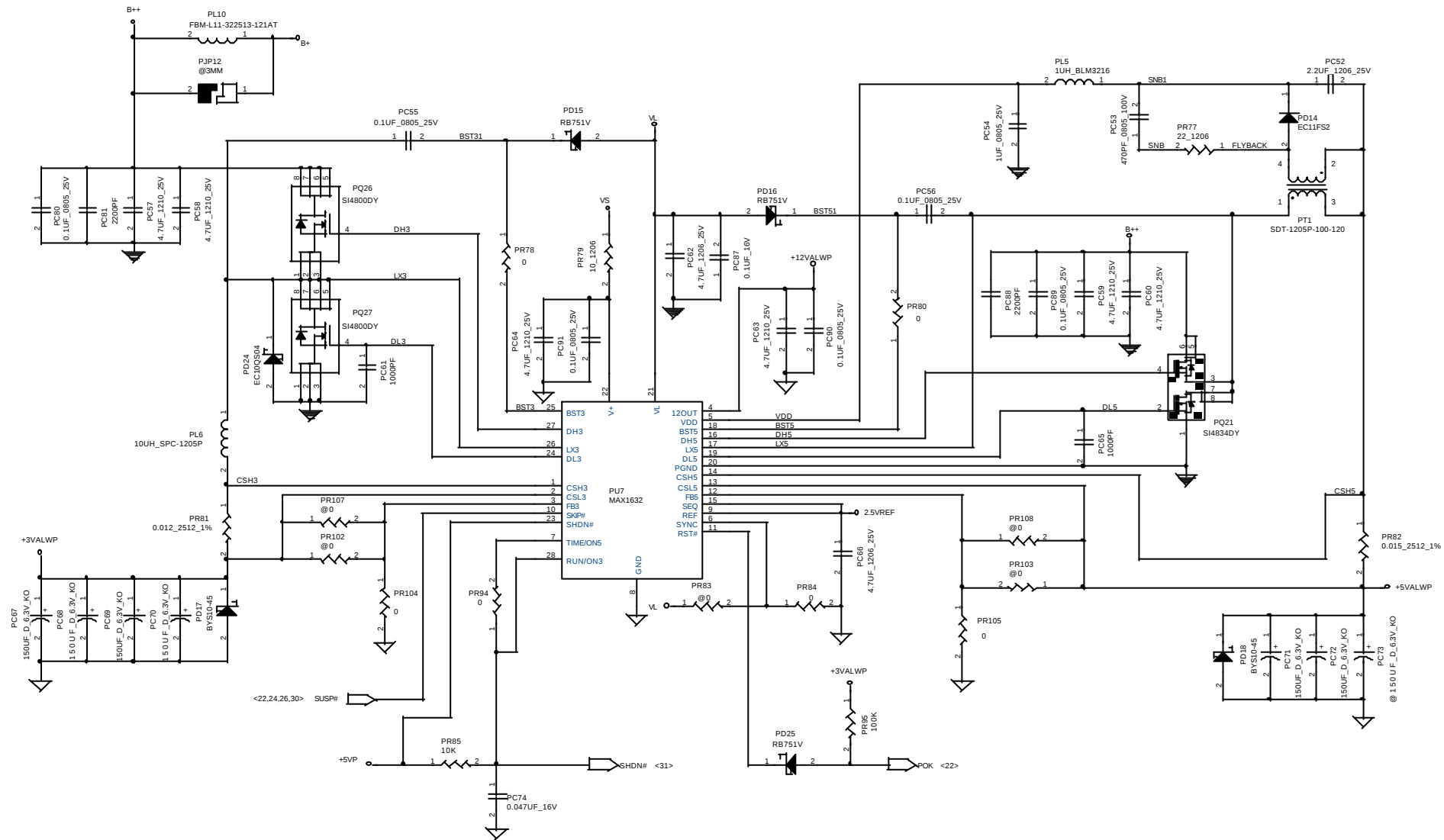
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
Title	Detector		
Size	Document Number	Rev	
	888L2 Main Board	2.0	
Date:	Thursday, January 11, 2001	Sheet	31 of 42



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
Title			
Charger			
Size	Document Number	Rev	
	888L2 Main Board	2.0	
Date:	Thursday, January 11, 2001	Sheet	32 of 42



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
Title	3.3V/5V/12V		
Size	Document Number	Rev	
	888L2 Main Board	2.0	
Date:	Thursday, January 11, 2001	Sheet	33 of 42

