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C	DIABLO 2.0 MP Build 2008.08.11																																																																																					
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F	<table><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td>DATE</td><td>CHANGE NO.</td><td>REV</td></tr></table>																						DATE	CHANGE NO.	REV			<table><tr><td>DRAWER</td><td>EE</td><td>DATE</td><td>POWER</td><td>DATE</td></tr><tr><td>DESIGN</td><td></td><td></td><td></td><td></td></tr><tr><td>CHECK</td><td></td><td></td><td></td><td></td></tr><tr><td>RESPONSIBLE</td><td></td><td></td><td></td><td></td></tr><tr><td>SIZE =</td><td colspan="3">3</td><td>VER:</td></tr><tr><td colspan="5">FILE NAME : XXXX-XXXXXX-XX</td></tr><tr><td>P/N</td><td colspan="4">XXXXXXXXXXXX</td></tr></table>	DRAWER	EE	DATE	POWER	DATE	DESIGN					CHECK					RESPONSIBLE					SIZE =	3			VER:	FILE NAME : XXXX-XXXXXX-XX					P/N	XXXXXXXXXXXX						<table><tr><td colspan="4">INVENTEC</td></tr><tr><td colspan="4">TITLE DIABLO 2.0 MV</td></tr><tr><td colspan="4">Release version : MP 02</td></tr><tr><td>SIZE A3</td><td>CODE CS</td><td>DOC. NUMBER 1310A22155-0-MTR</td><td>REV A02</td></tr><tr><td colspan="2">SHEET 1</td><td colspan="2">OF 61</td></tr></table>	INVENTEC				TITLE DIABLO 2.0 MV				Release version : MP 02				SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MTR	REV A02	SHEET 1		OF 61	
DATE	CHANGE NO.	REV																																																																																				
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DIABLO 2.0

MP Build

2008.08.11

DATE	CHANGE NO.	REV

	EE	DATE	POWER	DATE
DRAWER				
DESIGN				
CHECK				
RESPONSIBLE				
SIZE = 3 VER:				
FILE NAME : XXXX-XXXXXX-XX				
P/N	XXXXXXXXXXXX			

INVENTEC			
TITLE DIABLO 2.0 MV			
Release version : MP 02			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MTR	A02
SHEET		1	OF 61

1	2	3	4	5	6	7	8
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DIABLO 2.0

MP Build

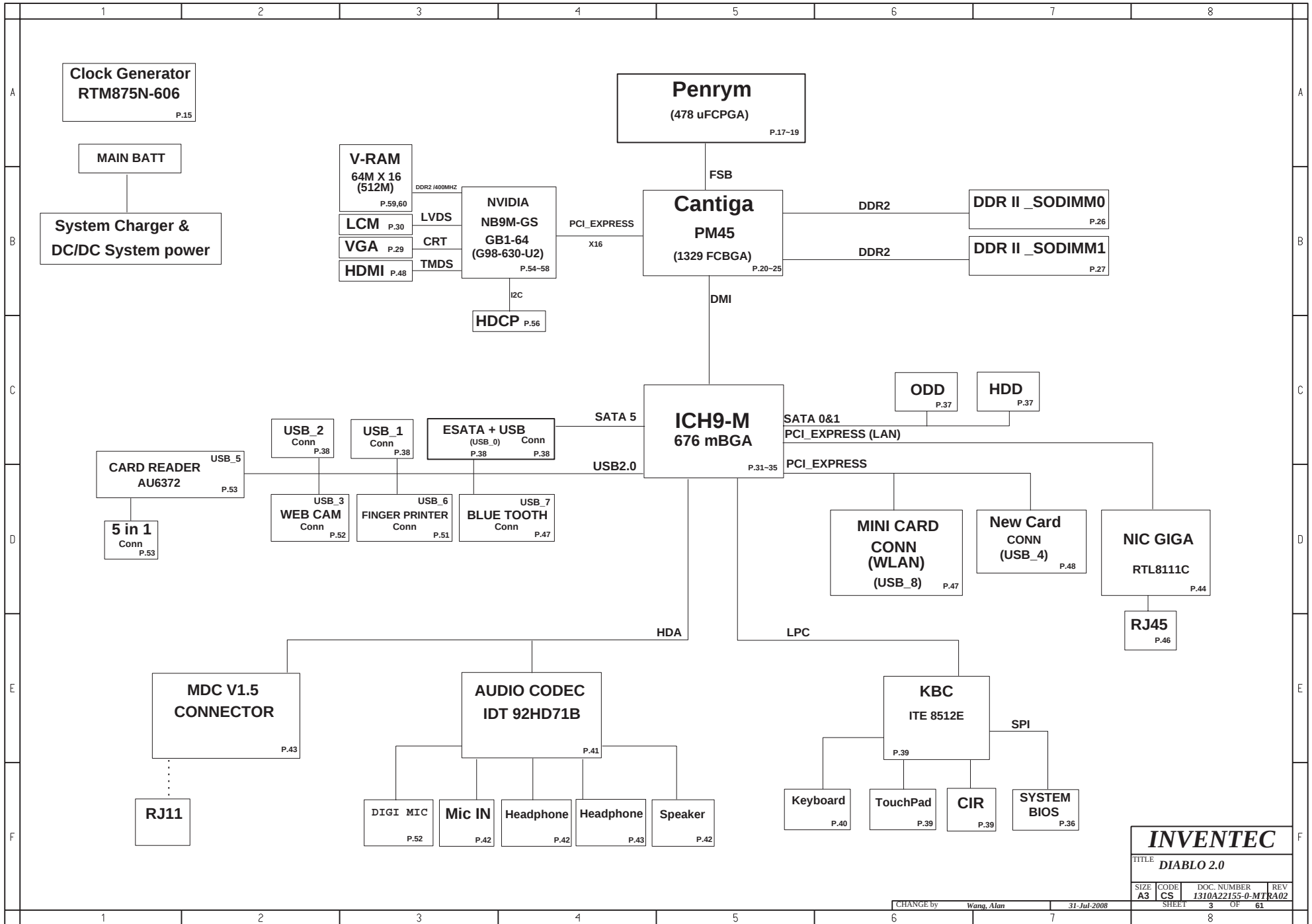
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DRAWER	EE	DATE	POWER	DATE
DESIGN				
CHECK				
RESPONSIBLE				

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FILE NAME : XXXX-XXXXXX-XX			
P/N	XXXXXXXXXXXX		

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TITLE DIABLO 2.0 MV			
Release version : MP 02			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MTR	A02
SHEET	1	OF	61



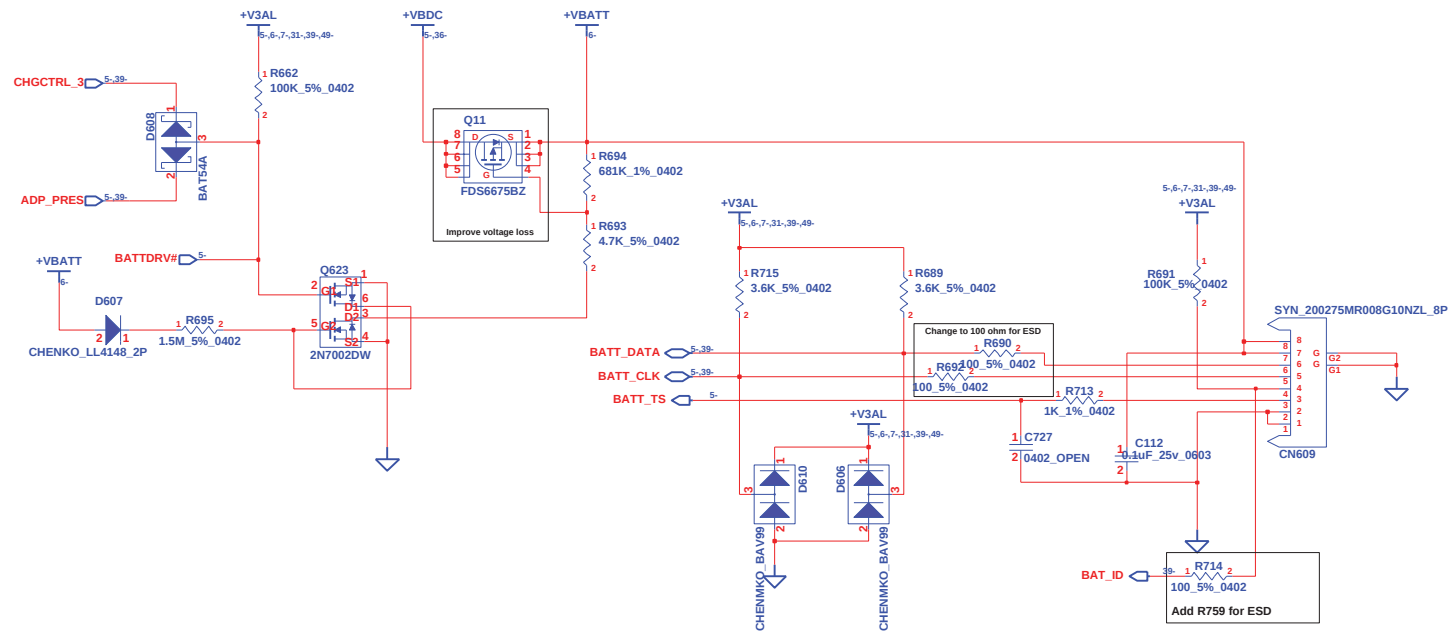
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DIABLO 2.0

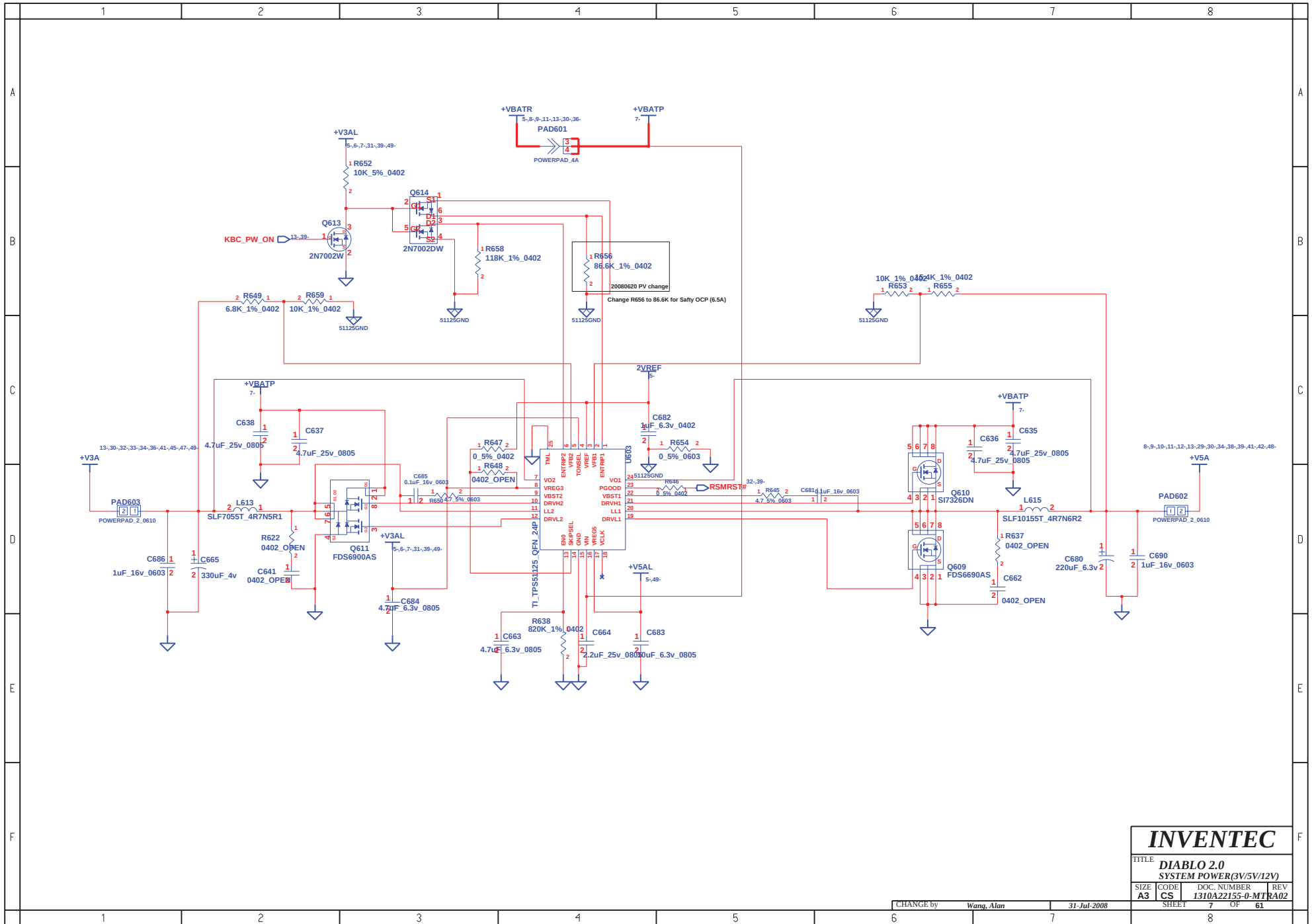
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A3	CS	1310A22155-0-MT	RA02

CHANGE by Wang, Alan 31-Jul-2008

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INVENTEC			
TITLE			
DIABLO 2.0 BATTERY CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MTR	A02
CHANGE by		SHEET	
Wang, Alan		6 OF 61	

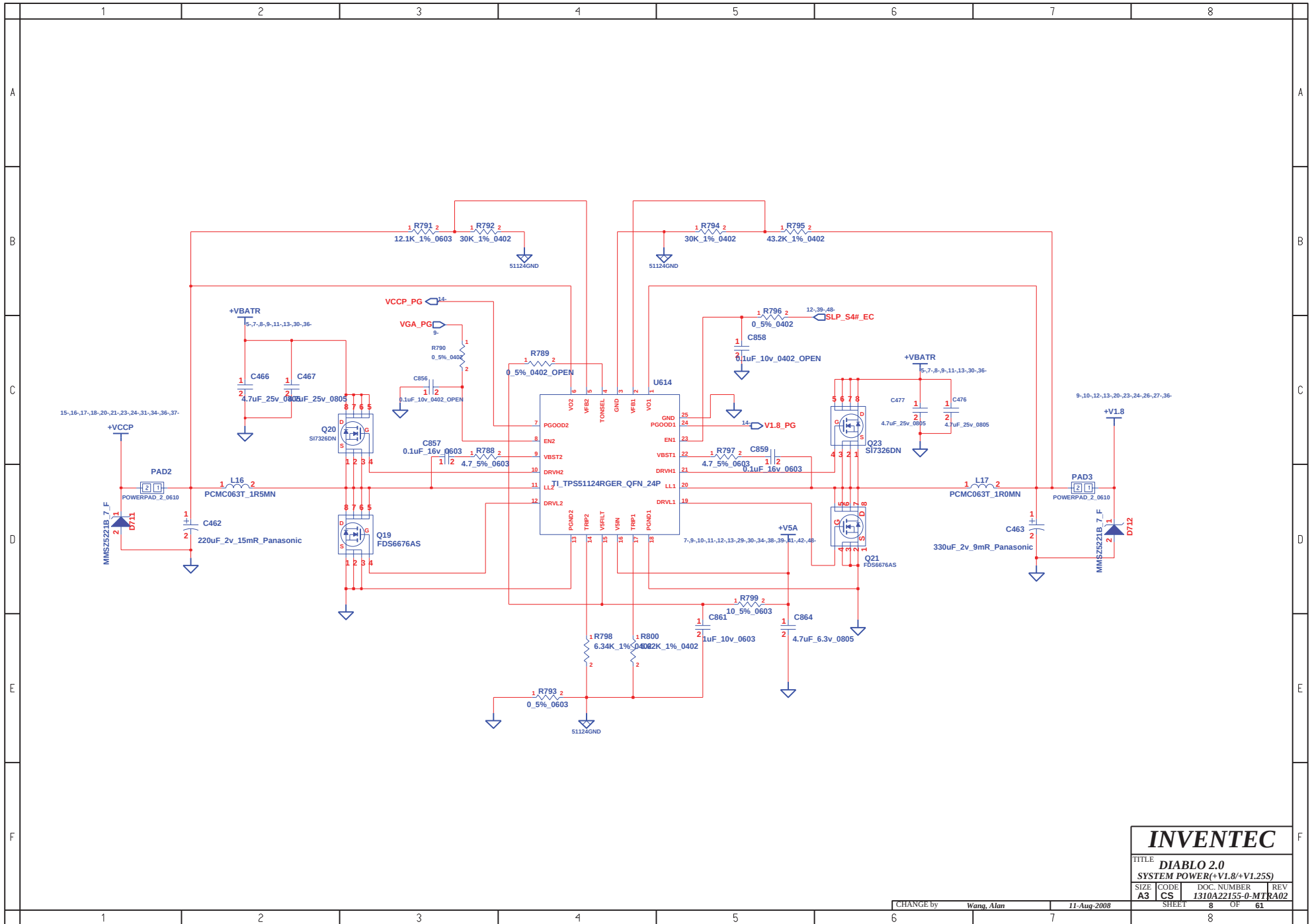


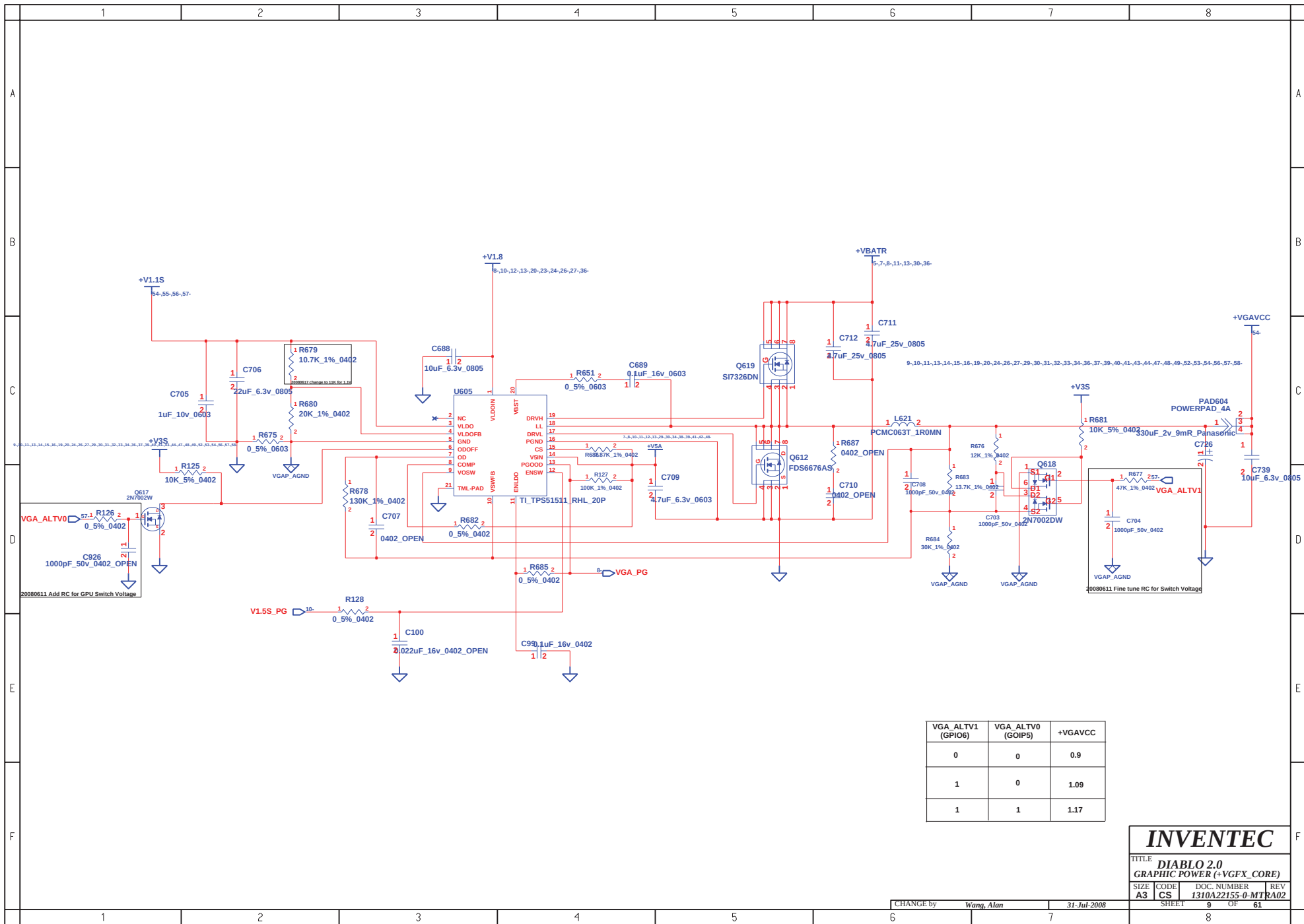
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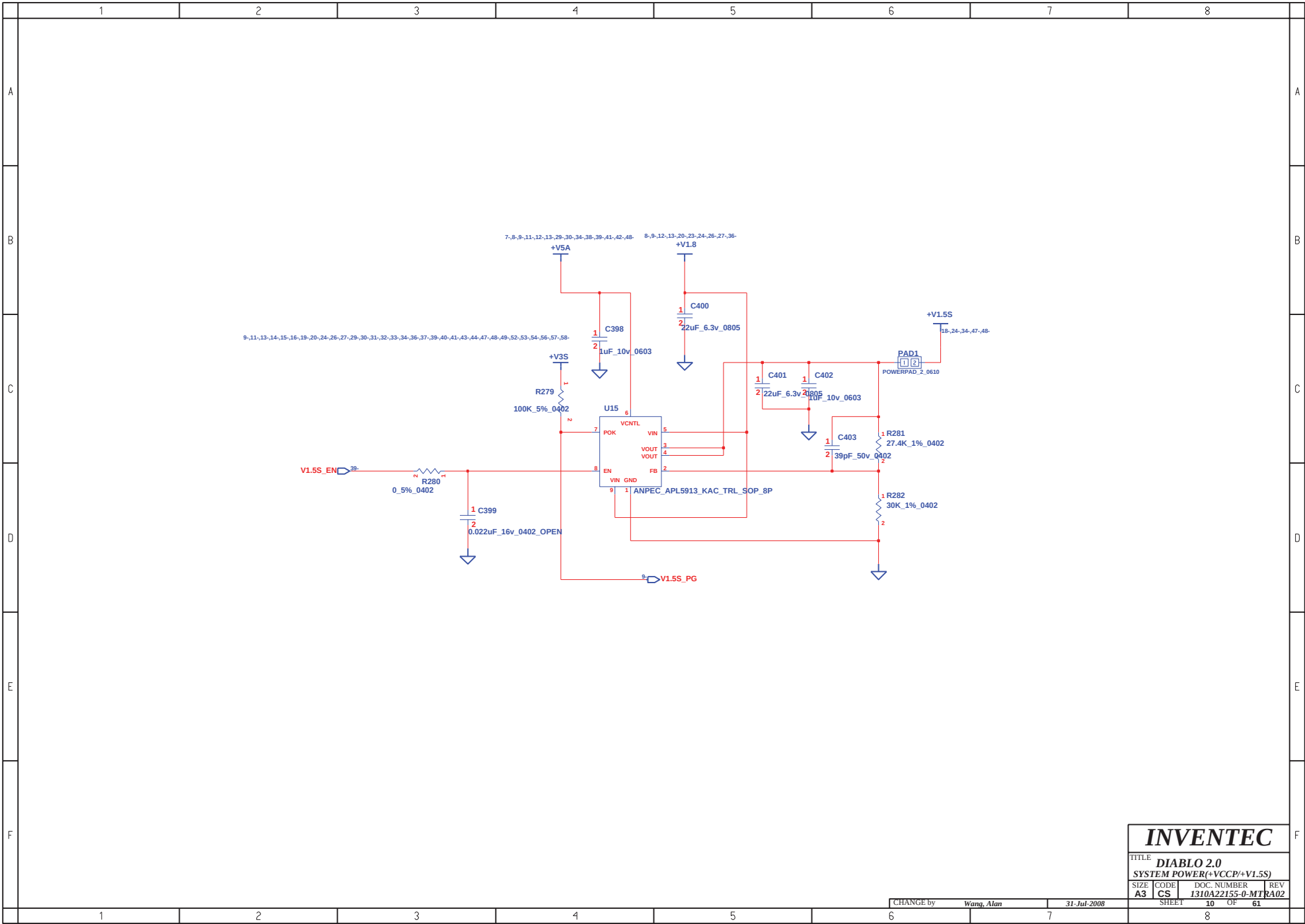
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SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MT	RA02
SHEET		7	OF 61

CHANGE by Wang, Alan 31-Jul-2008

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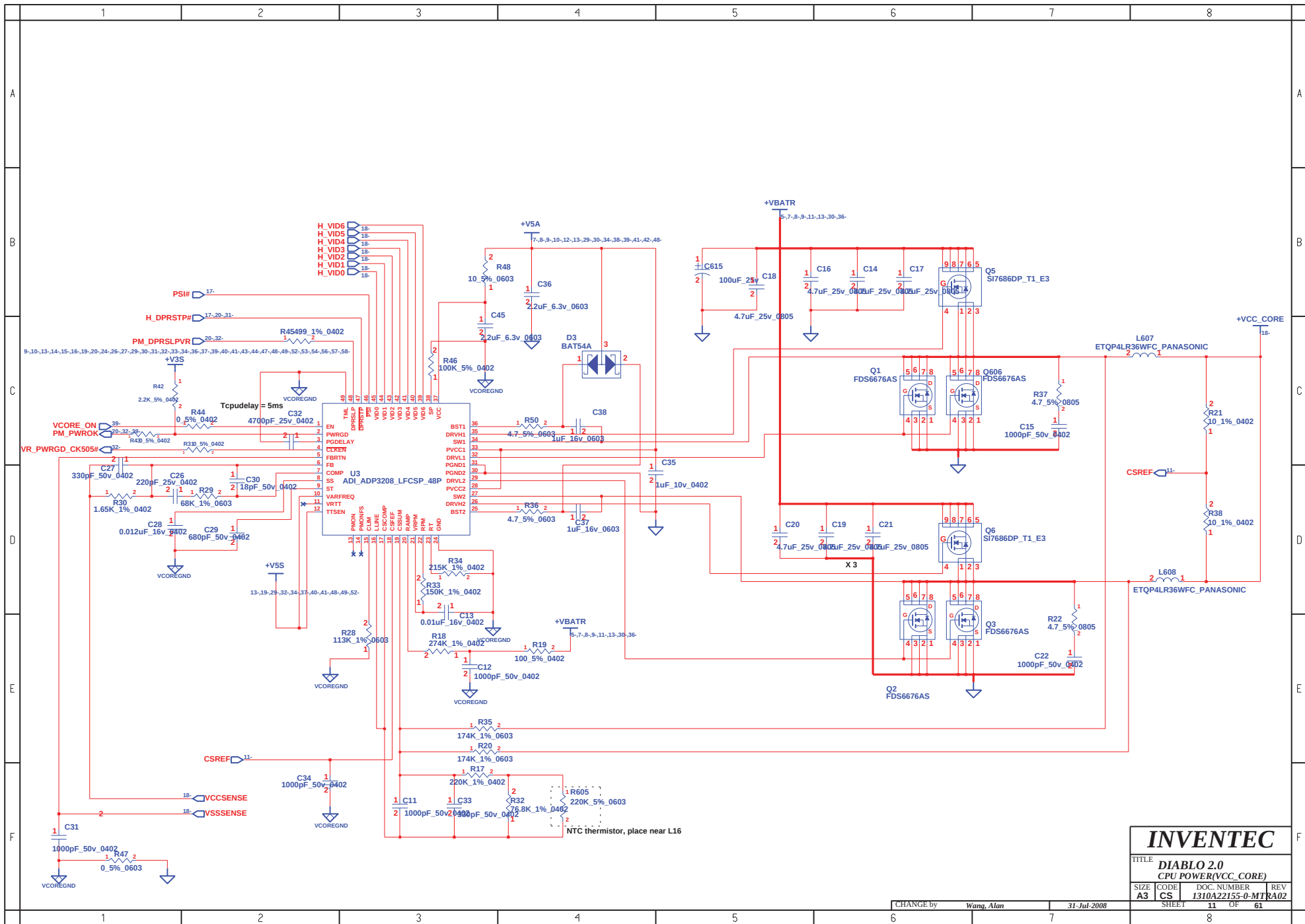
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DIABLO 2.0
SYSTEM POWER(+VCCP/+V1.5S)

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MT	RA02

CHANGE by Wang, Alan 31-Jul-2008

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INVENTEC

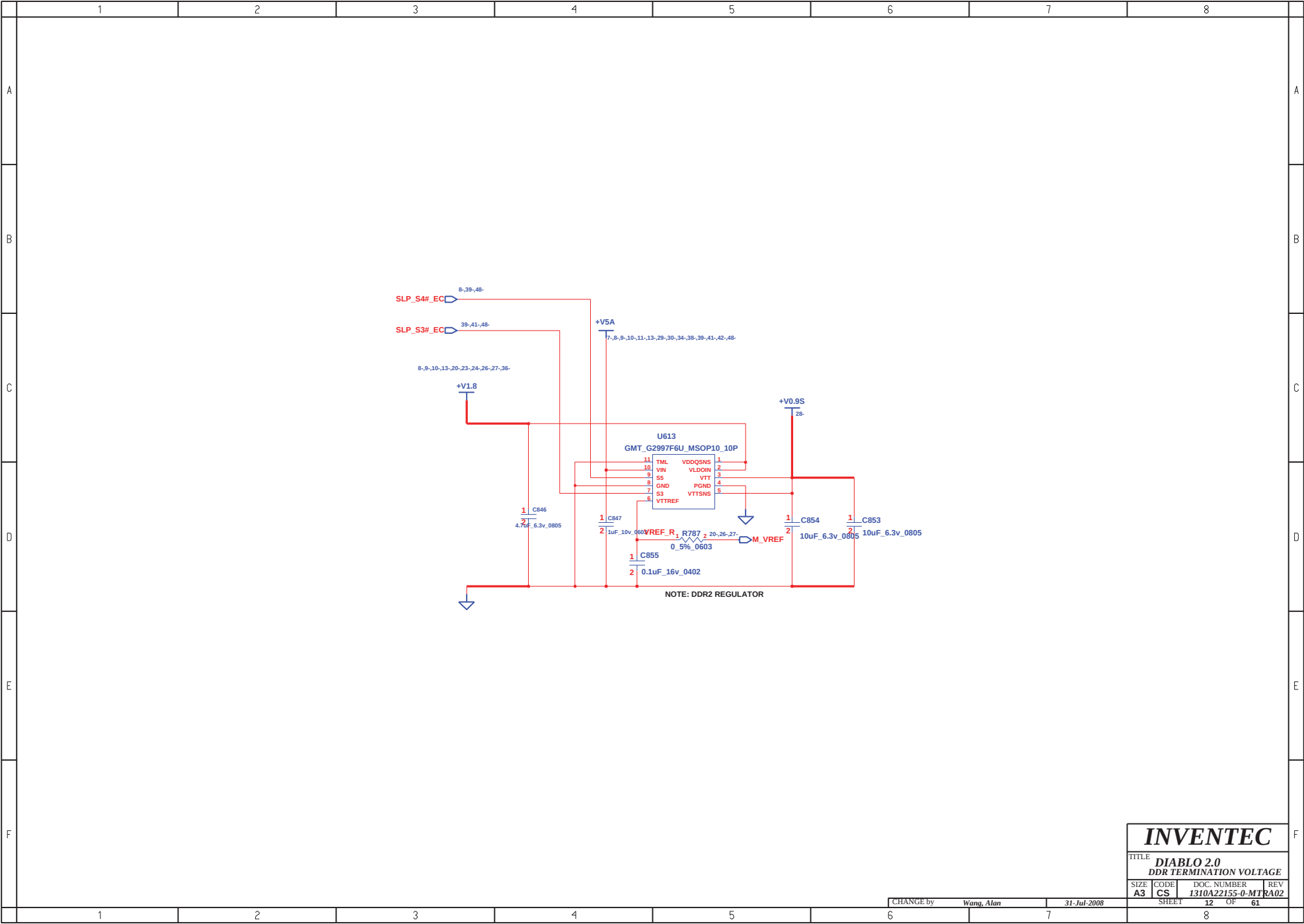
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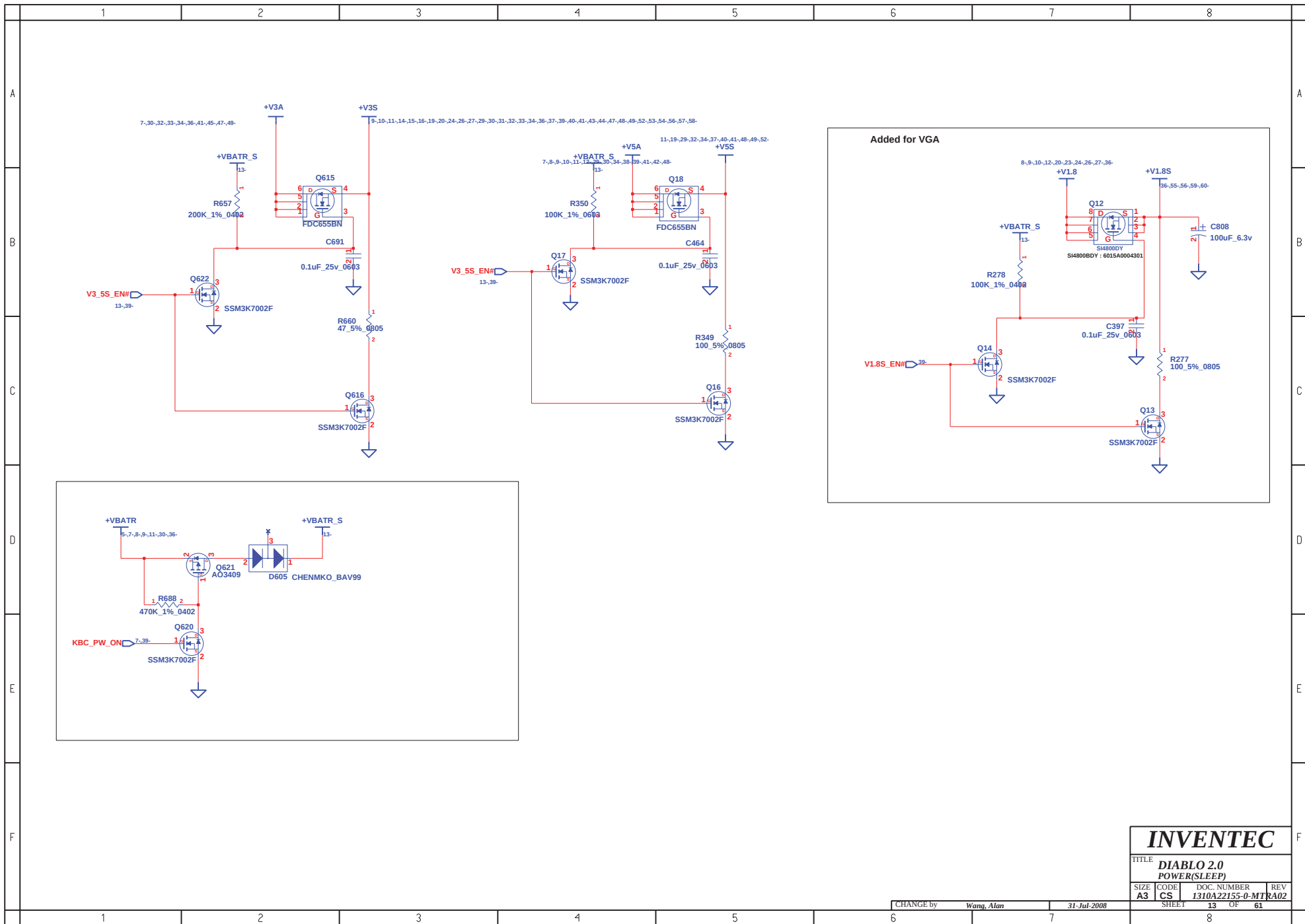
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DOC. NUMBER
1310A22155-0-MT

REV
RA02



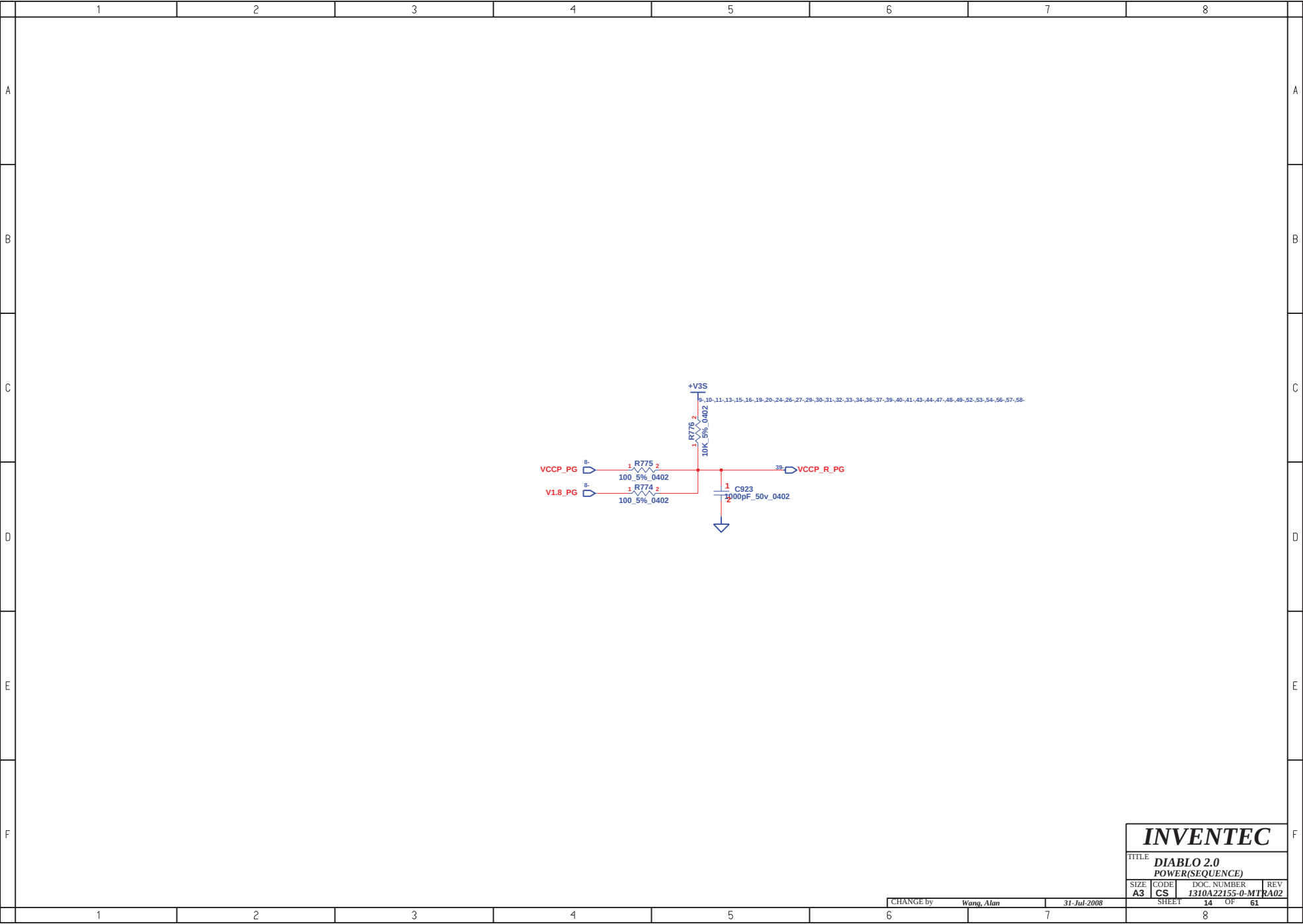


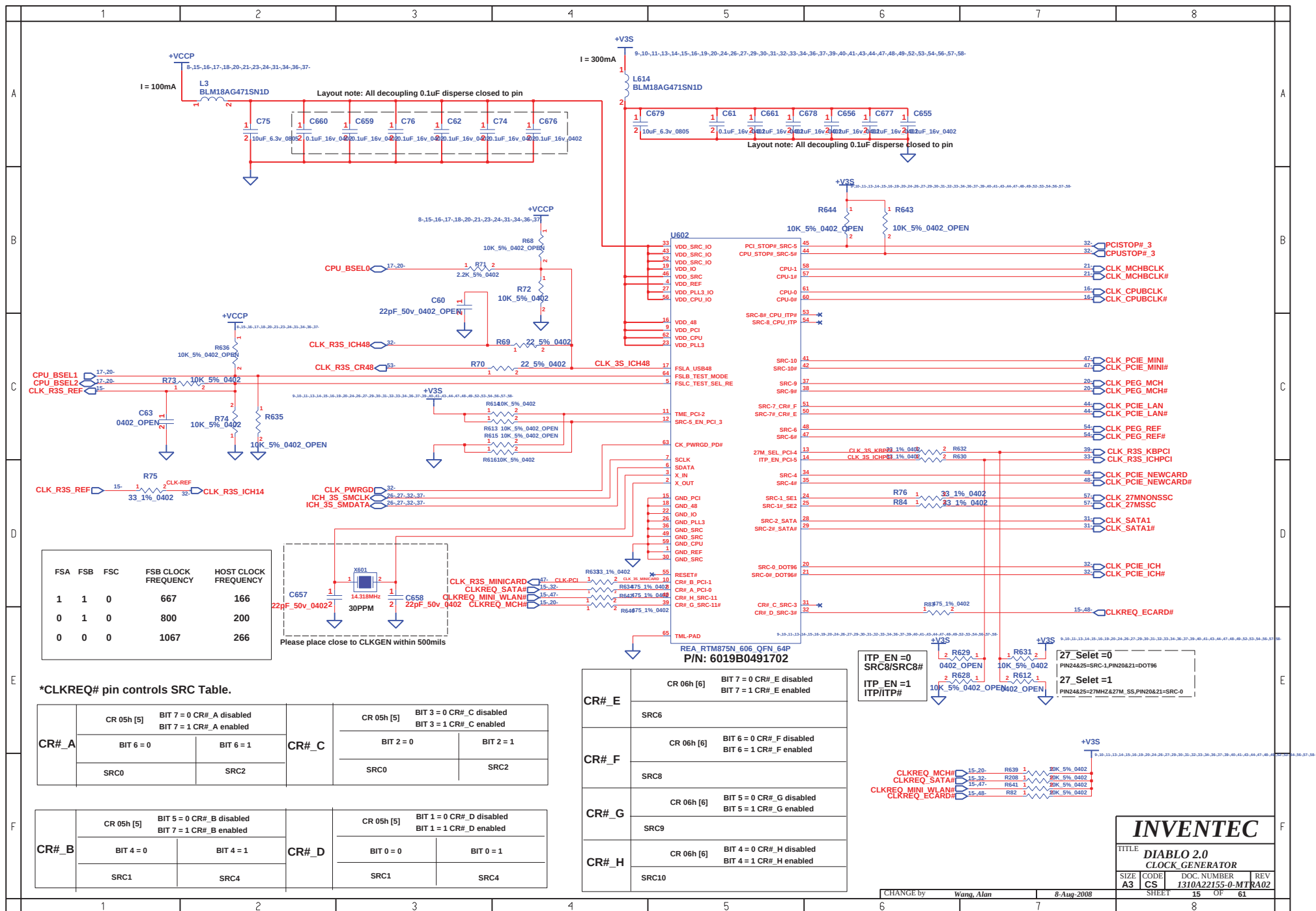
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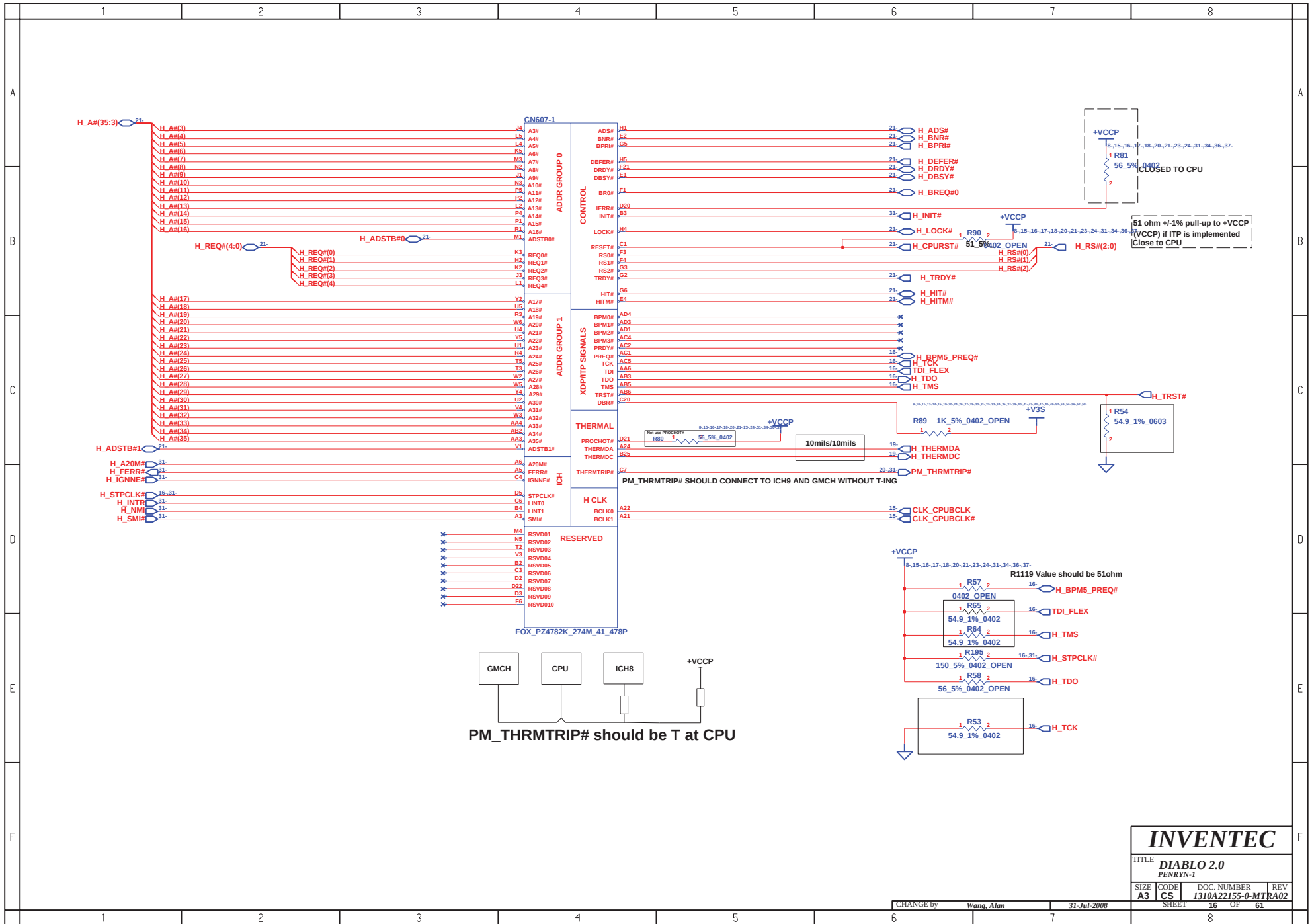
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DIABLO 2.0
POWER(SLEEP)

SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MTR	REV A02
SHEET 13		OF 61	

CHANGE by Wang, Alan 31-Jul-2008

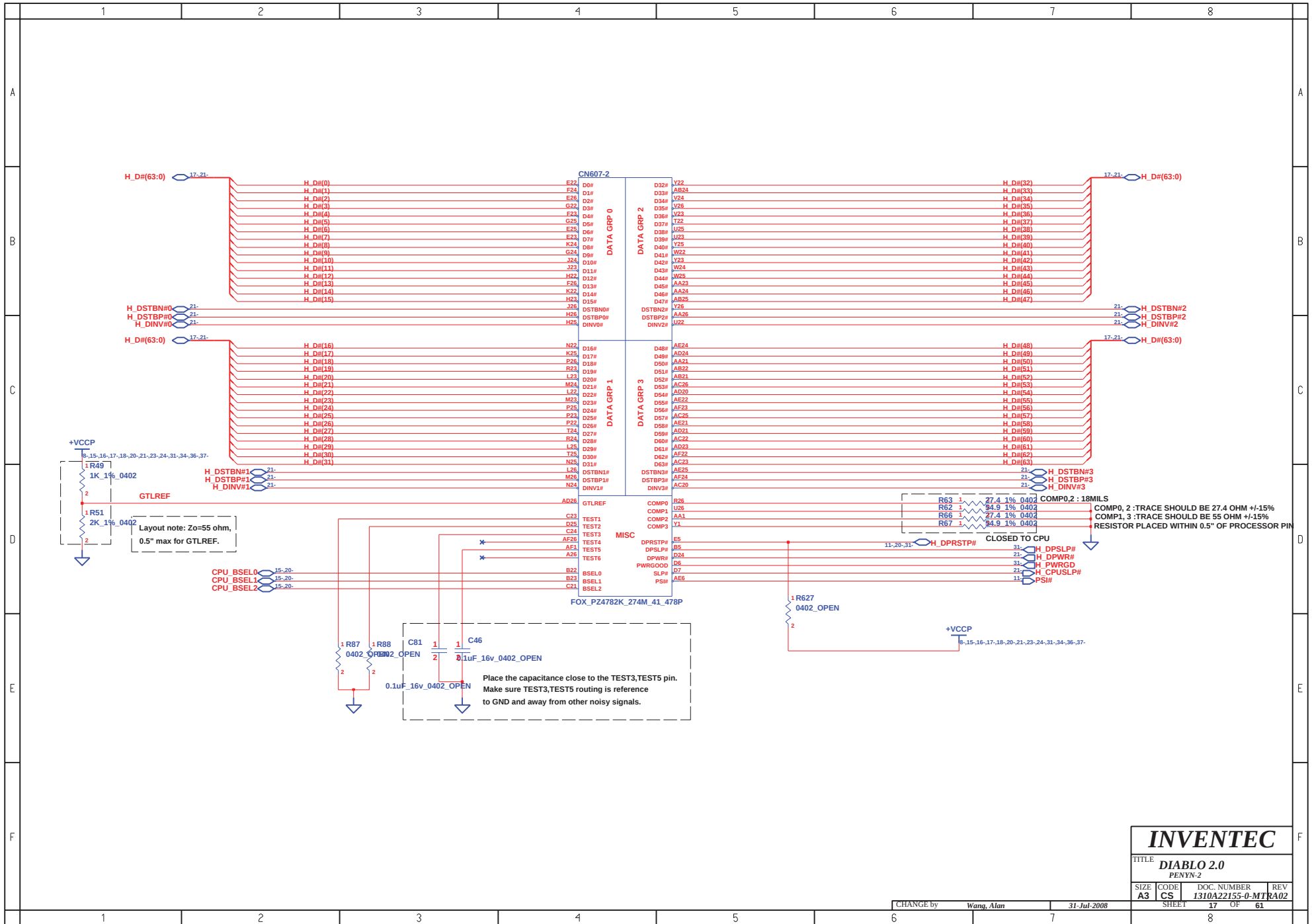


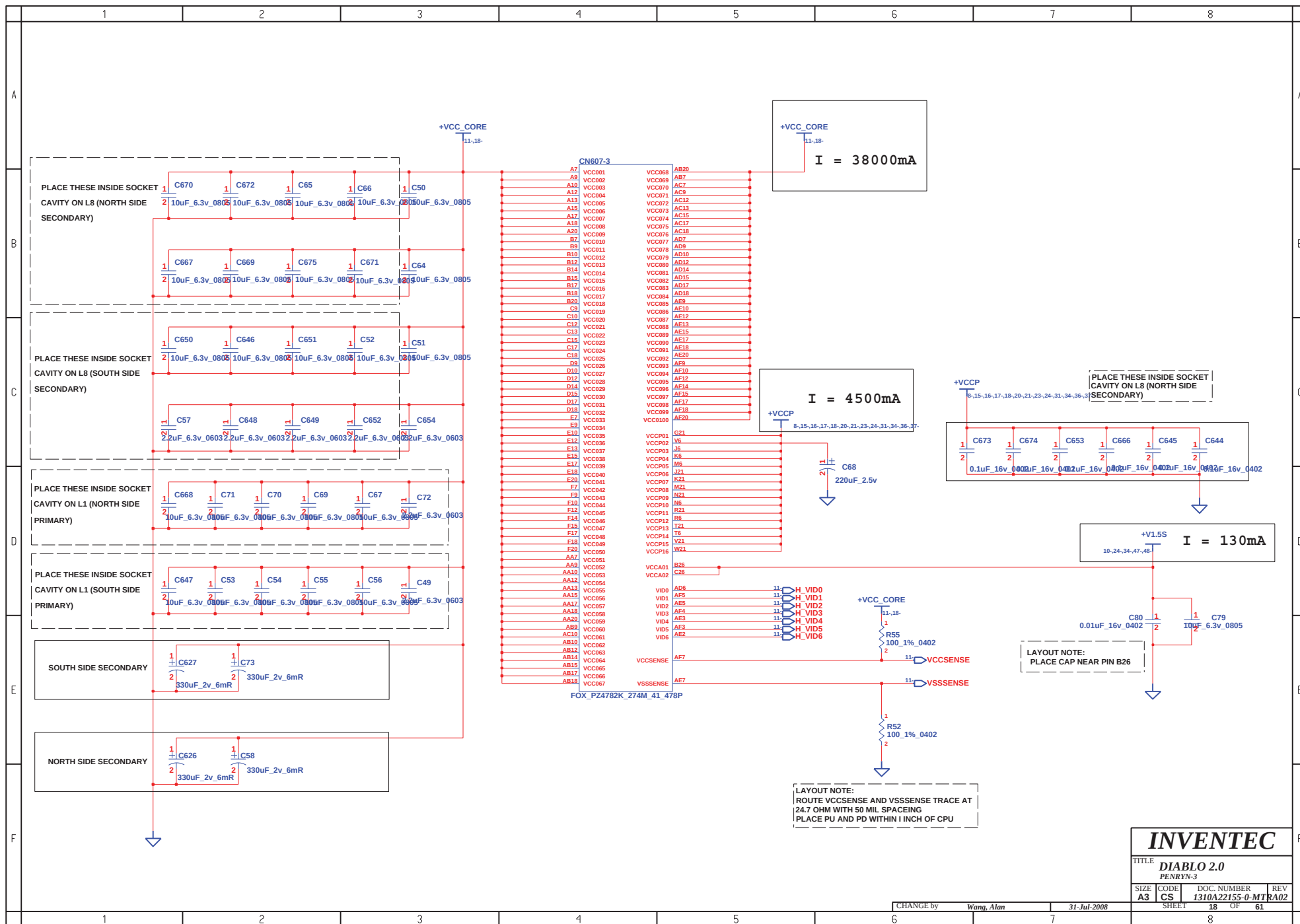




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TITLE DIABLO 2.0 PENRYN-1			
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CHANGE by Wang, Alan		31-Jul-2008	
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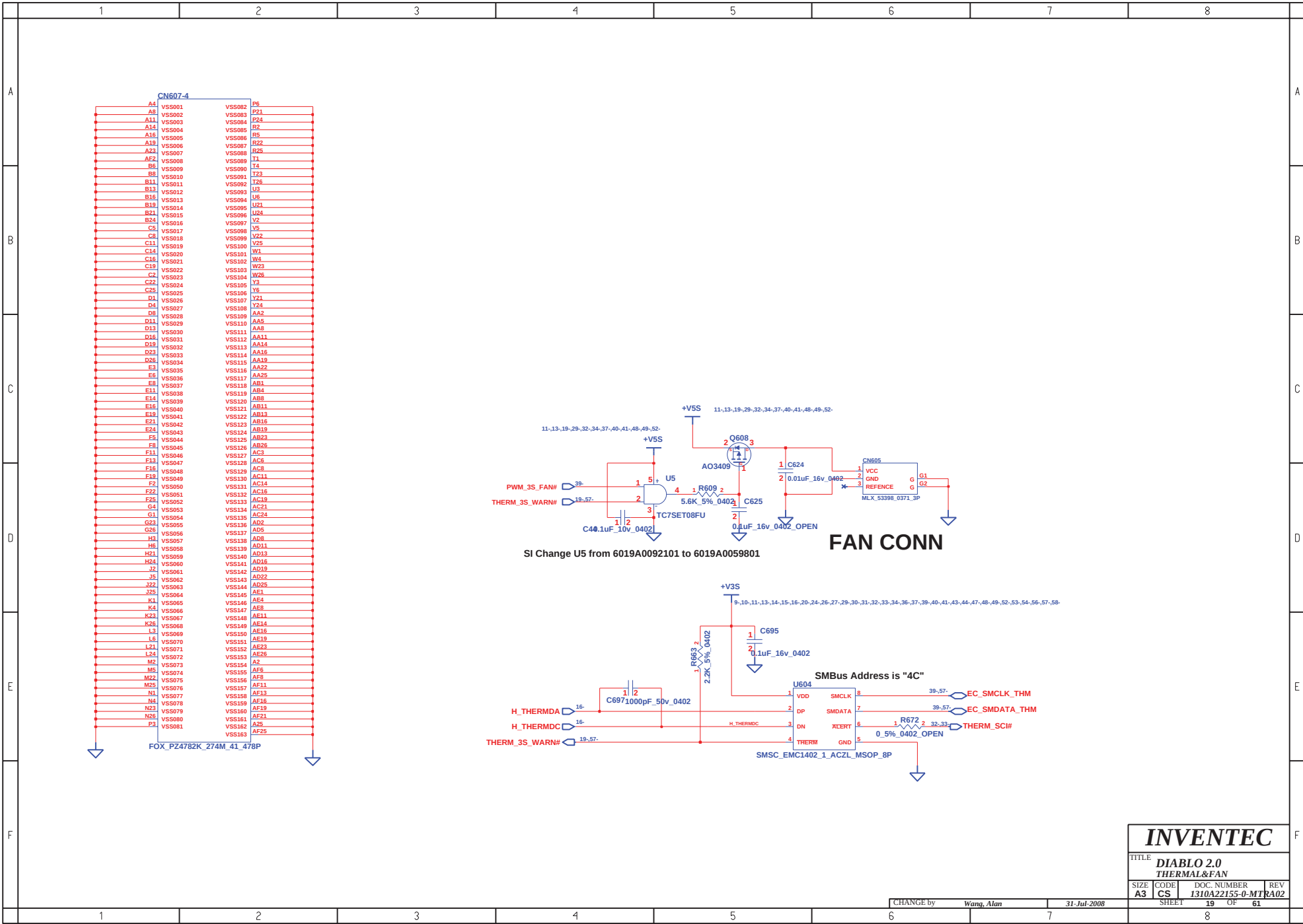




INVENTEC

TITLE DIABLO 2.0 PENRYN-3			
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CHANGE by Wang, Alan 31-Jul-2008



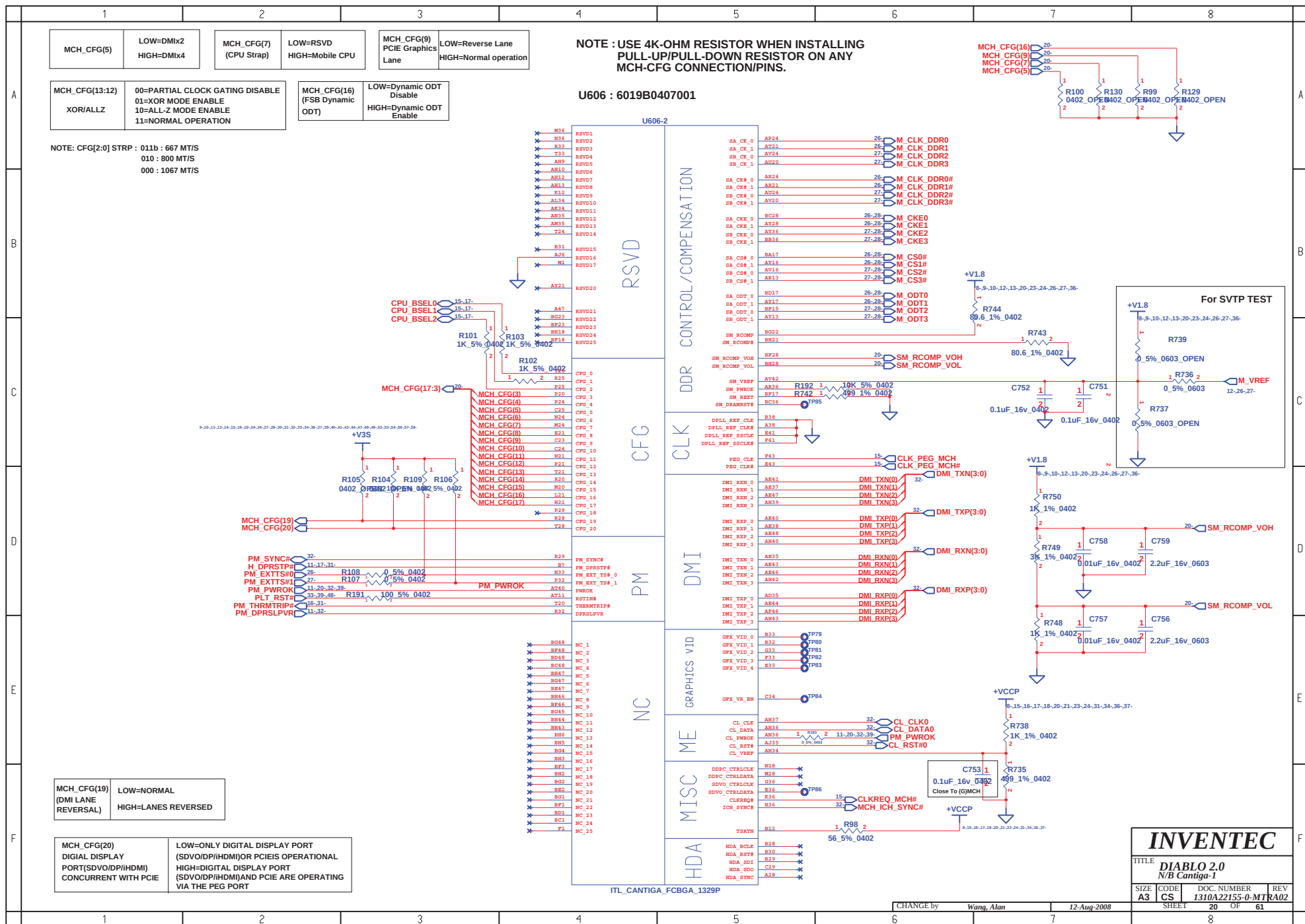
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TITLE
DIABLO 2.0
THERMAL&FAN

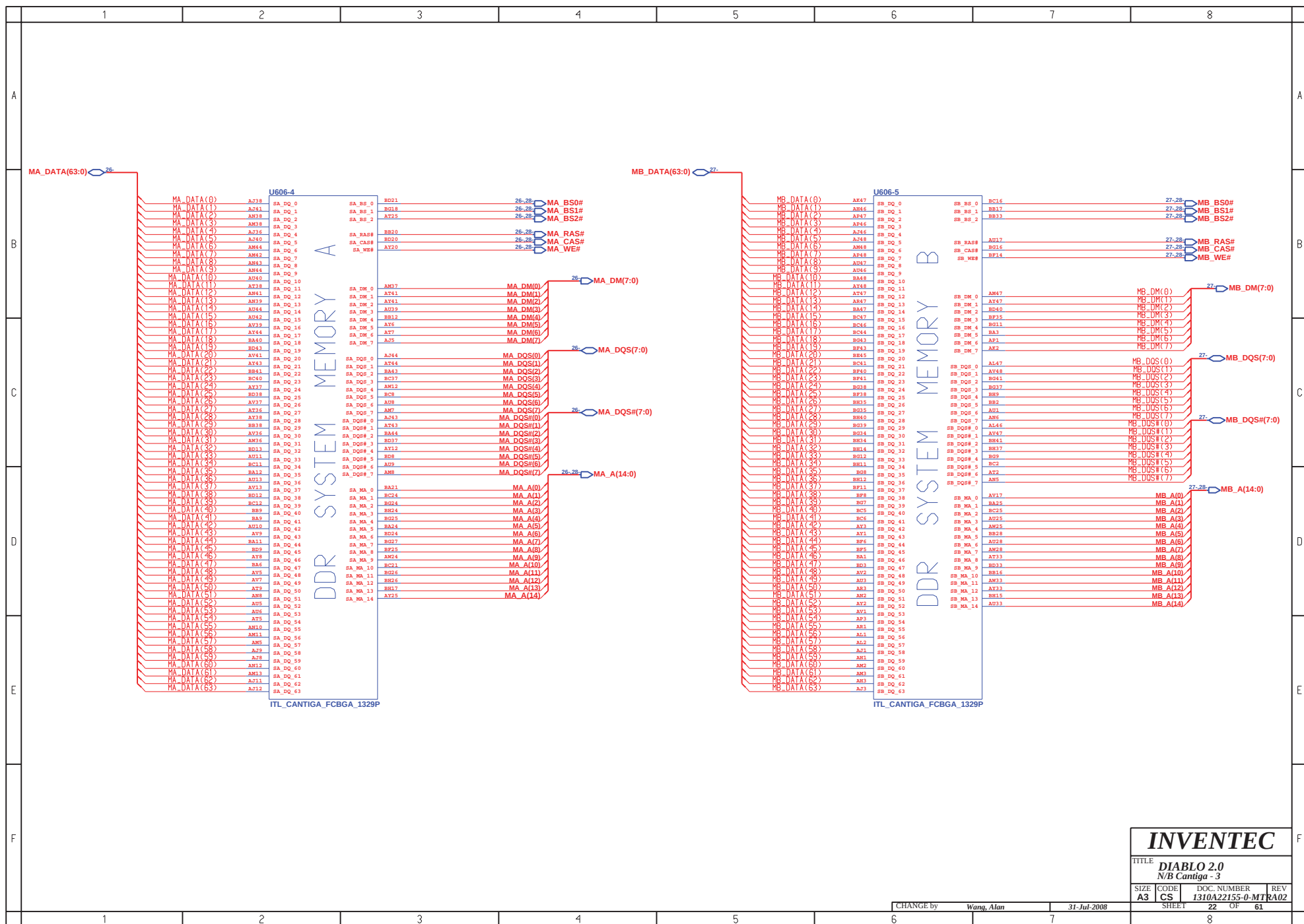
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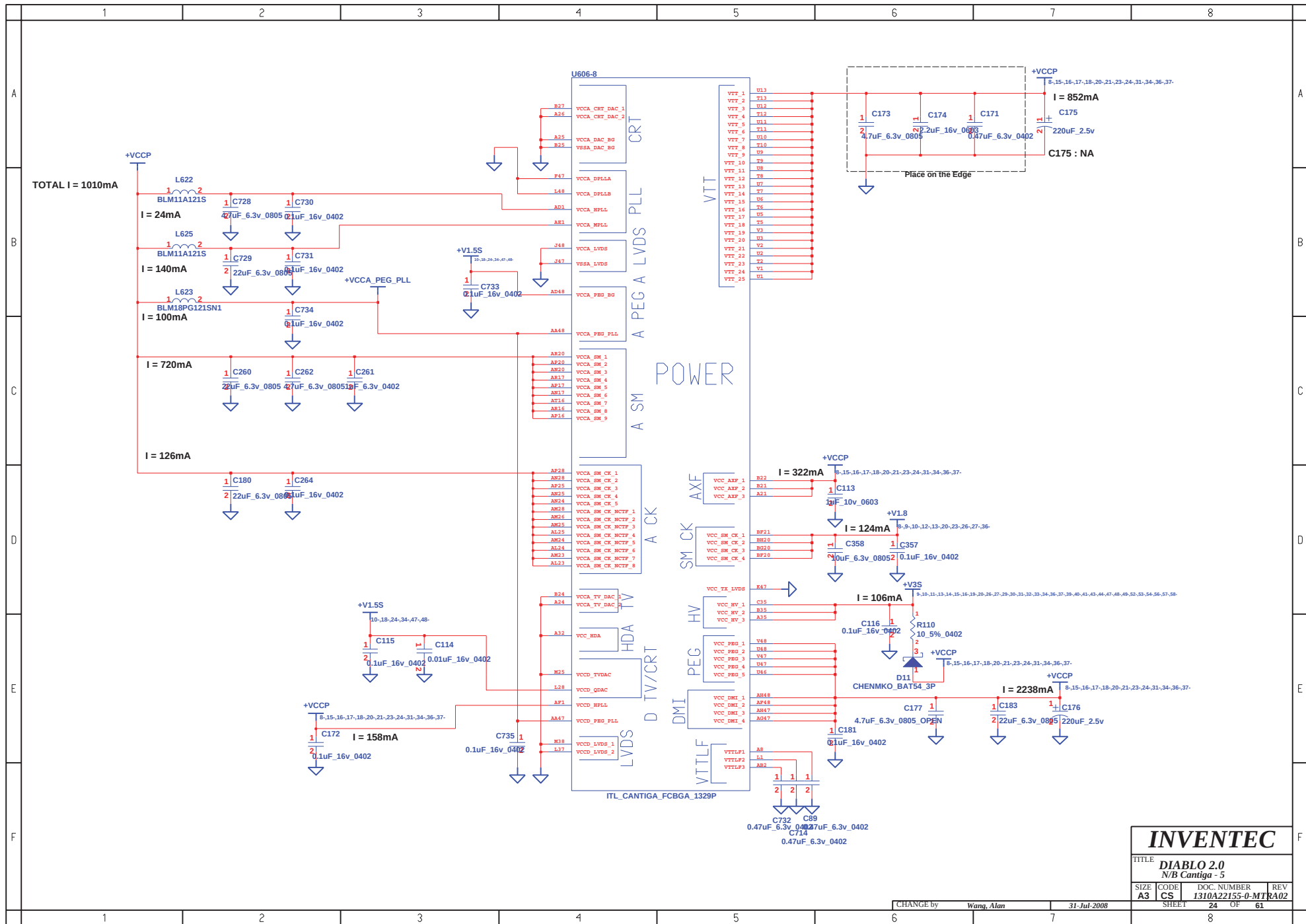
CHANGE by Wang, Alan 31-Jul-2008

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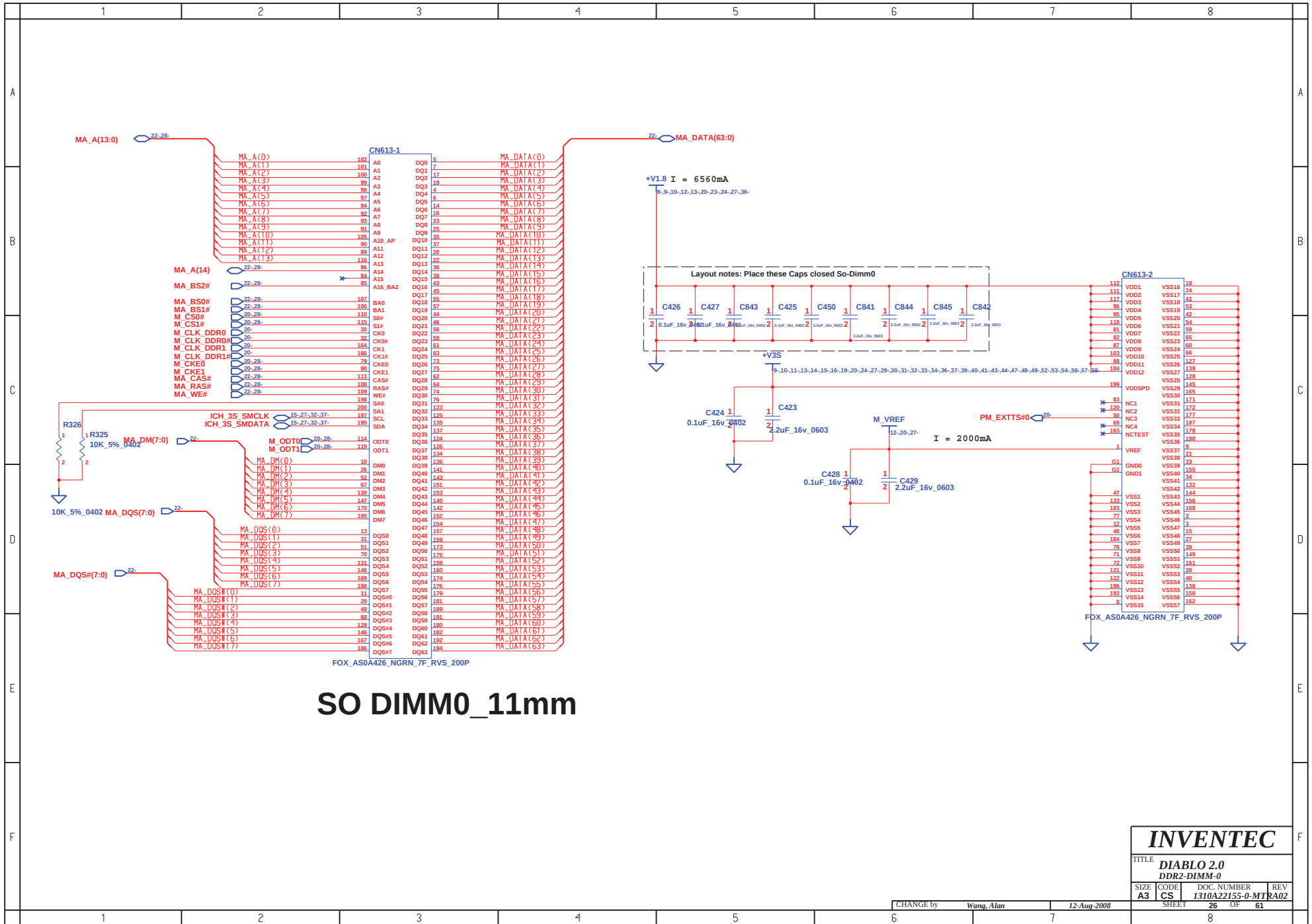
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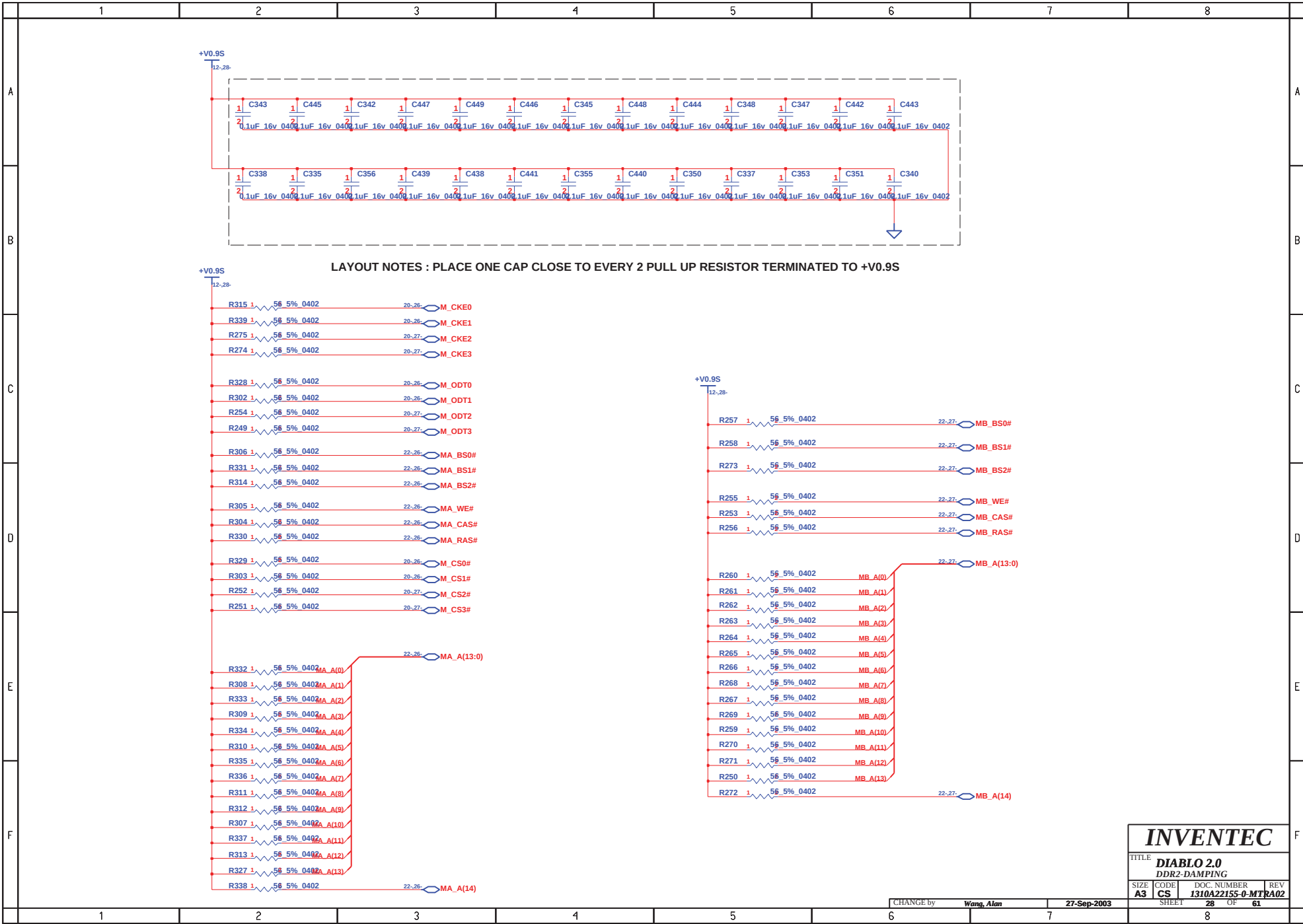
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SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MT	REV A02
SHEET 25		OF 61	

CHANGE by Wang, Alan 12-Aug-2008





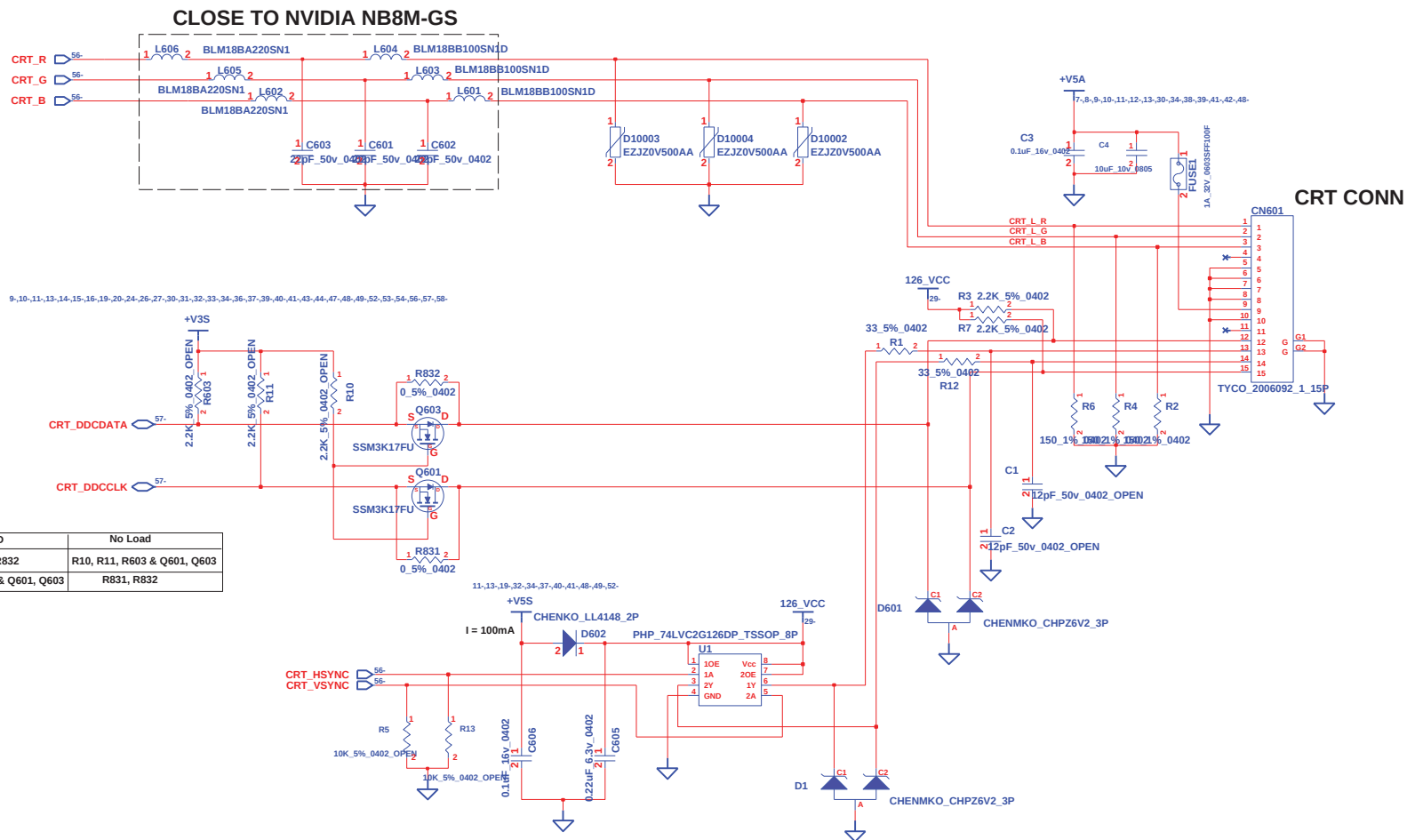
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TITLE
DIABLO 2.0
DDR2-DAMPING

SIZE	CODE	DOC. NUMBER	REV
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CHANGE by **Wang, Alan** 27-Sep-2003

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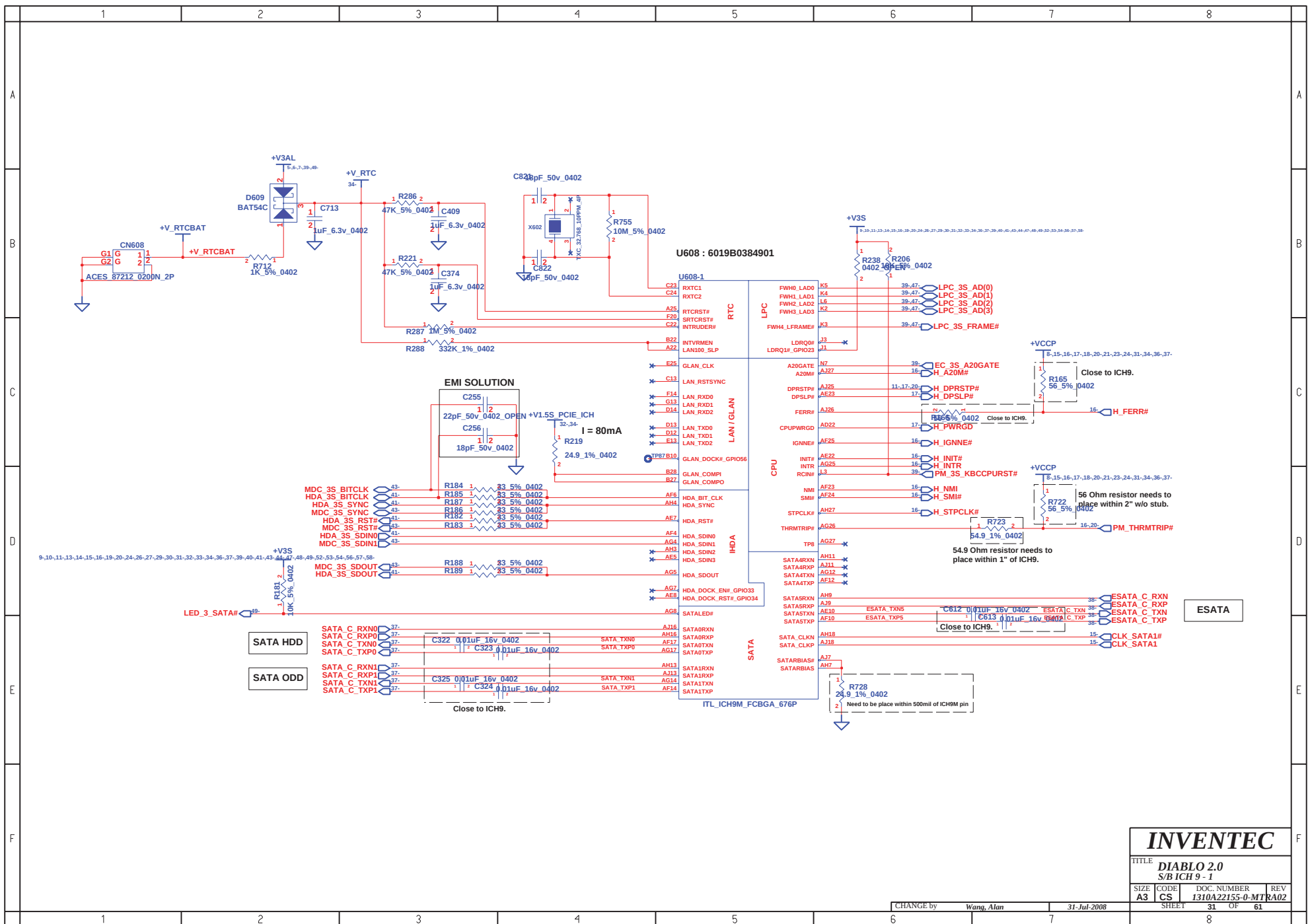
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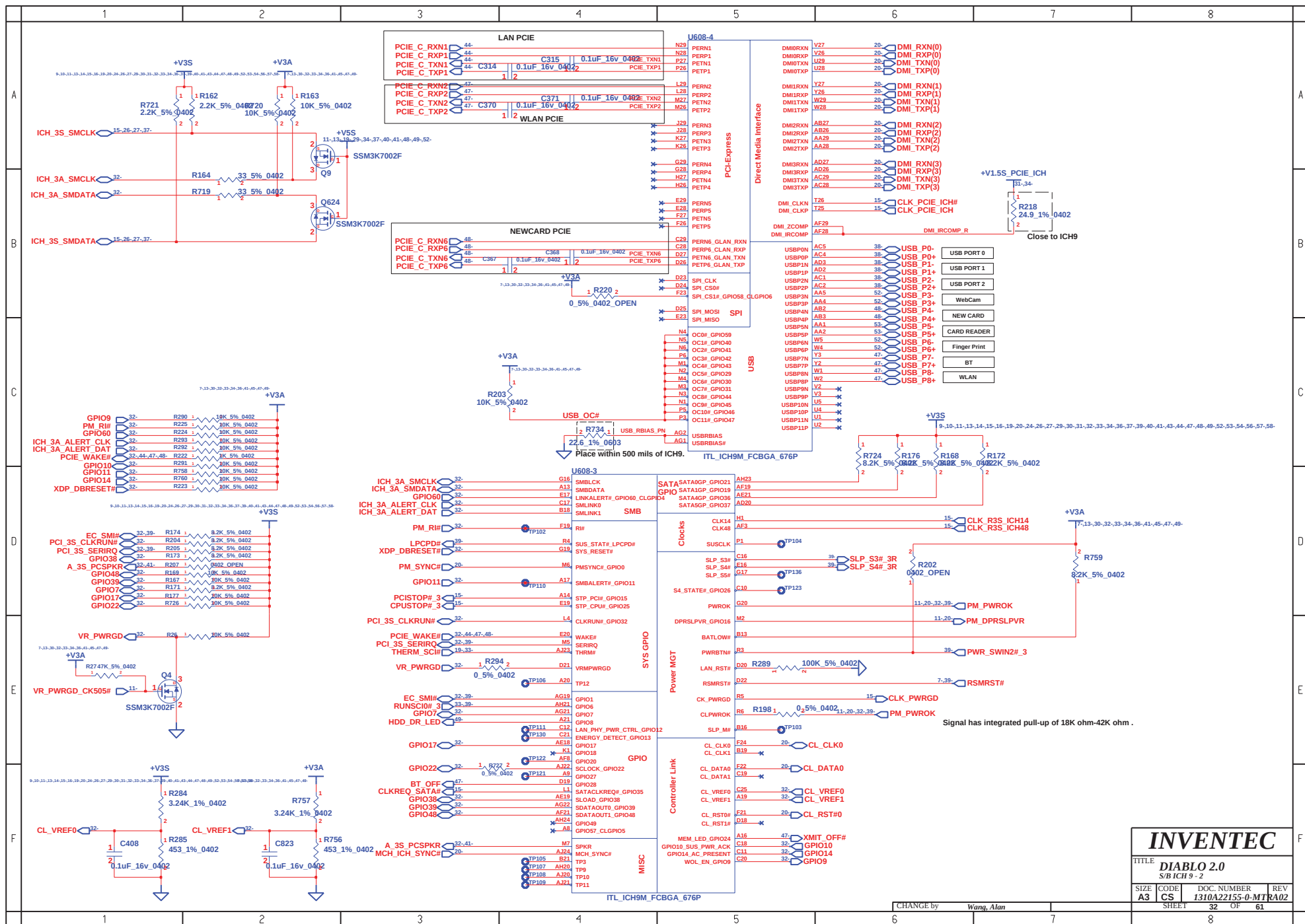
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DIABLO 2.0
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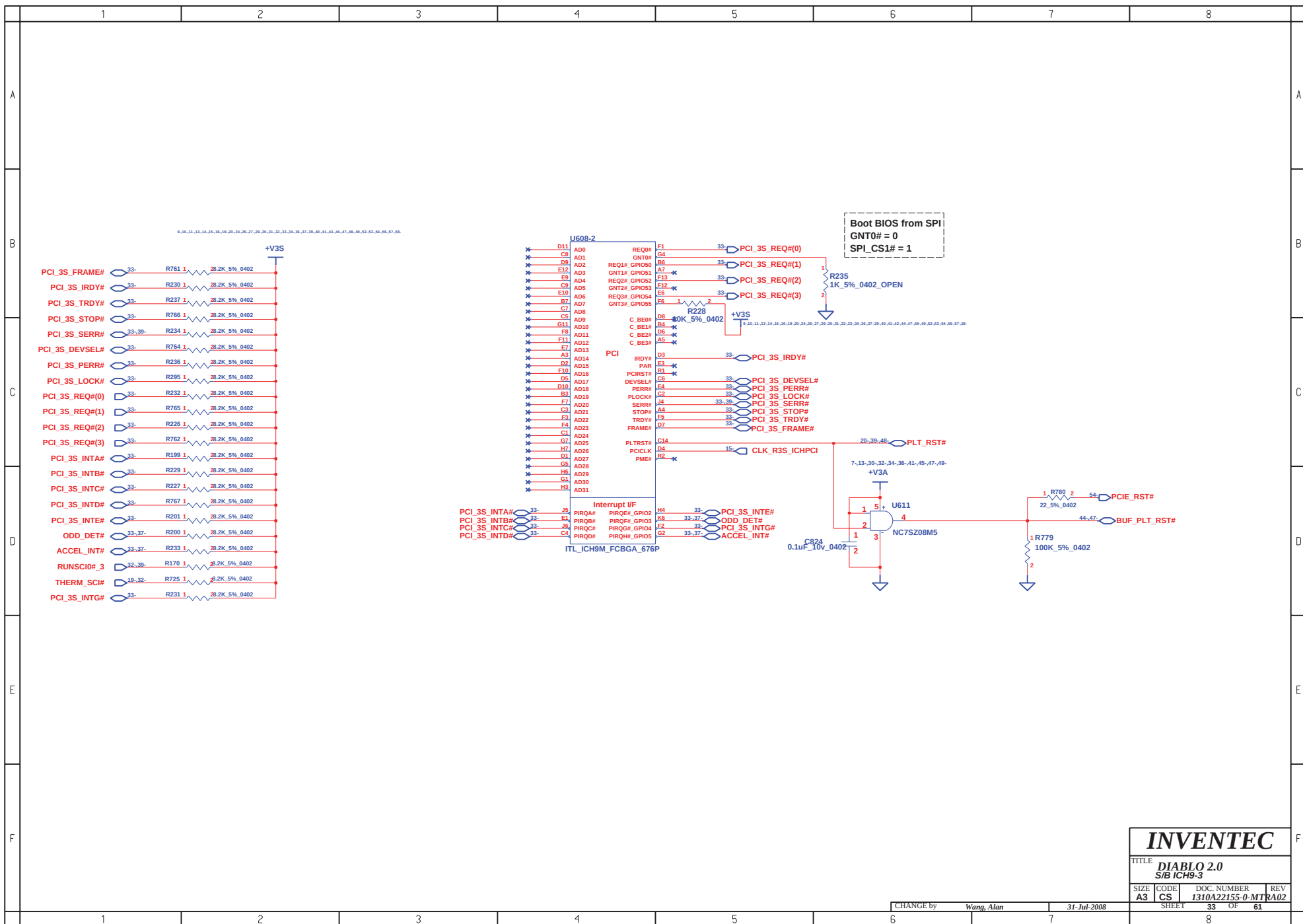
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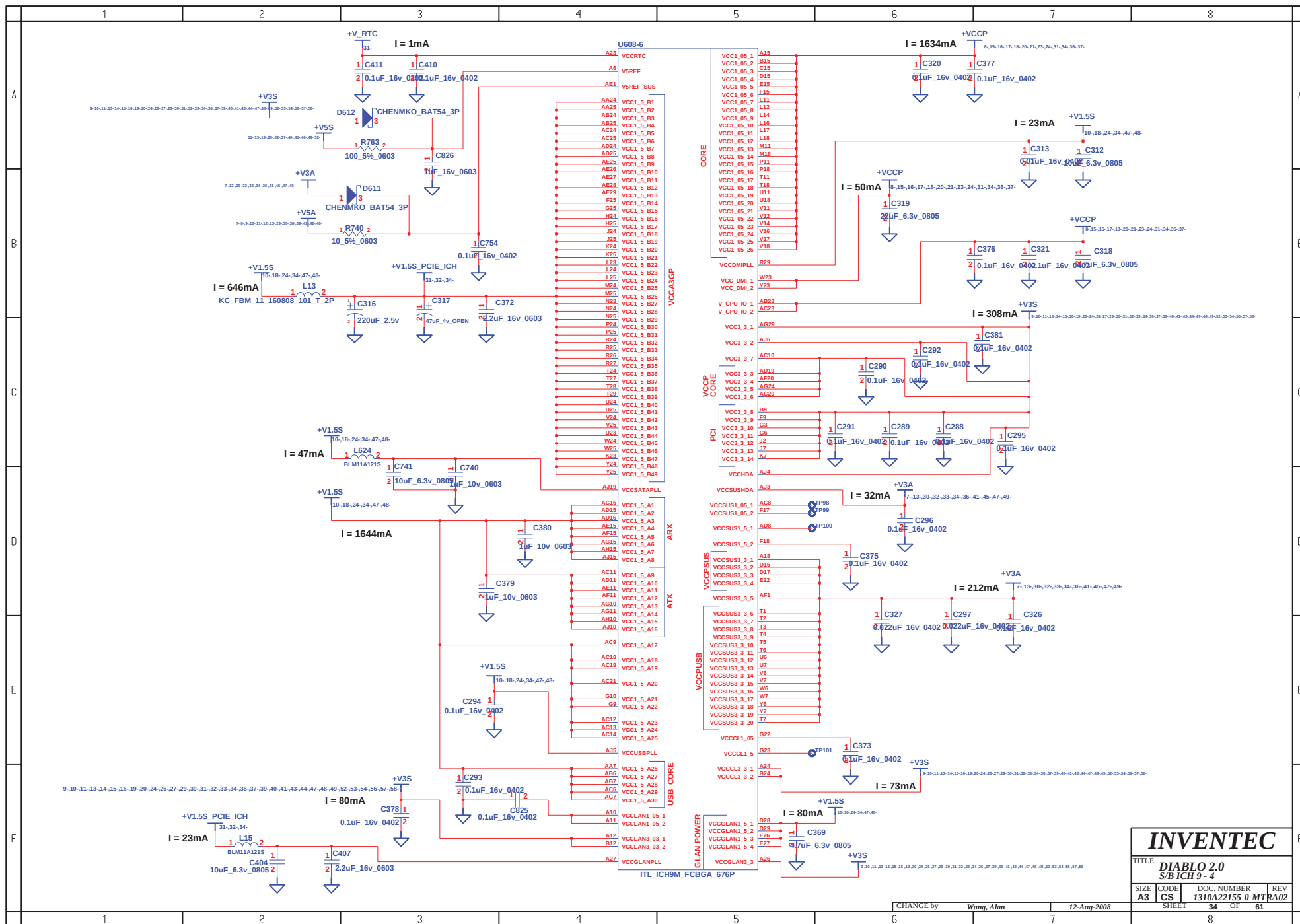
CHANGE by **Wang, Alan** 31-Jul-2008

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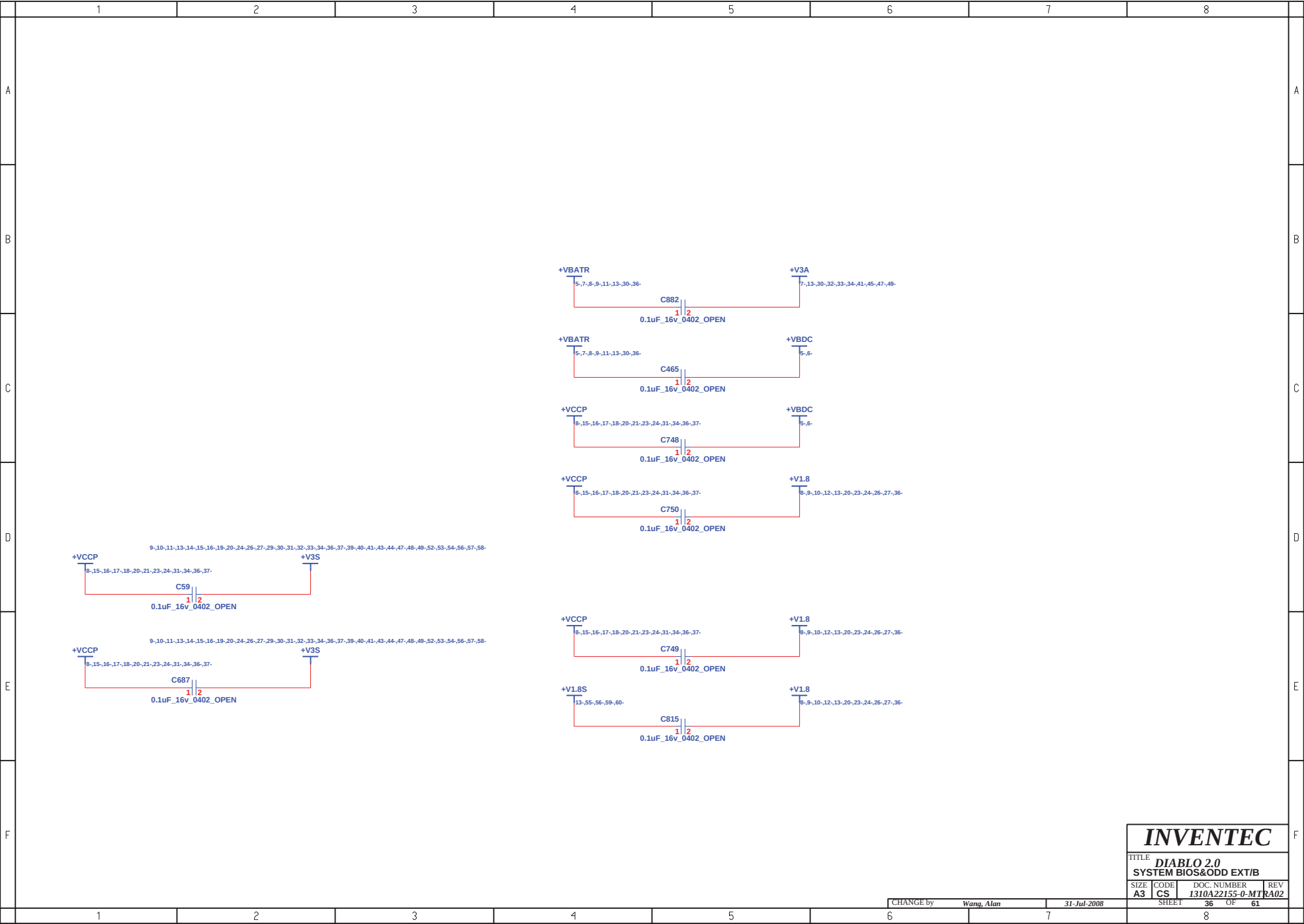


INVENTEC

TITLE
DIABLO 2.0
S/B ICH 9 - 4

SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MT1A02	REV A02
SHEET		34	OF 61

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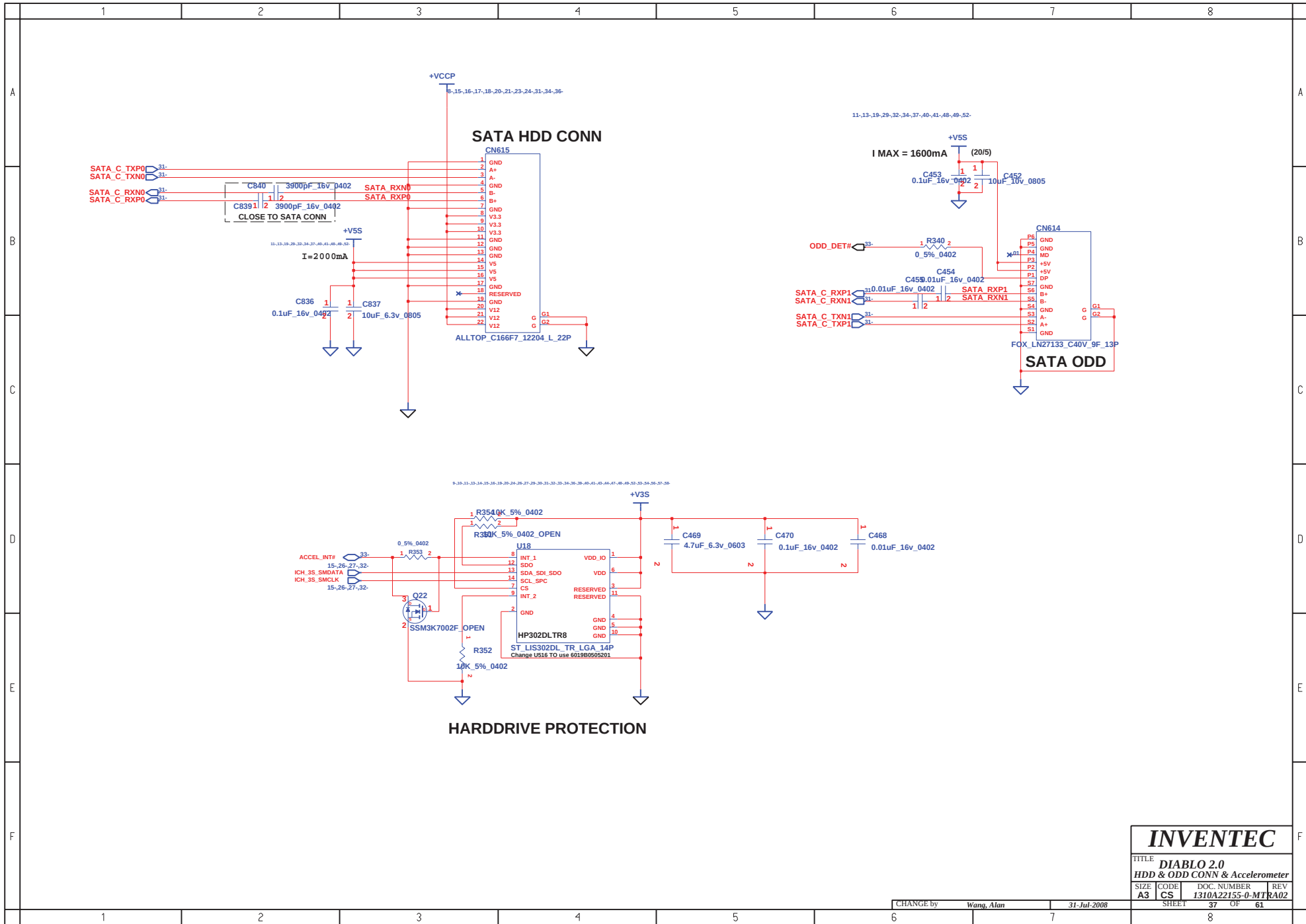
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SYSTEM BIOS&ODD EXT/B

SIZE	CODE	DOC. NUMBER	REV
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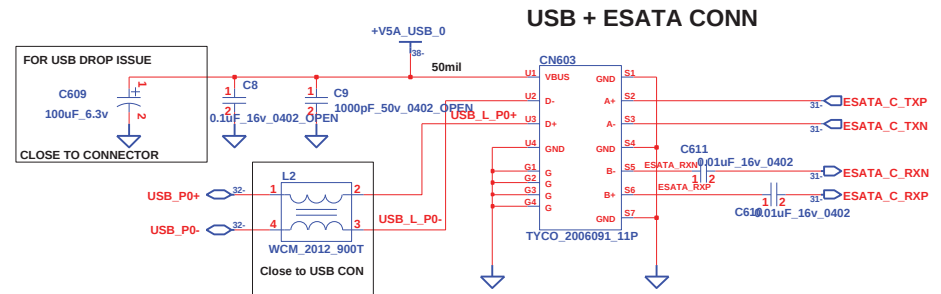
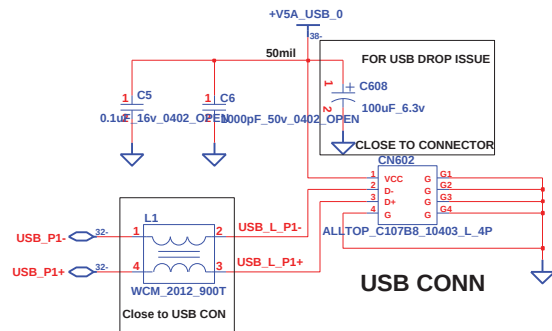
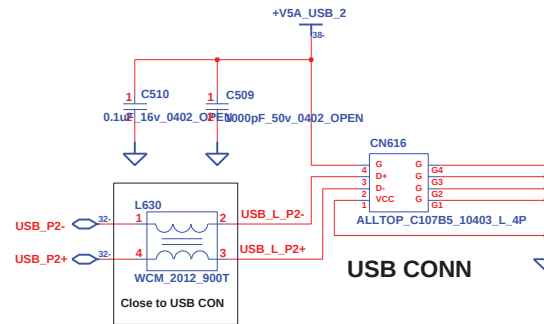
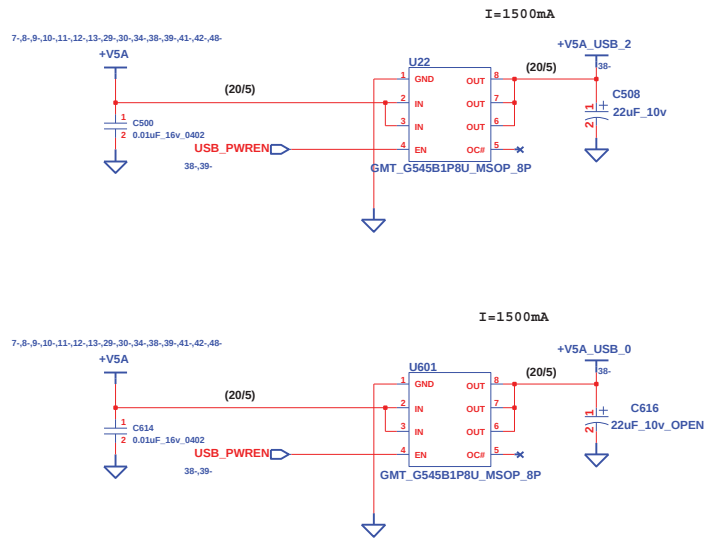
CHANGE by **Wang, Alan** 31-Jul-2008

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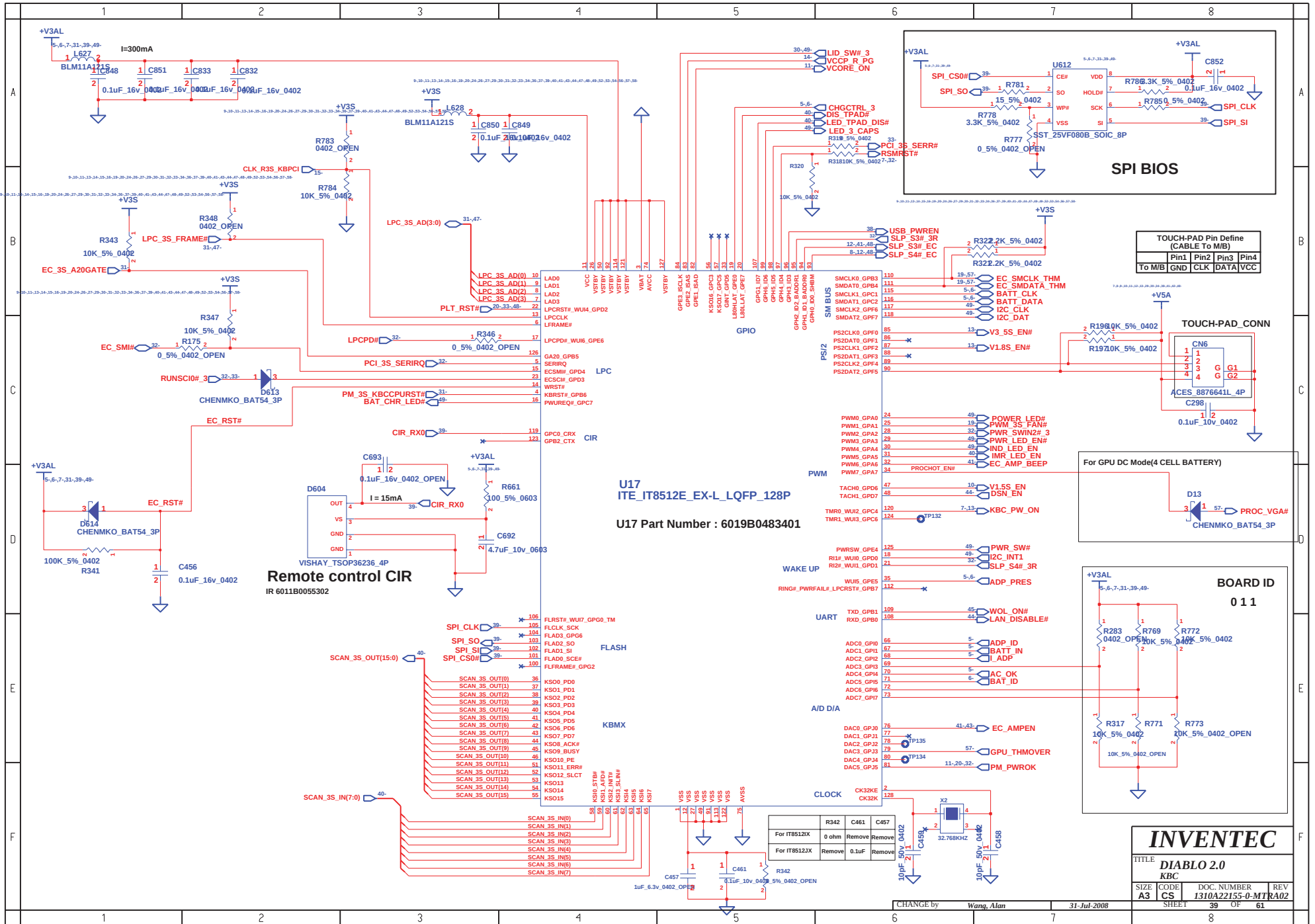


INVENTEC

TITLE DIABLO 2.0			
HDD & ODD CONN & Accelerometer			
SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MT	REV A02
CHANGE by Wang, Alan		31-Jul-2008	
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INVENTEC			
TITLE			
DIABLO 2.0			
USB CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MT	A02
CHANGE by		Wang, Alan	31-Jul-2008
SHEET		38	OF 61



1 2 3 4 5 6 7 8

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A

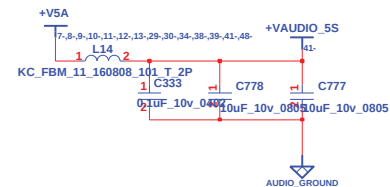
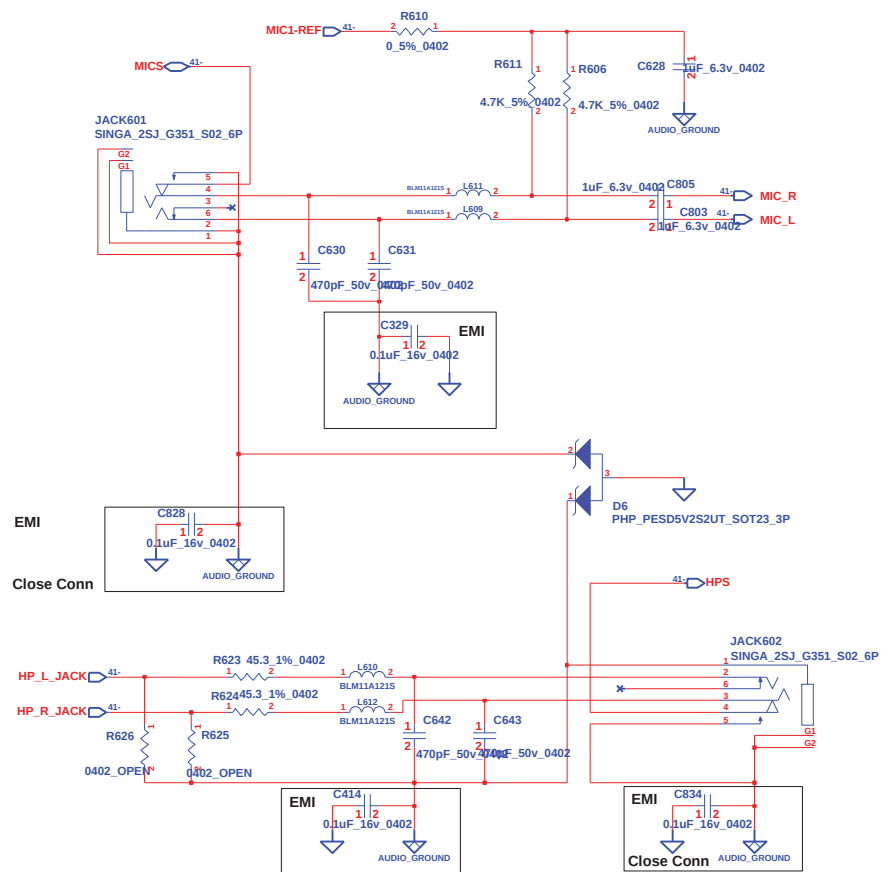
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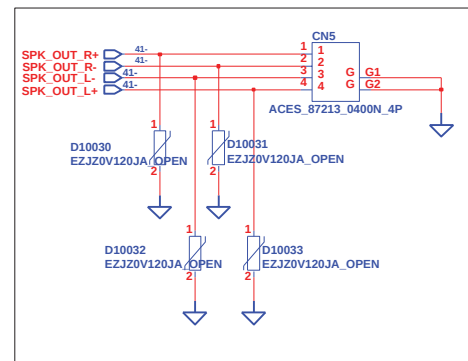
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INTERNAL SPEAKER



INVENTEC			
TITLE DIABLO 2.0 AMP & MIC & HP			
SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MTR	REV A02
CHANGE by Wang, Alan		31-Jul-2008	SHEET 42 OF 61

1 2 3 4 5 6 7 8

1 2 3 4 5 6 7 8

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A

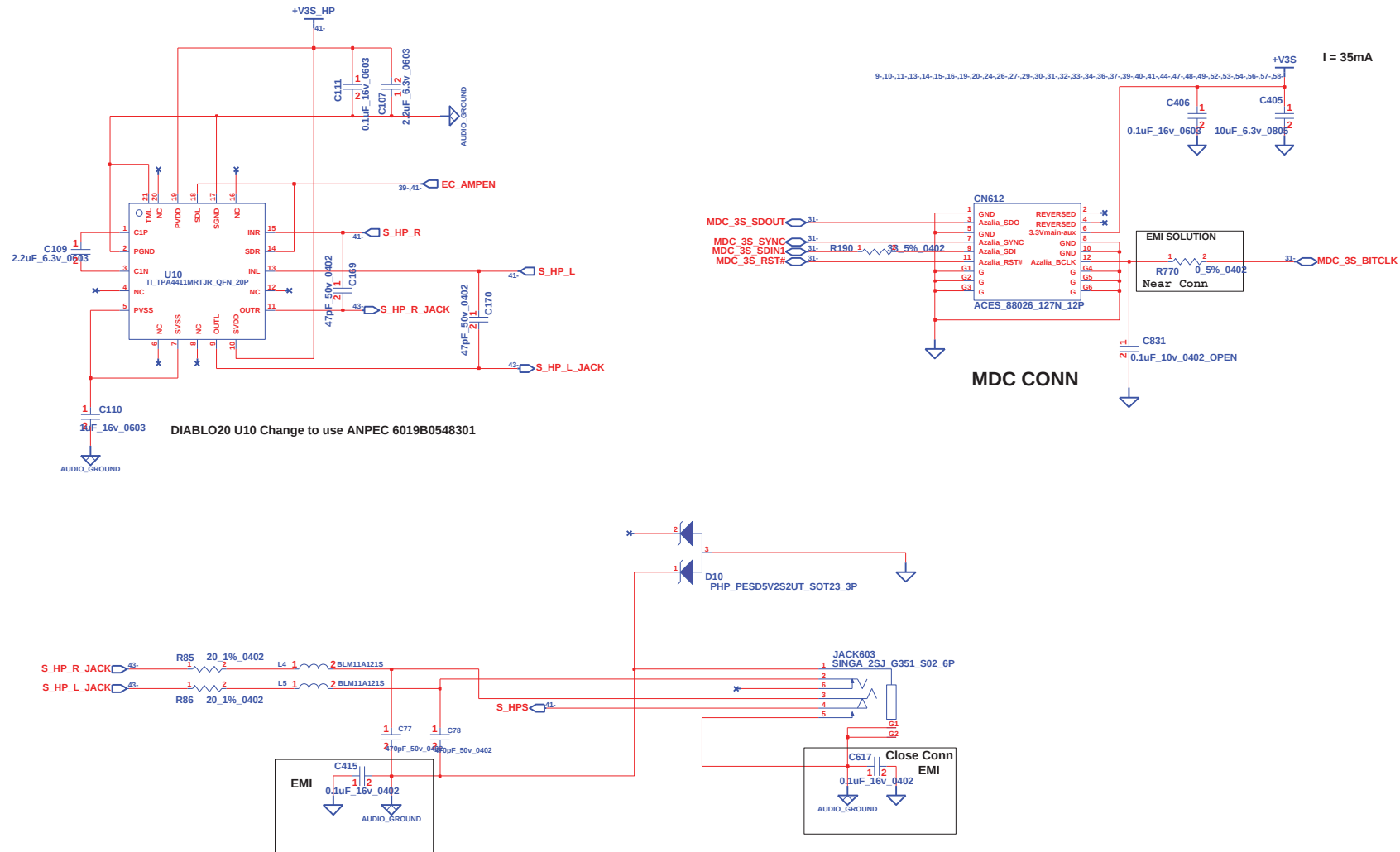
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INVENTEC

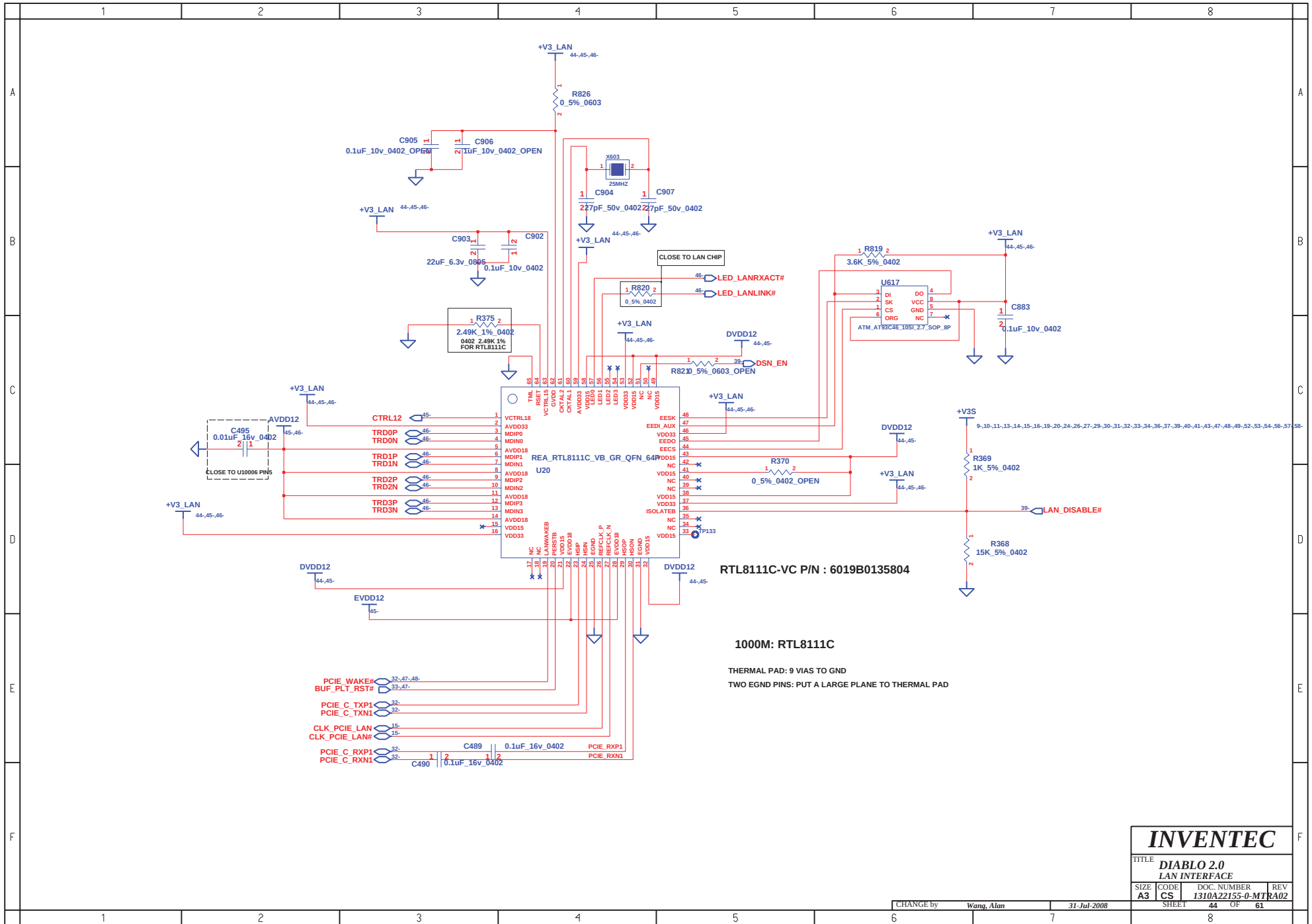
TITLE DIABLO 2.0

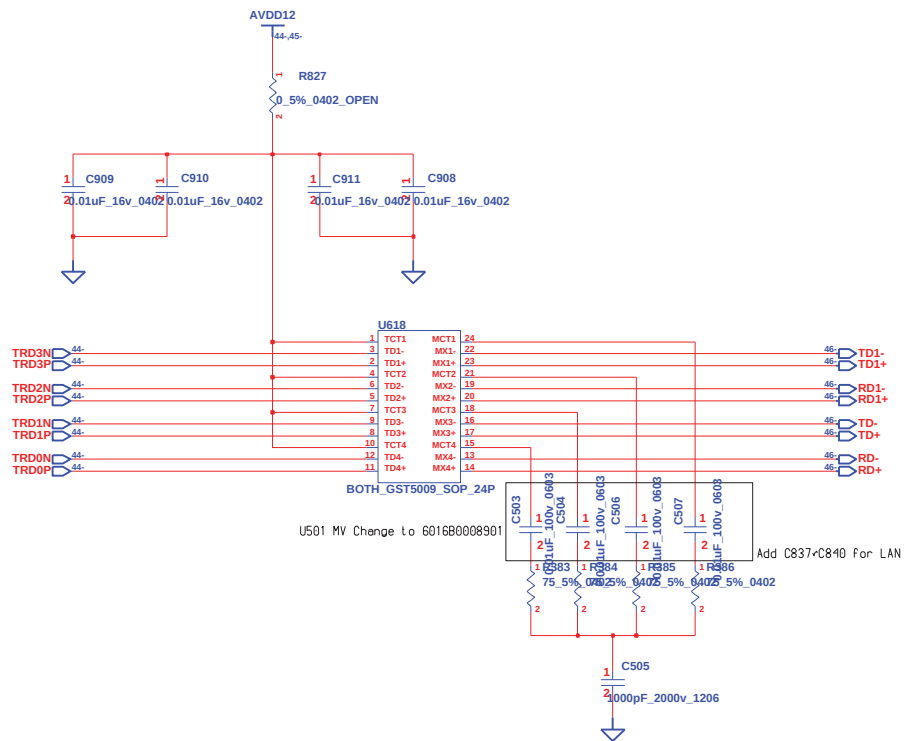
SIZE CODE DOC. NUMBER REV
A3 CS 1310A22155-0-MTR A02

CHANGE by Wang, Alan 31-Jul-2008

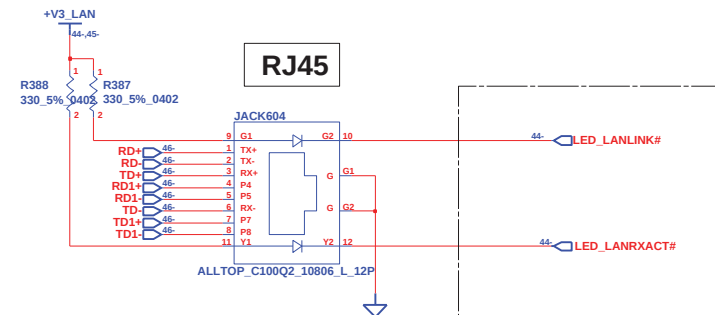
SHEET 43 OF 61

1 2 3 4 5 6 7 8

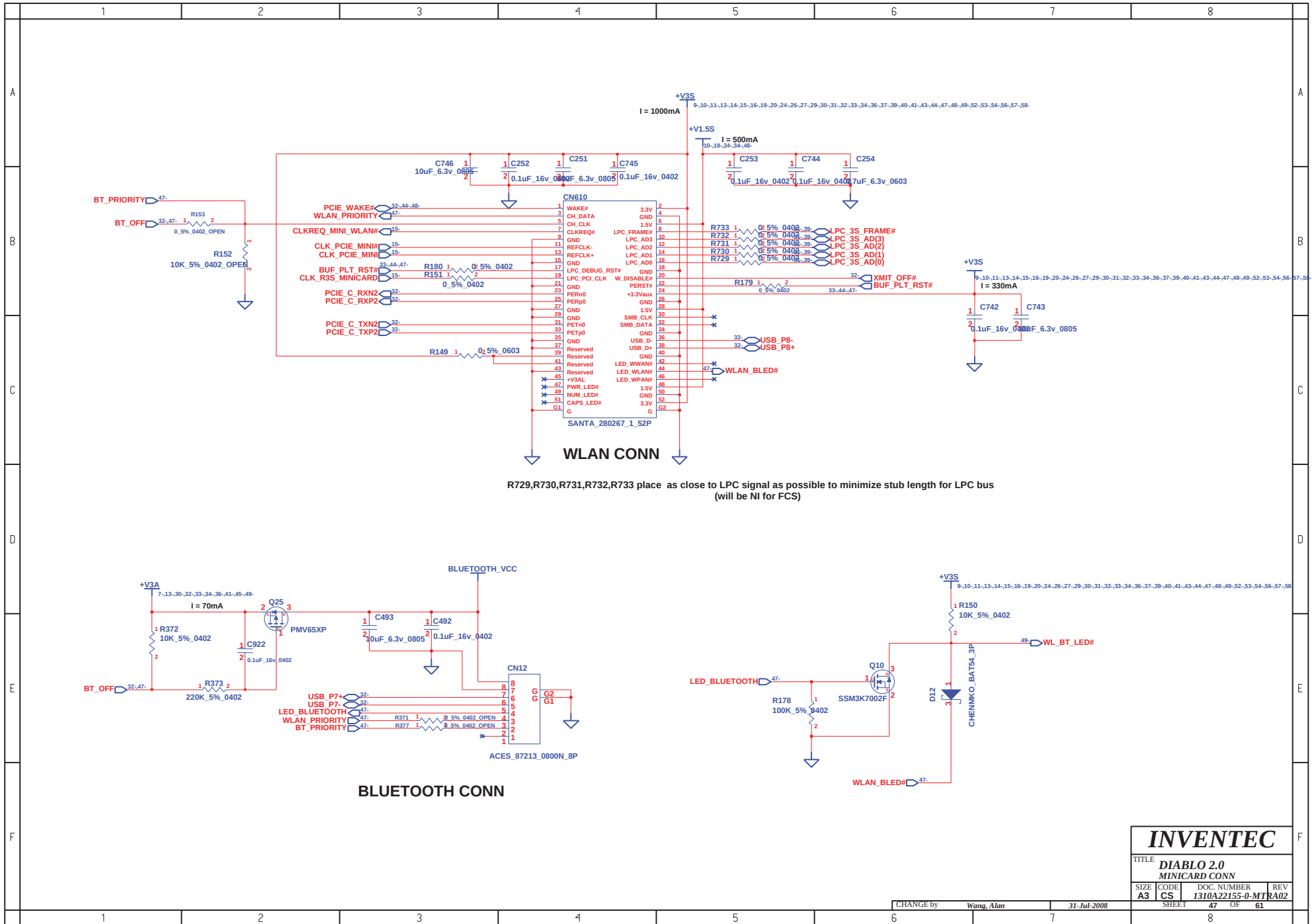




LAYOUT NOTE : Place termination resistors and caps as close to LAN controller as possible

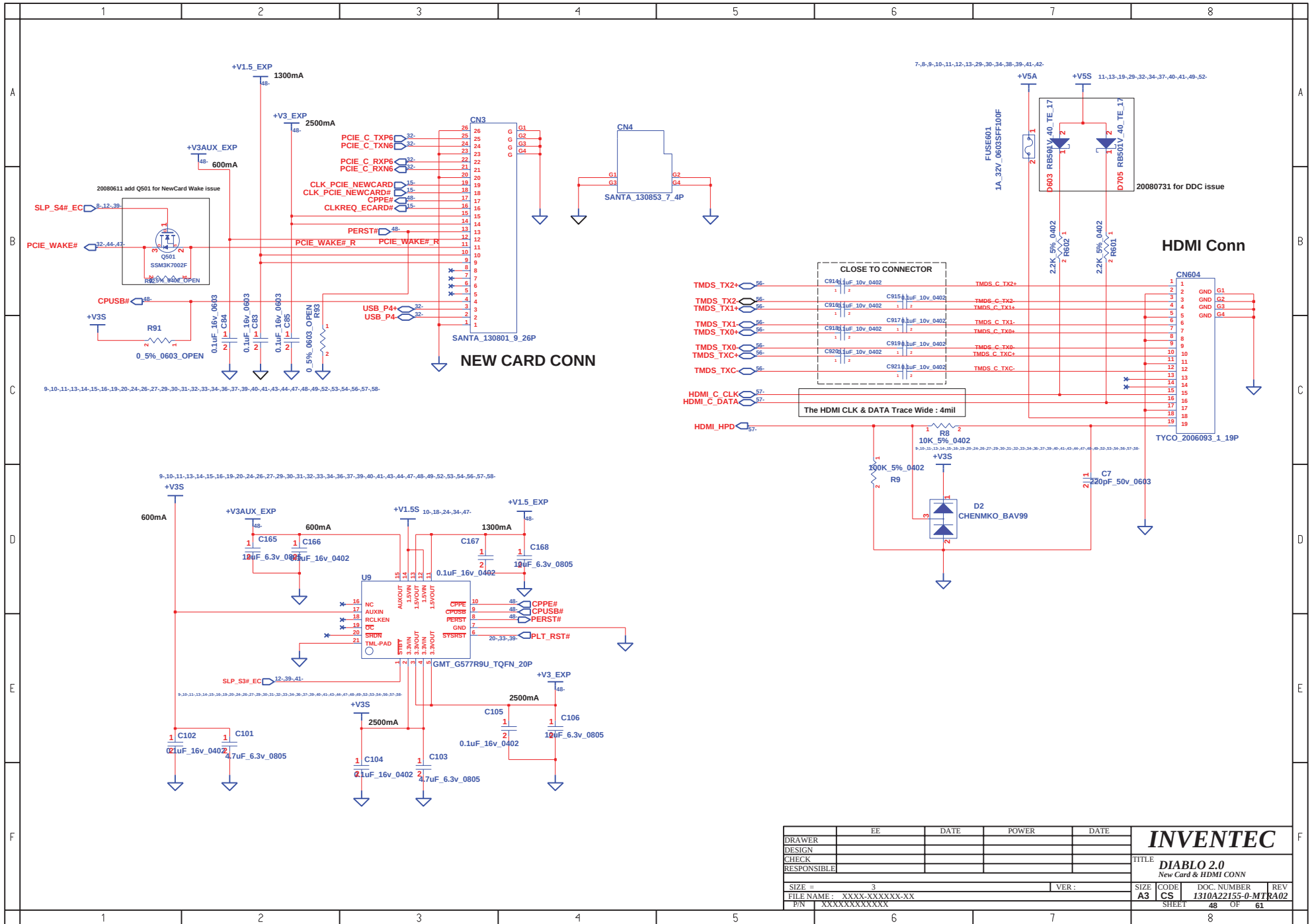


INVENTEC			
TITLE			
DIABLO 2.0 LAN RJ45 CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MTR	A02
CHANGE by		31-Jul-2008	
SHEET		46	OF 61



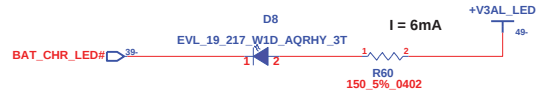
INVENTEC

TITLE DIABLO 2.0 MINICARD CONN			
SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MTR	REV A02
CHANGE by Wang, Alan		31-Jul-2008	
SHEET 47		OF 61	

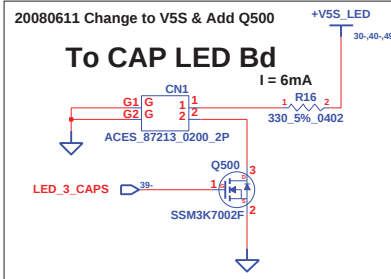
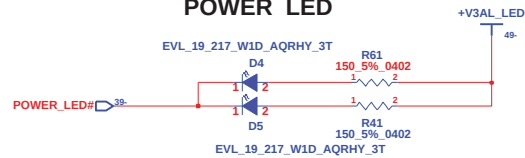


LED P/N: 6011B0072901

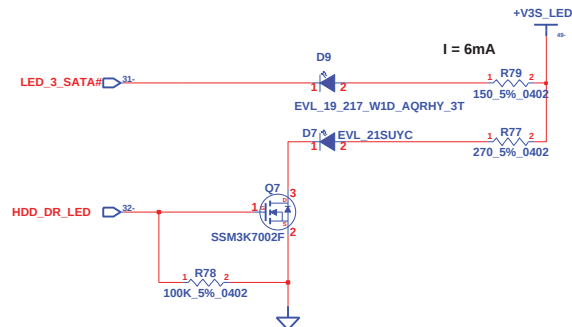
BATTERY-CHARGE LED



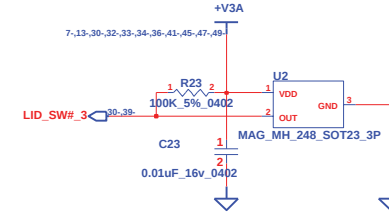
POWER LED



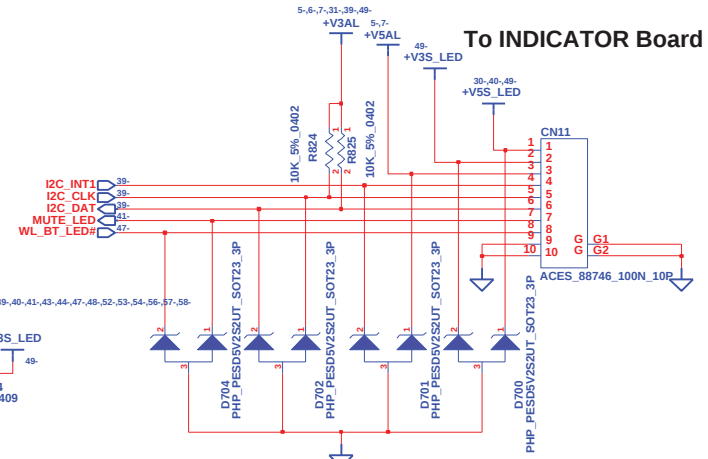
SATA LED & HDD-HALTED LED



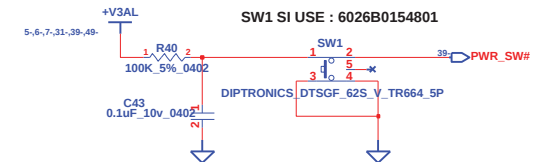
LID SWITCH



To INDICATOR Board

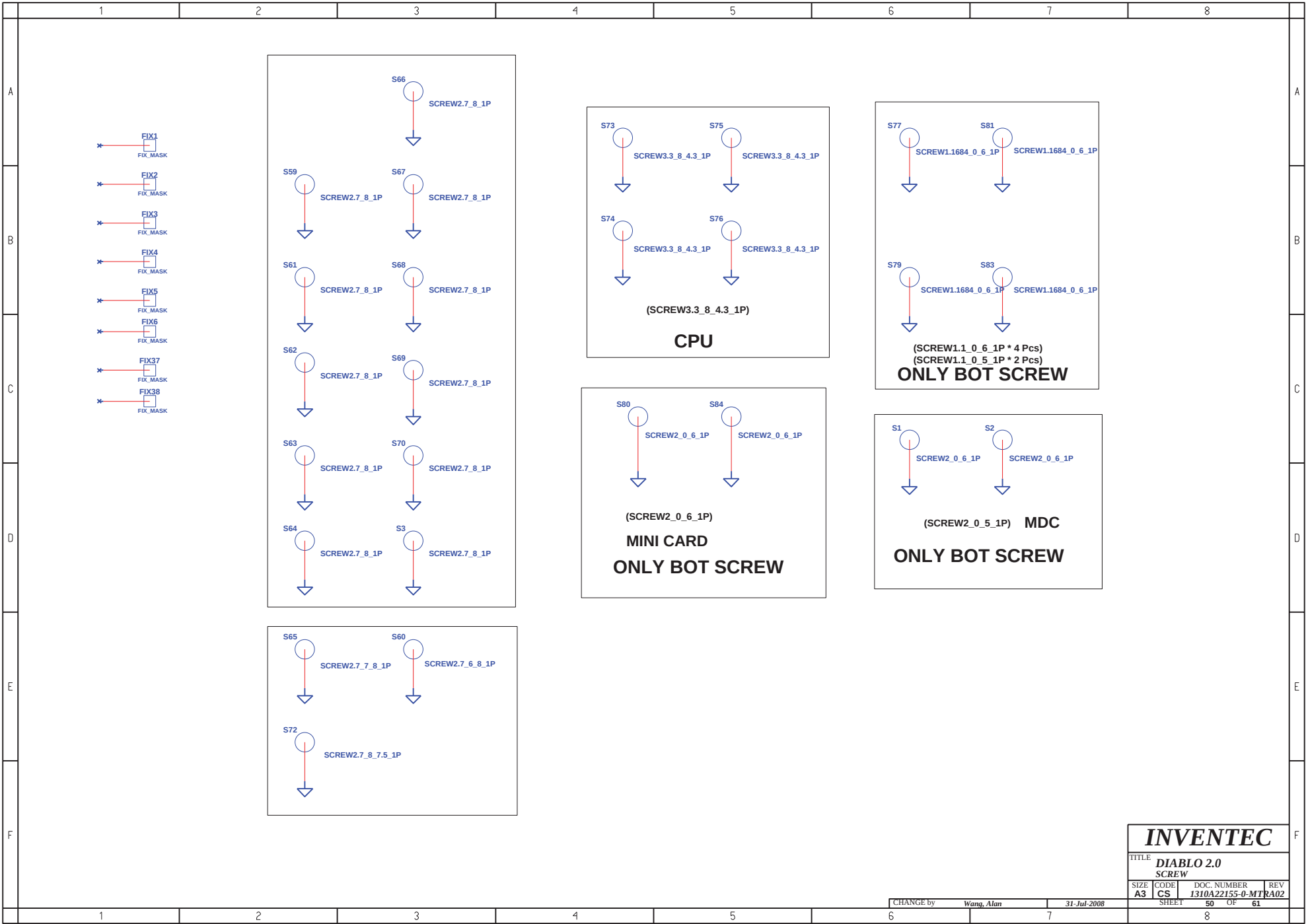


20080731 change to ESD Diode

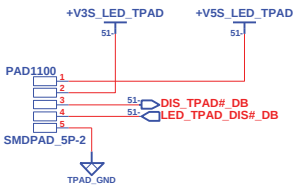
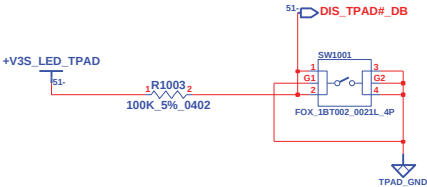


POWER BUTTON

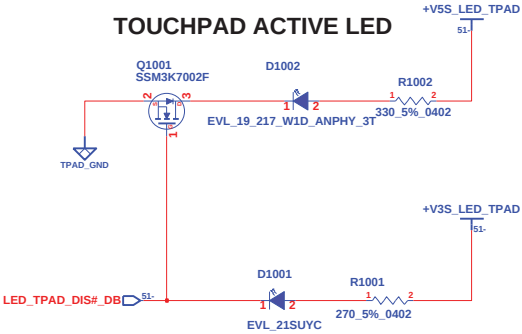
INVENTEC			
TITLE			
DIABLO 2.0			
BUTTON & LED			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MTR	RA02
CHANGE by		Wang, Alan	31-Jul-2008
SHEET		49	OF 61



TOUCHPAD-DISABLE BUTTOM_DB

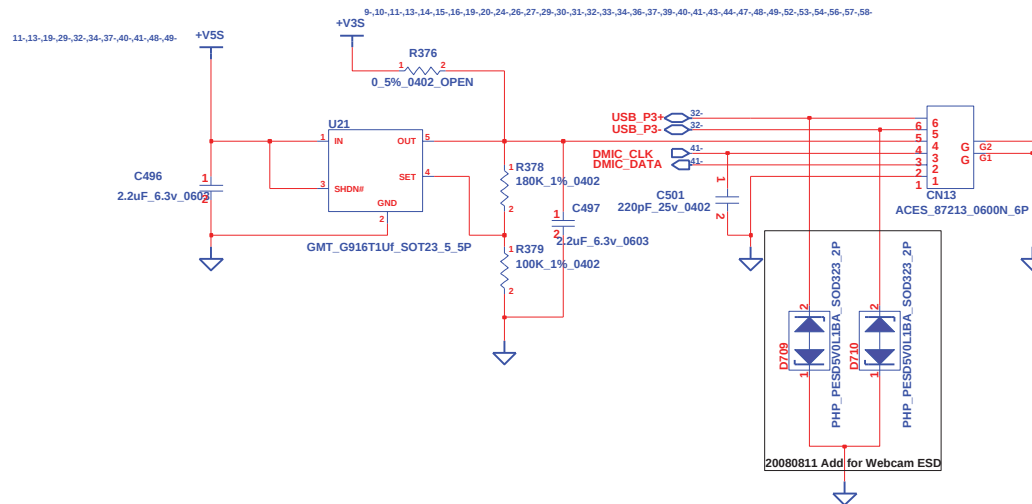
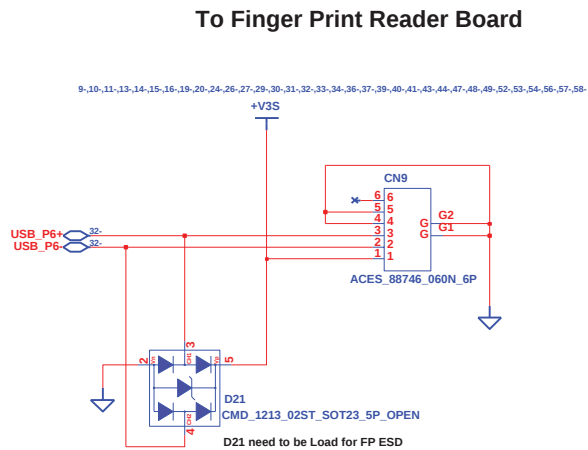


TOUCHPAD ACTIVE LED



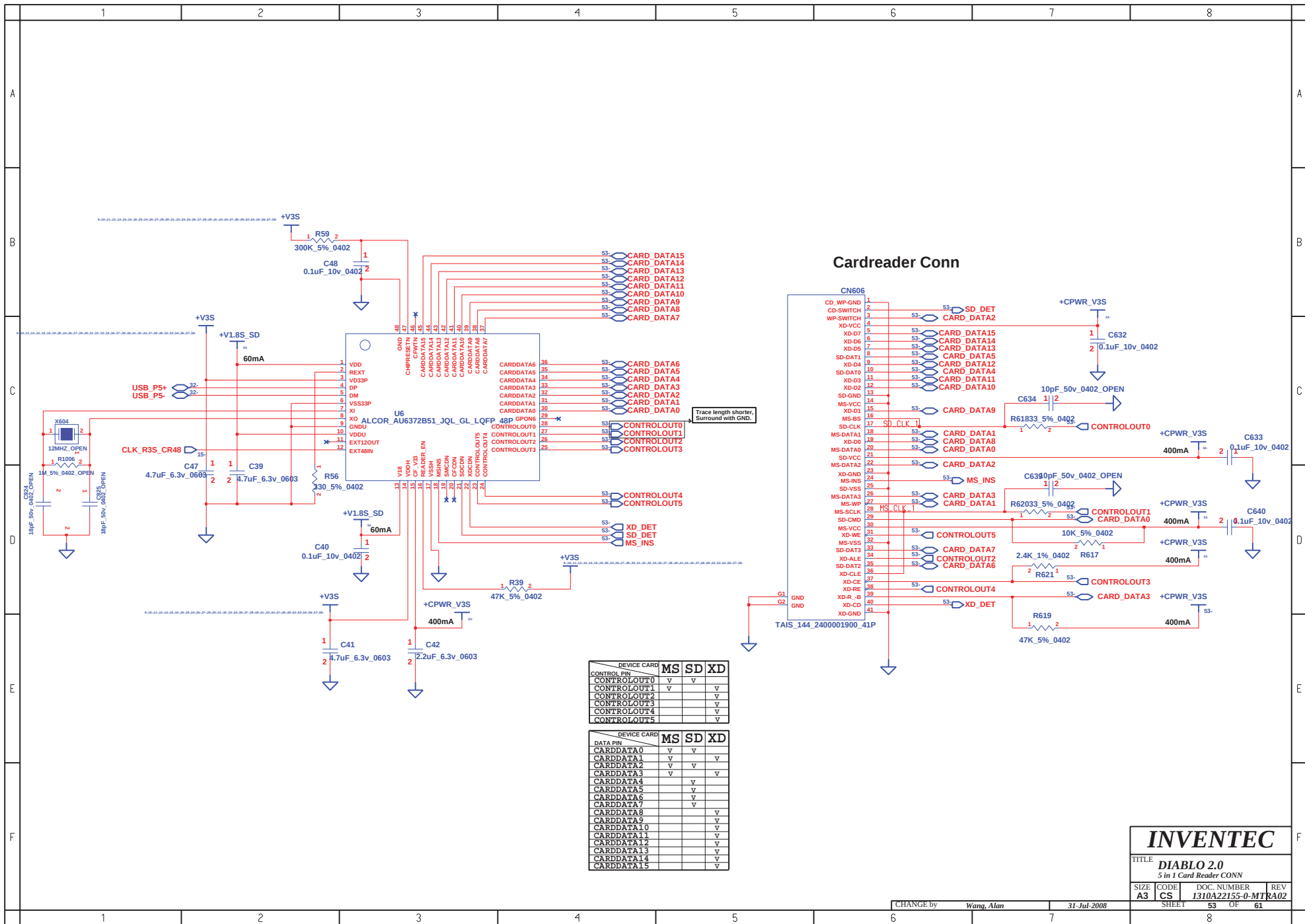
TouchPad Disable DB

INVENTEC			
TITLE DIABLO 2.0 FINGER PRINTER CONN.			
SIZE A3	CODE CS	DOC. NUMBER 1310A22192-0-MTR	REV A02
CHANGE by Wang, Alan		31-Jul-2008	
SHEET 51		OF 61	



Webcam CONN & DIGITAL MIC

INVENTEC			
TITLE			
DIABLO 2.0			
WEB CAM & FP CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MTR	A02
CHANGE by		11-Aug-2008	
SHEET		52	OF 61



INVENTEC

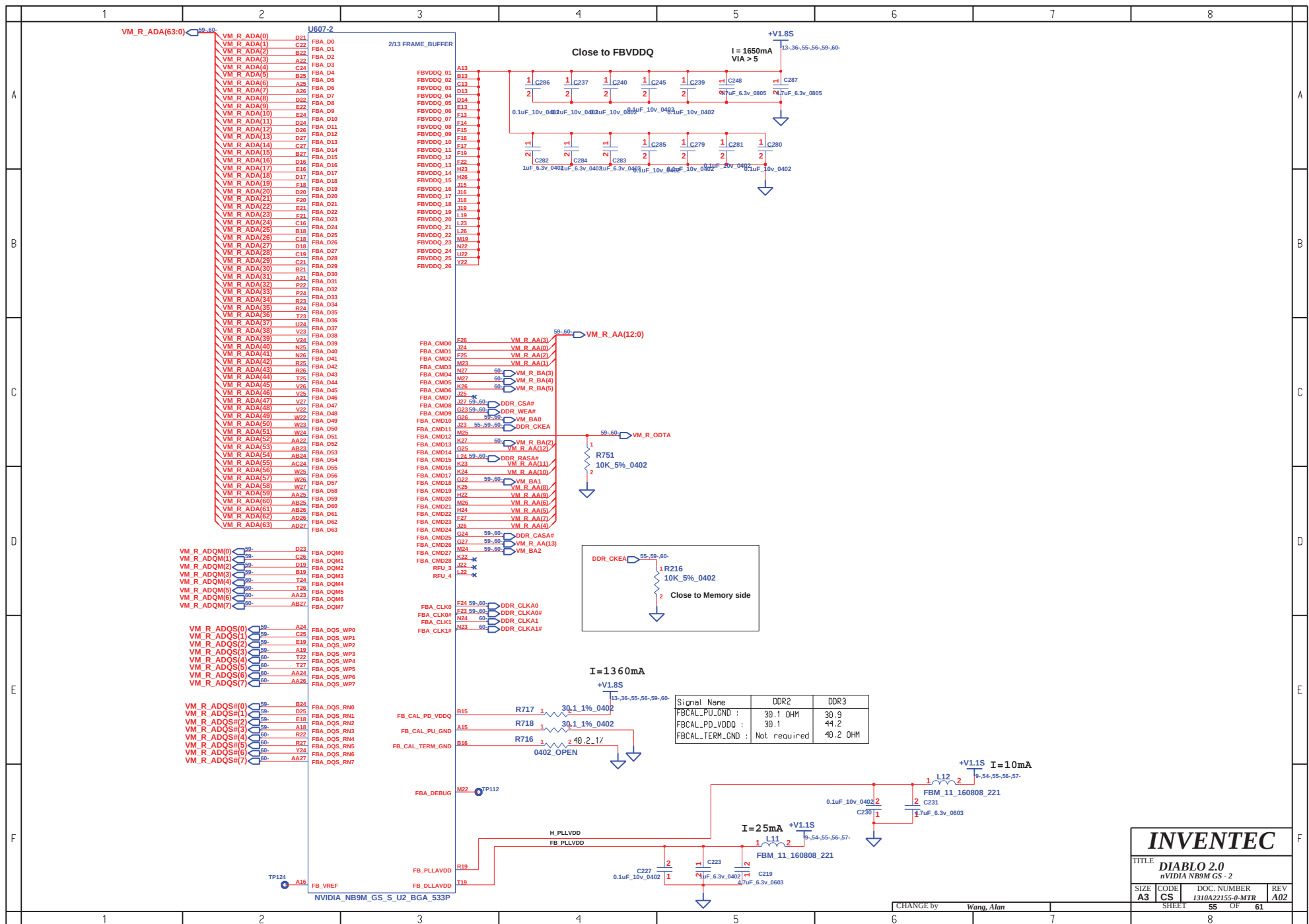
TITLE
DIABLO 2.0
5 in 1 Card Reader Conn

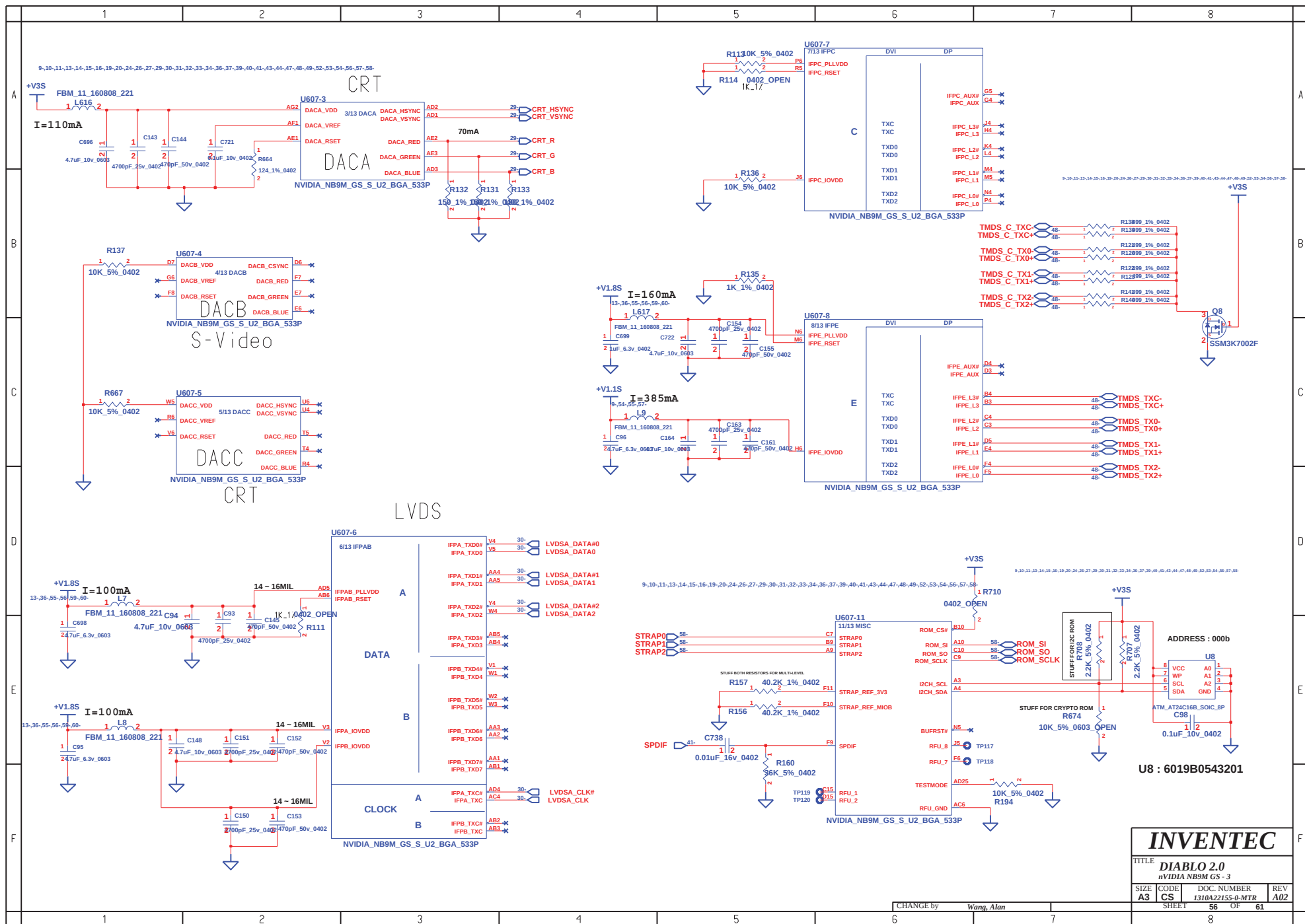
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MTR	A02

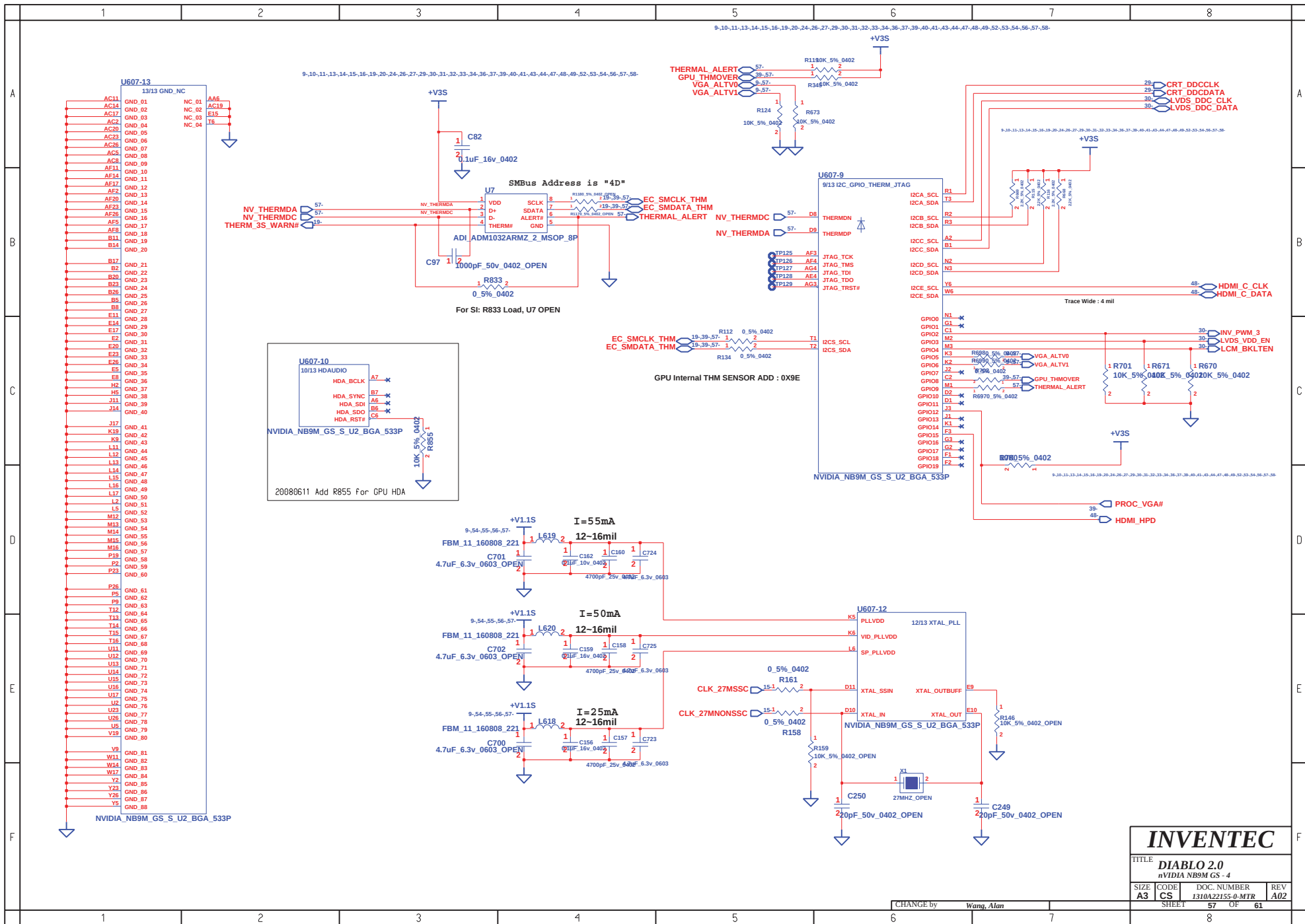
CHANGE by Wang, Alan 31-Jul-2008

SHEET 53 OF 61

For dynamic load : V_{GA}VCC, FBVDDQ & PEX_VDD ,two to three vias per amperer are recommended







INVENTEC

TITLE			
DIABLO 2.0			
nVIDIA NB9M GS - 4			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MTR	A02
SHEET		OF	61

CHANGE by Wang, Alan

8

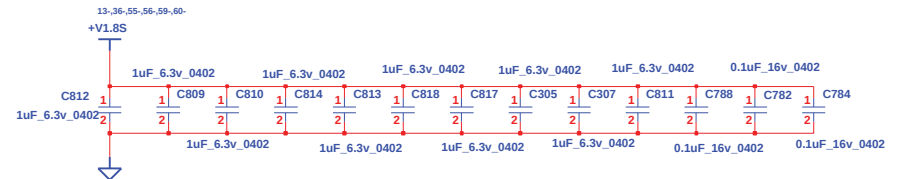
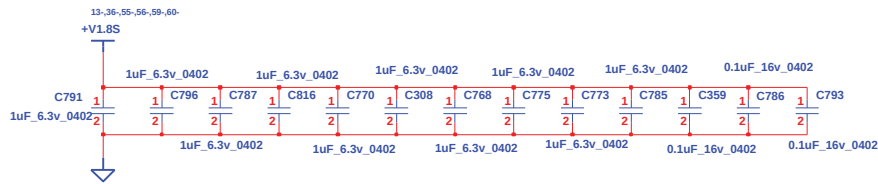
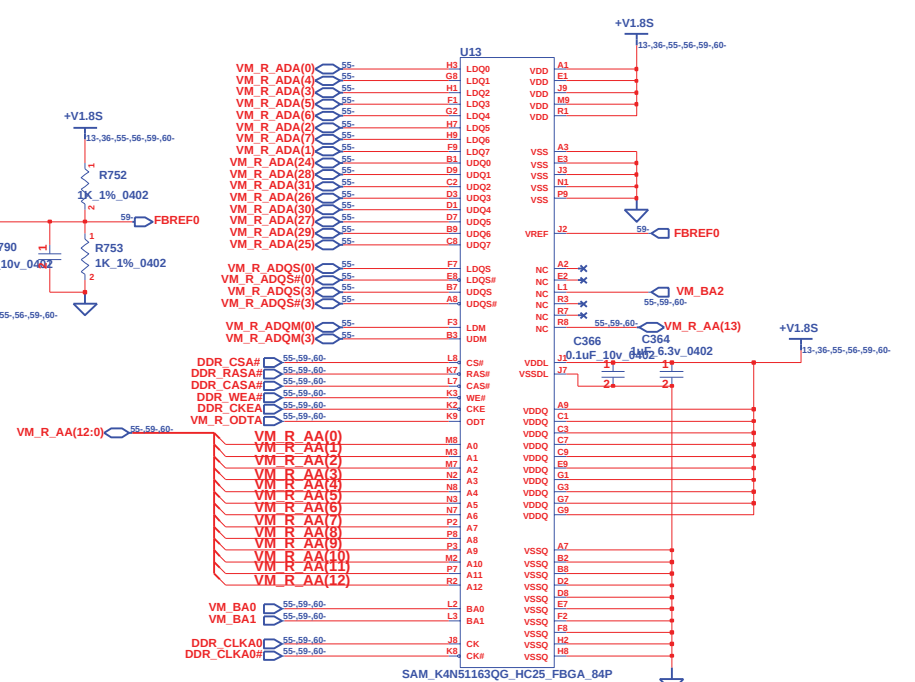
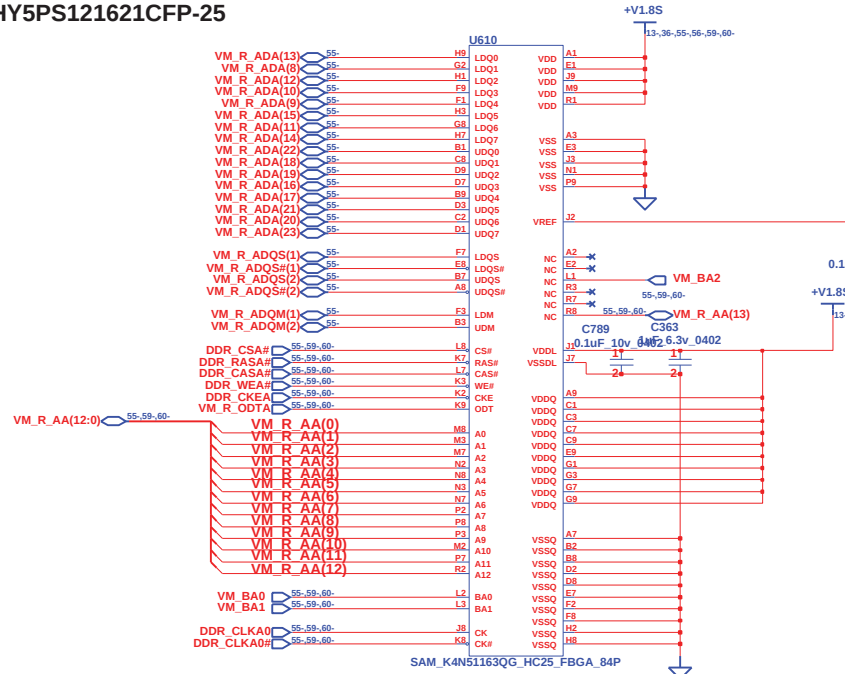
	1	2	3	4	5	6	7	8						
A	GB1 Family GPU Strap Options				GB1 Family GPU Strap Options (DIABLO 2.0) with Samsung GDDR2 64M X 16				A					
	Physical Strapping Pin	Power Rail	Logical Strapping Bit 3	Logical Strapping Pin 2	Logical Strapping Pin 1	Logical Strapping Pin 0	Physical Strapping Pin	Power Rail	Logical Strapping Bit 3	Logical Strapping Pin 2	Logical Strapping Pin 1	Logical Strapping Pin 0		
	ROM_SO	+V3S	XCLK_277	TVMODE [2]	TVMODE [1]	TVMODE [0]	ROM_SO	+V3S	1	1	1	0		
	ROM_SCLK	+V3S	PCI_DEVID [4]	SUB_VENDOR	SLOT_CLK_CFG	PEX_PLL_EN_TERM100	ROM_SCLK	+V3S	0	0	1	0		
	ROM_SI	+V3S	RAMCFG [3]	RAMCFG [2]	RAMCFG [1]	RAMCFG [0]	ROM_SI	+V3S	0	0	0	1		
	STRAP2	+V3S	PCI_DEVID [3]	PCI_DEVID [2]	PCI_DEVID [1]	PCI_DEVID [0]	STRAP2	+V3S	1	0	0	1		
	STRAP1	+V3S	3GIO_PADCFG [3]	3GIO_PADCFG [2]	3GIO_PADCFG [1]	3GIO_PADCFG [0]	STRAP1	+V3S	0	0	0	1		
	STRAP0	+V3S	USER [3]	USER [2]	USER [1]	USER [0]	STRAP0	+V3S	1	1	1	1		
B	Logical Strap Bit Mapping				NB9M-GS RAMCFG[3:0]				GB1 Family GPU Strap Options (DIABLO 2.0) with Hynix GDDR2 64M X 16				B	
	Resistor Values	Pull - up to VDD	Pull - down to GND	32M X 16 GDDR2	RAMCFG [3]	RAMCFG [2]	RAMCFG [1]	RAMCFG [0]	Physical Strapping Pin	Power Rail	Logical Strapping Bit 3	Logical Strapping Pin 2	Logical Strapping Pin 1	Logical Strapping Pin 0
	5K ohm	1000	0000	Samsung	0	1	0	1	ROM_SO	+V3S	1	1	1	0
	10K ohm	1001	0001	Qimonda	0	1	1	0	ROM_SCLK	+V3S	0	0	1	0
	15K ohm	1010	0010	Hynix	0	1	1	1	ROM_SI	+V3S	0	0	0	0
	20K ohm	1011	0011	64M X 16 GDDR2	RAMCFG [3]	RAMCFG [2]	RAMCFG [1]	RAMCFG [0]	STRAP2	+V3S	1	0	0	1
	25K ohm	1100	0100	Samsung	0	0	0	1	STRAP1	+V3S	0	0	0	1
	30K ohm	1101	0101	Qimonda	0	0	1	0	STRAP0	+V3S	1	1	1	1
	35K ohm	1110	0110	Hynix	0	0	0	0						
	45K ohm	1111	0111											
C														C
D														D
E														E
F														F
	CHANGE by Wang, Alan													
	1	2	3	4	5	6	7	8	SHEET 58 OF 61					

INVENTEC

TITLE
DIABLO 2.0
nVIDIA NB9M GS - 5

SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MTR	REV A02
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DIABLO 2.0 USE MEMORY RVL
SANSUNG K4N56163QG_HC25
HYNIX HY5PS121621CFP-25

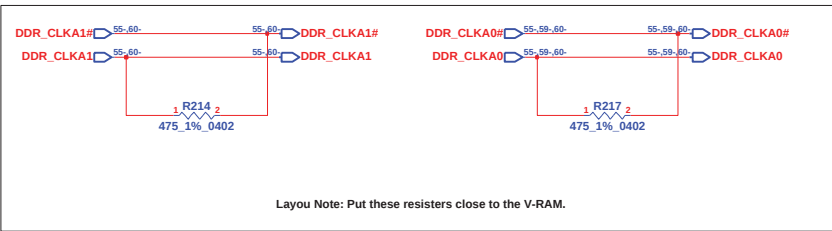
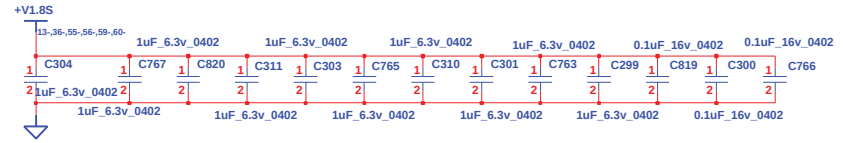
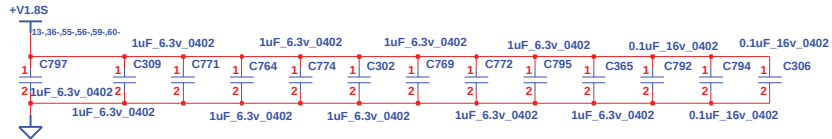
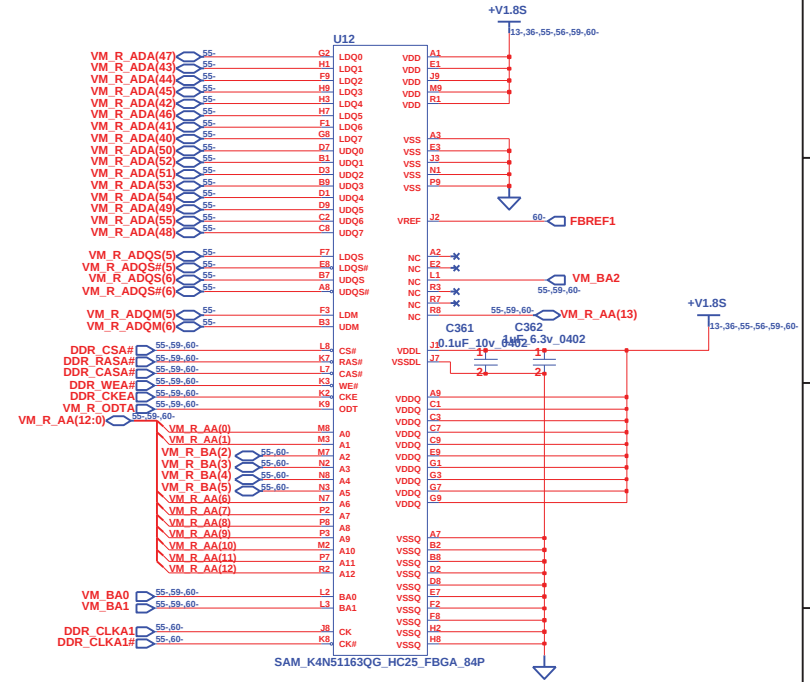
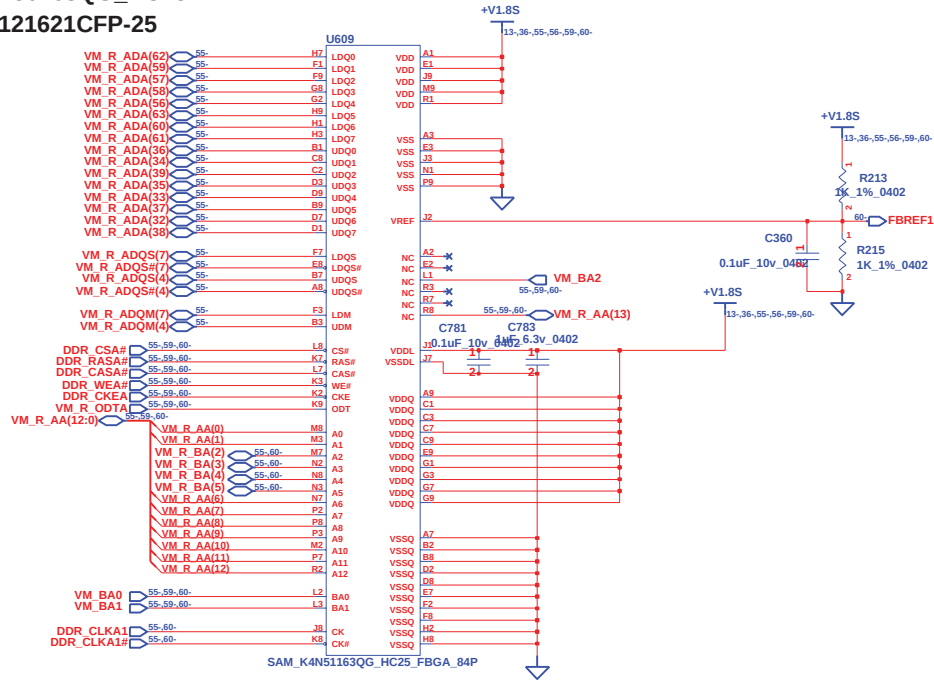


INVENTEC

TITLE DIABLO 2.0 VRAM (1/2)			
SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MTR	REV A02
SHEET 59 OF 61			

CHANGE by Wang, Alan

DIABLO 2.0 USE MEMORY RVL
SANSUNG K4N56163QG_HC25
HYNIX HY5PS121621CFP-25



Layout Note: Put these resistors close to the V-RAM.

INVENTEC

TITLE DIABLO 2.0 VRAM (2/2)			
SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MTR	REV A02
SHEET		OF	61

CHANGE by Wang, Alan