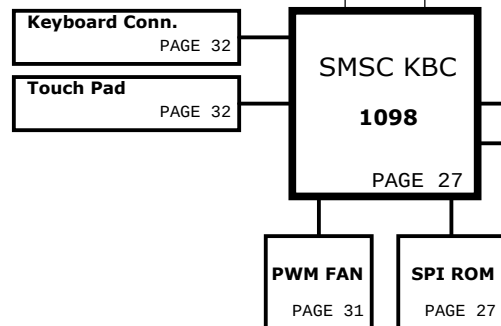


LAYER 8 : BOT



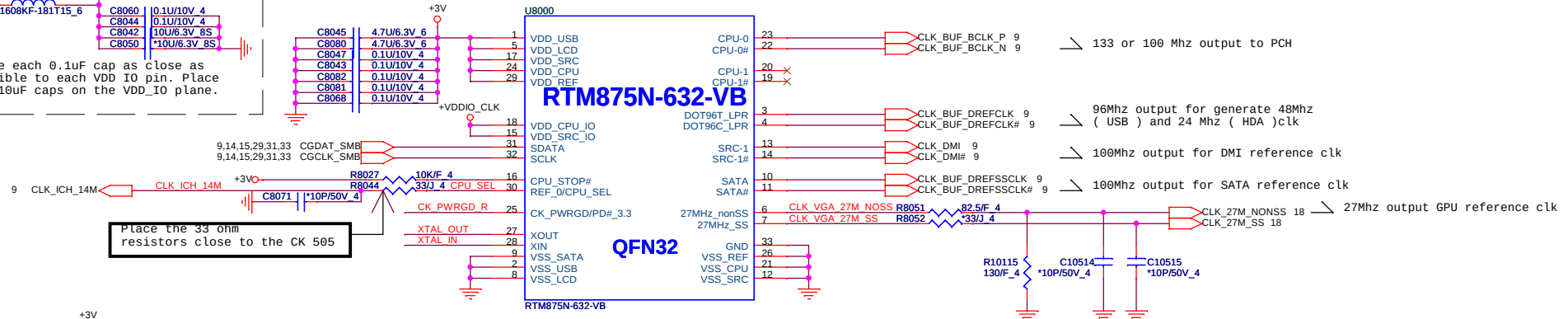
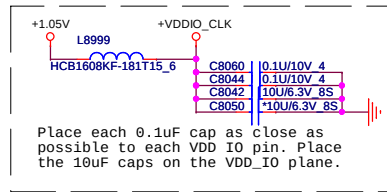
power State	+RTC_CELL	+VIN +3VPCU	+3VS5 +5VS5	+5VSUS +1.5VSUS	+5V +3V +1.8V_GFX +1.8V +1.5V +1.5V_CPU +1.1V_VTT +1.05V +1.0V_GFX +VGA_CORE +VCORE
S0	ON	ON	ON	ON	ON
S1	ON	ON	ON	ON	ON
S3	ON	ON	ON	ON	OFF
S4/S5 AC	ON	ON	ON	OFF	OFF
S4/S5 DC Only	ON	ON	OFF	OFF	OFF
AC/DC No Exist	ON	OFF	OFF	OFF	OFF

	SOURCE	BATTERY 0x16	CLK GEN 0xD2	Thermal IC 0x98(Write) / 0x99(Read)	G-SENSOR 0x3A(Write) /0x3B(Read)	WLAN	SO-DIMM DIMM0: 0xA0 DIMM1: 0xA2	SMSC 1098	GPU thermal sensor
SMBCLK SMBDATA	PCH	X	Y	Y	Y	Y	Y	X	X
SMB_CLK_ME1 SMB_DAT_ME1	PCH	X	X	X	X	X	X	Y	Y
AB1A_CLK AB1A_DATA	SMSC 1098	Y	X	X	X	X	X	X	X

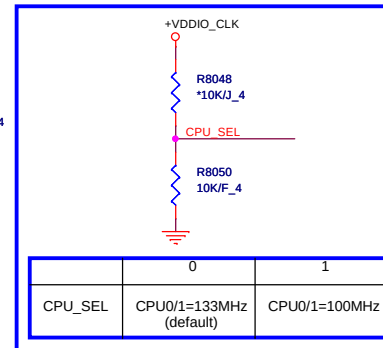
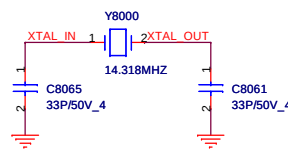
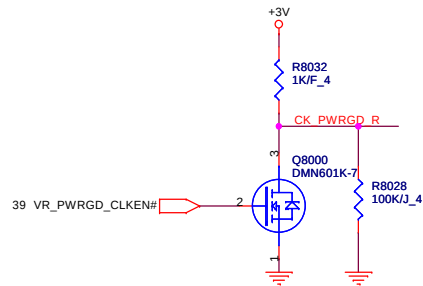


PROJECT : SX6
Quanta Computer Inc.

Size Custom	Document Number power rails	Rev 2B
Date: Tuesday, December 15, 2009 Sheet 2 of 43		

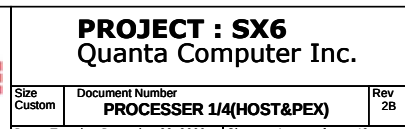


AL000875002IC OTHER(32P) RTM875N-632-VB-GRT(QFN)Realtek
AL8SP585000IC OTHER(32P)SLG8SP585VTR(QFN)Silego



PROJECT : SX6
Quanta Computer Inc.

Size Custom	Document Number CLOCK GEN (9LRS3197)	Rev 2B
Date: Tuesday, December 15, 2009	Sheet 3 of 43	

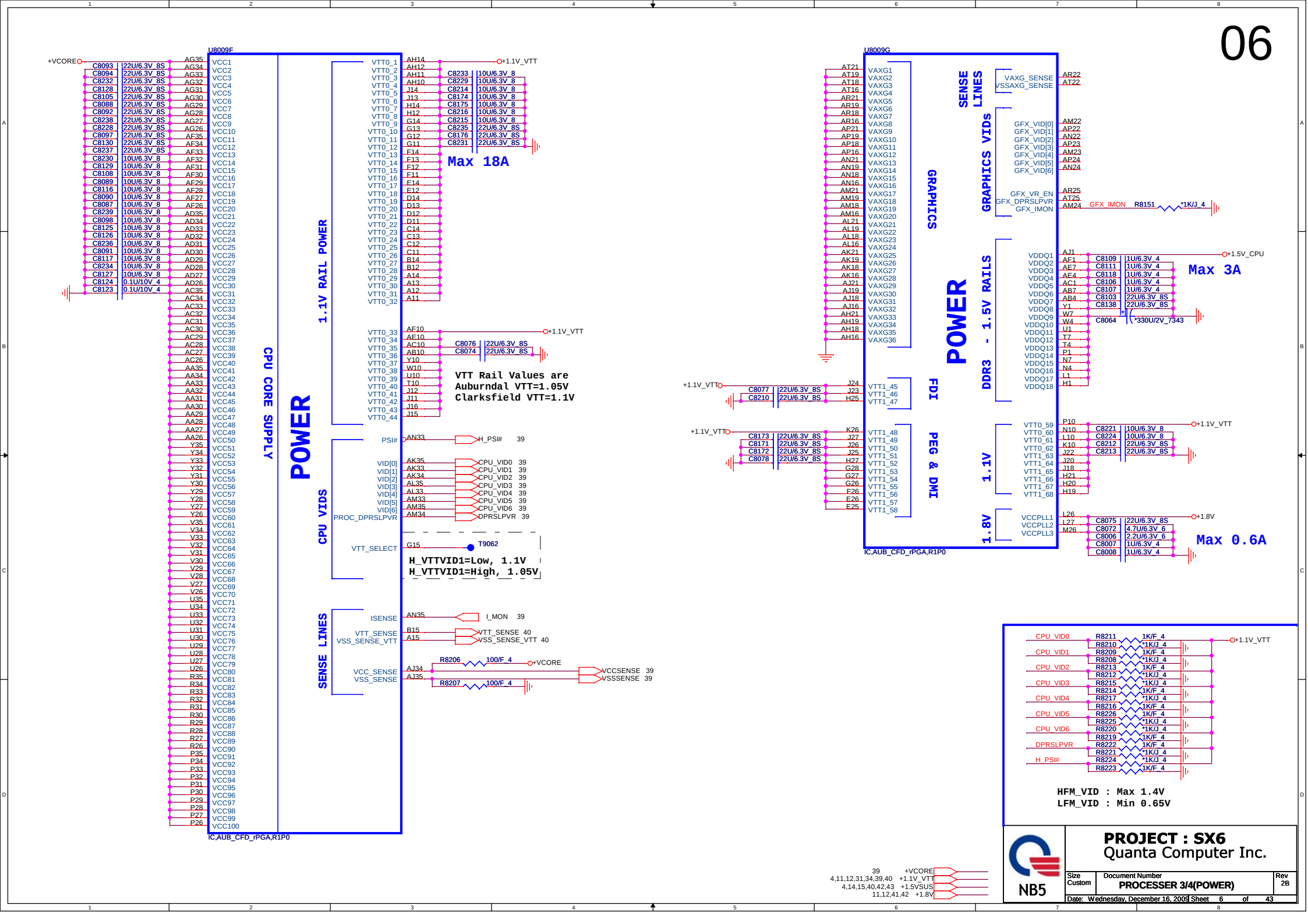


AUBURNDALE/CLARKSFIELD PROCESSOR (DDR3)



PROJECT : SX6
Quanta Computer Inc.

Size Custom	Document Number PROCESSOR 2/4(DDR)	Rev 2B
Date: Tuesday, December 15, 2009	Sheet 5 of 43	



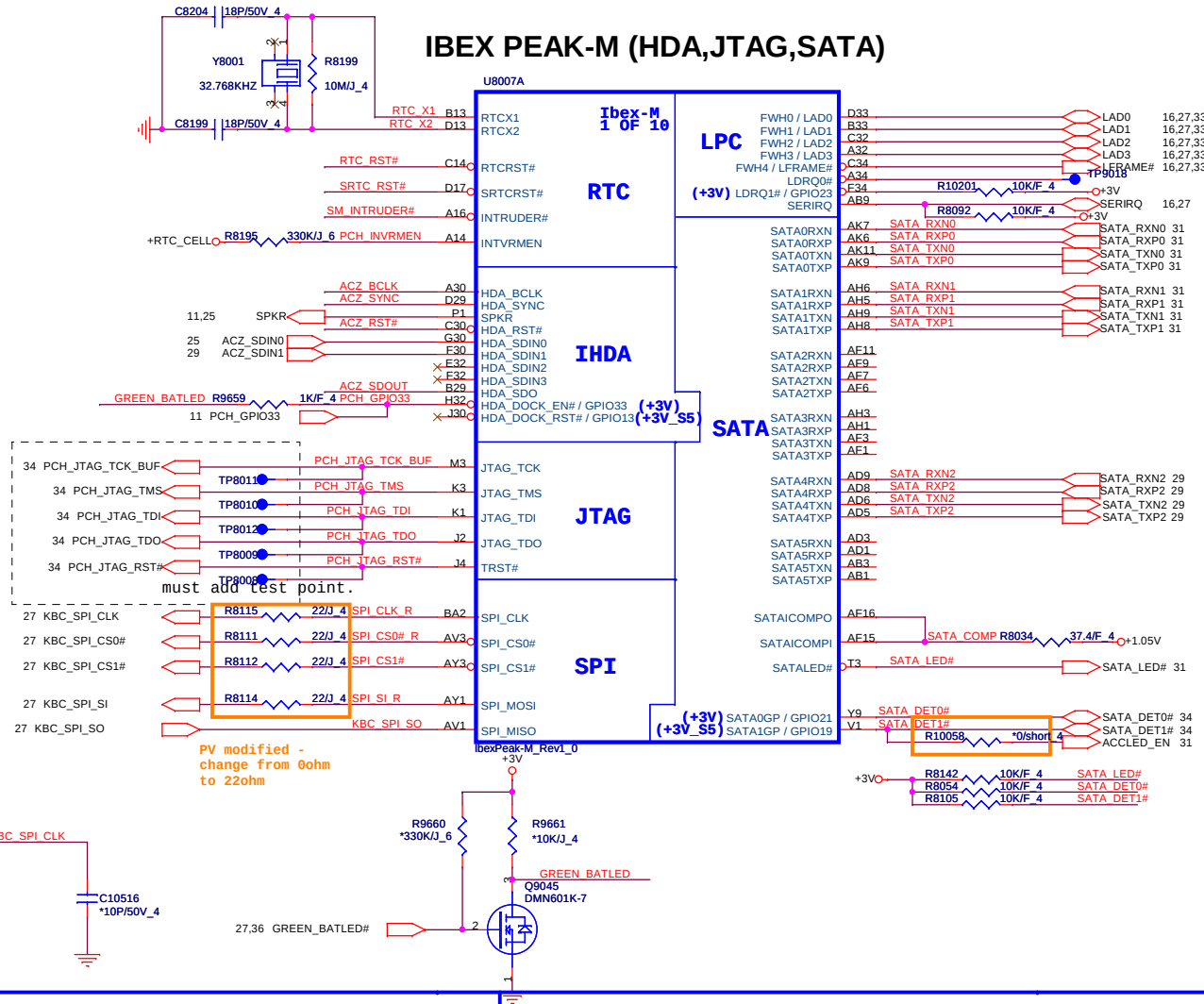


 NB5	Size Custom	Document Number PROCESSER 4/4(GND)	Rev :
	Date: Tuesday, December 15, 2009		Sheet 7 of 43

 NB5	Size Custom	Document Number PROCESSER 4/4(GND)	Rev :
	Date: Tuesday, December 15, 2009		Sheet 7 of 43

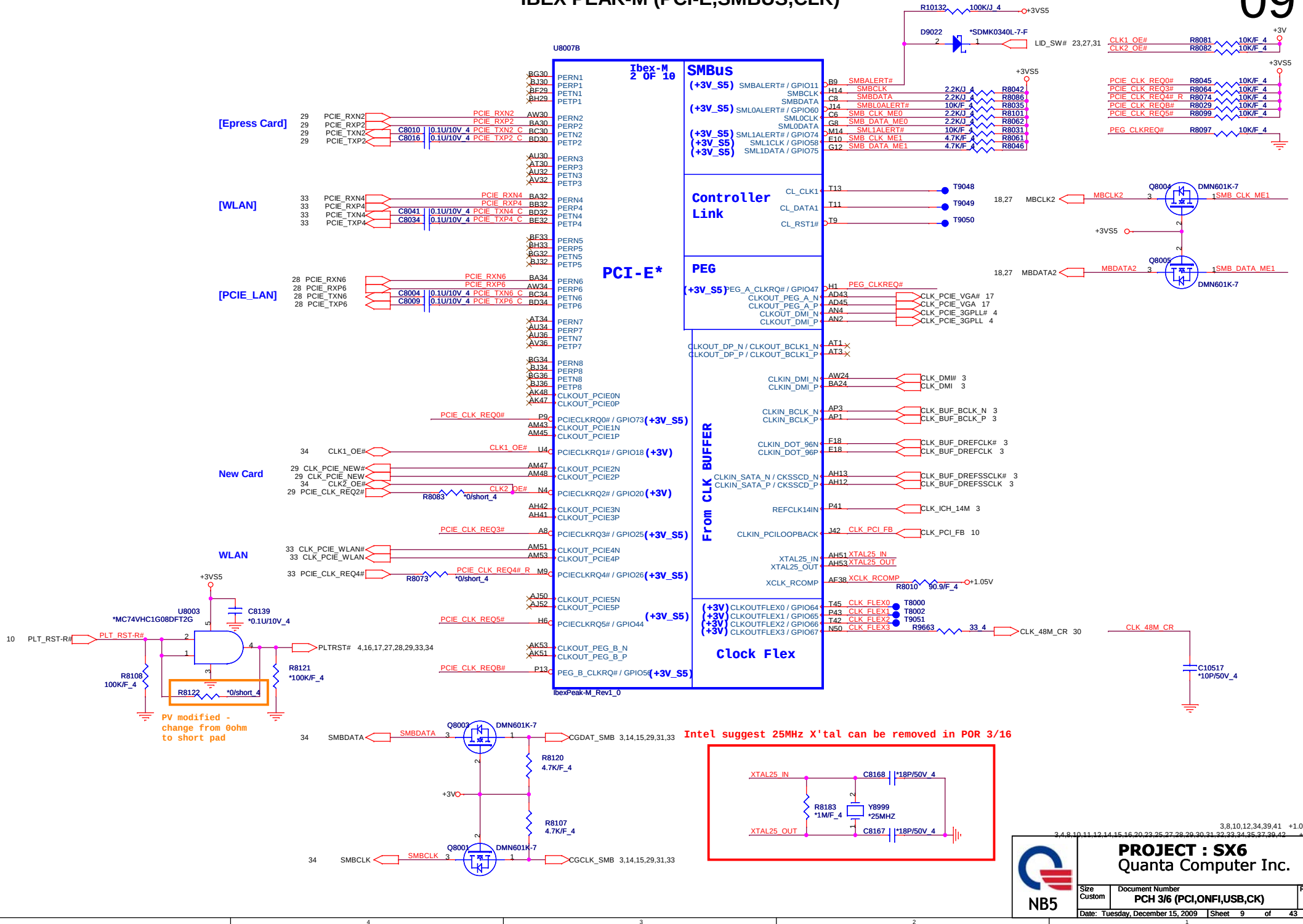
IN1VRMEN - Integrated SUS 1.1V VRM Enable
High - Enable Internal VRs

IBEX PEAK-M (HDA,JTAG,SATA)

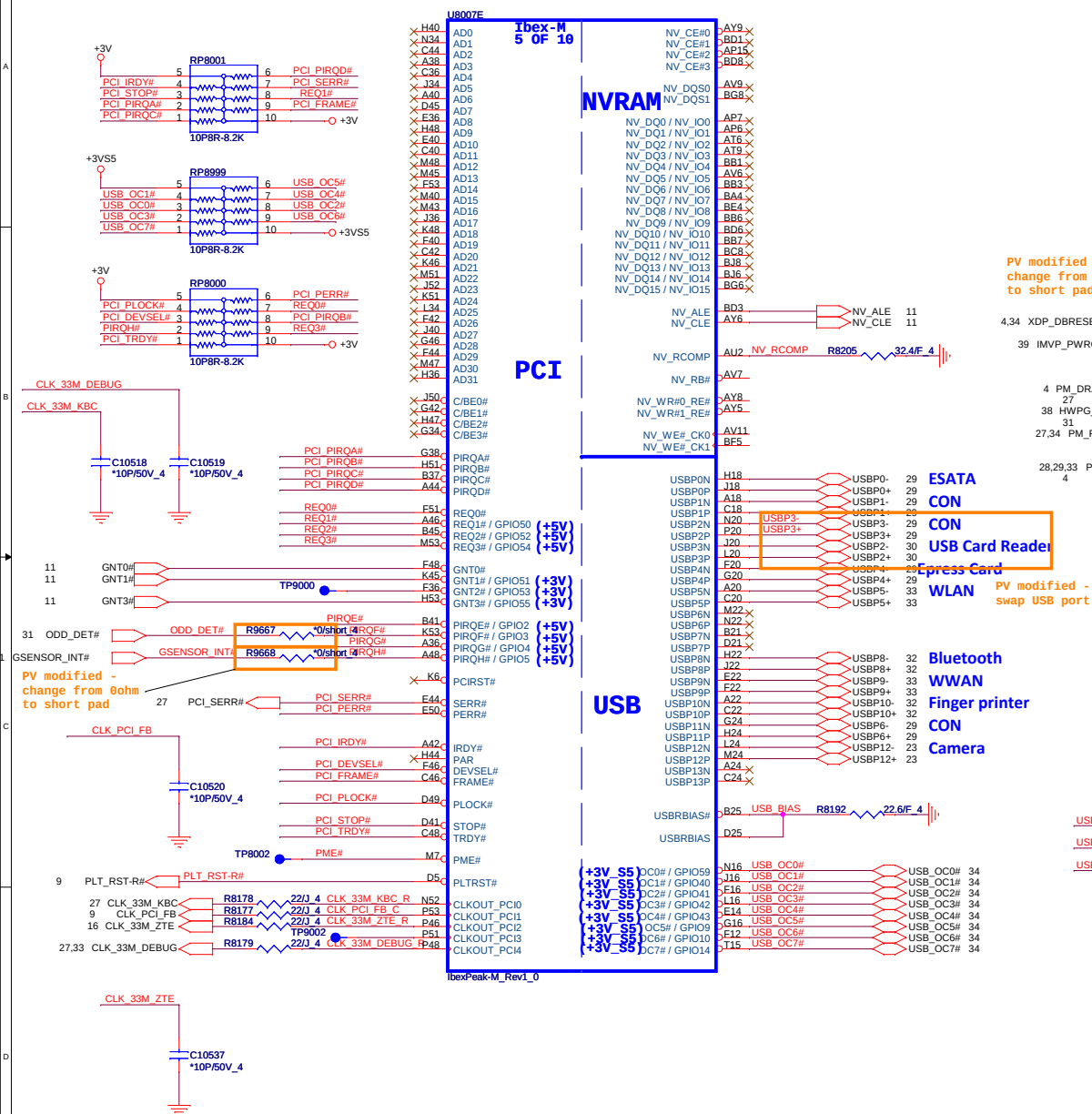


IBEX PEAK-M (PCI-E,SMBUS,CLK)

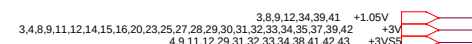
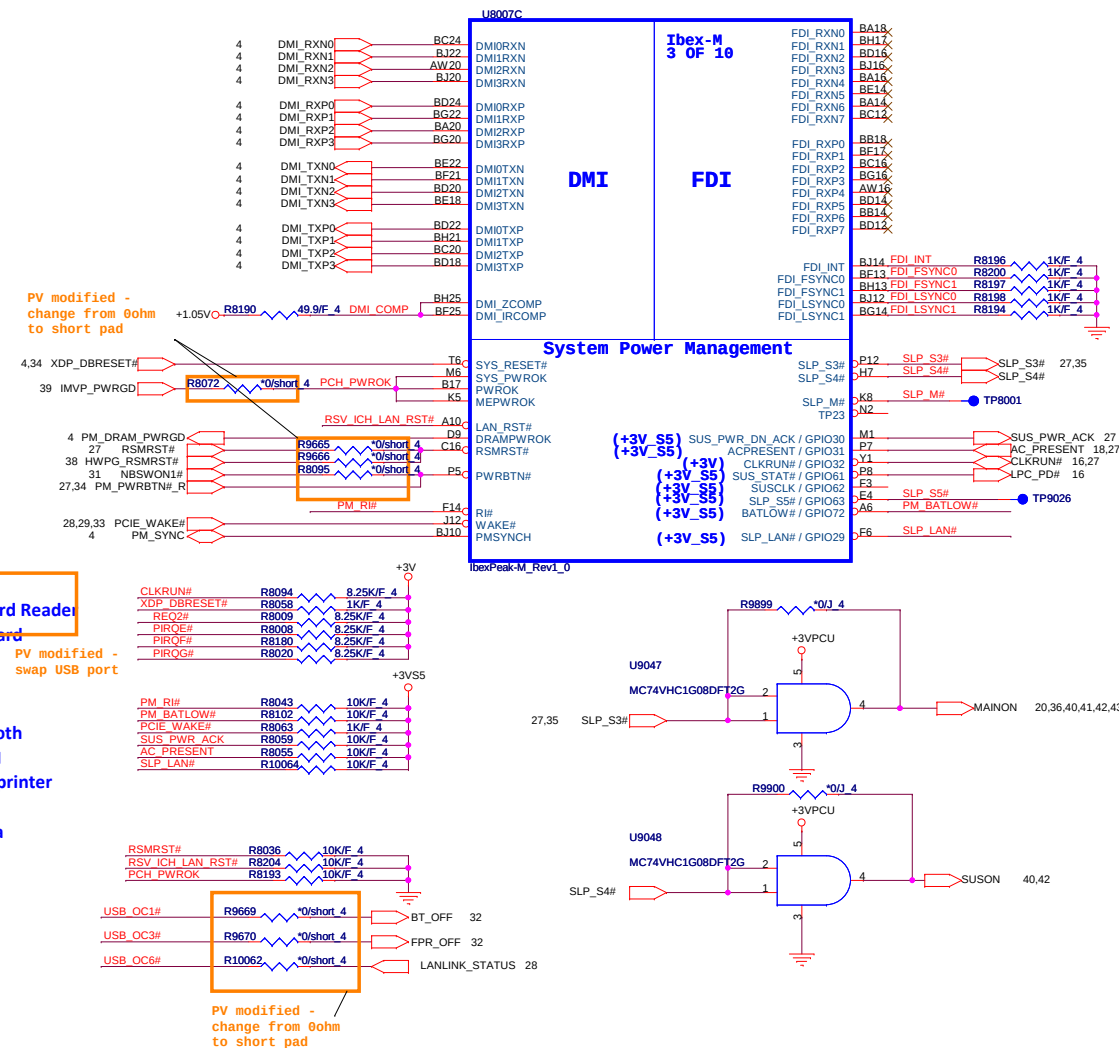
09



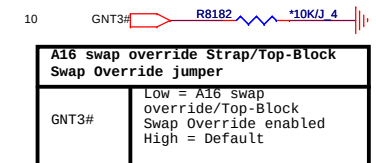
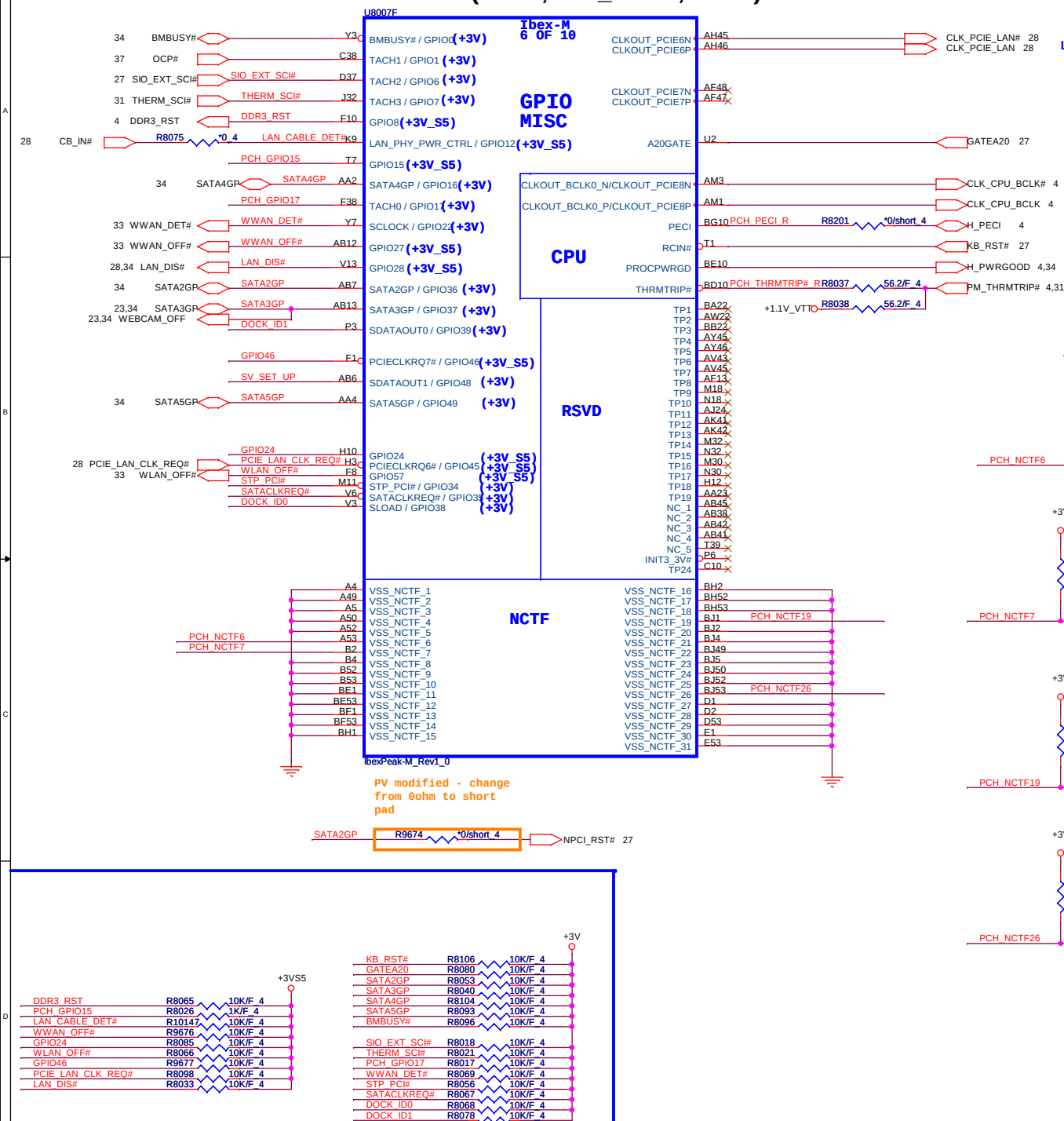
IBEX PEAK-M (PCI,USB,NVRAM)



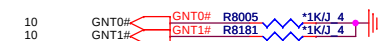
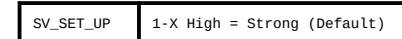
IBEX PEAK-M (DMI,FDI,GPIO)



IBEX PEAK-M (GPIO,VSS_NCTF,RSVD)



SV SET UP R8076 10K/F 4 +3V



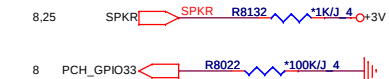
Boot BIOS Strap		
PCI_GNT0#	GNT#1	Boot BIOS Location
0	0	LPC
0	1	Reserved (NAND)
1	0	PCI
1	1	SPI



Danbury Technology Enabled	
NV_ALE	High = Enable Low = Disable

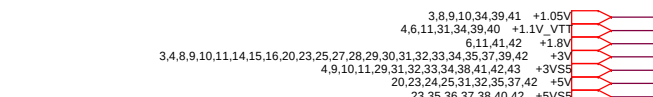
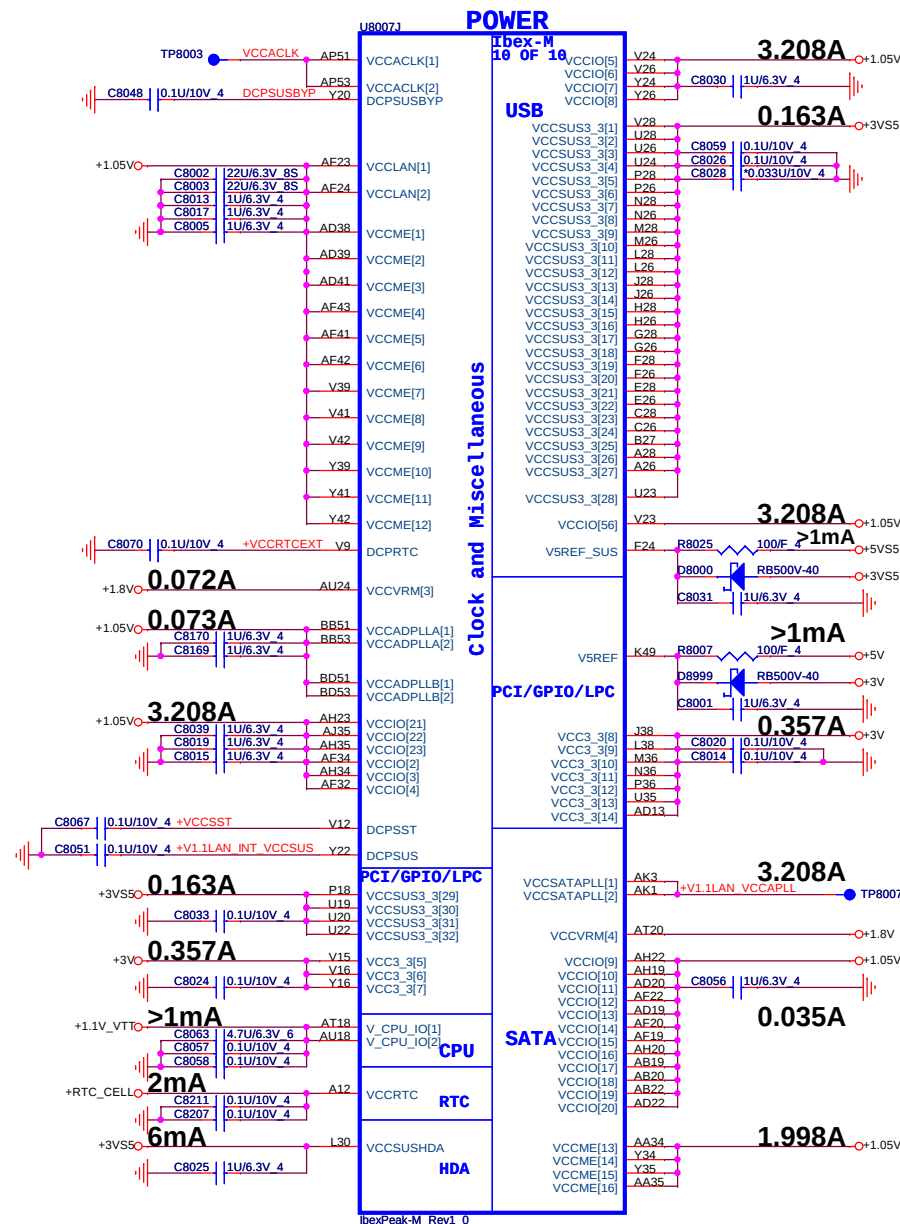
DMI Termination Voltage	
NV_CLE	Set to Vcc when LOW Set to Vcc/2 when HIGH

No Reboot Strap



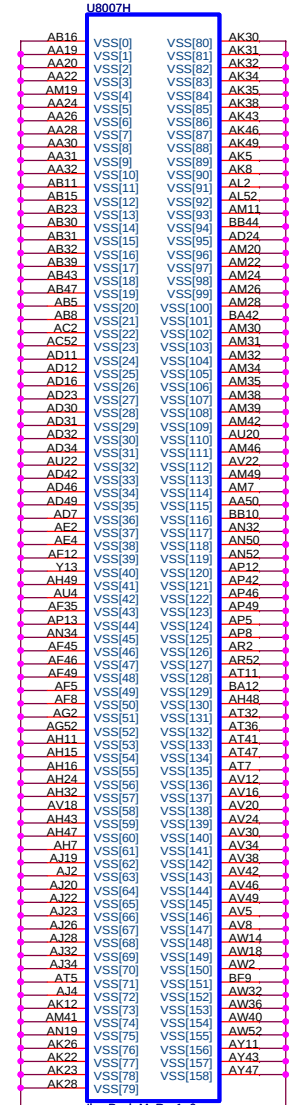
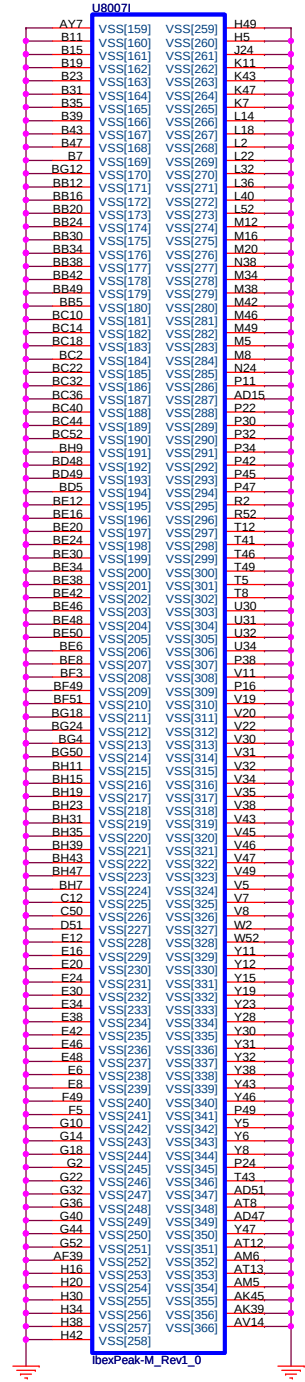
PROJECT : SX6
Quanta Computer Inc.

Size Custom	Document Number PCH 4/6 (GPIO & Strap)	Rev 2B
Date: Tuesday, December 15, 2009		Sheet 11 of 43

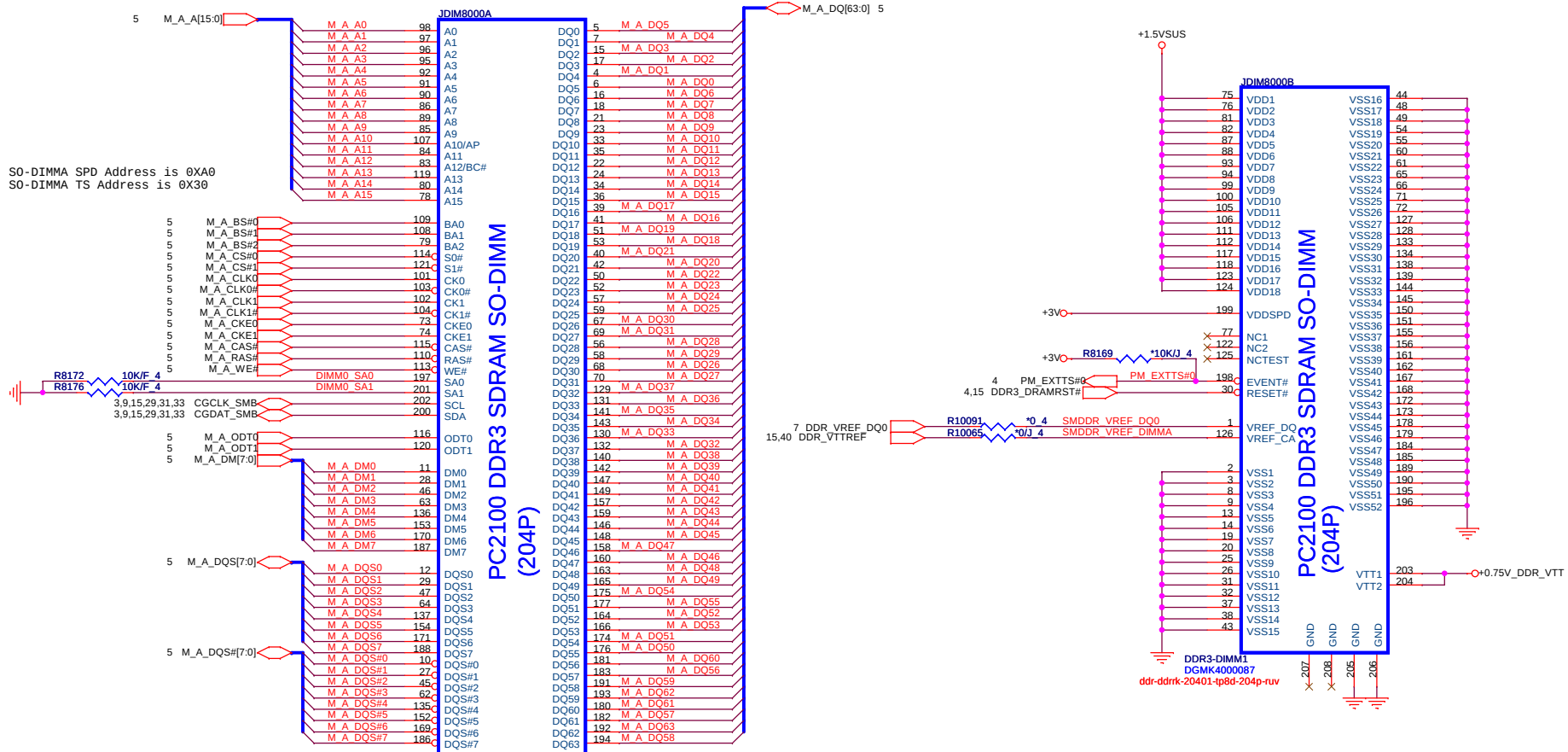


IBEX PEAK-M (GND)

IBEX PEAK-M (GND)

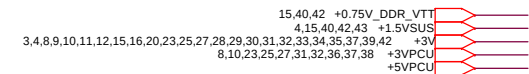
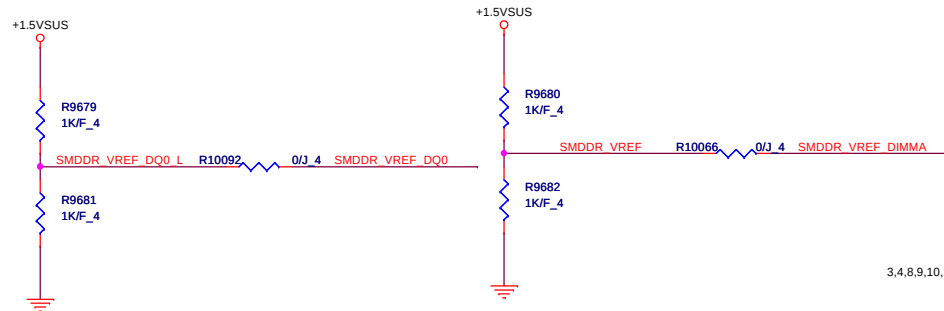
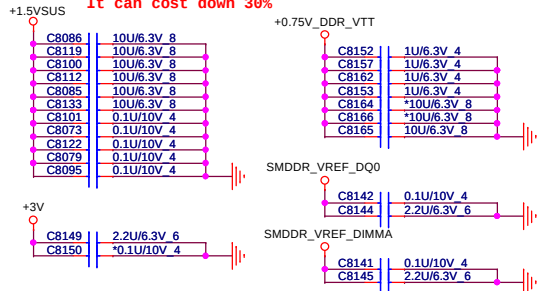


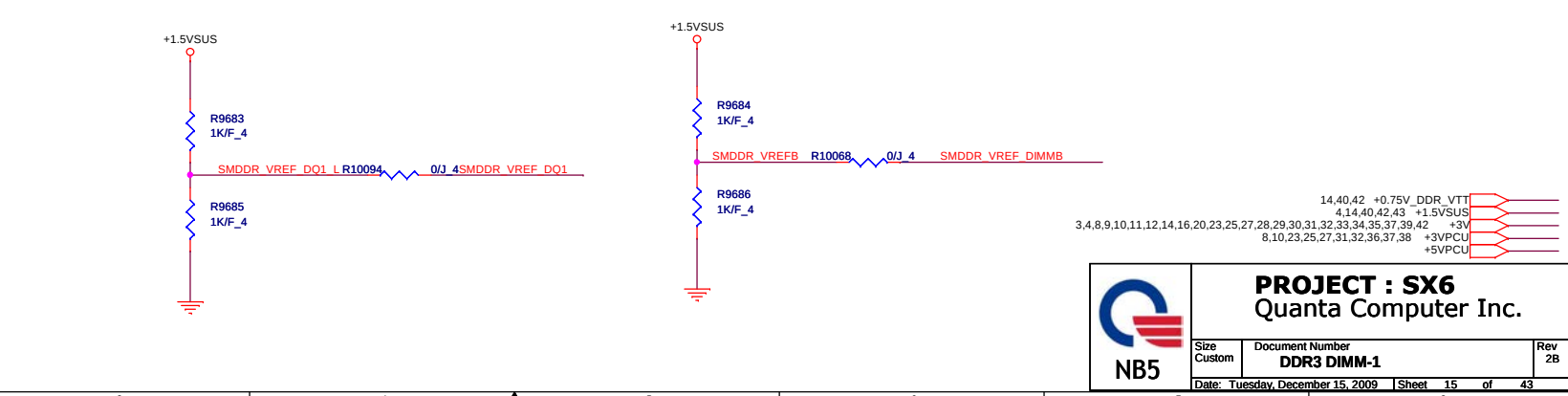
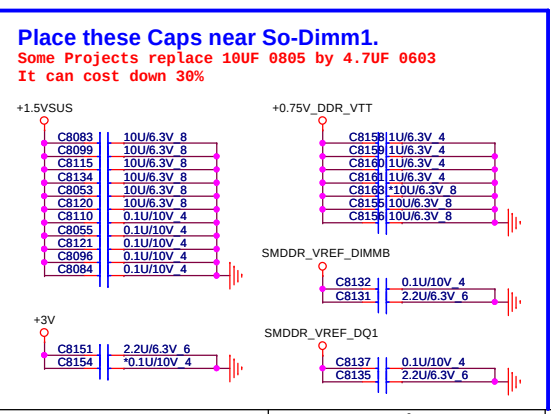
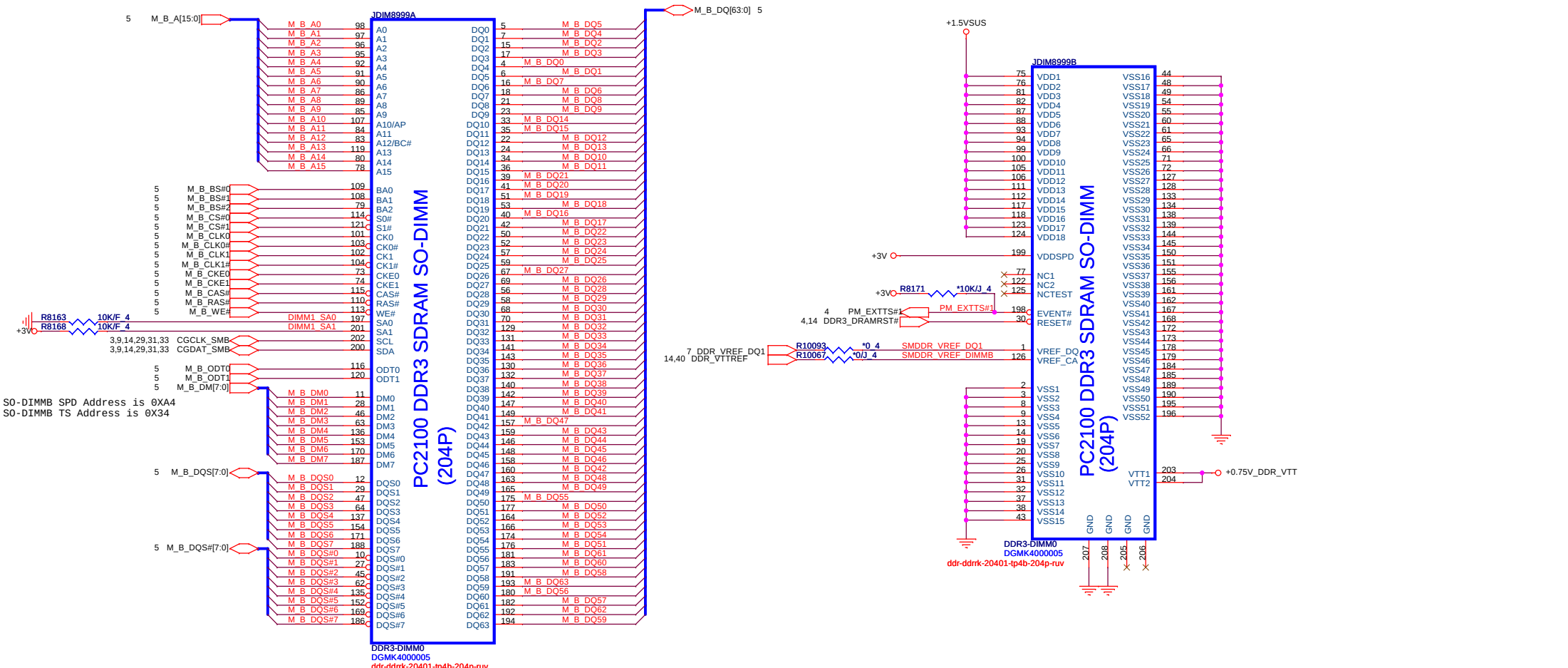
bexPeak-M_Rev1_0

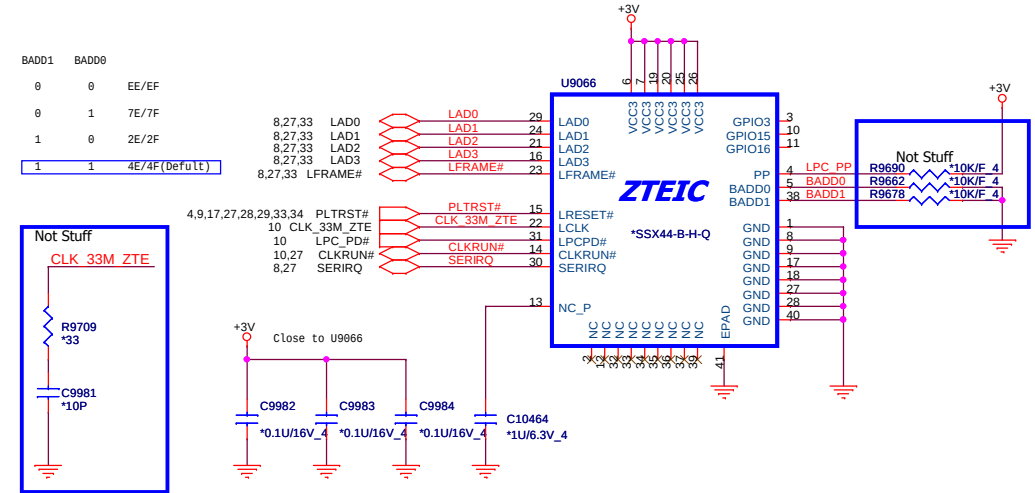
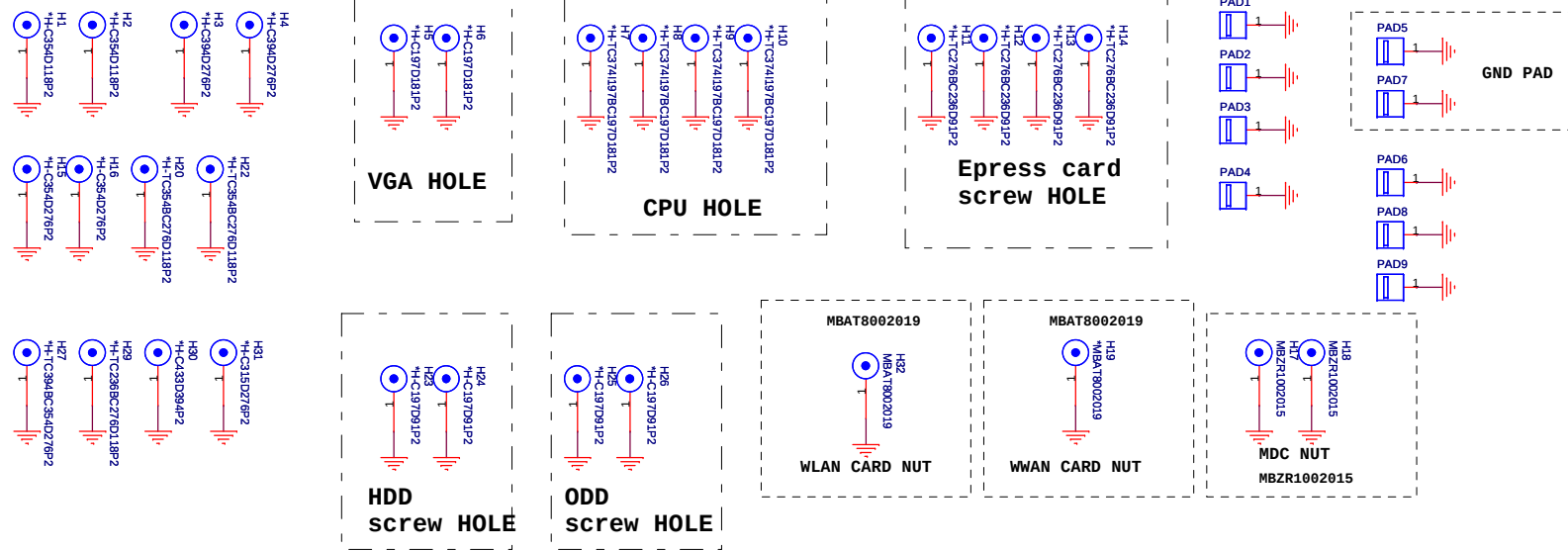


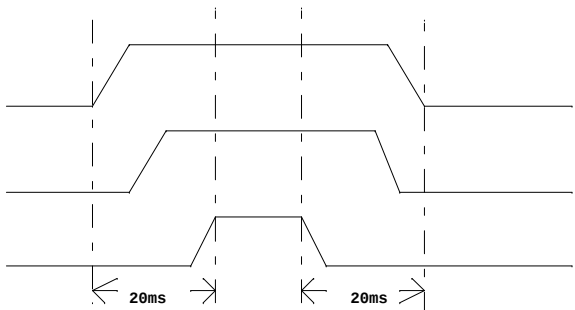
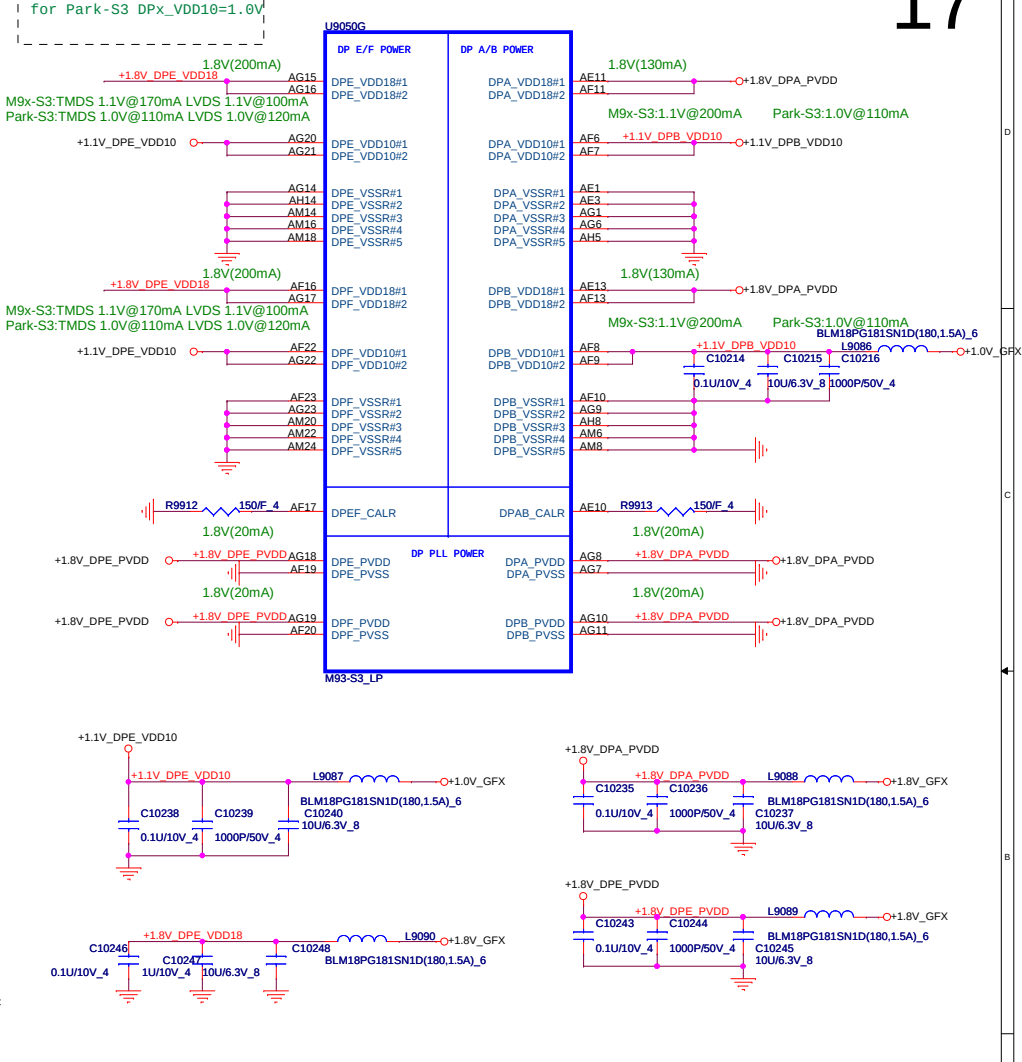
Place these Caps near So-Dimm0.

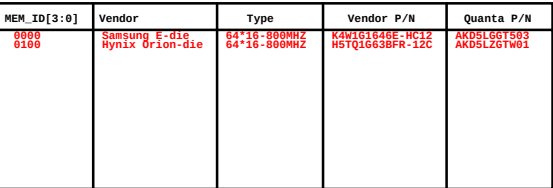
Some Projects replace 10UF 0805 by 4.7UF 0603 11/6
It can cost down 30%











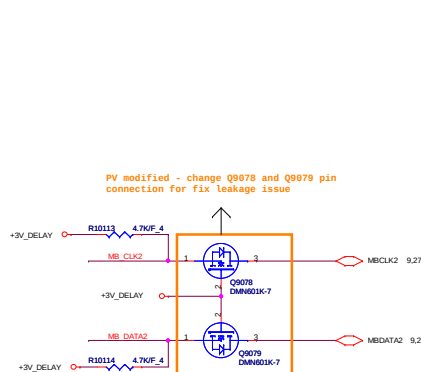
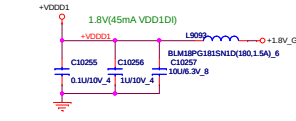
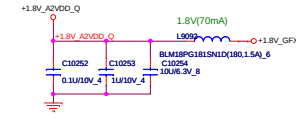
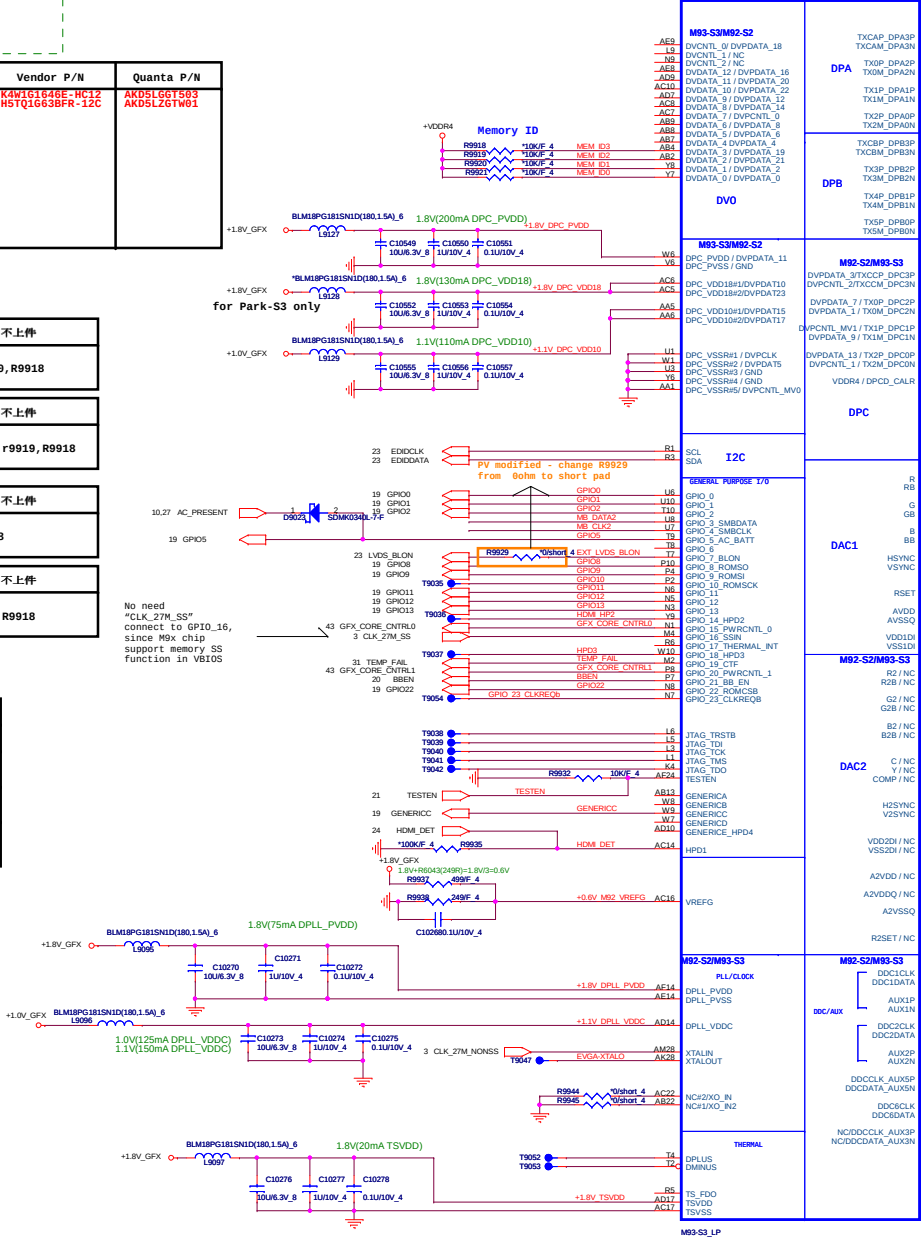
Hynix 512M E100	上件	不上件
	R9919	R9921, R9920, R9918
Samsung 512M 0000	上件	不上件
	NA	R9921, R9920, R9919, R9918

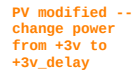
Hynix 1G 0110	上件	不上件
	R9919, R9920	R9921, R9918

Samsung 1G 0010	上件	不上件
	R9920	R9921, R9919, R9918

	PWRCNTL1	PWRCNTL0	V-CORE
	1	1	0.9V
	1	0	0.95V
	0	1	1.05V
	0	0	1.1V

	BBEN	BBP
L	0	V-CORE
H	1	+1.8V

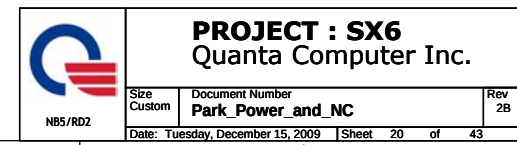


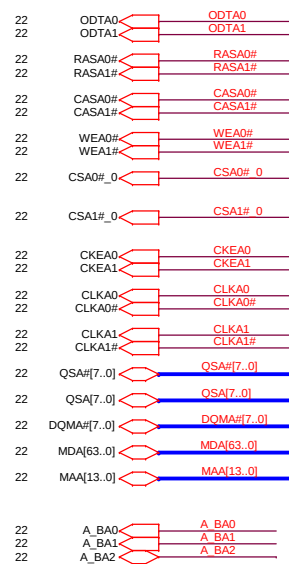


Memory Aperture size

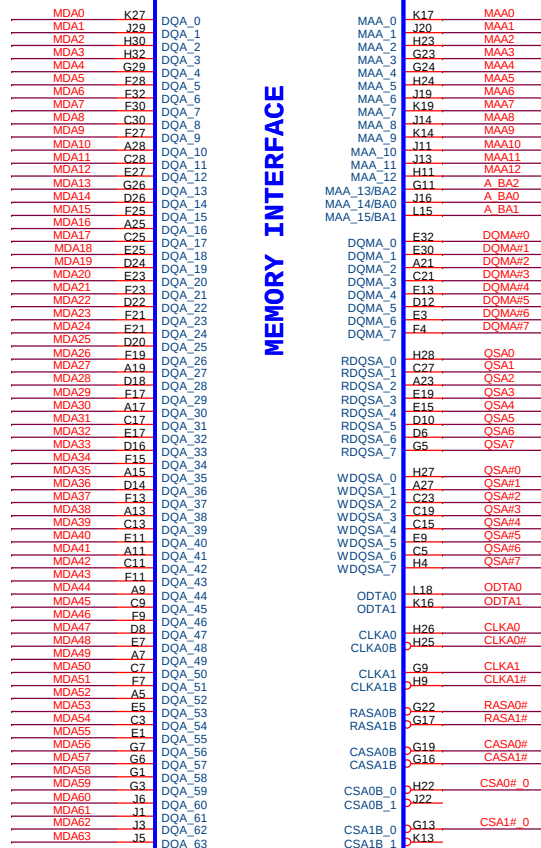
GPIO9 BIOSROM		GPIO13 ROMIDCFG2	GPIO12 ROMIDCFG1	GPIO11 ROMIDCFG0
0	128M	0	0	0
0	256M	0	0	1
0	64M	0	1	0
0	32M	0	1	1
0	512M	1	0	0
0	1G	1	0	1
0	2G	1	1	0
0	4G	1	1	1

It is a shared pin strap with CONFIG[2:0] if BIOS_ROM_EN is set to 0.

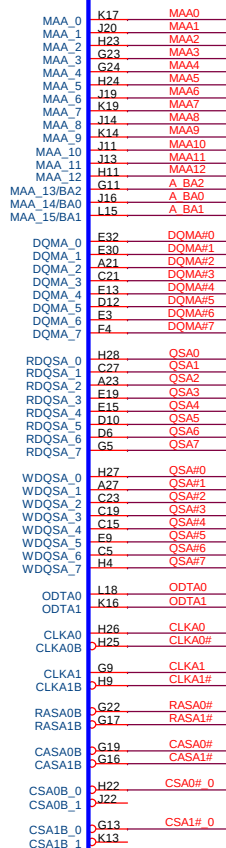




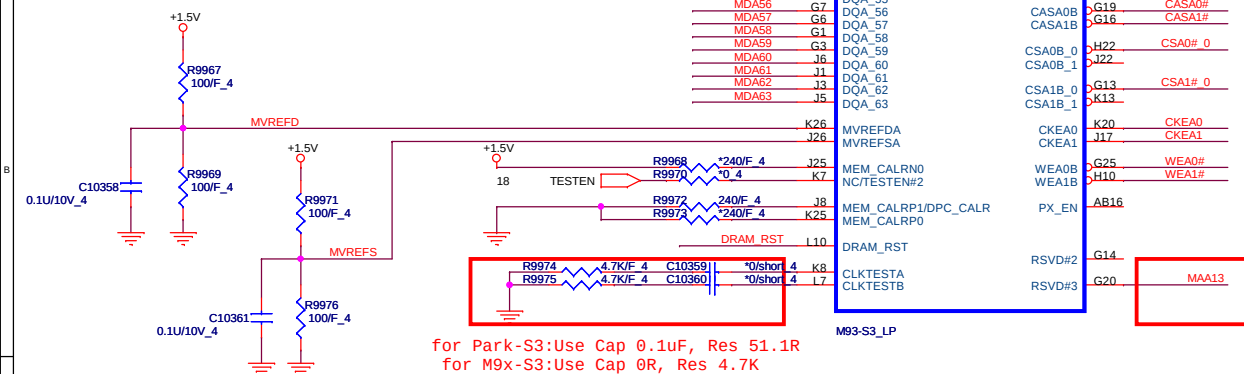
support 1Gbit
VRAM (64M X 16)



MEMORY INTERFACE



for Park-S3:Use only
for M9x-S3: no support



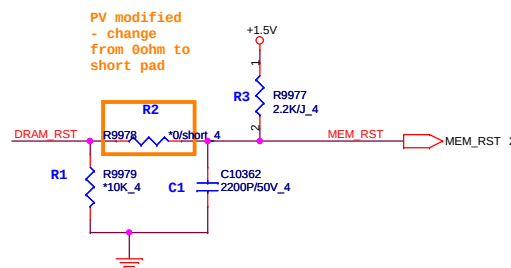
for Park-S3:Use Cap 0.1uF, Res 51.1R
for M9x-S3:Use Cap 0R, Res 4.7K

	M9x-S2/S3	Park-S3
MEM_CALRN0(J25)	NC	240R
MEM_CALRP0(K25)	NC	240R
MEM_CALRP1(J8)	240R	150R
TESTEN2#2(K7)	NC	0R
R1	NC	10K
R2	0R	680R
R3	2.2K	NC
C1	2.2nF	68pF

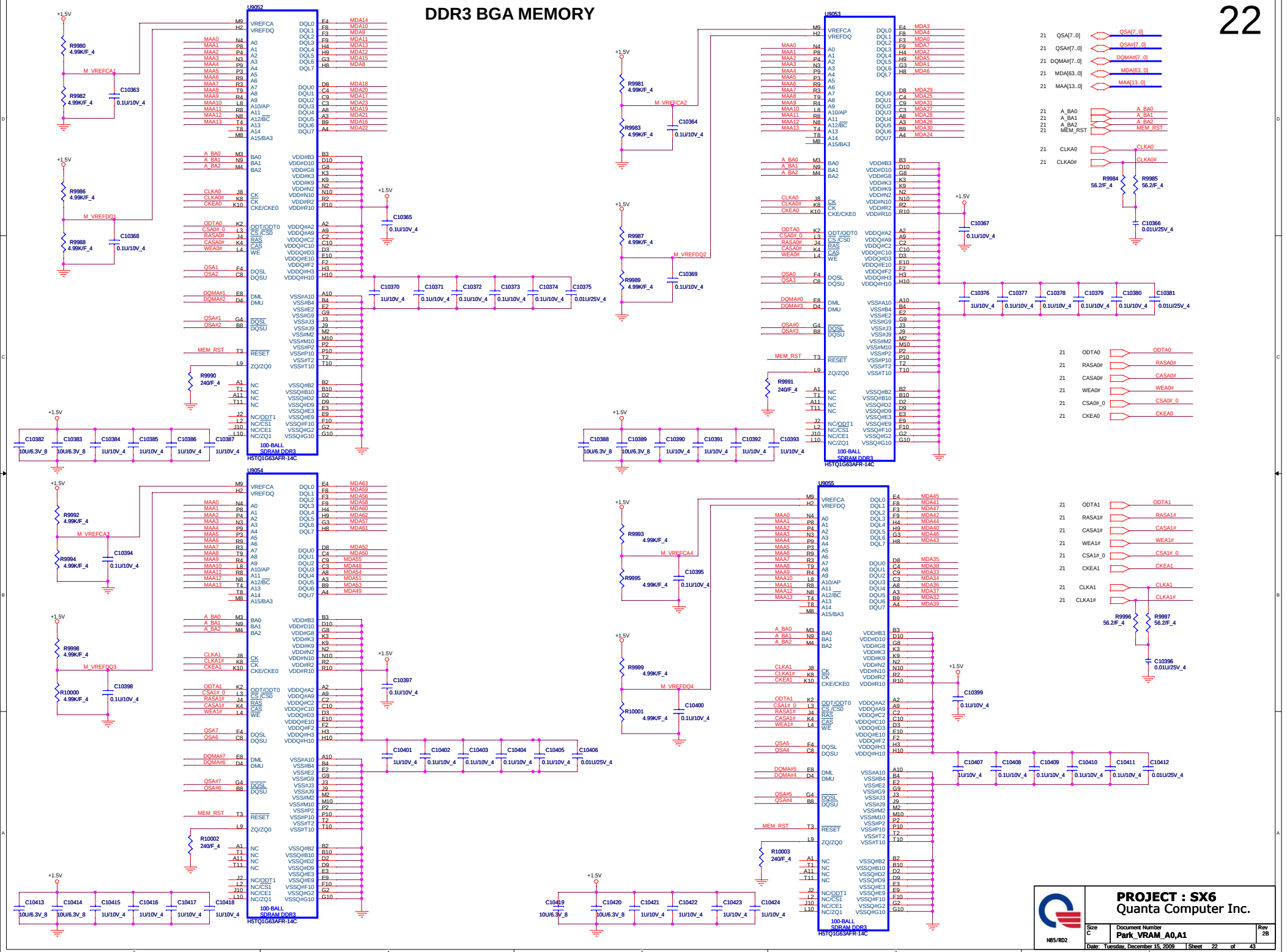
240R: CS12402FB03
150R: CS11502FB21

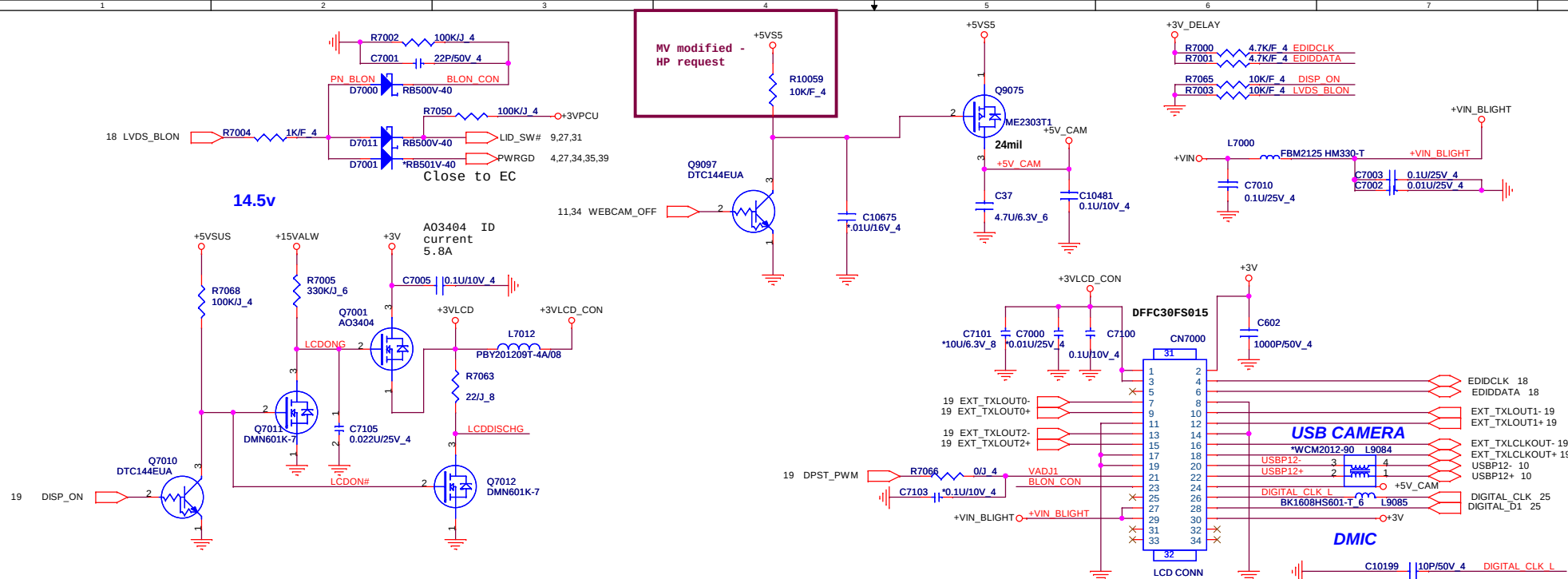
0R: CS00002JB38
680R: CS16802JB27

2.2nF: CH22206KB16
68pF: CH06806JB01

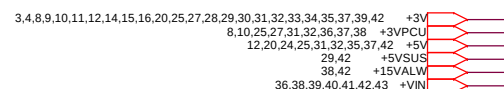
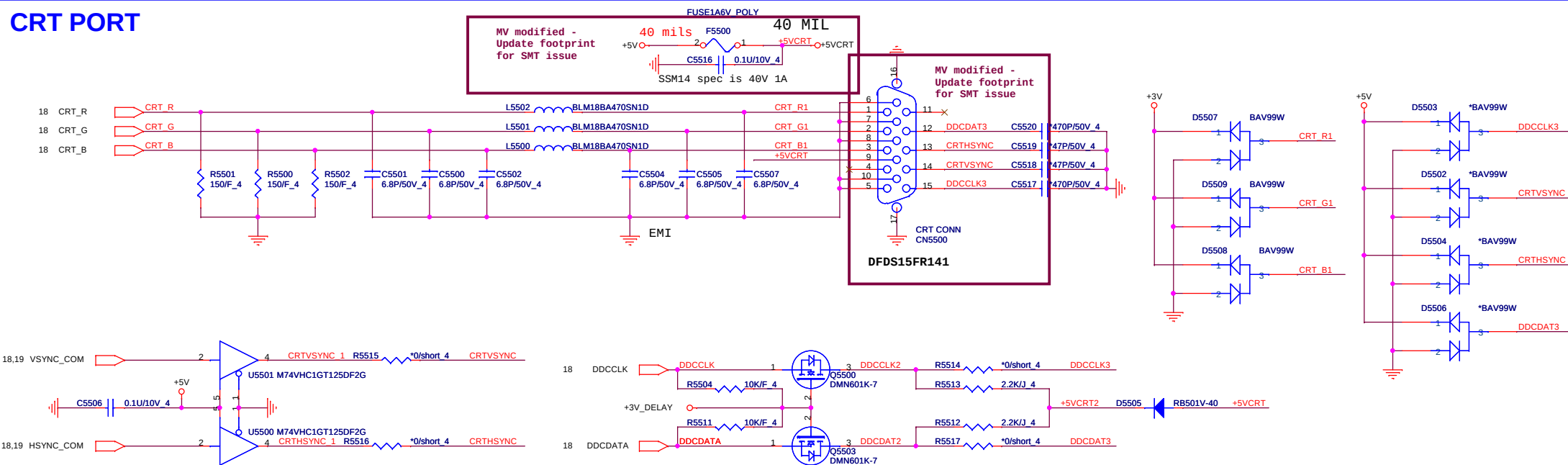


22



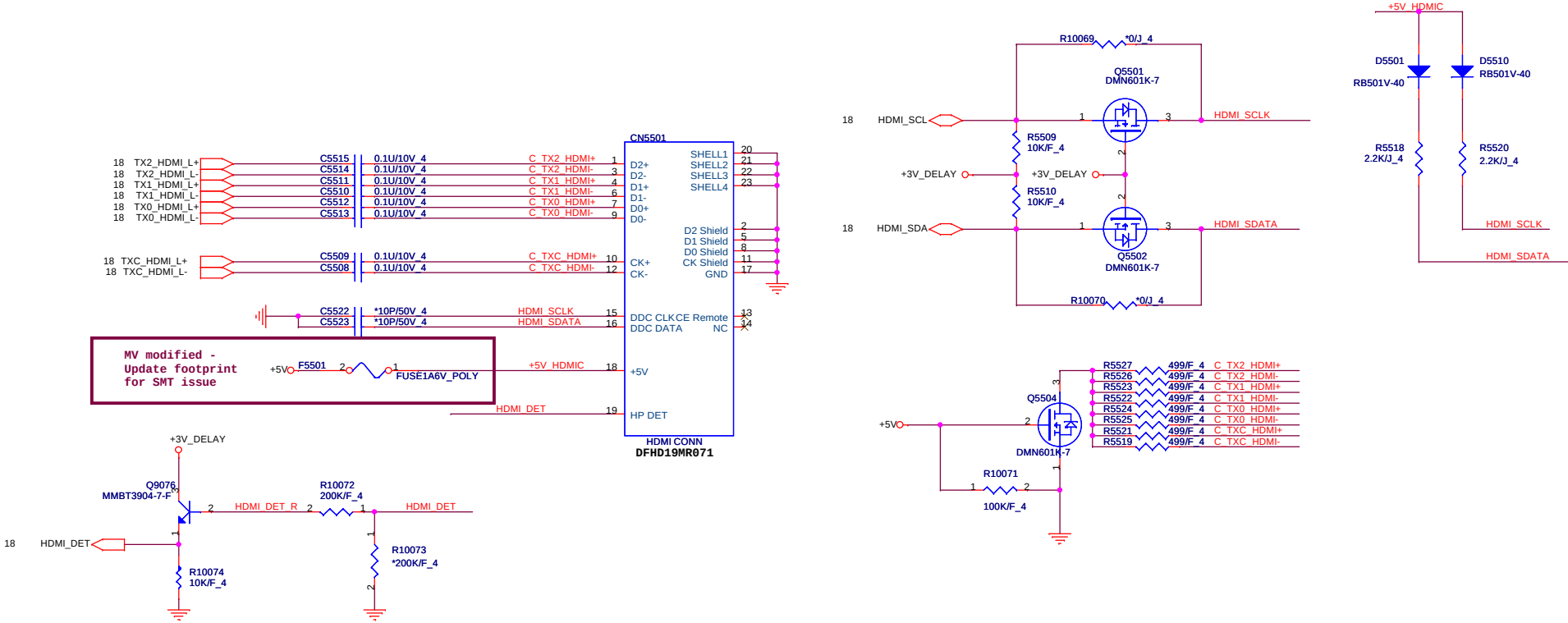


CRT PORT



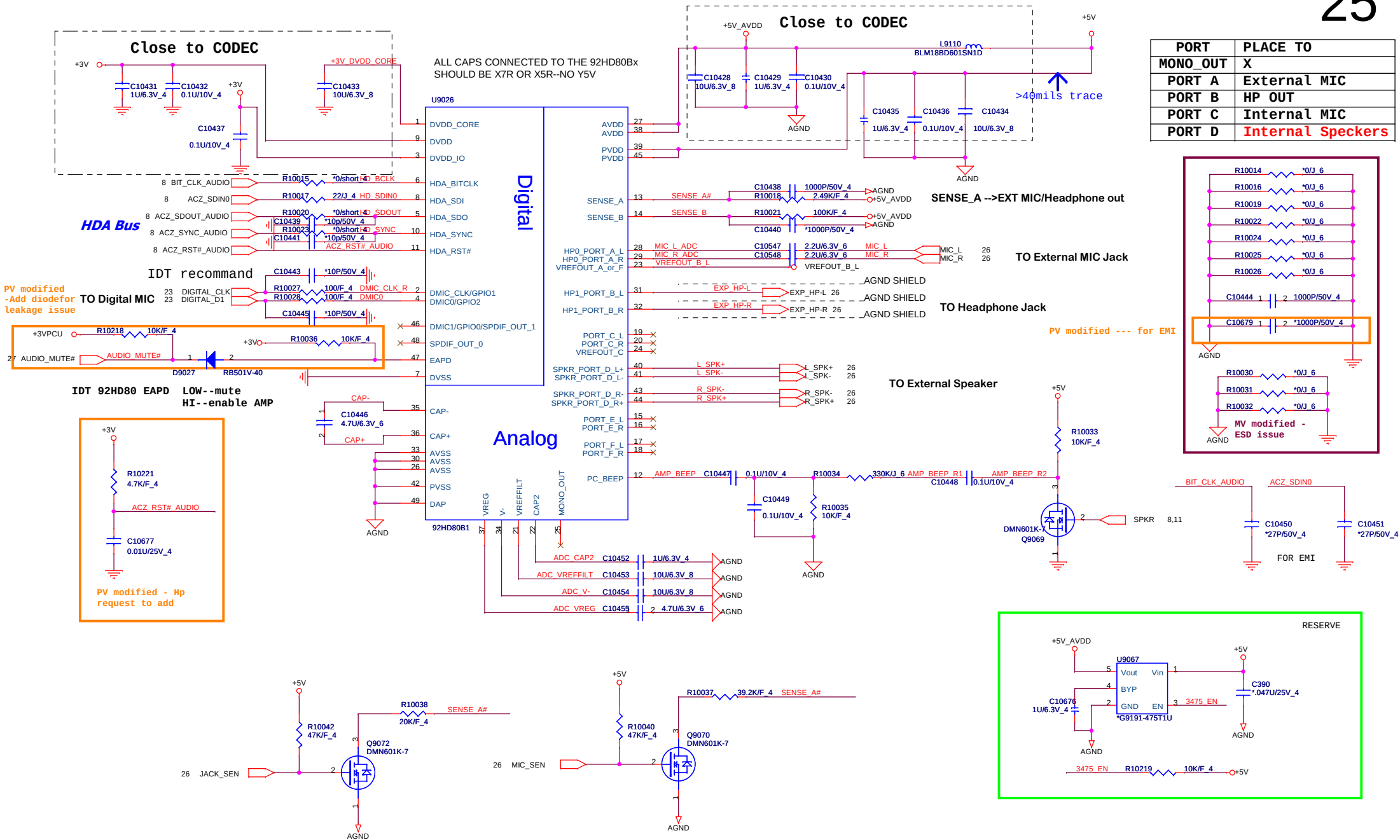
PROJECT : SX6
Quanta Computer Inc.

Size Custom	Document Number LCD CONN/LID function	Rev 2E
Date: Tuesday, December 15, 2009		Sheet 23 of 43



3,4,8,9,10,11,12,14,15,16,20,23,25,27,28,29,30,31,32,33,34,35,37,39,42 +3V
12,20,23,25,31,32,35,37,42 +5V

PORT	PLACE TO
MONO_OUT	X
PORT A	External MIC
PORT B	HP OUT
PORT C	Internal MIC
PORT D	Internal Speckers



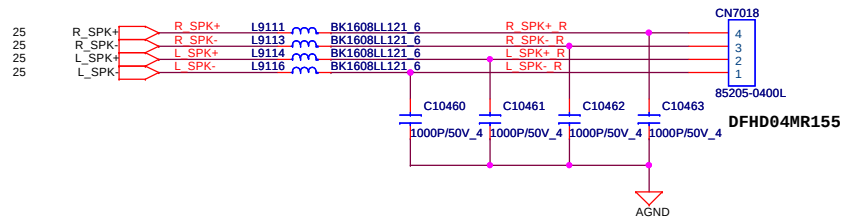
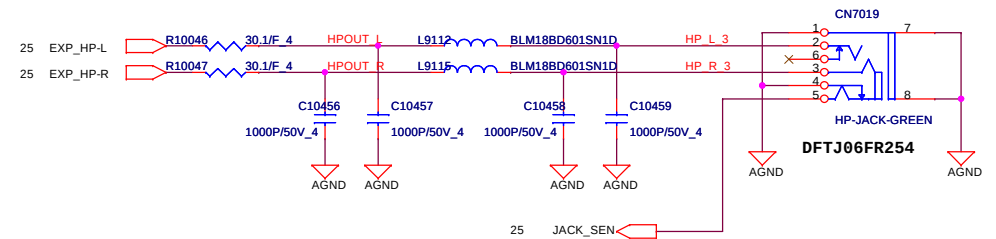
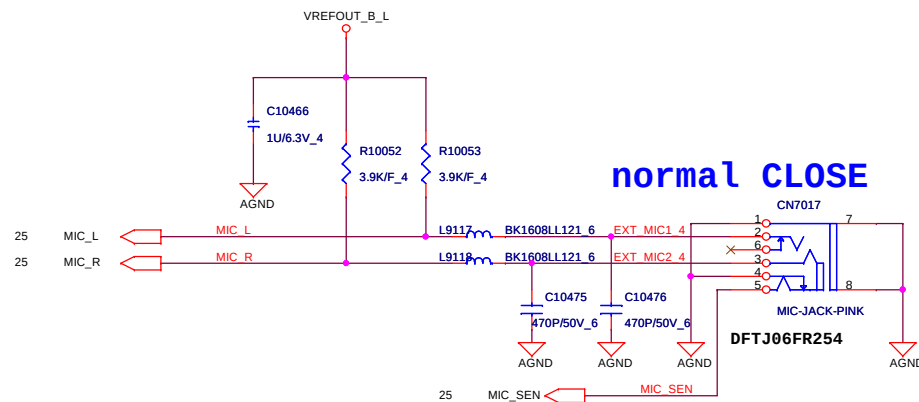
PROJECT : SX6
Quanta Computer Inc.

Size	Document Number	Rev
Custom	IDT92HD80	2B
Date: Thursday, December 17, 2009	Sheet 25 of 43	

3,4,8,9,10,11,12,14,15,16,20,23,27,28,29,30,31,32,33,34,35,37,39,42
12,20,23,24,31,32,35,37,42

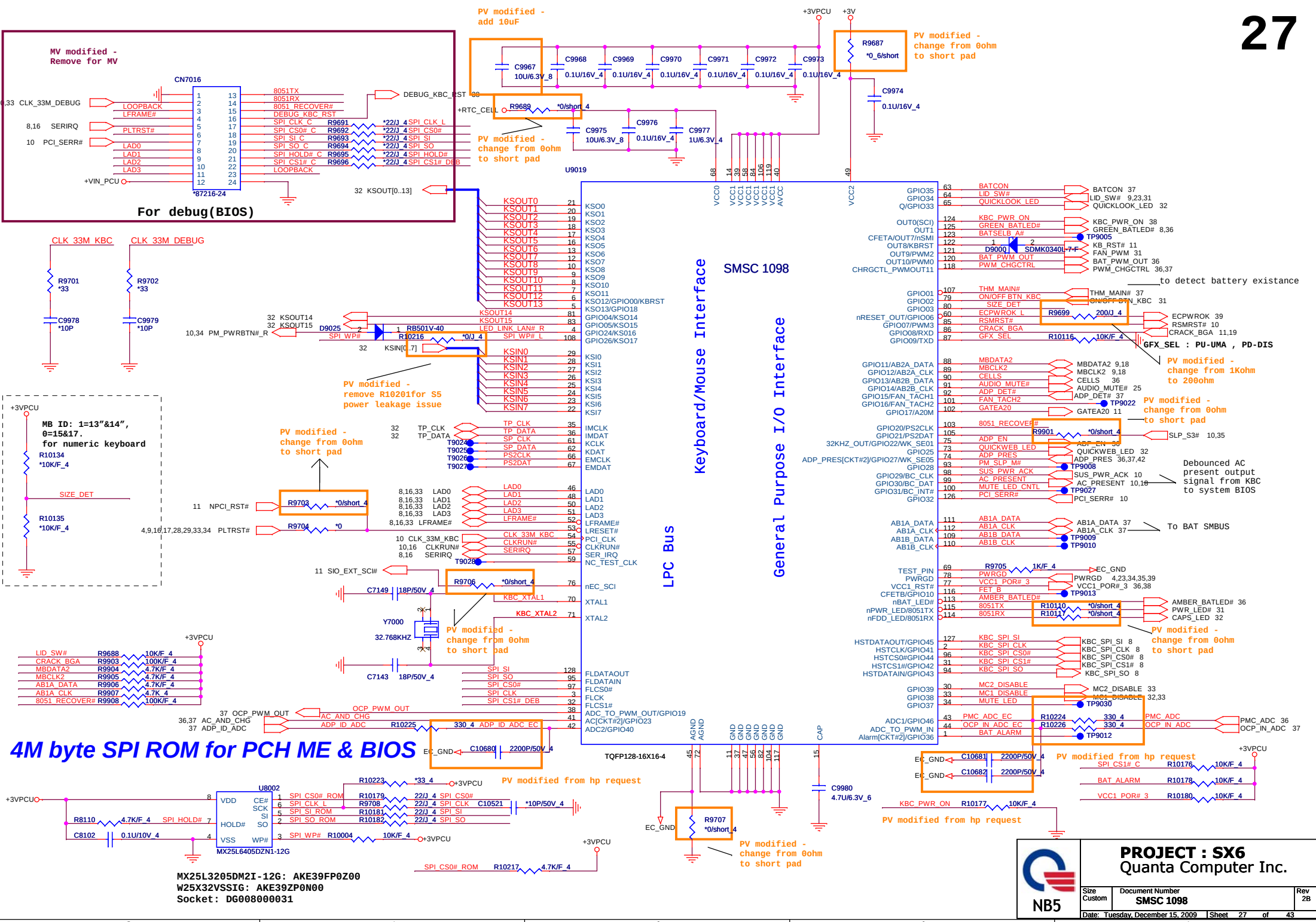
+3V

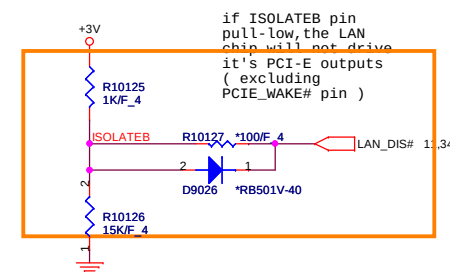
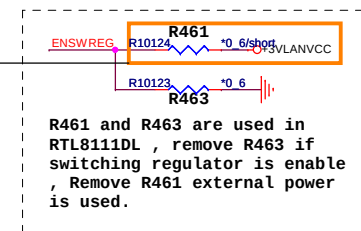
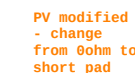
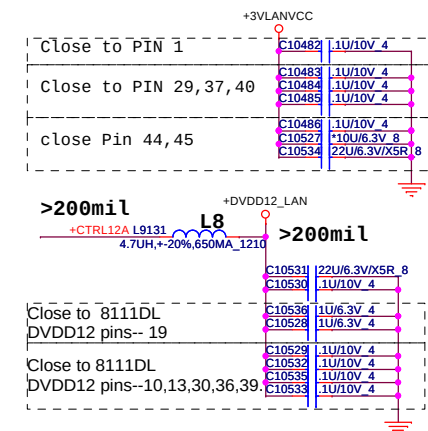
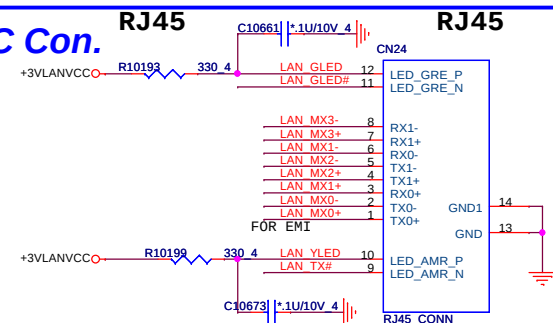
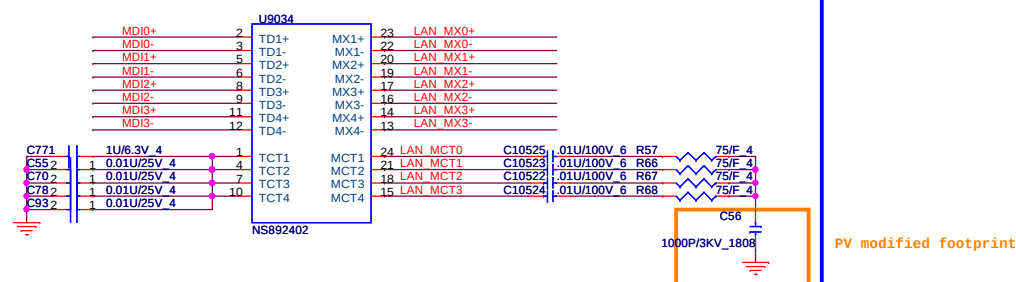
+5V

INT. SPEAKER**Headphone Jack****normal CLOSE****EXT Mic Jack****normal CLOSE**

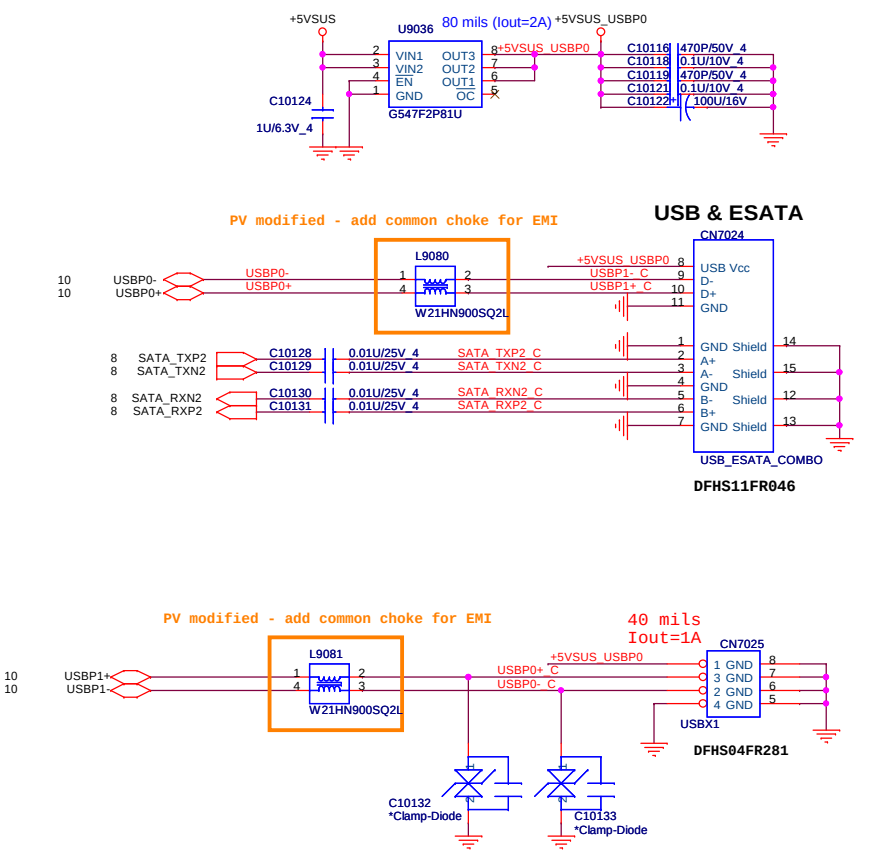
Note: MIC_SEN# is electrically floating when no jack is inserted and shorted to ground when jack is present.

Note: JACK_SEN# is electrically floating when no jack is inserted and shorted to ground when jack is present.

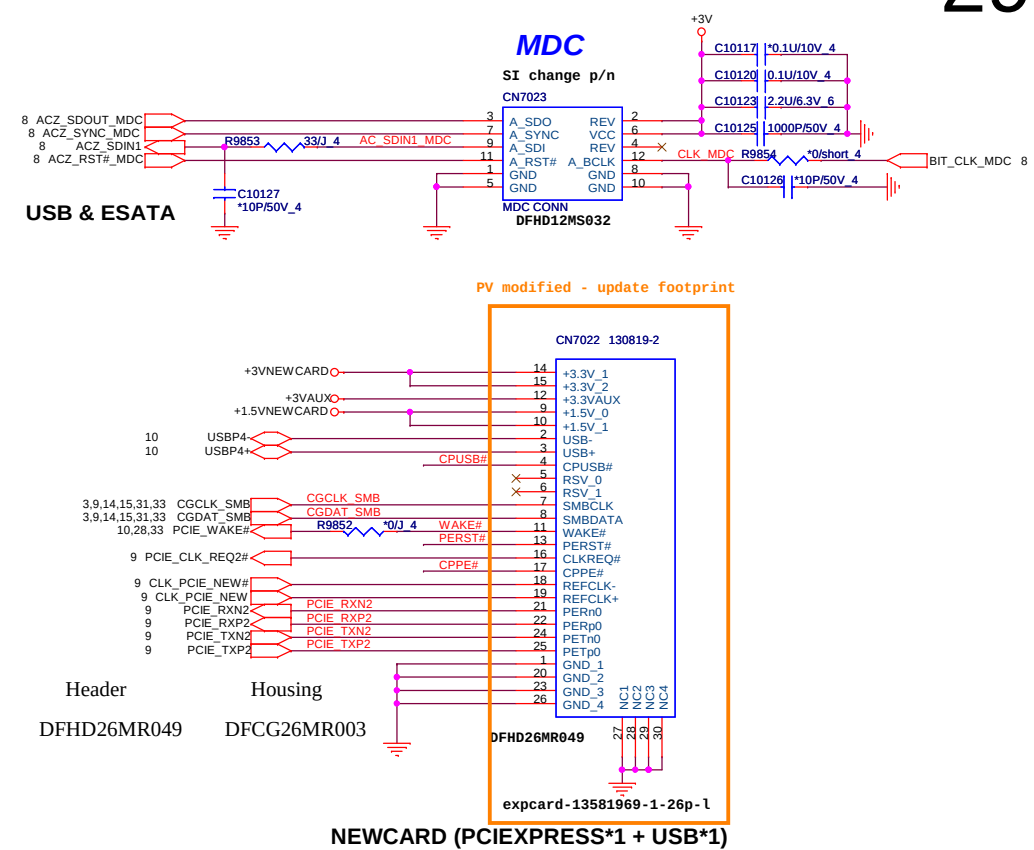


Lan and MDC Con. RJ45

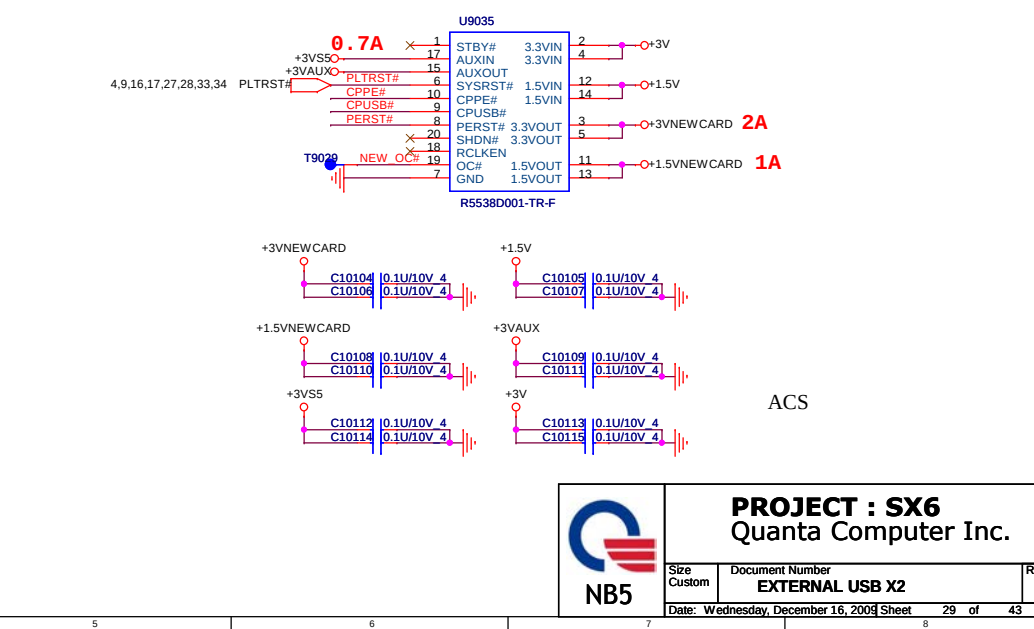
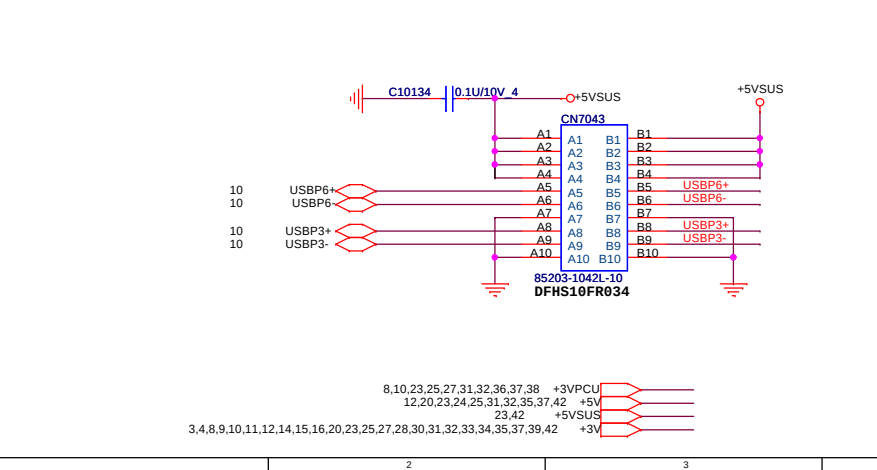
LEFT SIDE USBX1 and E-SATA/USB COMBO

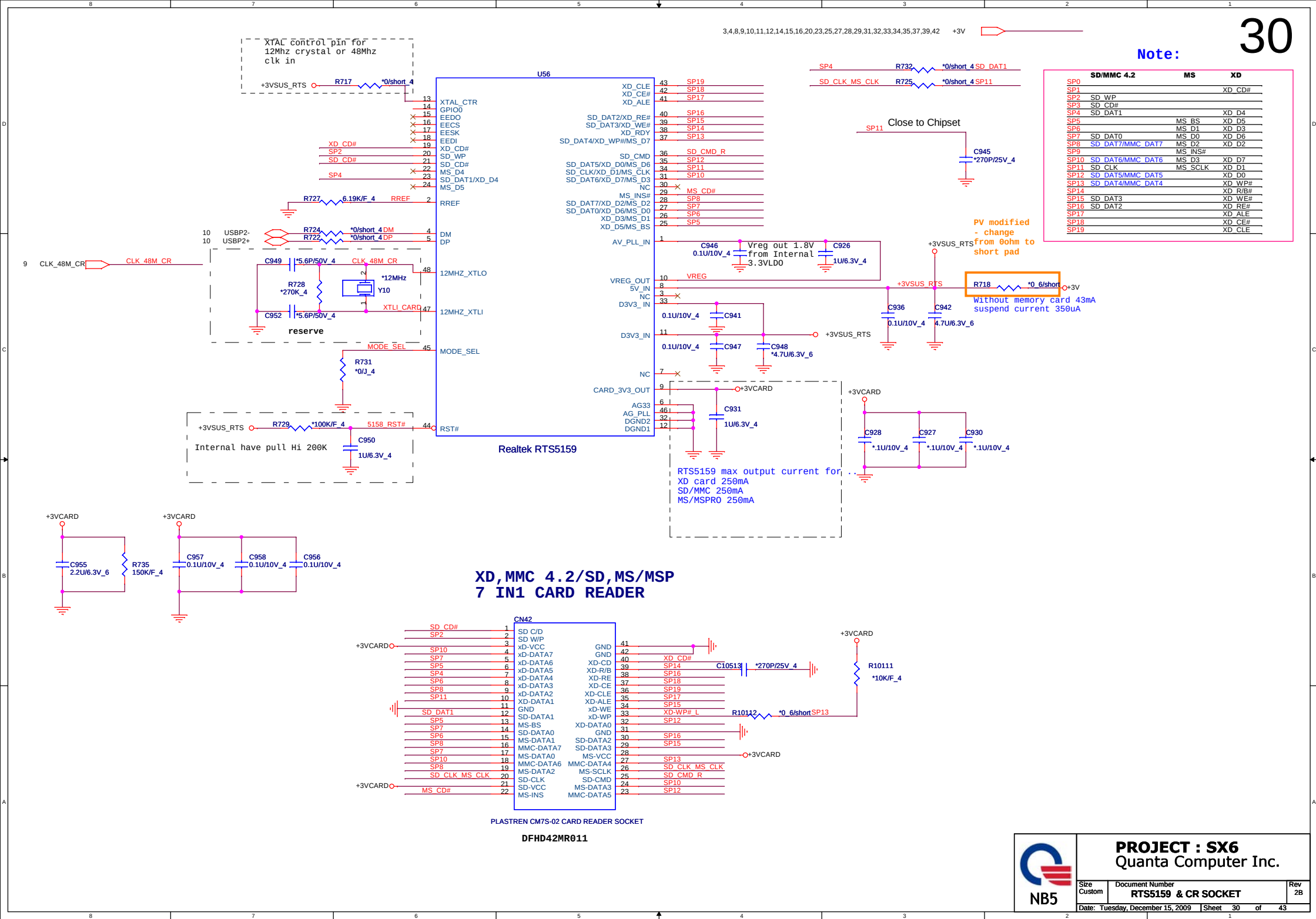


Modem CONN

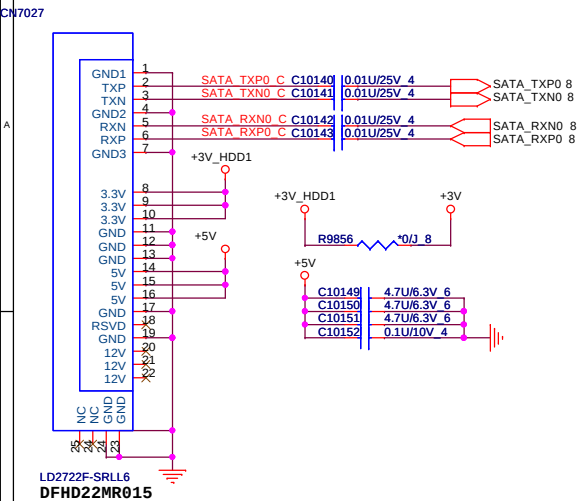


RIGHT SIDE USBX2

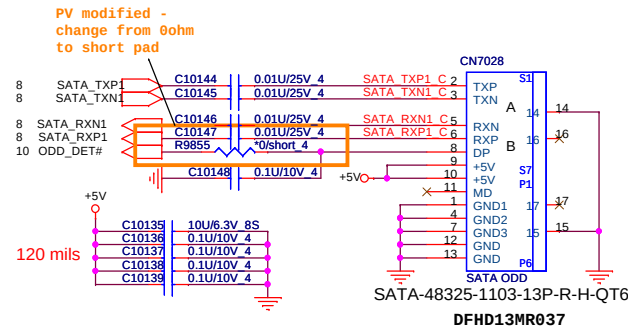




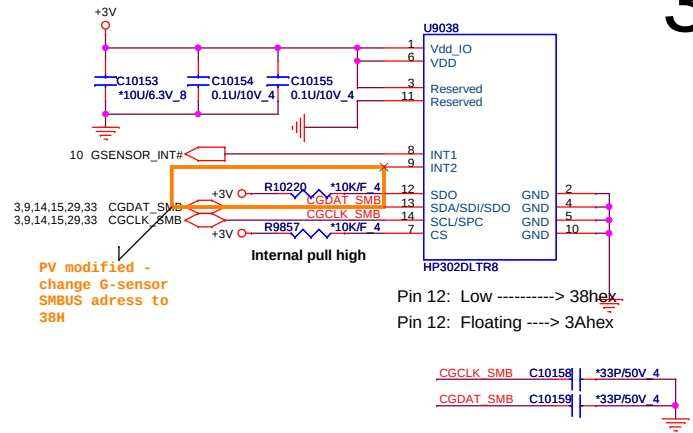
SATA HDD CONNECTOR



SATA CD-ROM

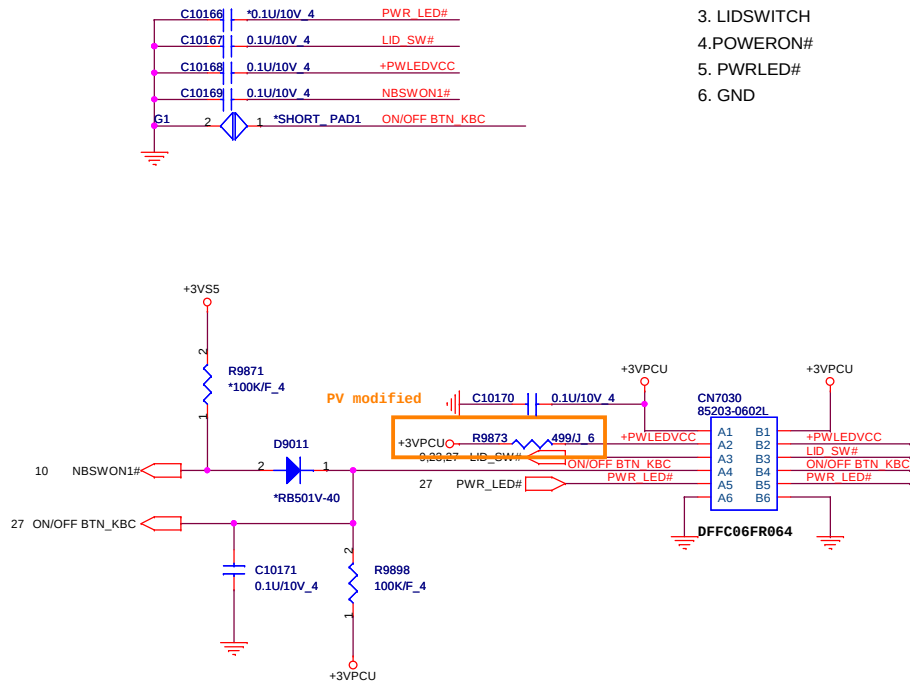


Accelerometer Sensor

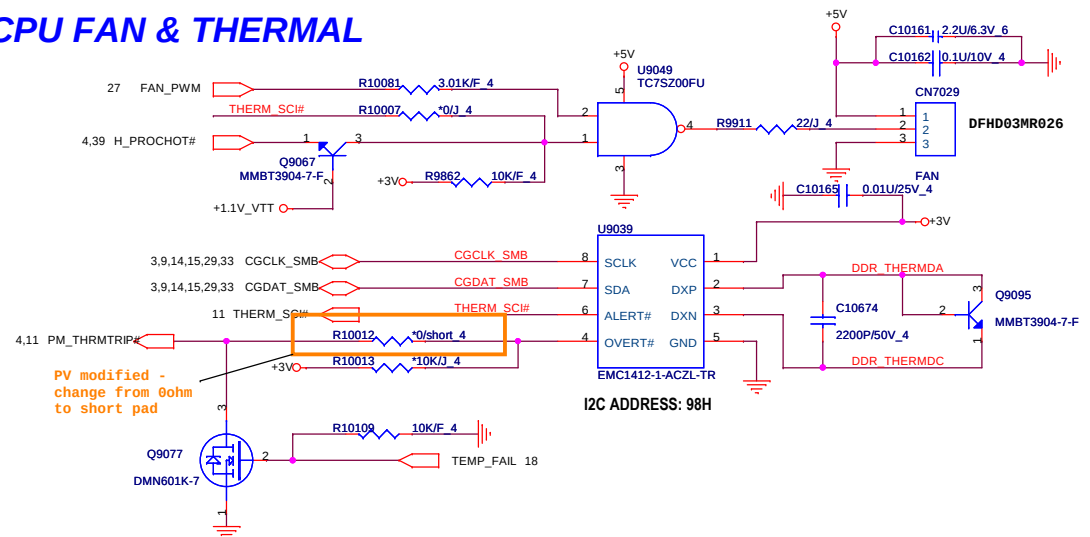


POWER BOTTON CONNECT

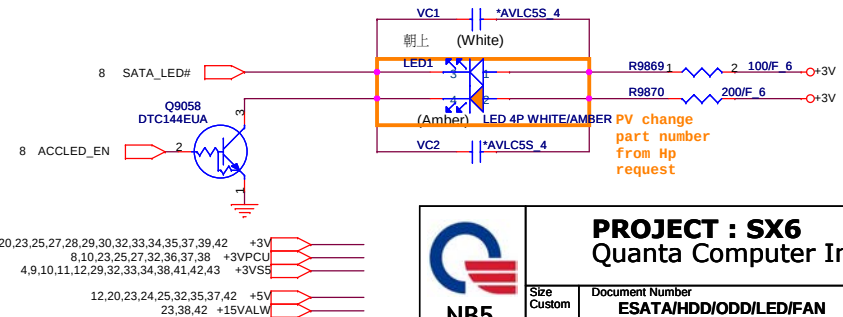
- 1.+3VPCU(LIDSWITCH PWR)
- 2.LEDVCC(+3VPCU)
- 3.LIDSWITCH
- 4.POWERON#
- 5.PWRLED#
- 6.GND



CPU FAN & THERMAL



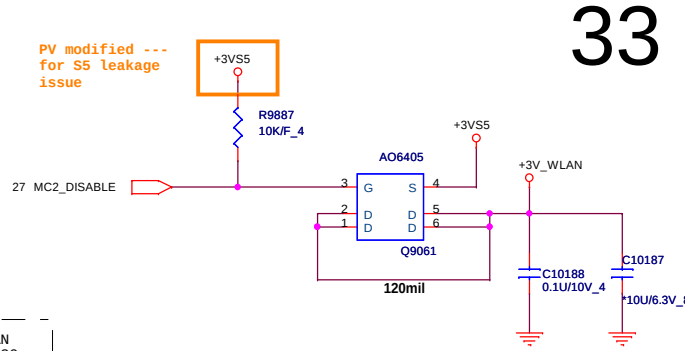
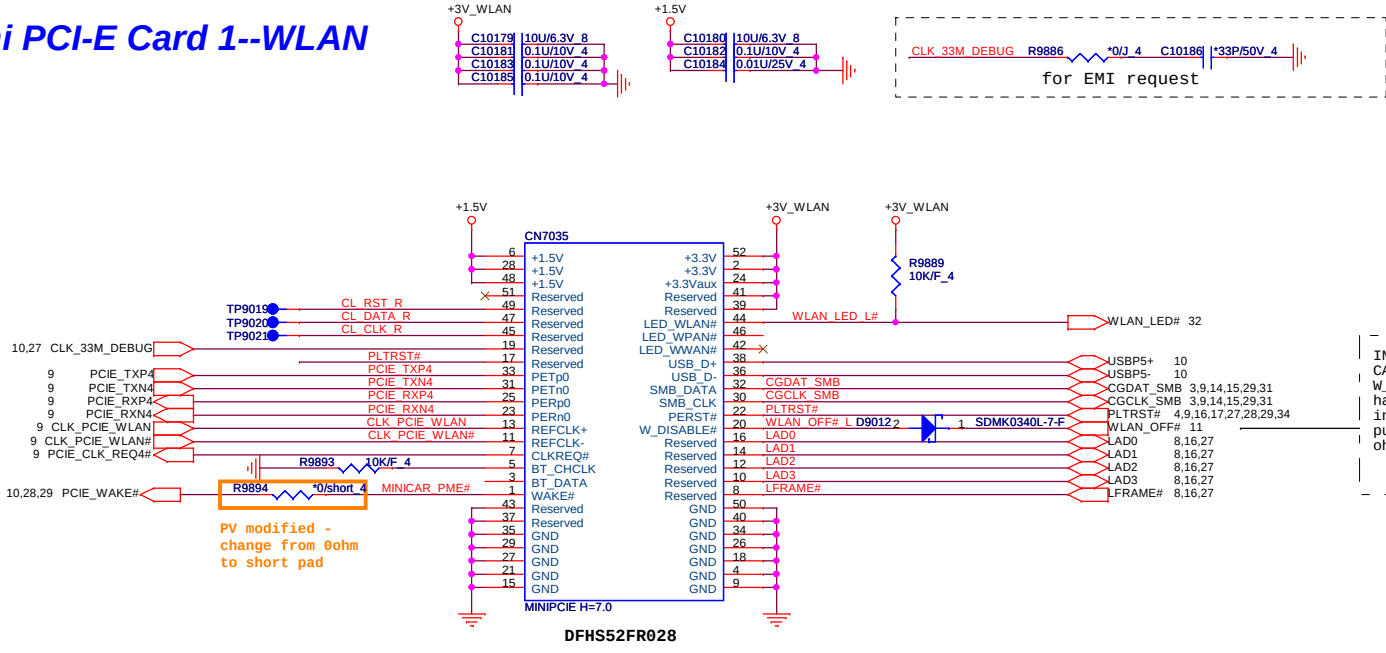
LED



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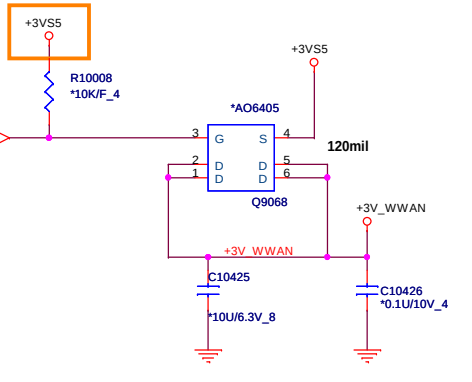
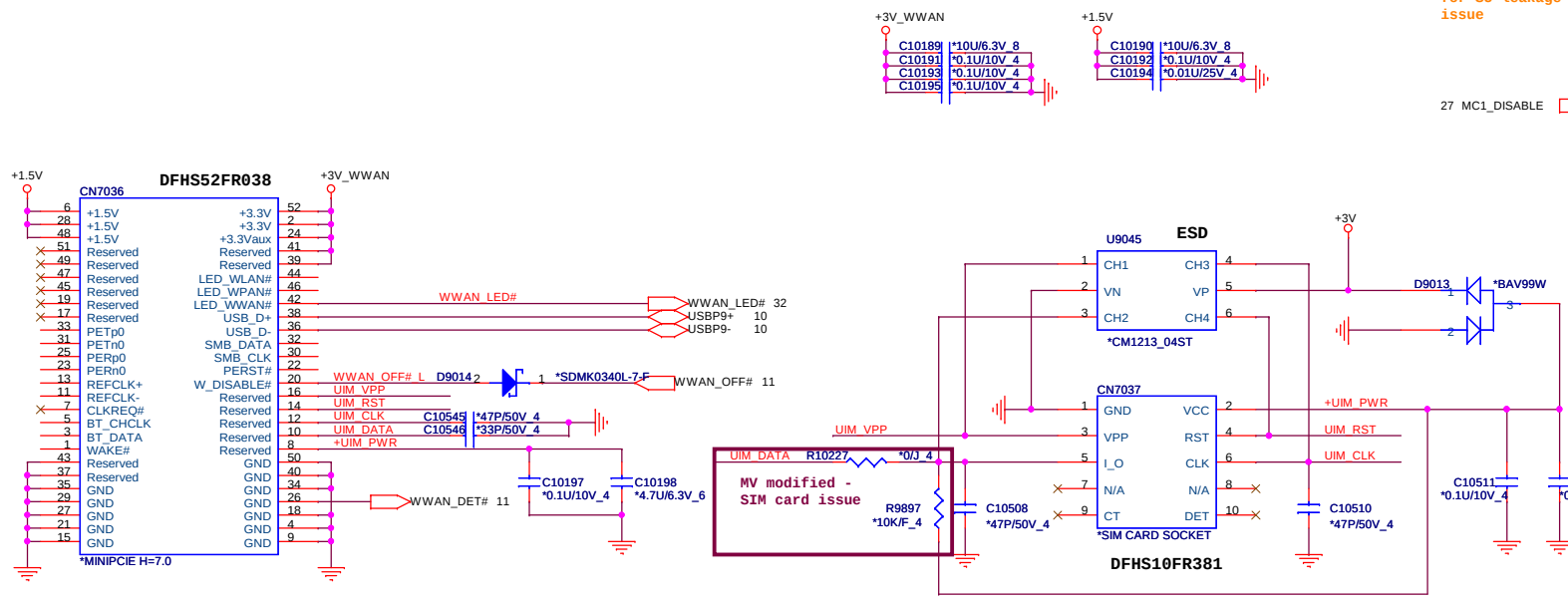
Size	Document Number	Rev
Custom	ESATA/HDD/ODD/LED/FAN	2B
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Mini PCI-E Card 1--WLAN



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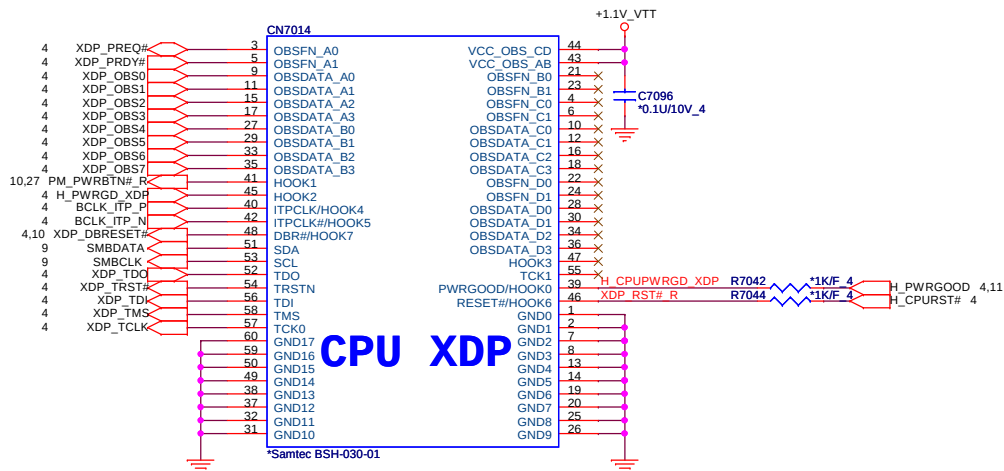
Mini PCI-E Card 2 --WWAN



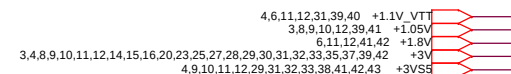
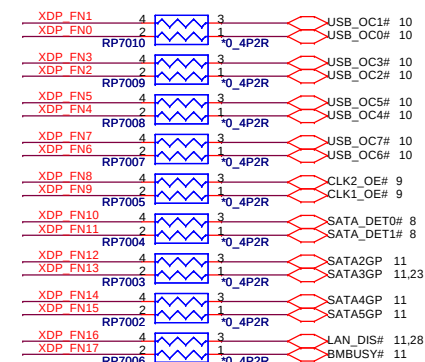
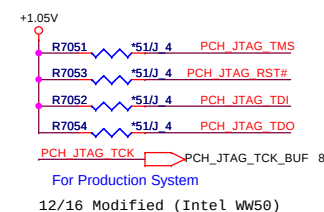
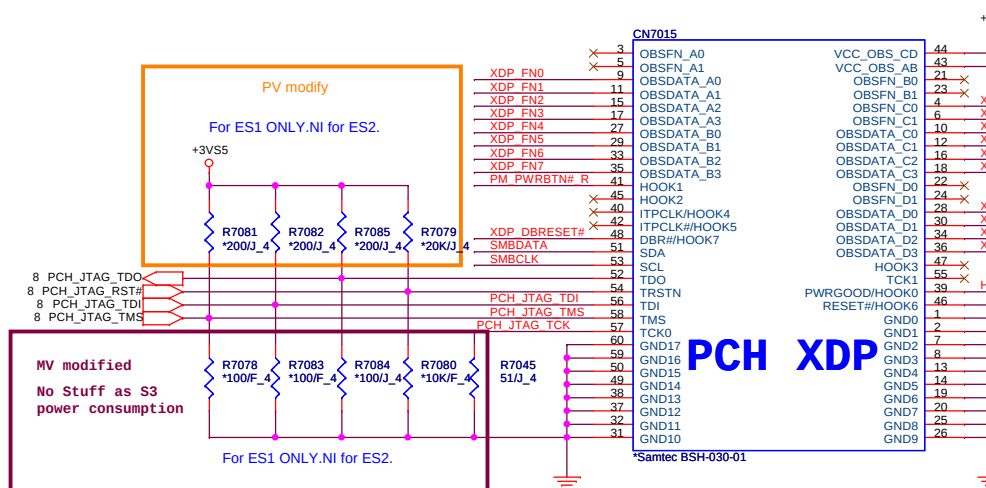
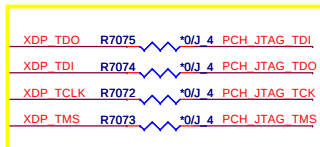
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Size Custom	Document Number	Rev 2B
MINI PCIE CONN X2		
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3,4,8,9,10,11,12,14,15,16,20,23,25,27,28,29,30,31,32,34,35,37,39,42 +1.5V
8,10,23,25,27,31,32,36,37,38 +3V
12,20,23,24,25,31,32,35,42 +5V

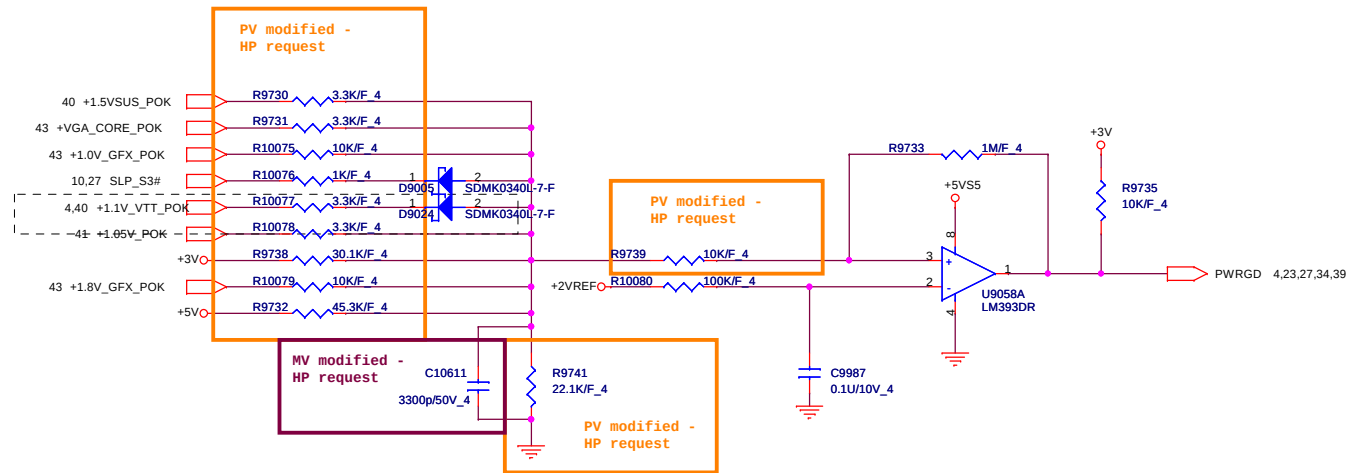


Reserve for BSDL



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Custom	XDP/BRAIDWOOD	2B
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DC-IN Connector

