

S-SERIES 2009

INVICTA (Intel-Discrete)

MV Build (A03) - Final

2009.03.18

DATE	CHANGE NO.	REV

DRAWER	EE	DATE	POWER	DATE	INVENTEC TITLE <i>INVICTA Cycle1</i>
DESIGN					
CHECK					
RESPONSIBLE					
SIZE =	3		VER :		SIZE
FILE NAME :	XXXX-XXXXXX-XX				A3 CS
P/N	XXXXXXXXXXXX				DOC. NUMBER
					1310A22527-0-MTR
					REV
					A03
					SHEET
					1 OF 54

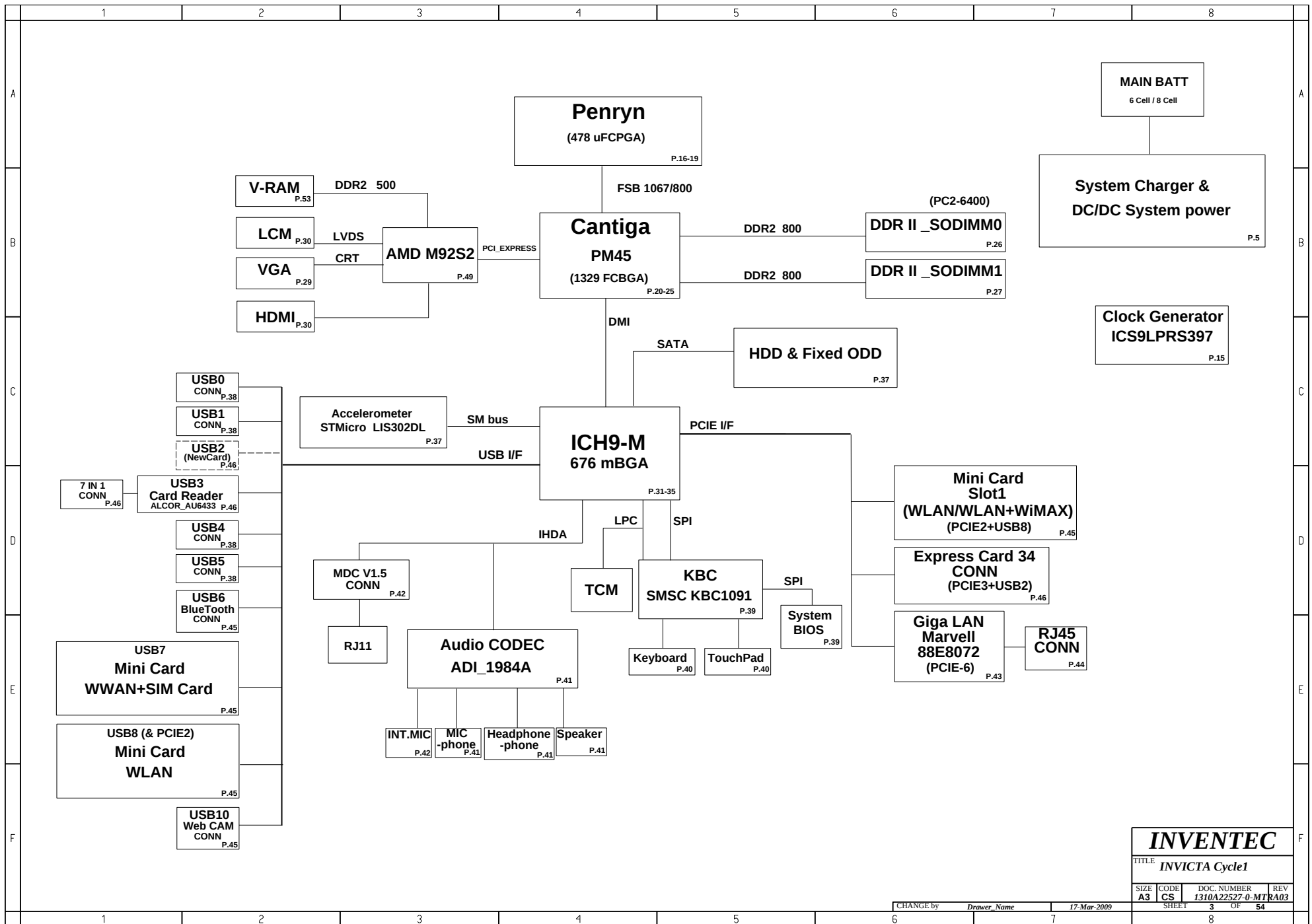
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5- DC & Battery Charger & OCP.	15- Clock Generator.	36- Power, LID, APP SW/B.
6- Select & Battery CONN.	16- CPU Penryn-1.	37- HDD & ODD,Accelerometer,15" ODD EXT/B.
7- System Power (+V3A/+V5A).	17- CPU Penryn-2.	38- USB CONN.
8- System Power (+V1.8/+VCCP) .	18- CPU Penryn-3.	39- KBC, BIOS ROM, Debug Port.
9- Graphic Power (+VDD_CORE & +VPCIE).	19- XDP & Thermal & Fan.	40- Internal Keyboard & TouchPad CONN.
10- System Power (+V1.5S) .	20- N/B Cantiga-1.	41- Audio CODEC & Earphone & Speaker.
11- CPU Power (+VCC_CORE).	21- N/B Cantiga-2.	42- Audio MIC & MDC CONN.
12- DDR Termination Voltage(+V0.9S).	22- N/B Cantiga-3.	43- Giga LAN controller.
13- Power (Sleep: +V5S/+V3S/+V1.8S).	23- N/B Cantiga-4.	44- Magnetic & RJ45 CONN.
14- Power (Sequence).	24- N/B Cantiga-5.	45- Mini Card(WLAN/WWAN), BT.
	25- N/B Cantiga-6.	46- New Card & 4 in 1 Card (SD/MMC/MS/XD).
	26- DDR2-DIMM0.	47- LEDs, TCM CONN.
	27- DDR2-DIMM1.	48- Screws, Crack testing circuit.
	28- DDR2-Termination.	49- AMD M92S2-1.
	29- VGA CONN.	50- AMD M92S2-2.
	30- LCD,WebCam,HDMI CONN.	51- AMD M92S2-3.
	31- S/B ICH9-1.	52- AMD M92S2-4.
	32- S/B ICH9-2.	53- Video RAM-1 (DDR2).
	33- S/B ICH9-3.	54- Video RAM-2 (DDR2).
	34- S/B ICH9-4.	
	35- S/B ICH9-5.	

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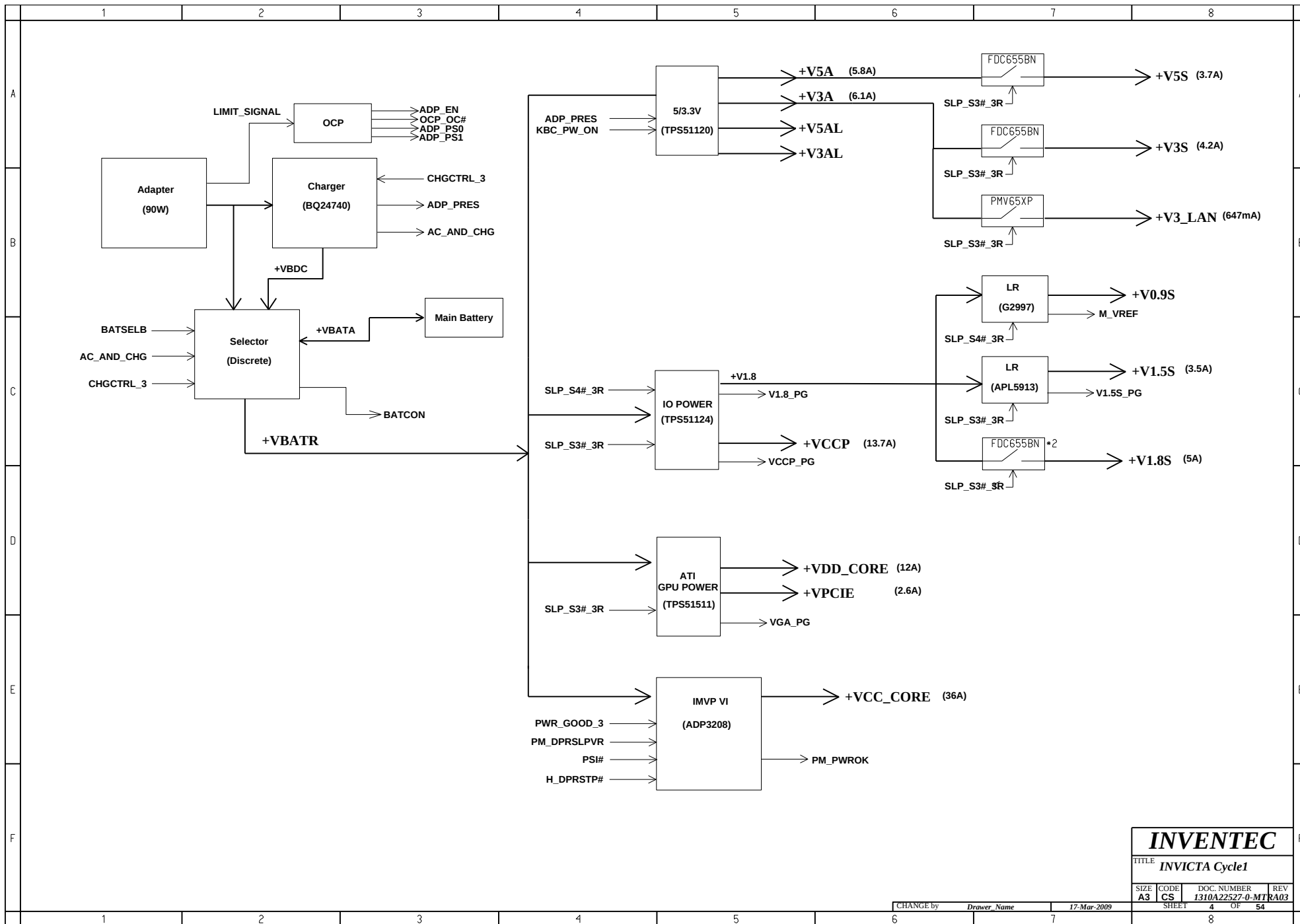
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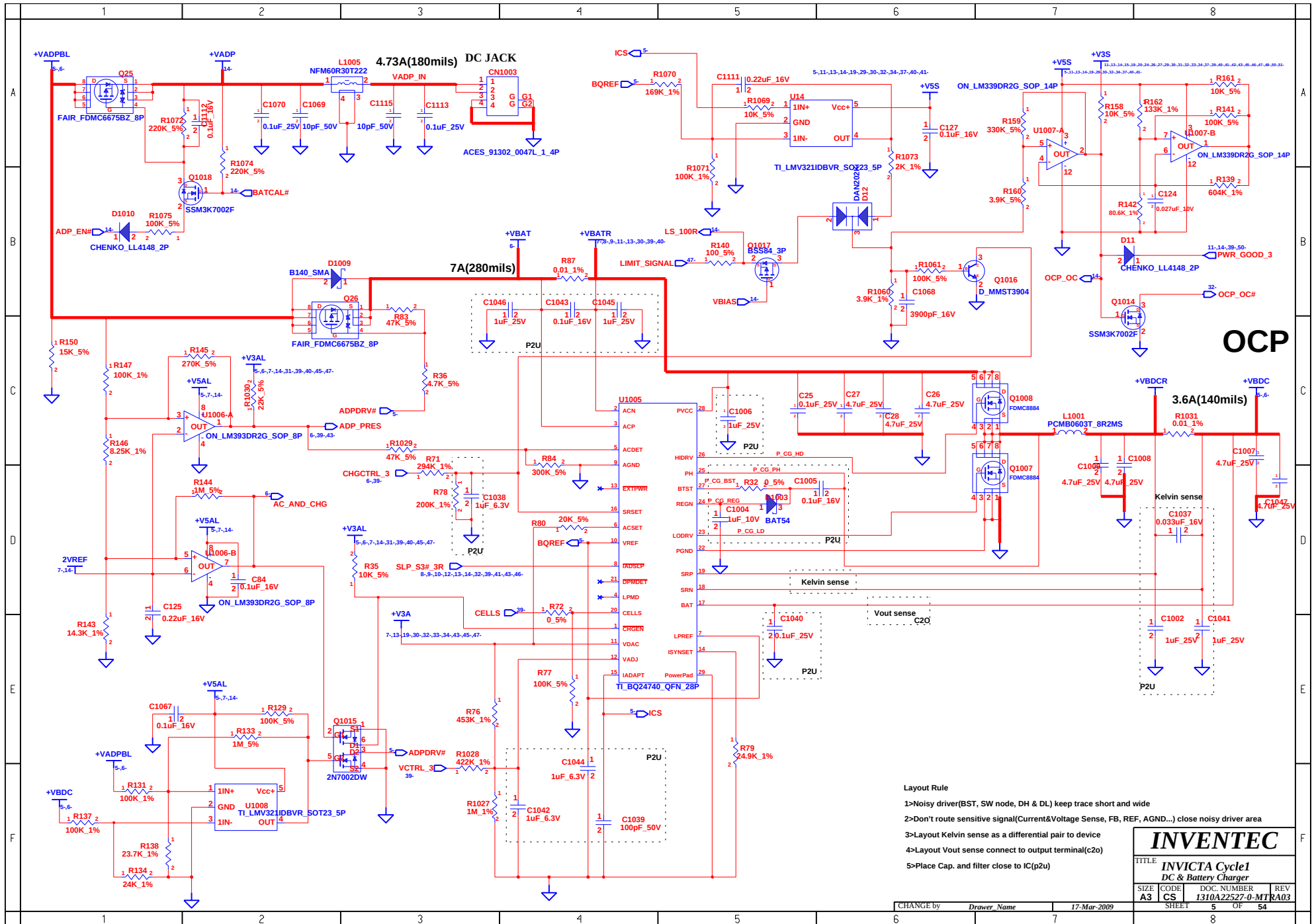
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INVICTA Cycle1

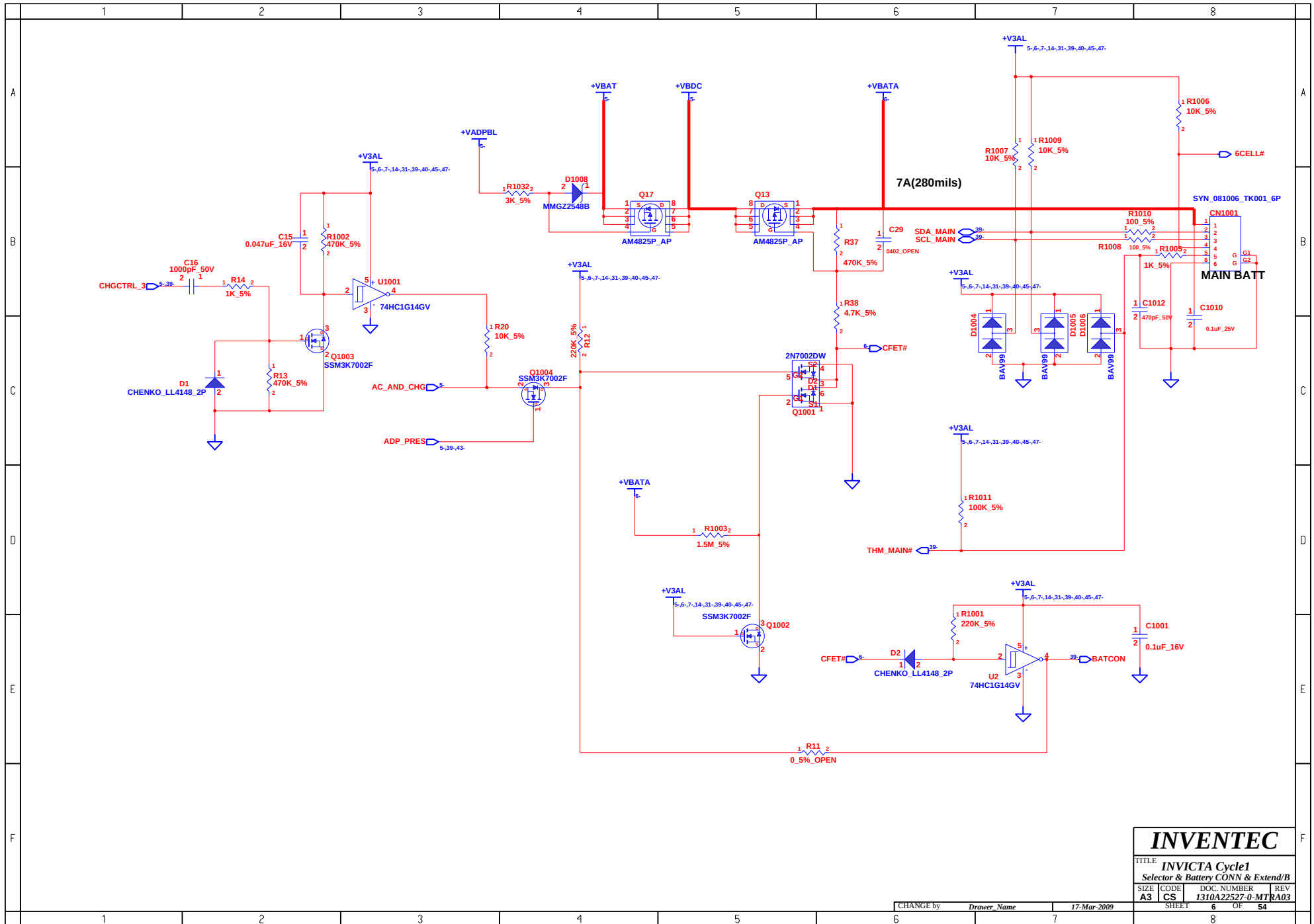
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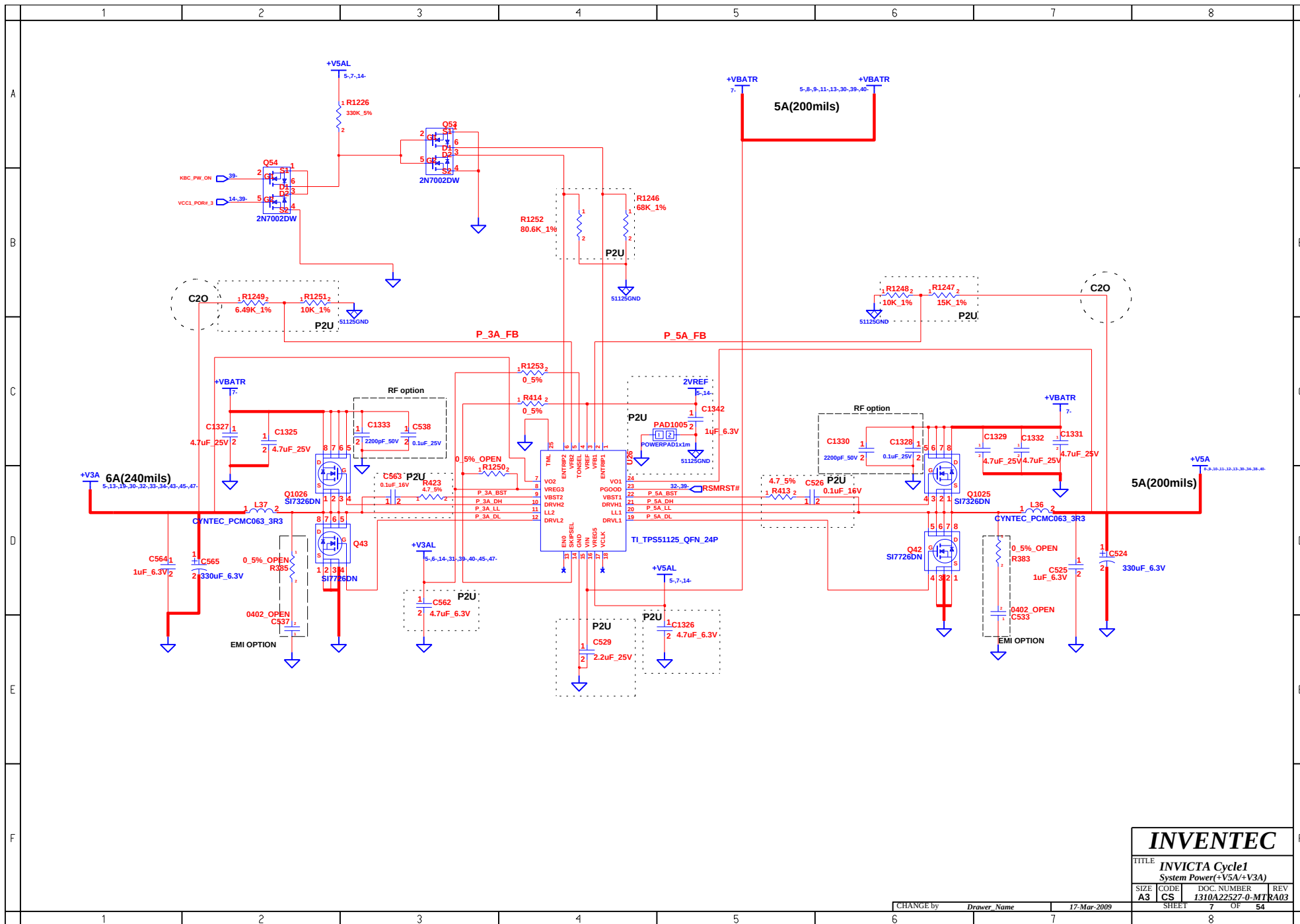
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1 2 3 4 5 6 7 8







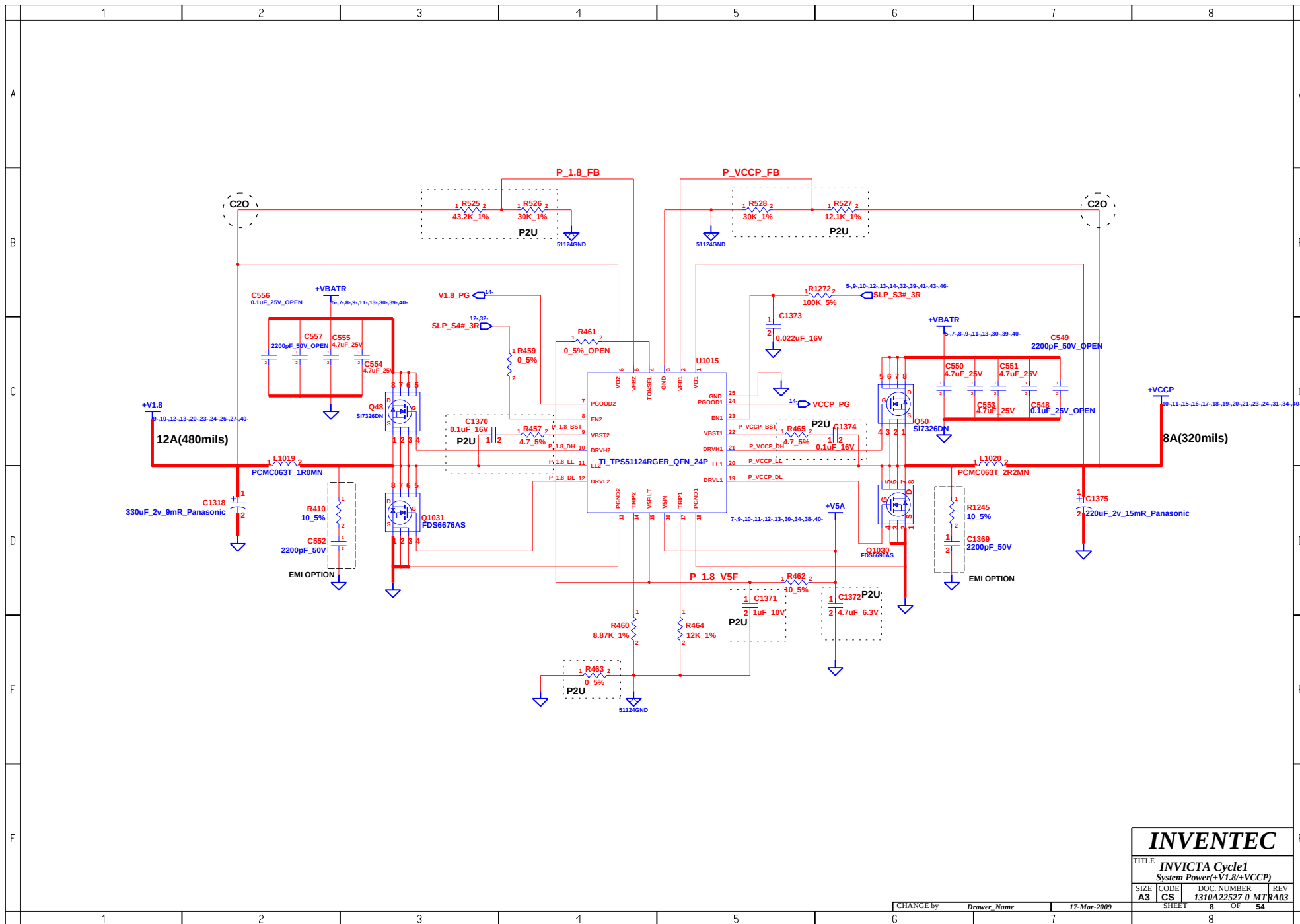


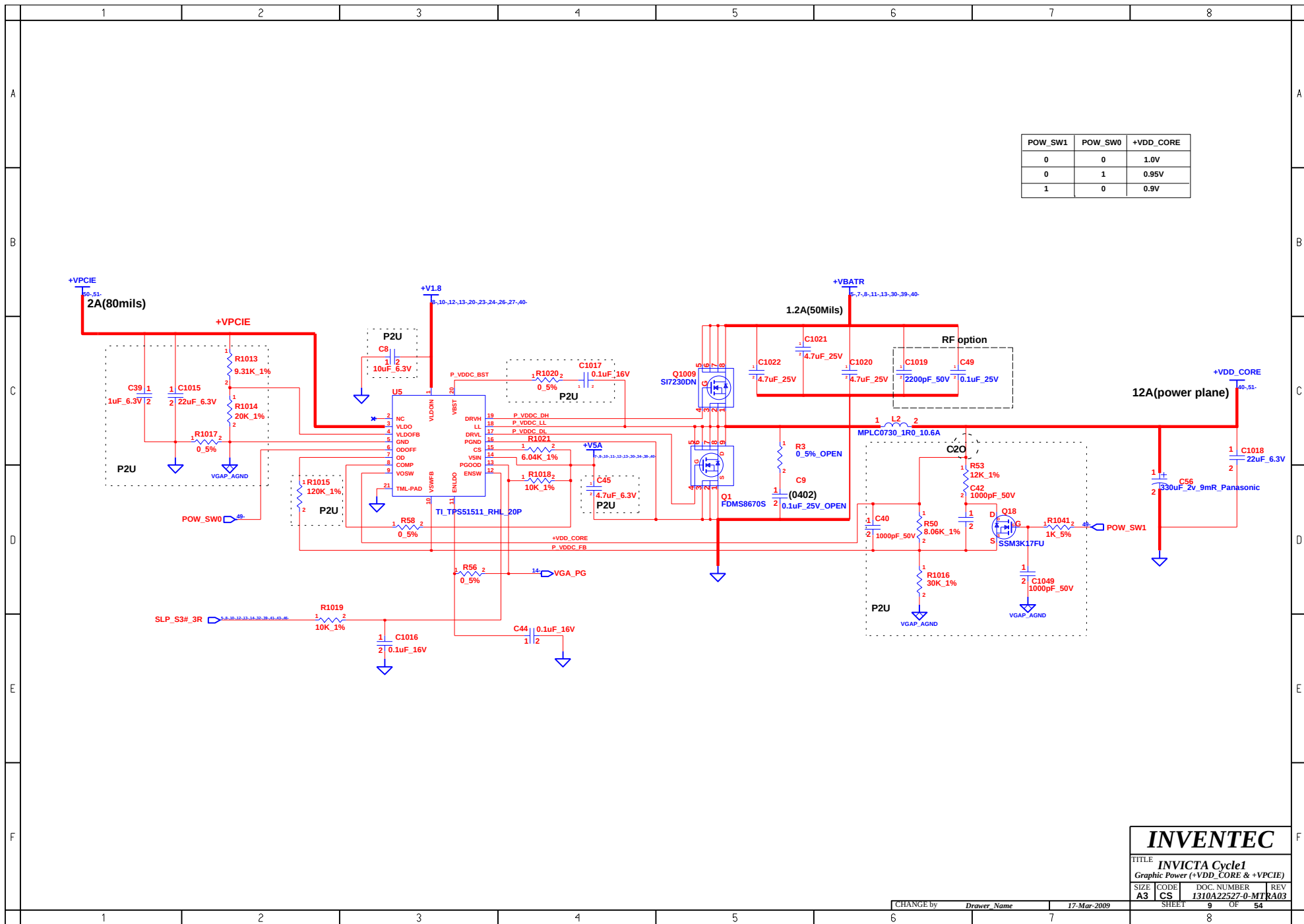
INVENEC

TITLE			
INVICTA Cycle1			
System Power(+V5A/+V3A)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310AZ2527-0-MT	RA03

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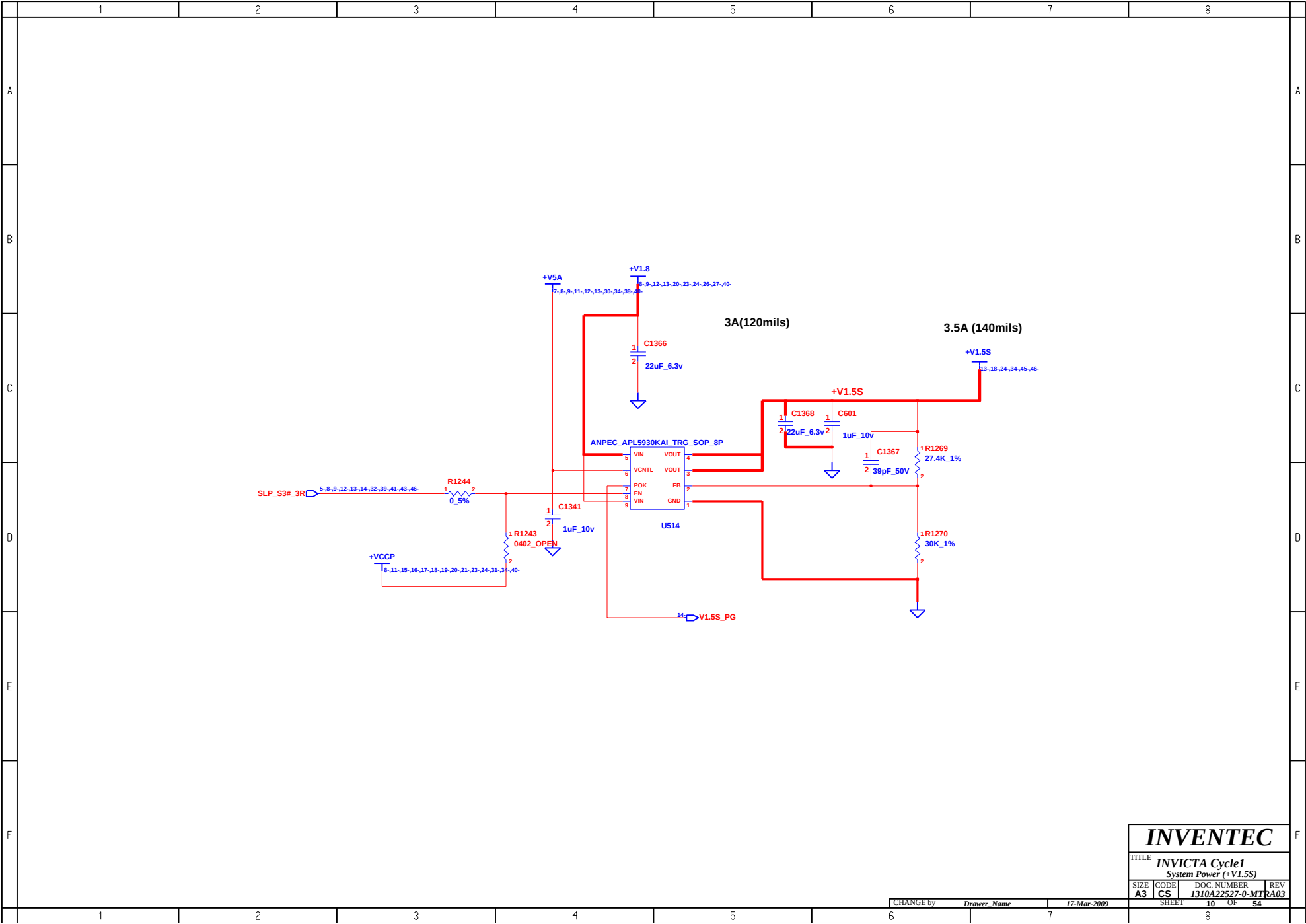
POW_SW1	POW_SW0	+VDD_CORE
0	0	1.0V
0	1	0.95V
1	0	0.9V

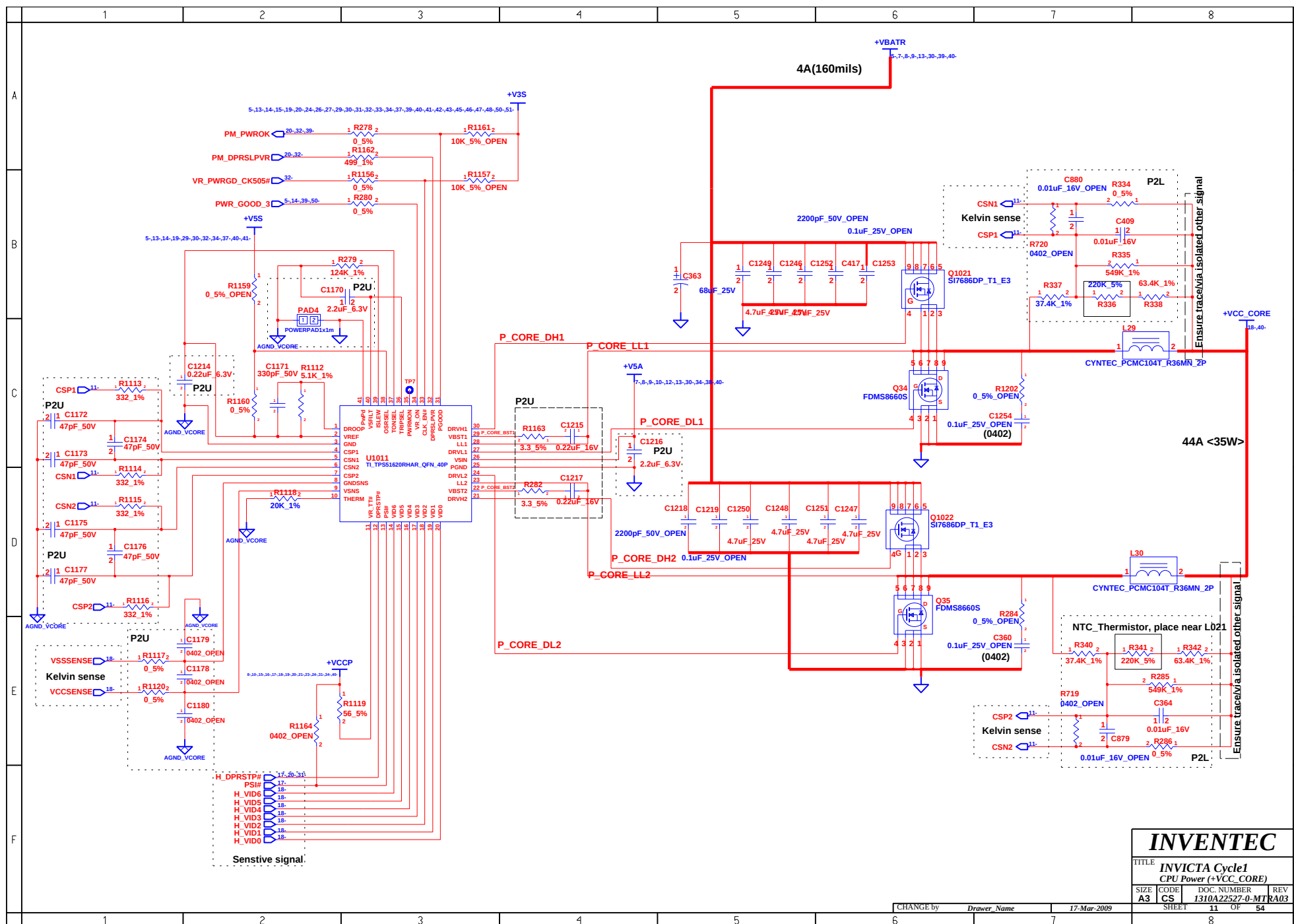
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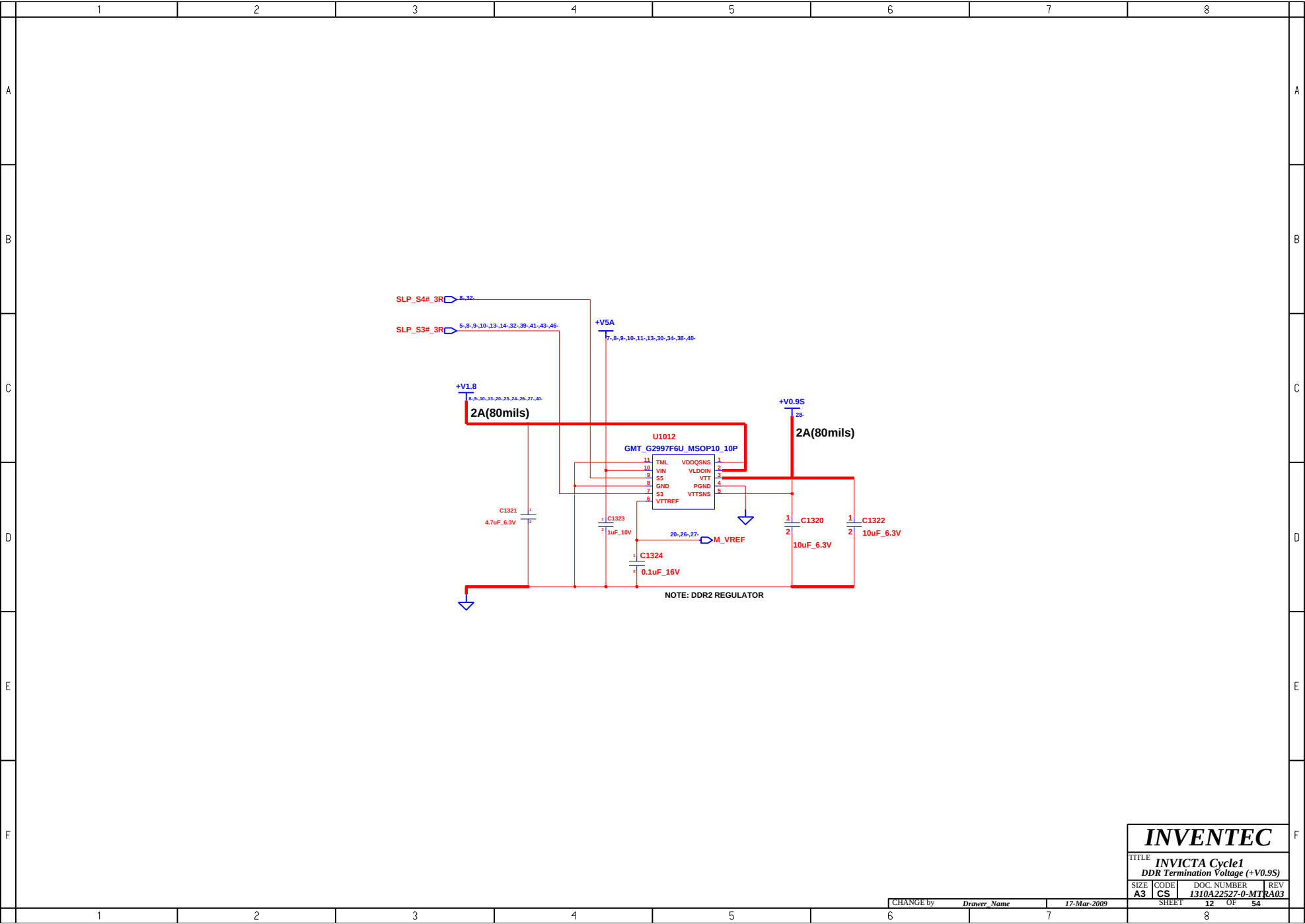
TITLE INVICTA Cycle1
Graphic Power (+VDD_CORE & +VPCIE)

SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A22527-0-MT	RA03

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1	2	3	8







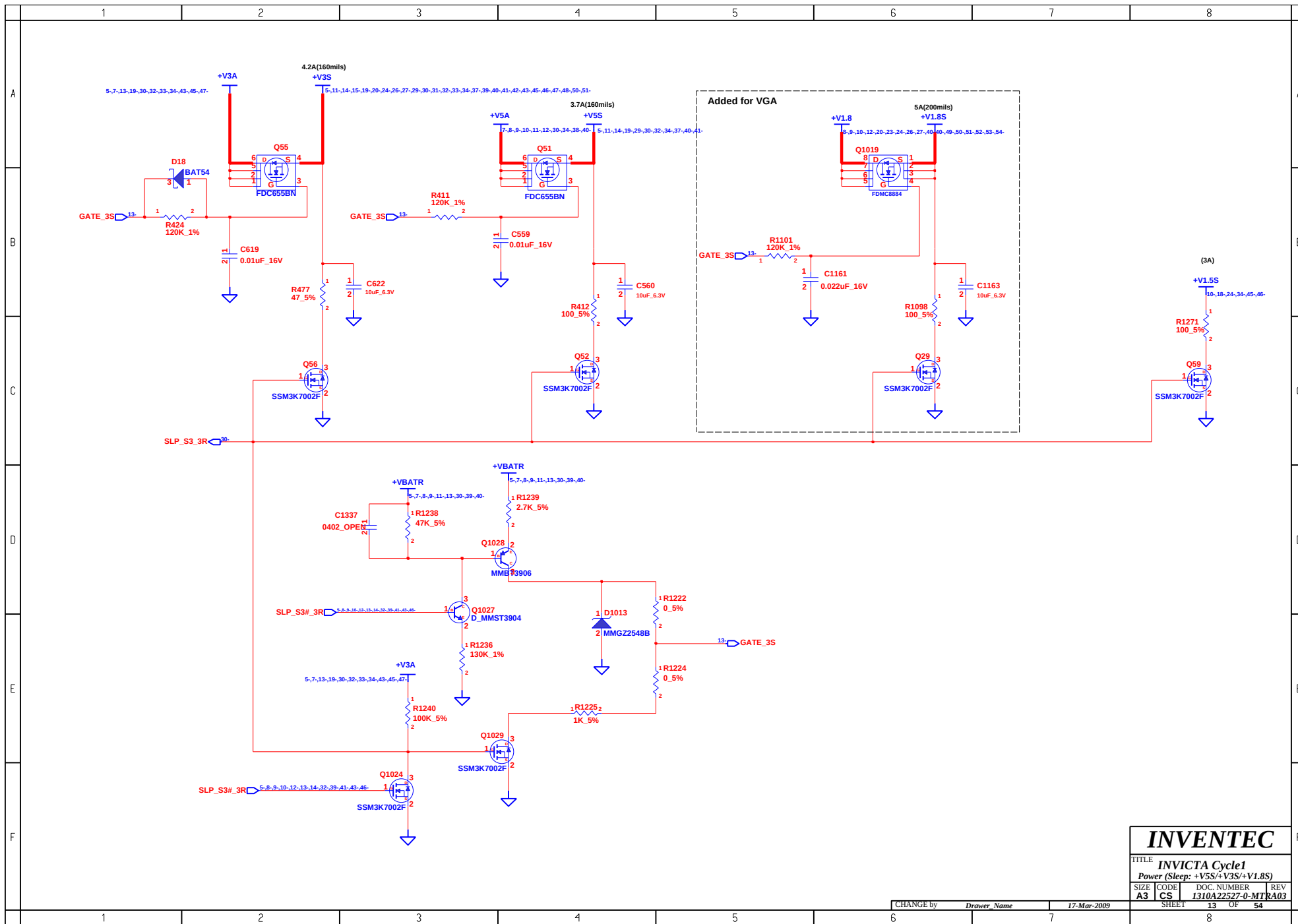
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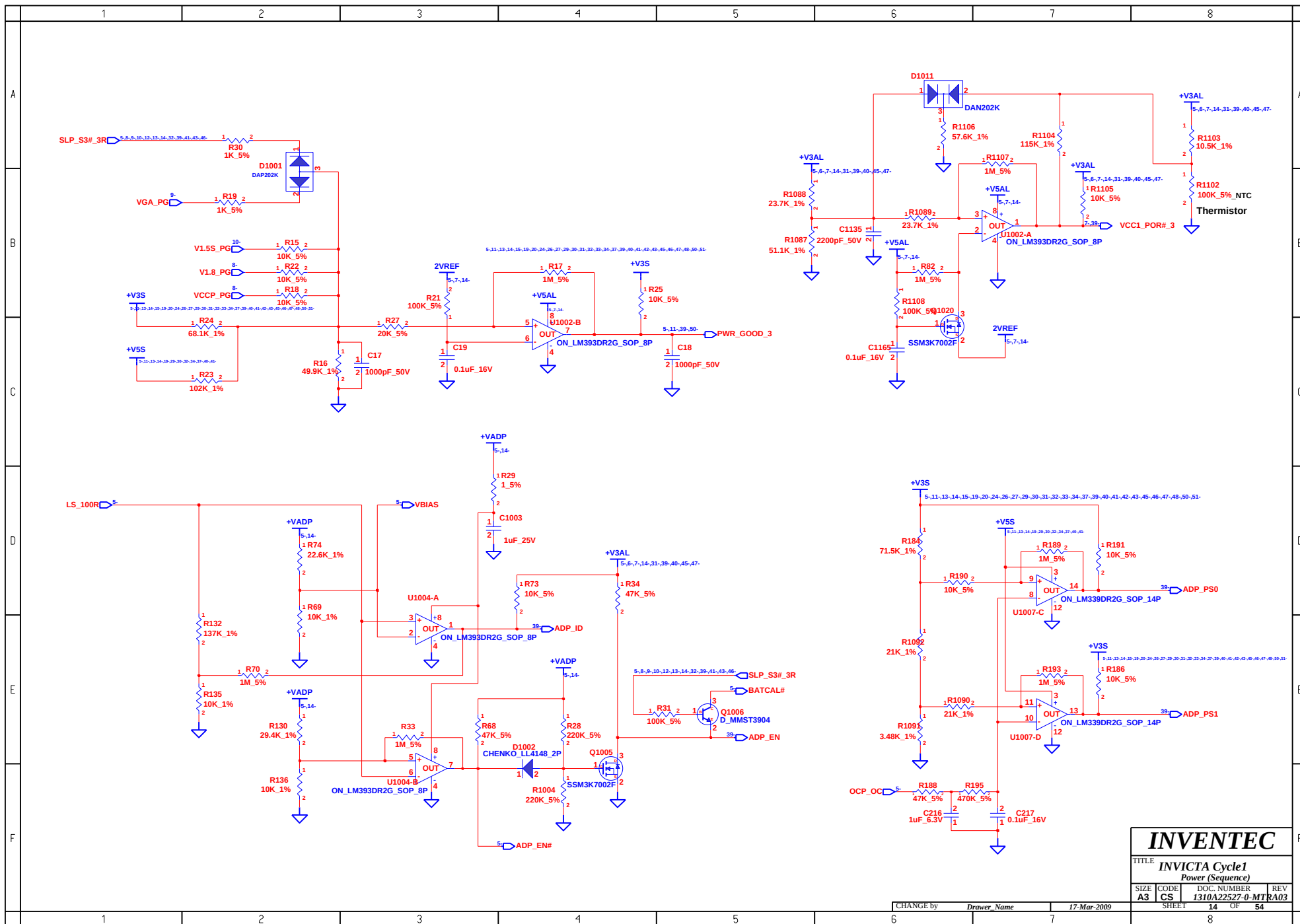
TITLE **INVICTA Cycle1**
DDR Termination Voltage (+V0.9S)

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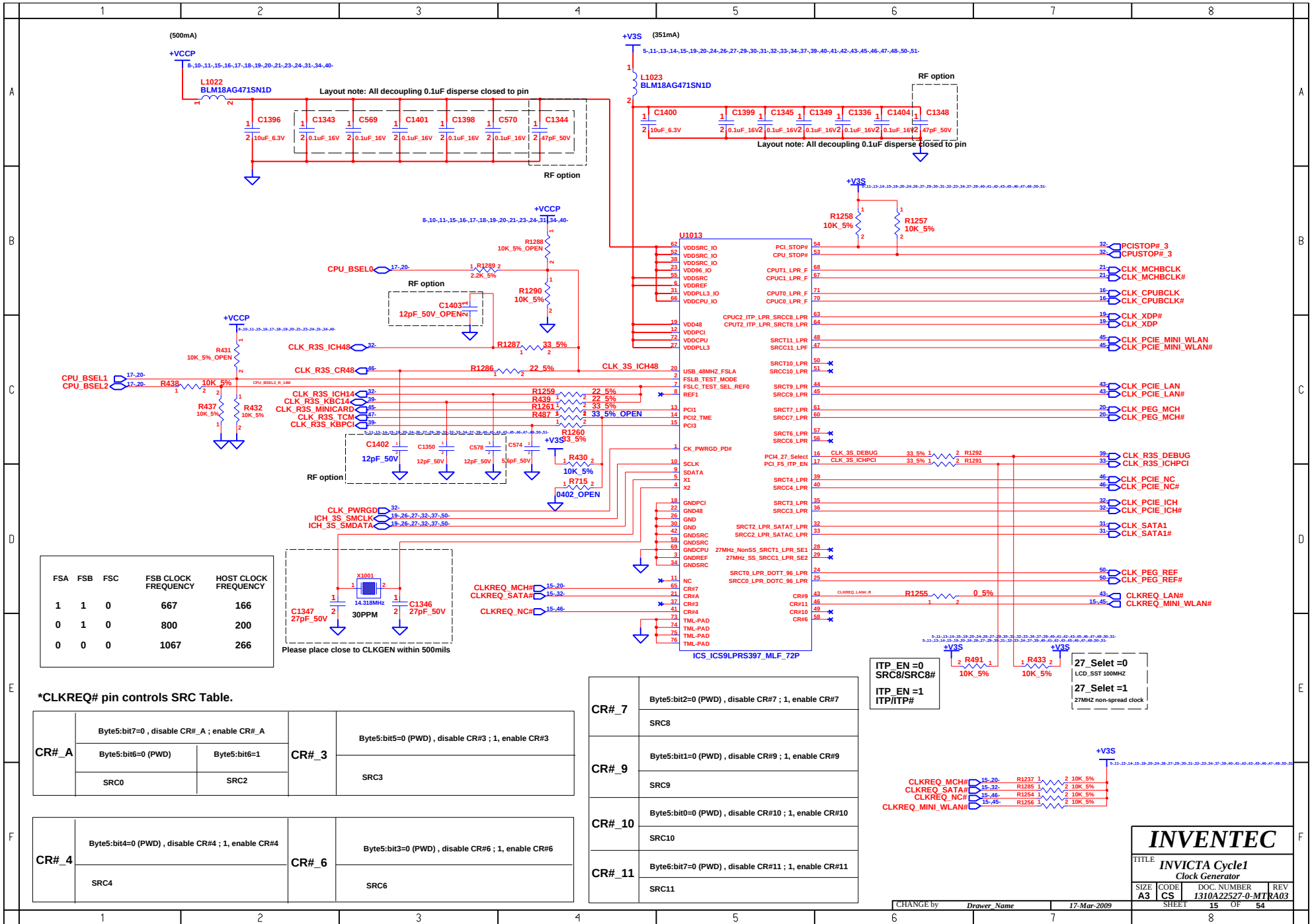
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TITLE
INVICTA Cycle1
Power (Sequence)

SIZE CODE DOC NUMBER REV
A3 CS 1310A22527-0-MTR A03

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FSA	FSB	FSC	FSB CLOCK FREQUENCY	HOST CLOCK FREQUENCY
1	1	0	667	166
0	1	0	800	200
0	0	0	1067	266

*CLKREQ# pin controls SRC Table.

CR#_A	Byte5:bit7=0 (PWD), disable CR#_A; enable CR#_A		CR#_3	Byte5:bit5=0 (PWD), disable CR#3; 1, enable CR#3	
	Byte5:bit6=0 (PWD)	Byte5:bit6=1		SRC3	
	SRC0	SRC2			

CR#_4	Byte5:bit4=0 (PWD) , disable CR#4 ; 1, enable CR#4	CR#_6	Byte5:bit3=0 (PWD) , disable CR#6 ; 1, enable CR#6
	SRC4		SRC6

CR#_7	Byte5:bit2=0 (PWD), disable CR#7; 1, enable CR#7	SRC8
CR#_9	Byte5:bit1=0 (PWD), disable CR#9; 1, enable CR#9	SRC9
CR#_10	Byte5:bit0=0 (PWD), disable CR#10; 1, enable CR#10	SRC10
CR#_11	Byte6:bit7=0 (PWD), disable CR#11; 1, enable CR#11	SRC11

ITP_EN=0
SRC8/SRC8#
ITP_EN=1
ITP/ITP#

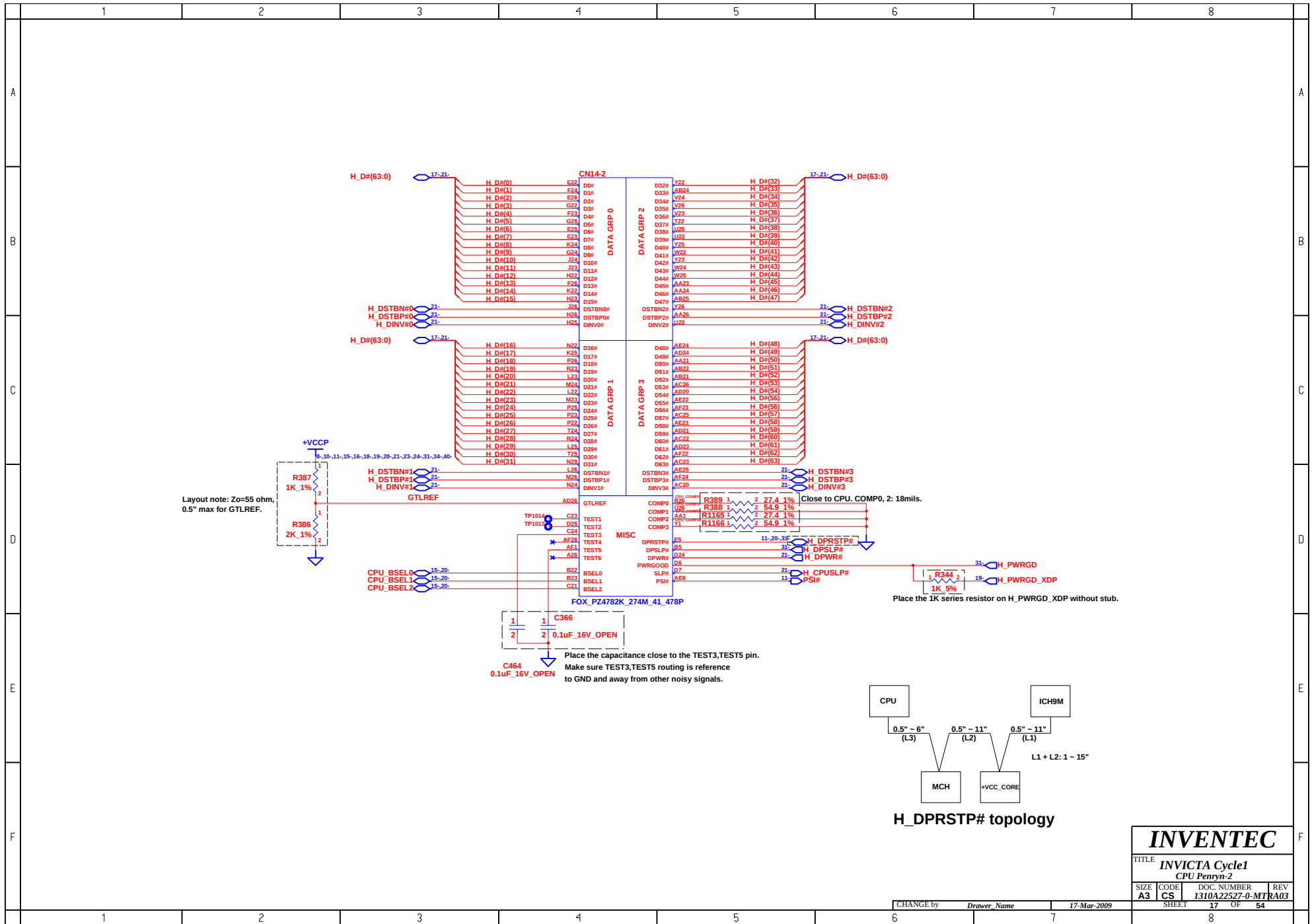
27_Select=0
LCD_SST 100MHz
27_Select=1
27MHz non-spread clock

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TITLE
INVICTA Cycle1
Clock Generator

SIZE CODE DOC. NUMBER REV
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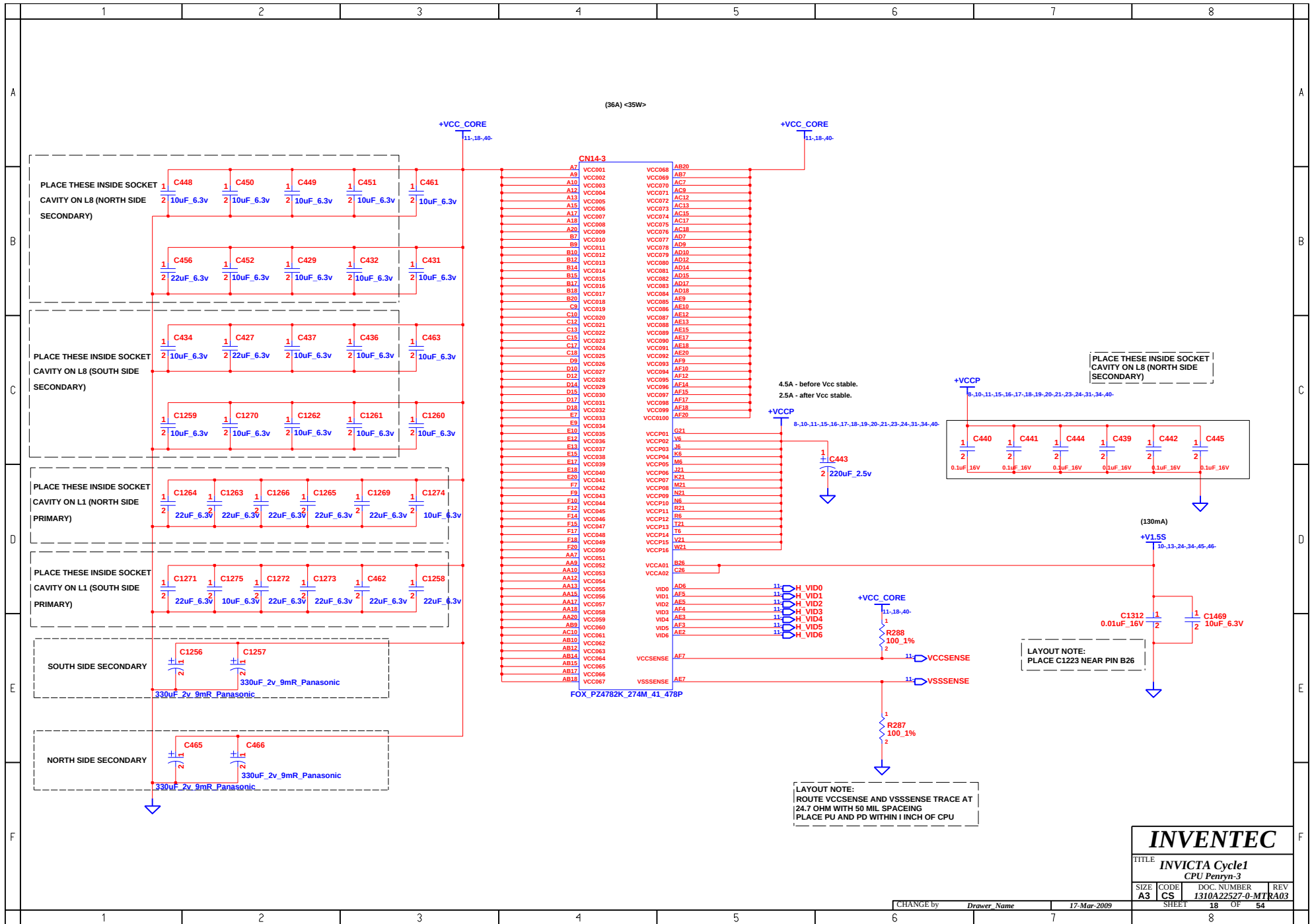
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TITLE
INVICTA Cycle1
CPU Penryn-2

SIZE CODE DOC. NUMBER REV
A3 CS 1310A22527-0-MT A03

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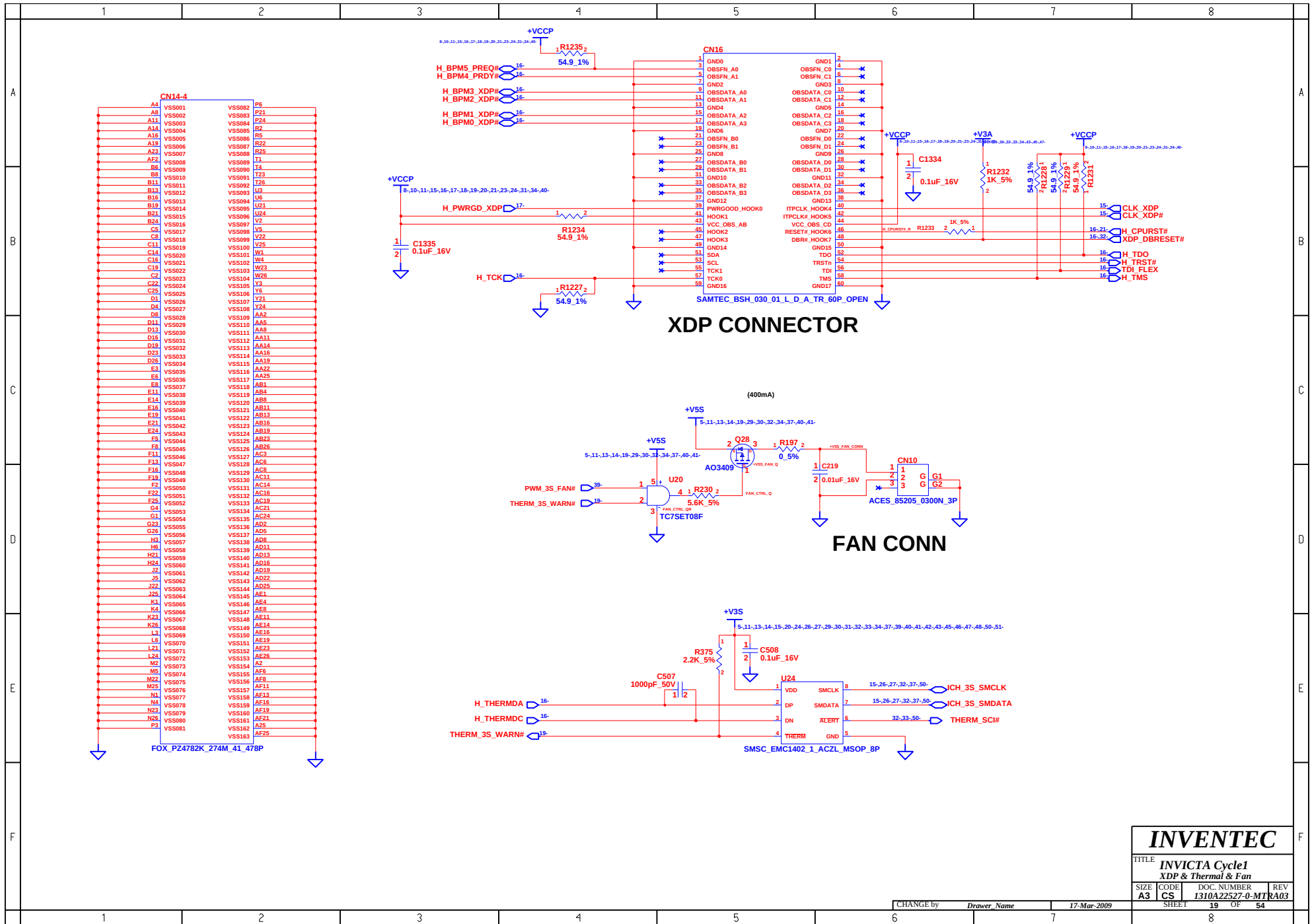


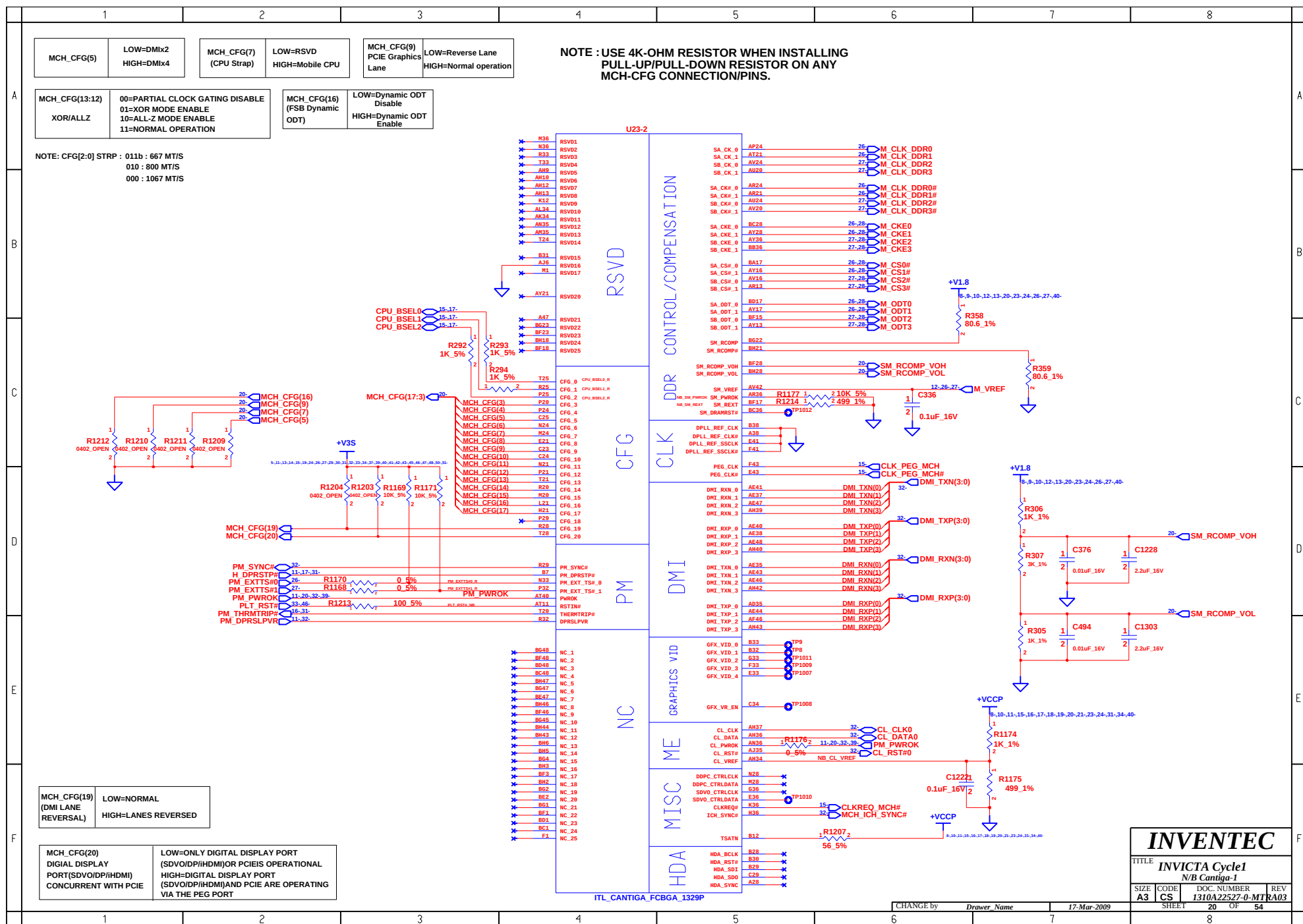
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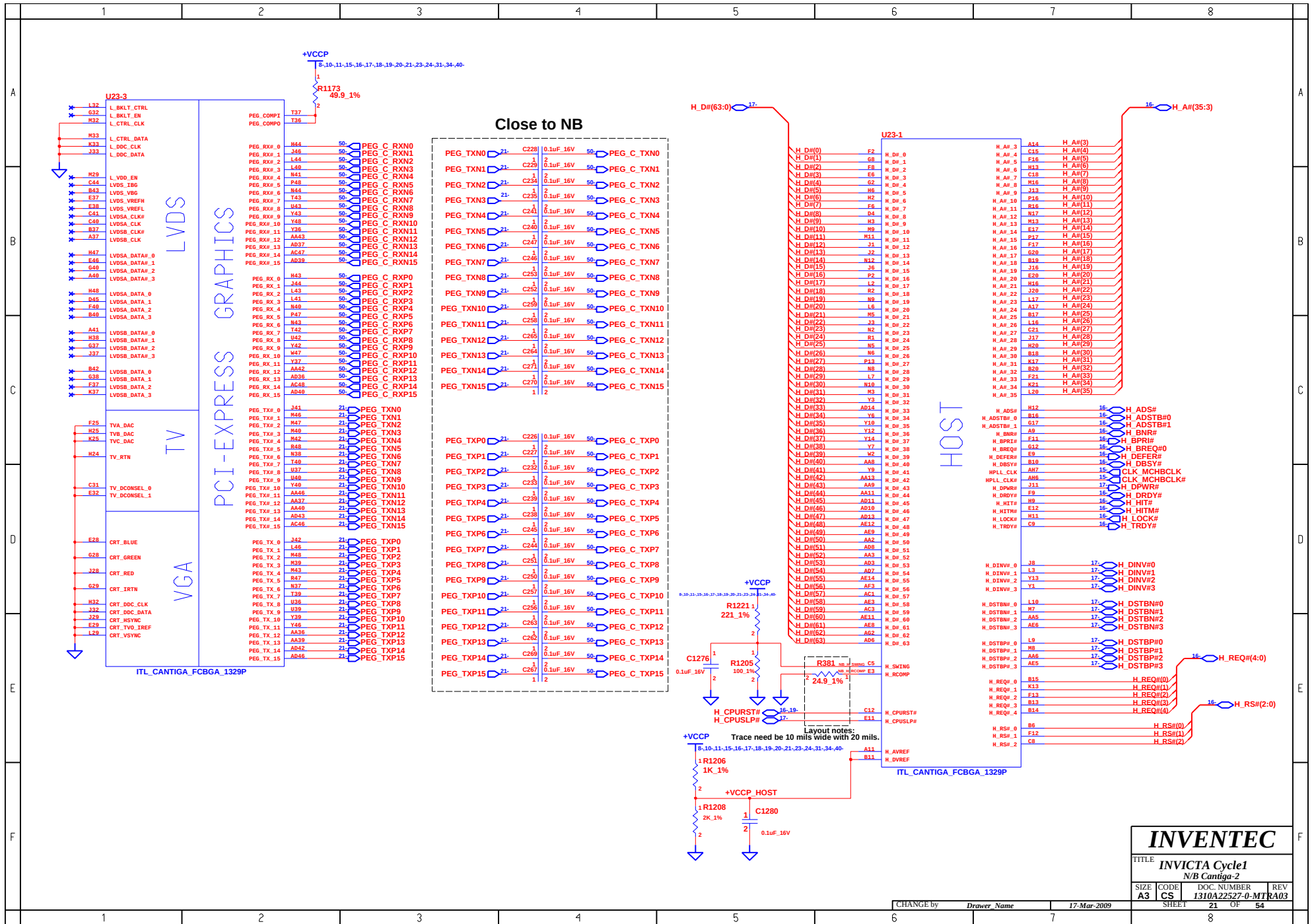
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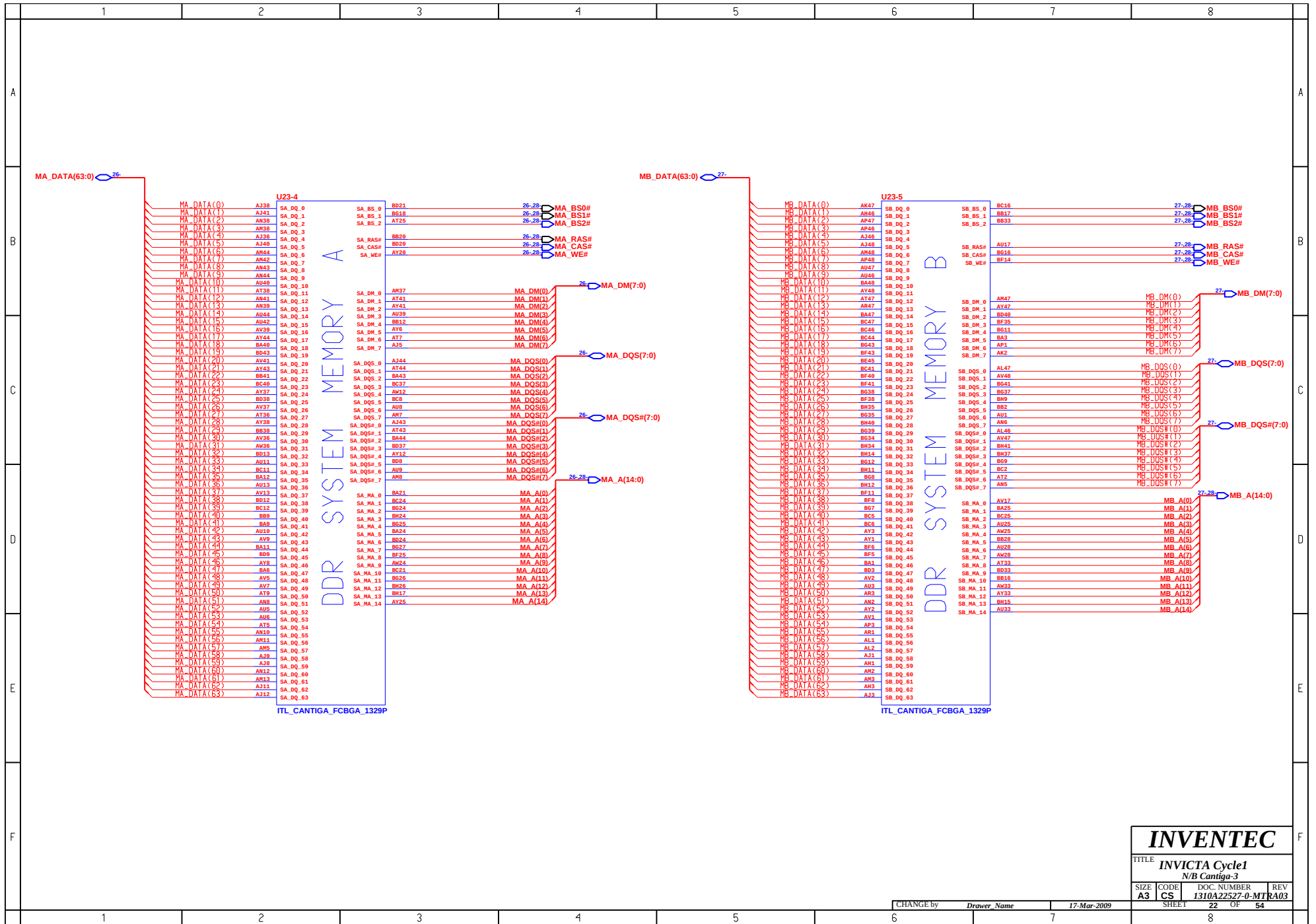
CHANGE by	Drawer Name	17-Mar-2009
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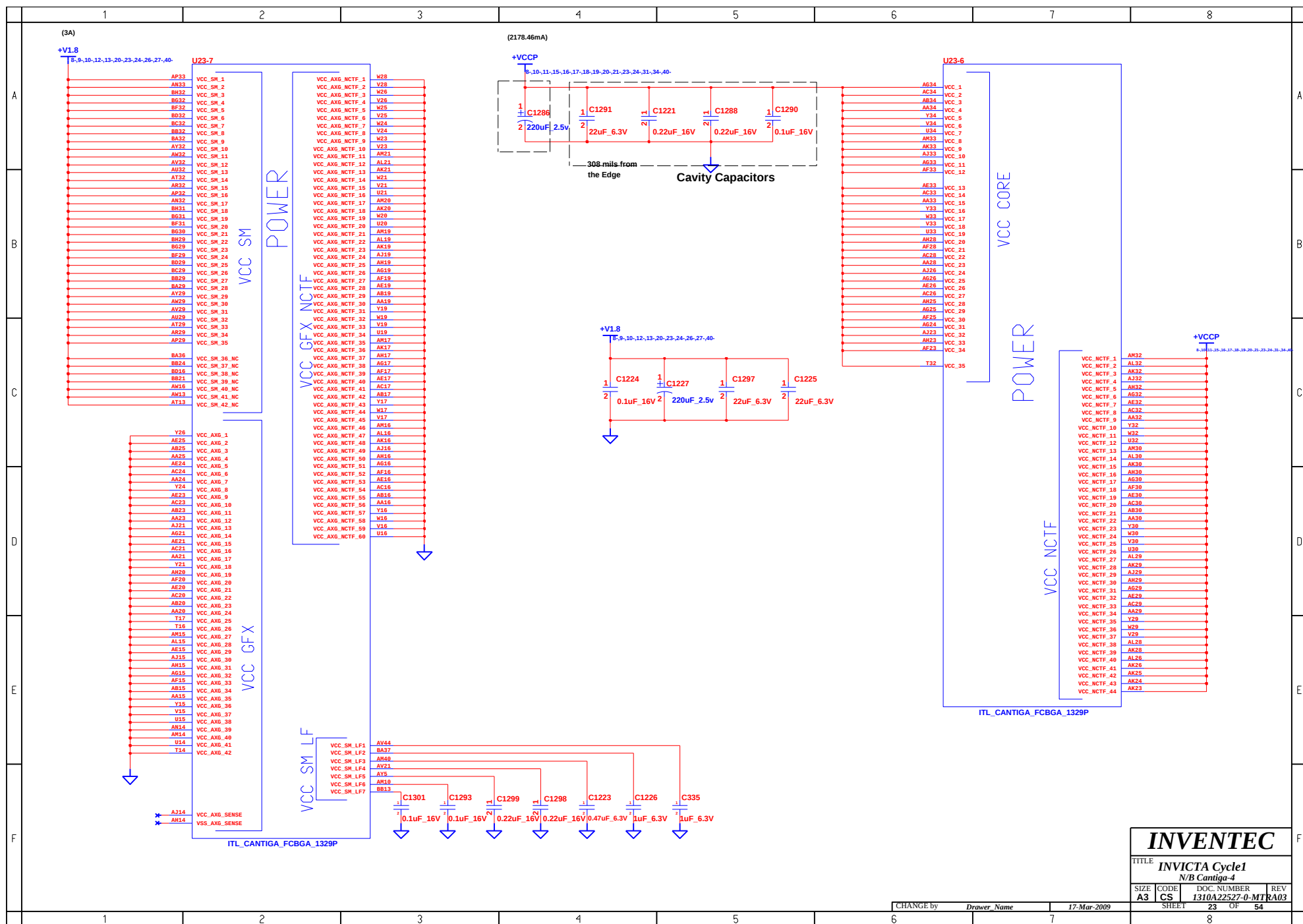


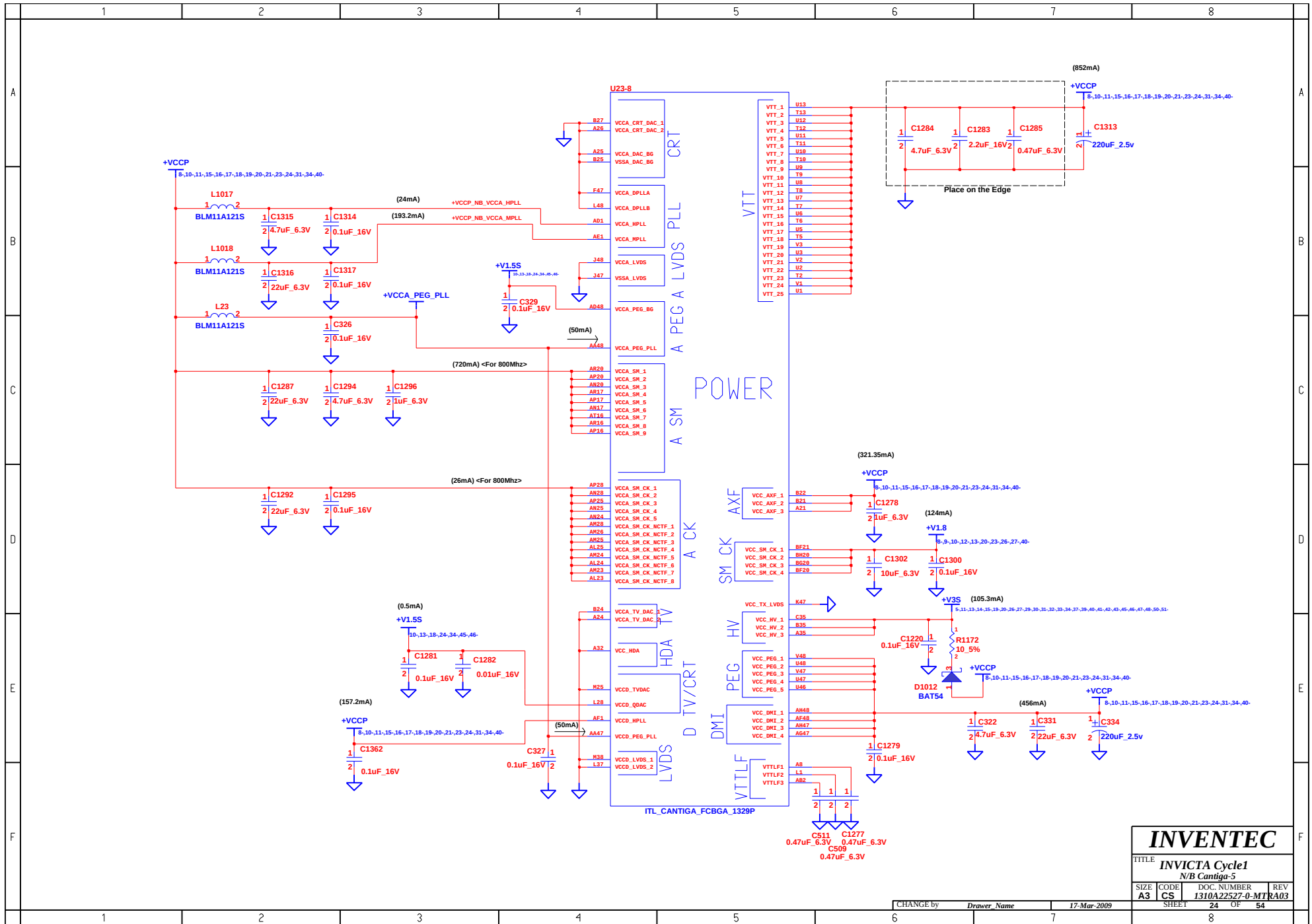
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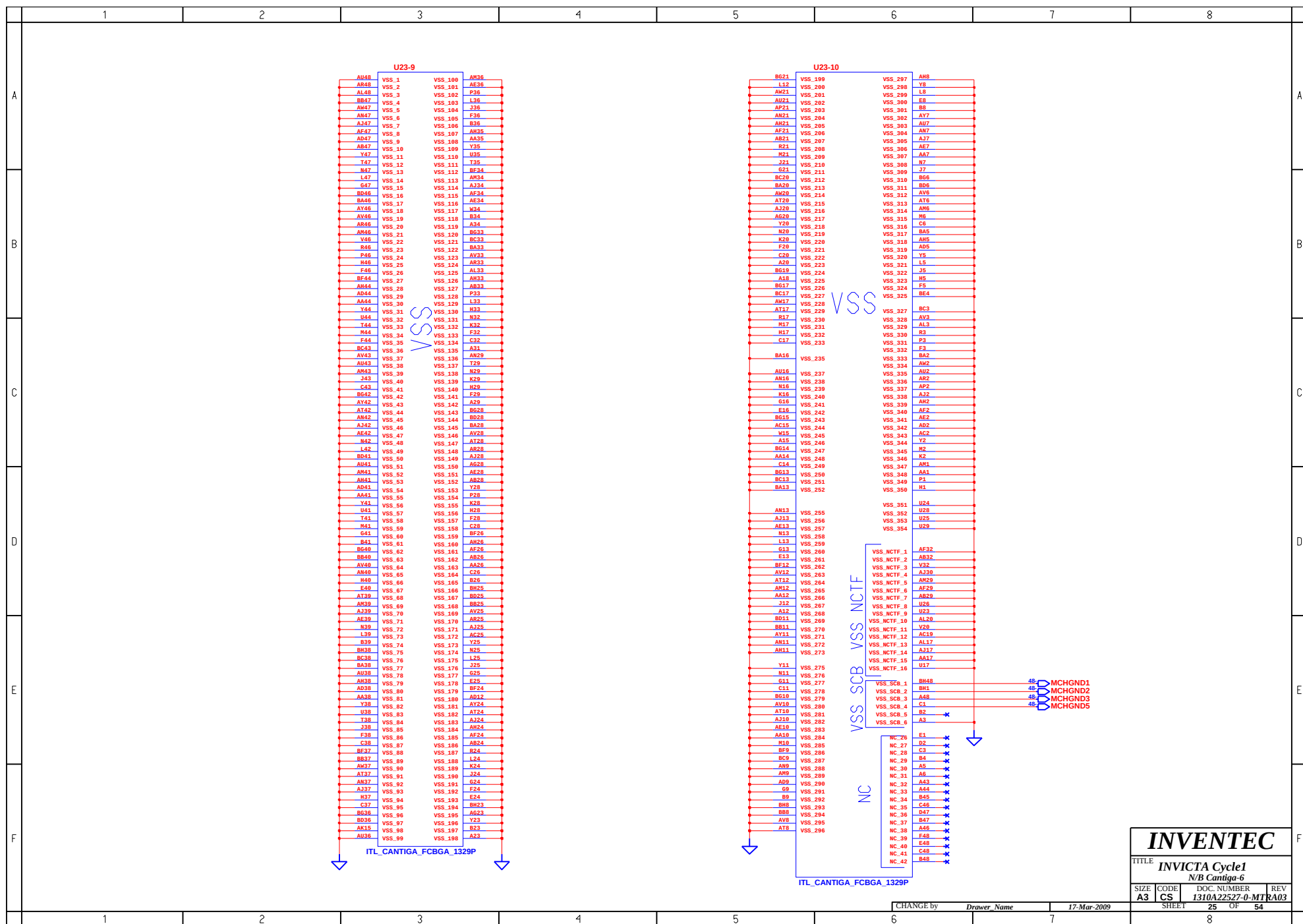
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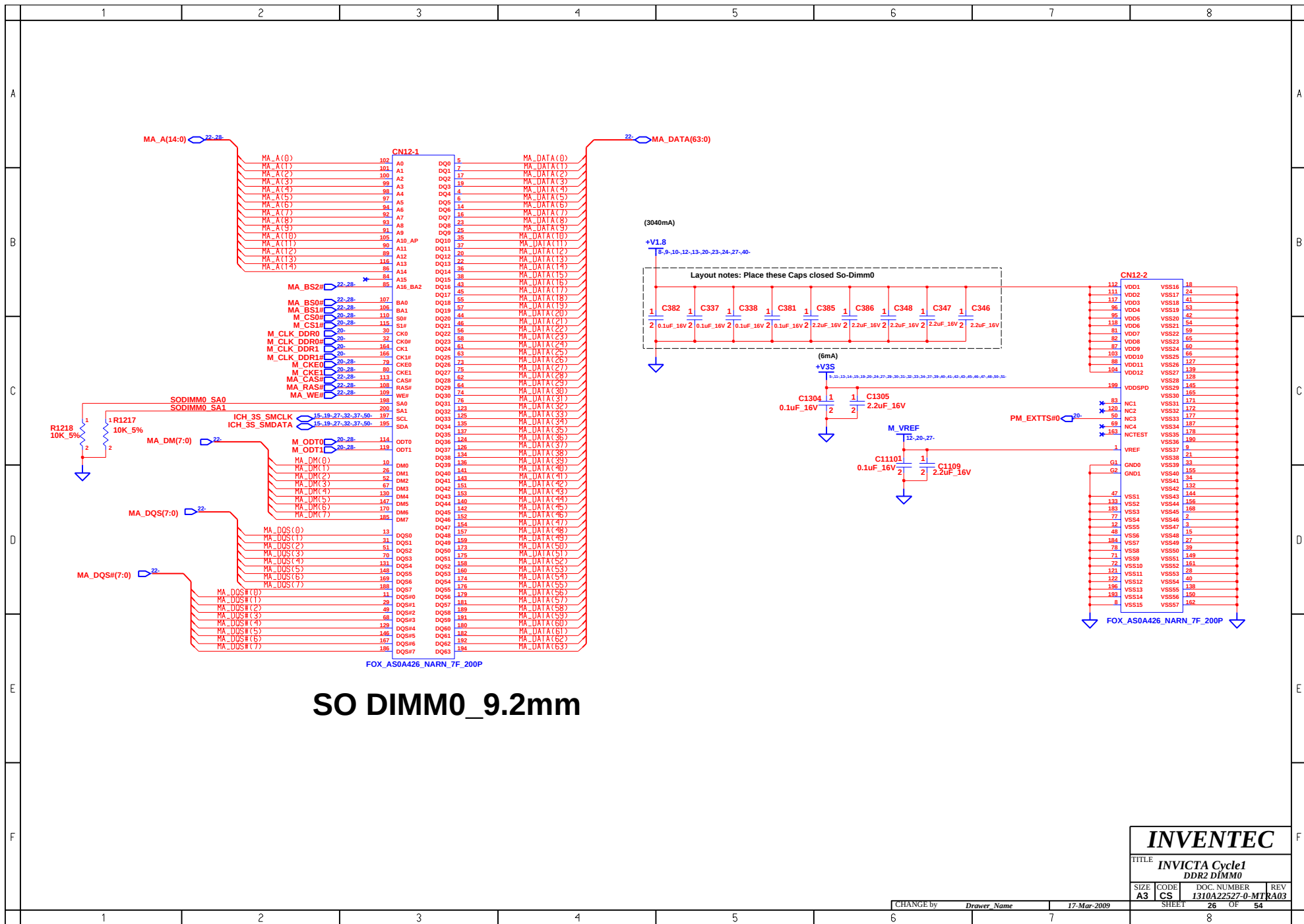




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TITLE INVICTA Cycle1 N/B Cantiga-5			
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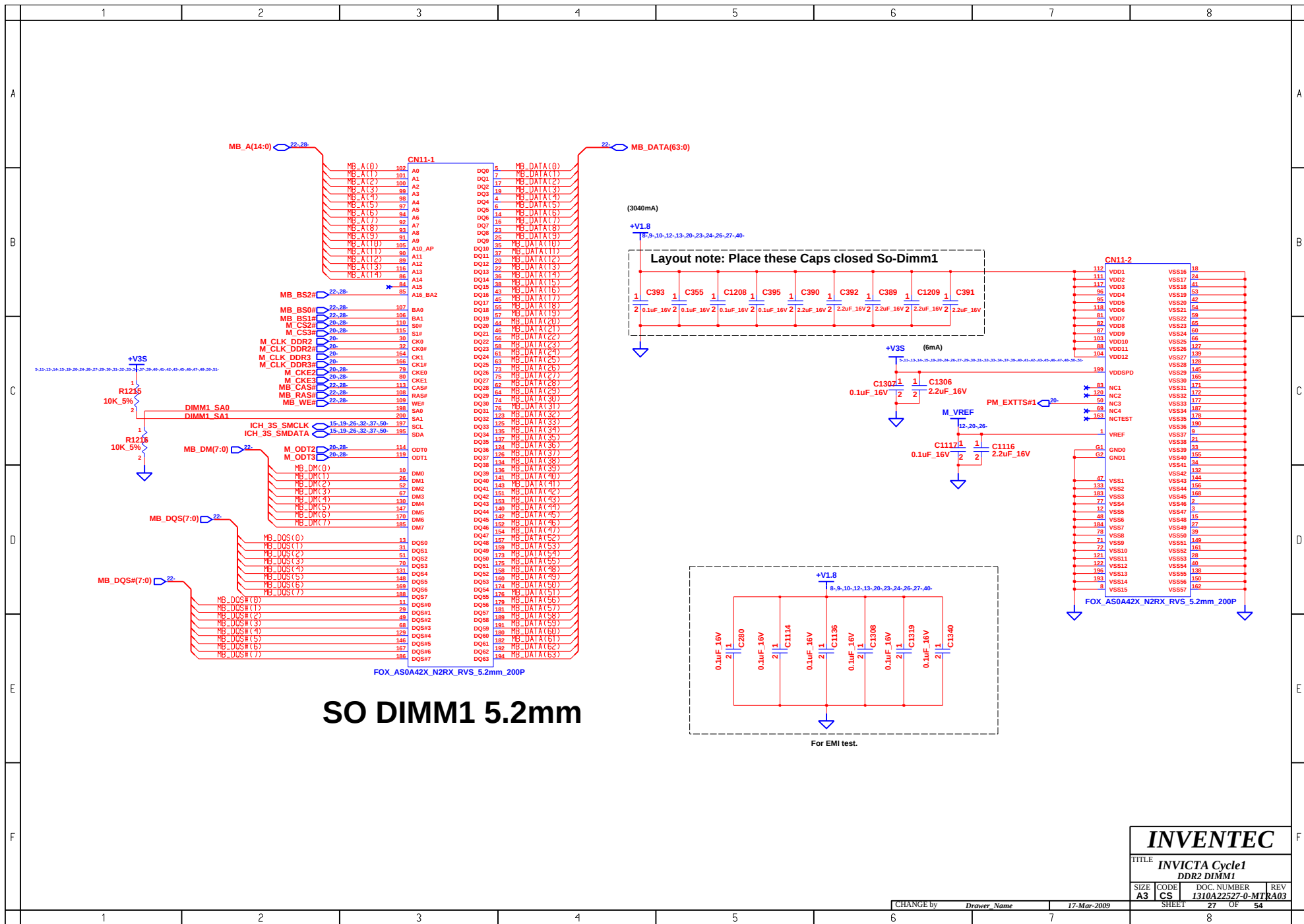
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TITLE
**INVICTA Cycle1
DDR2 DIMM0**

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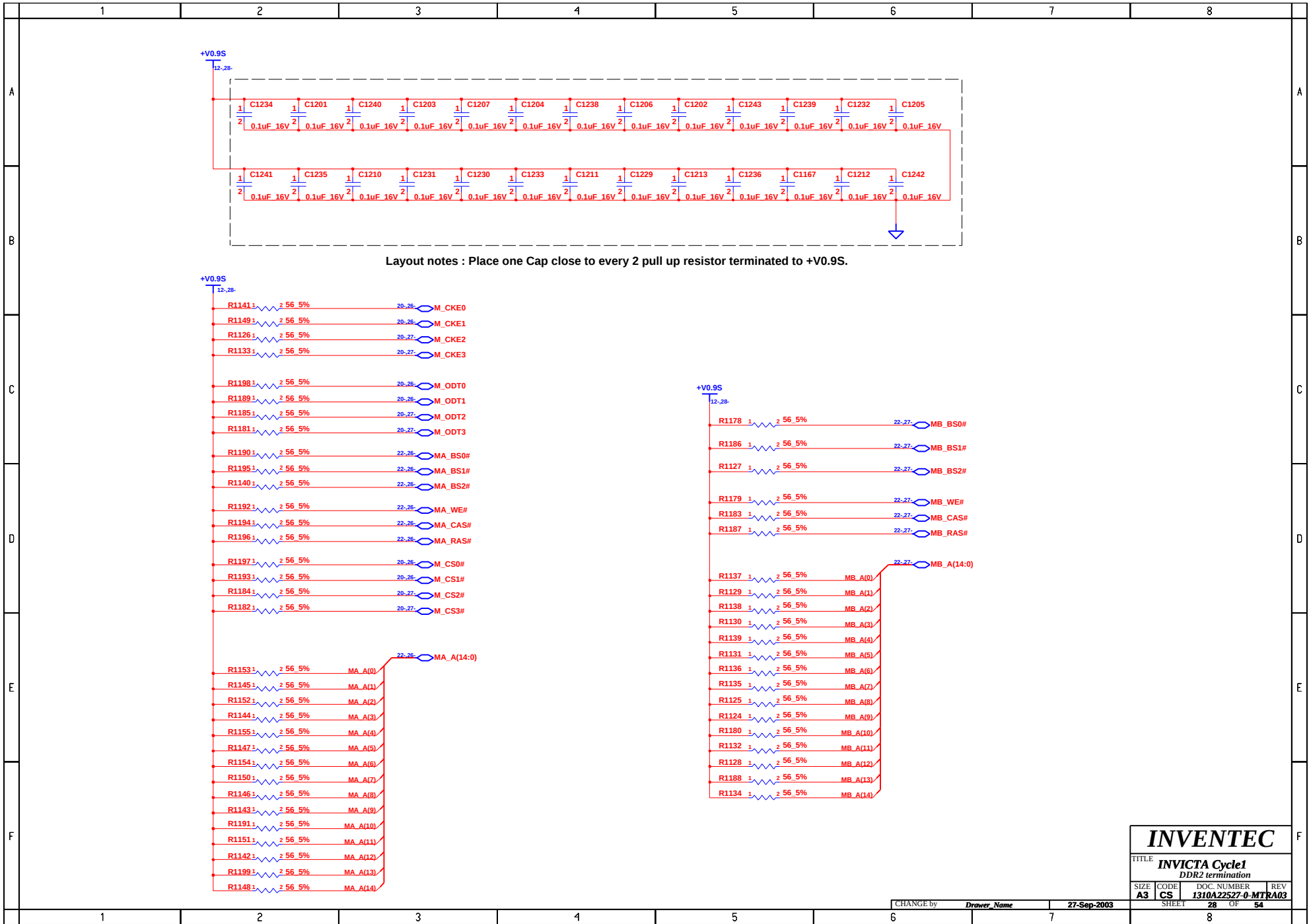
SO DIMM1 5.2mm

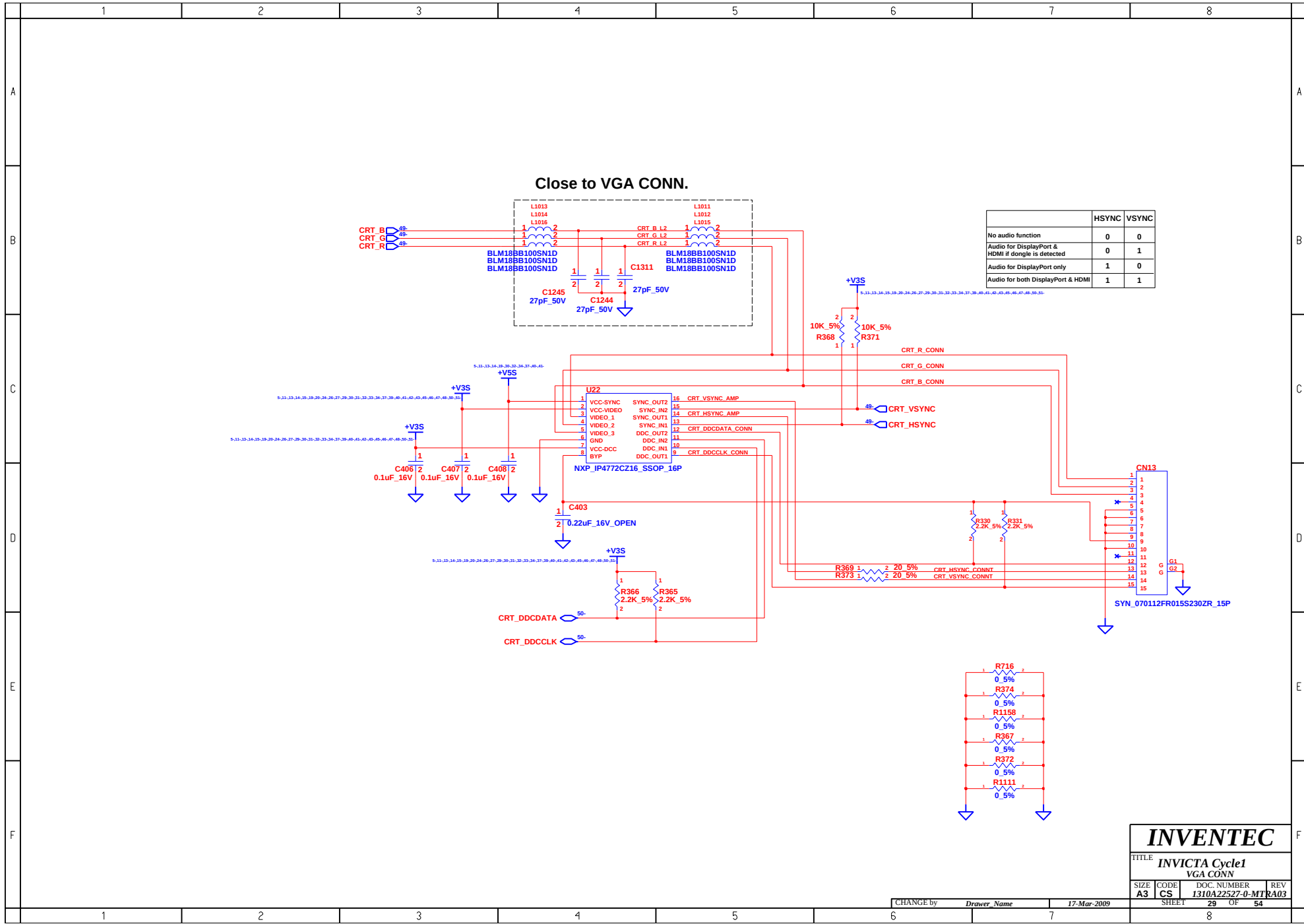
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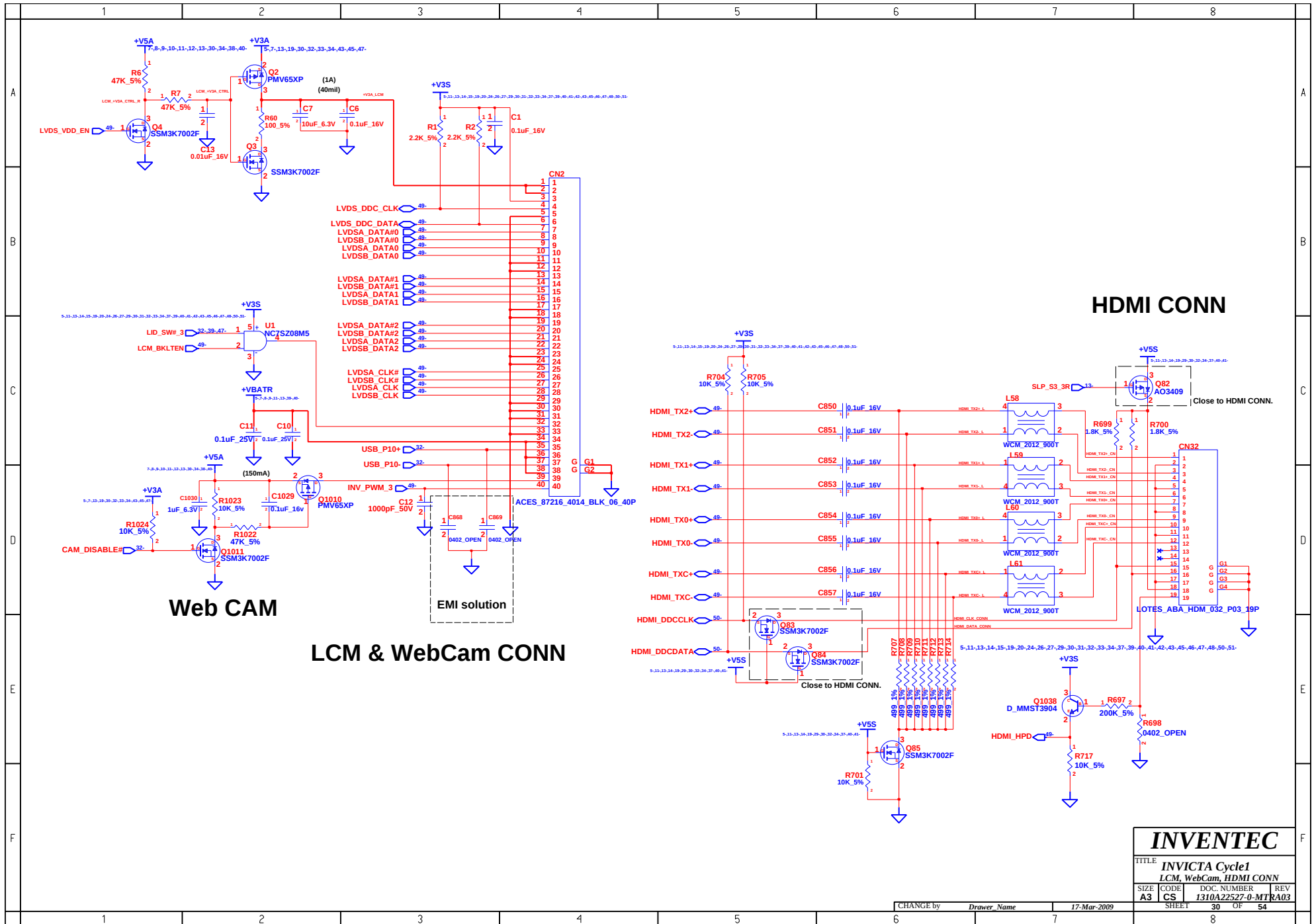
TITLE INVICTA Cycle1 DDR2 DIMM1			
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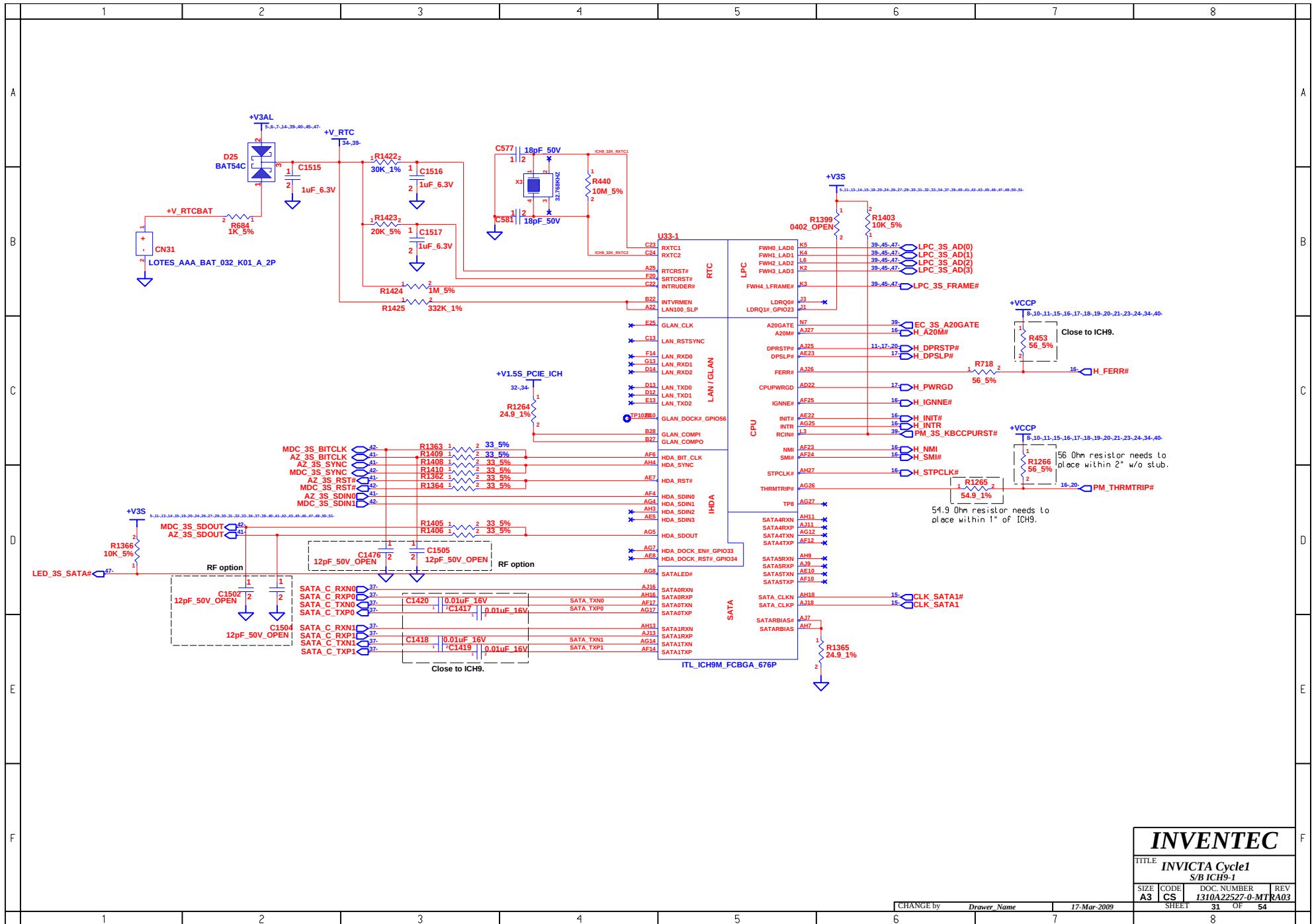
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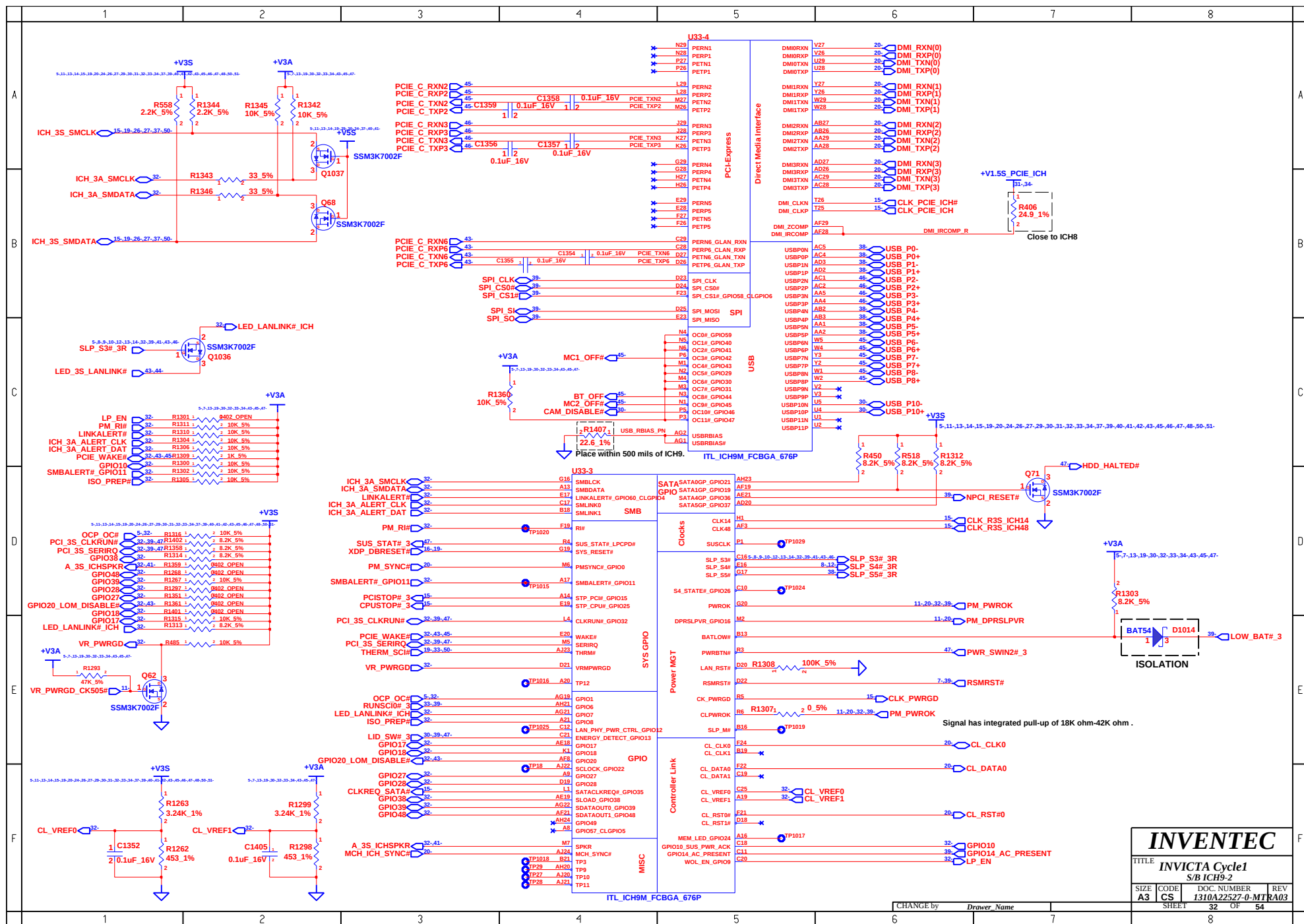
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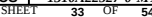


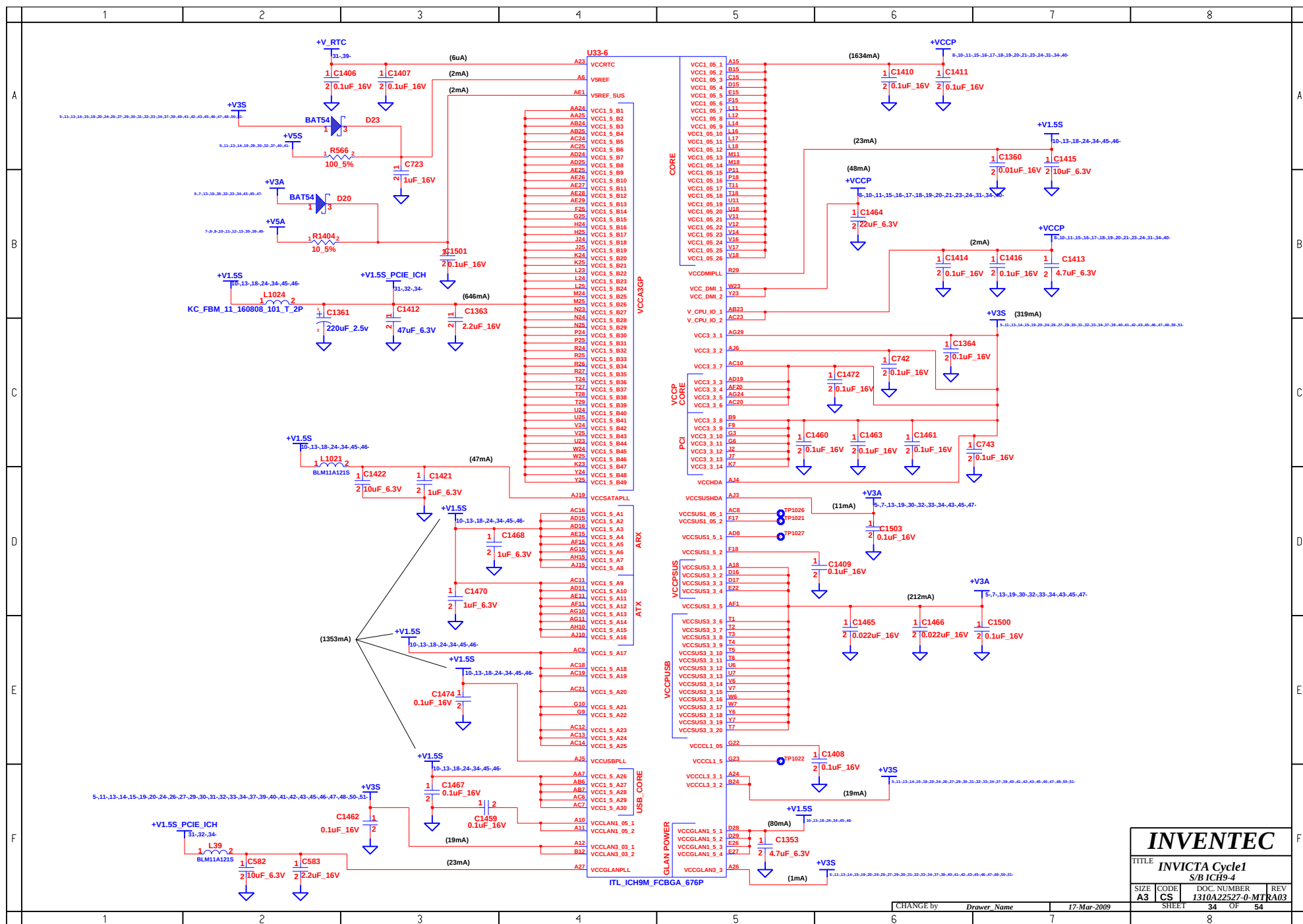


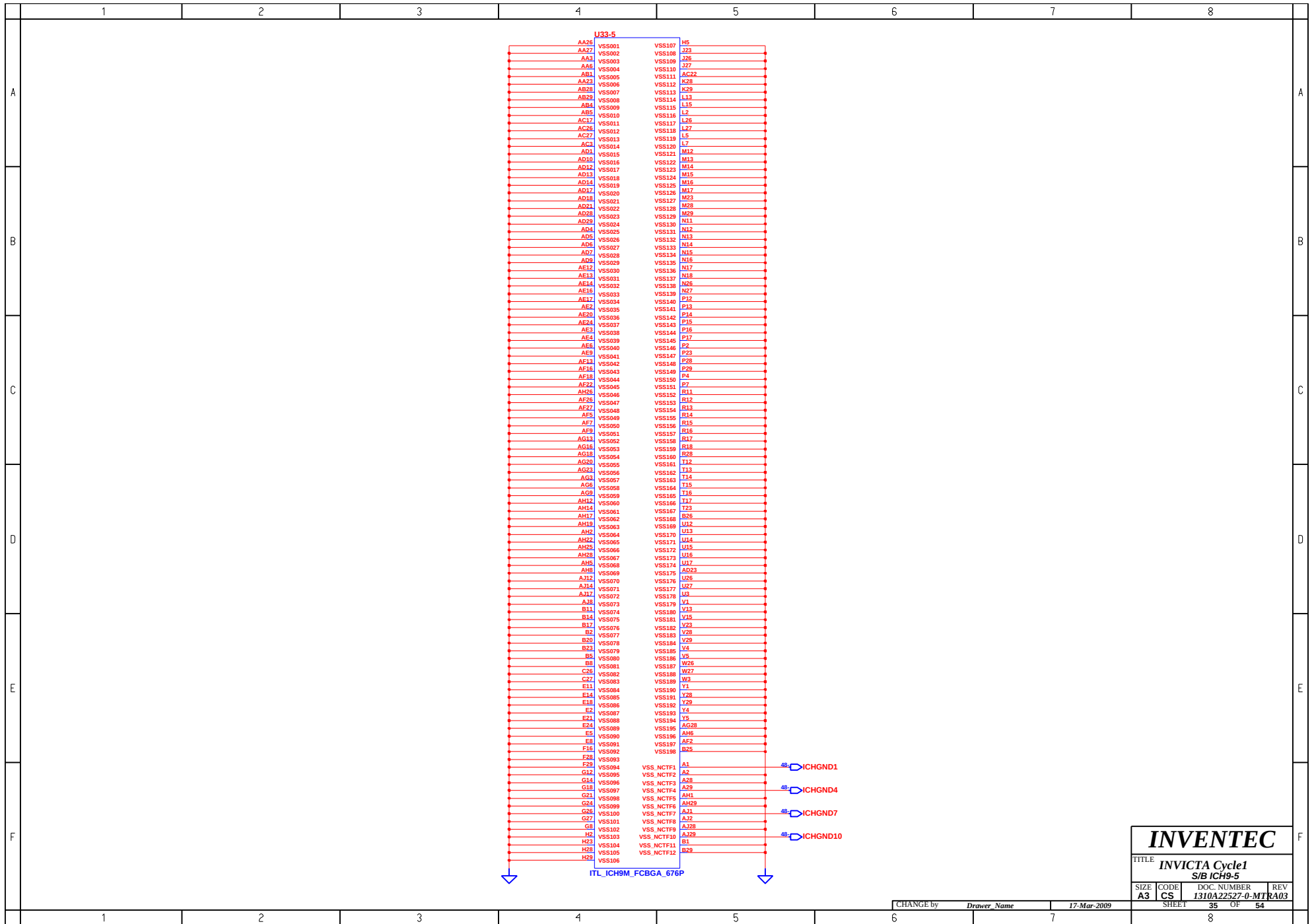




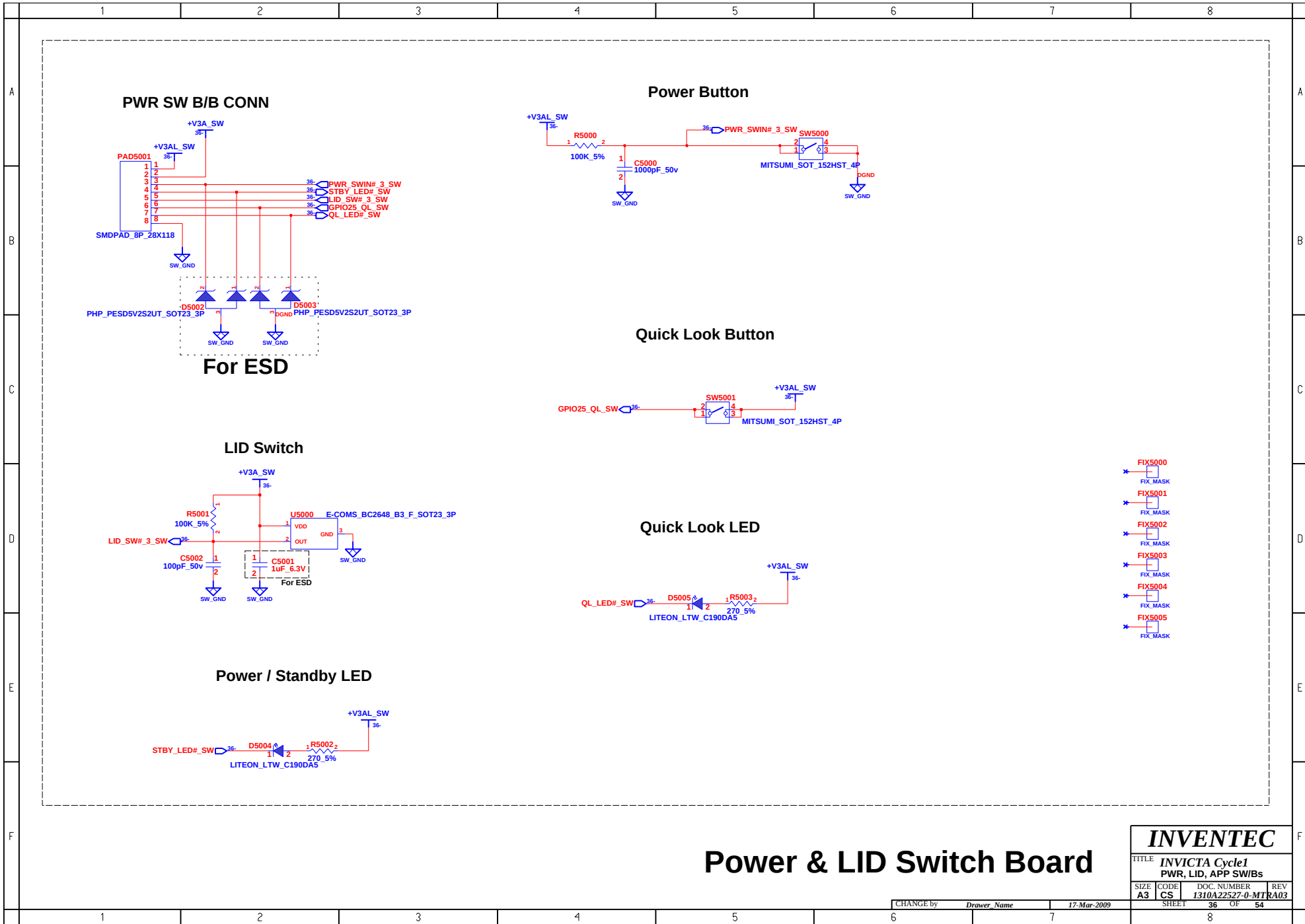


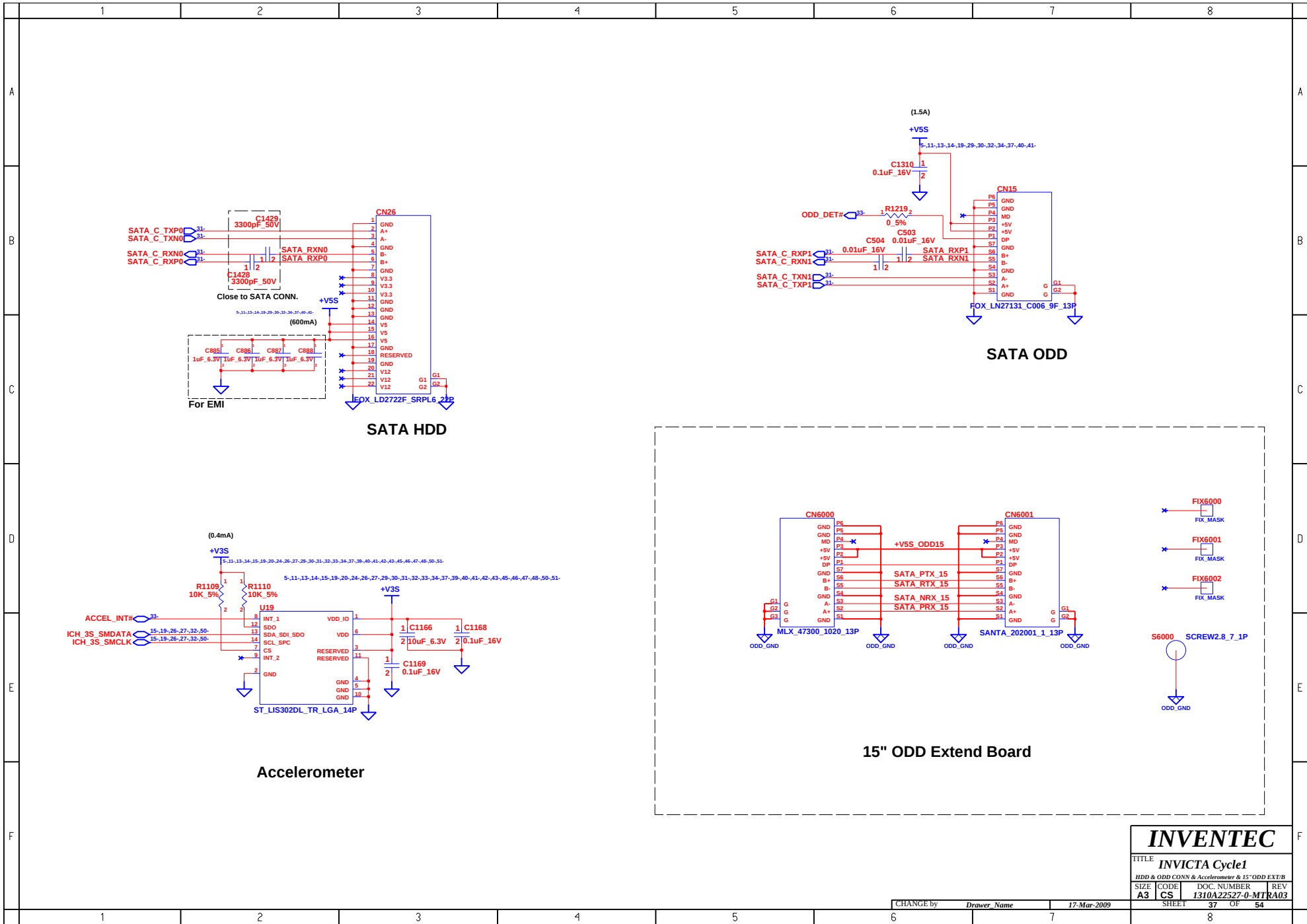


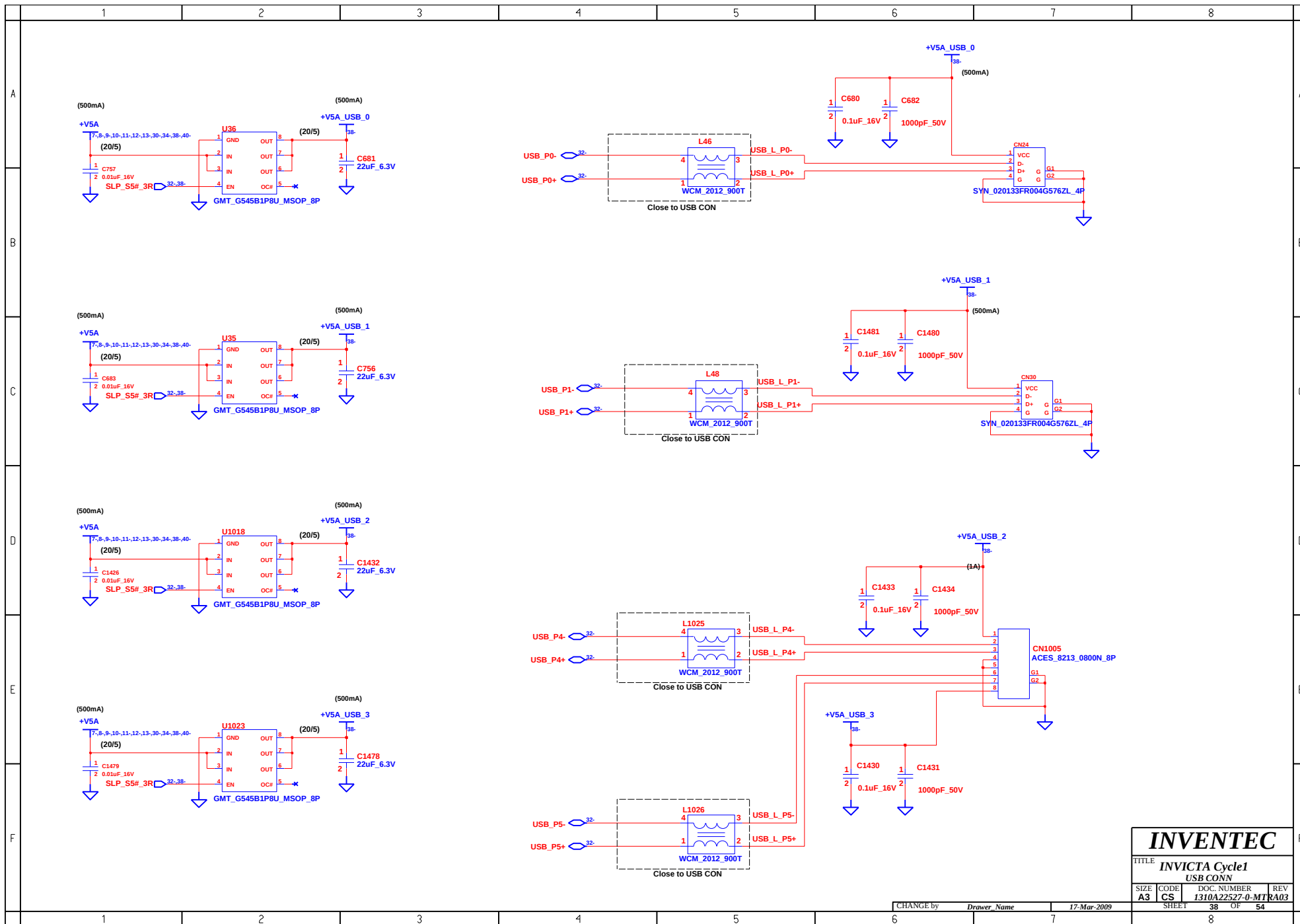




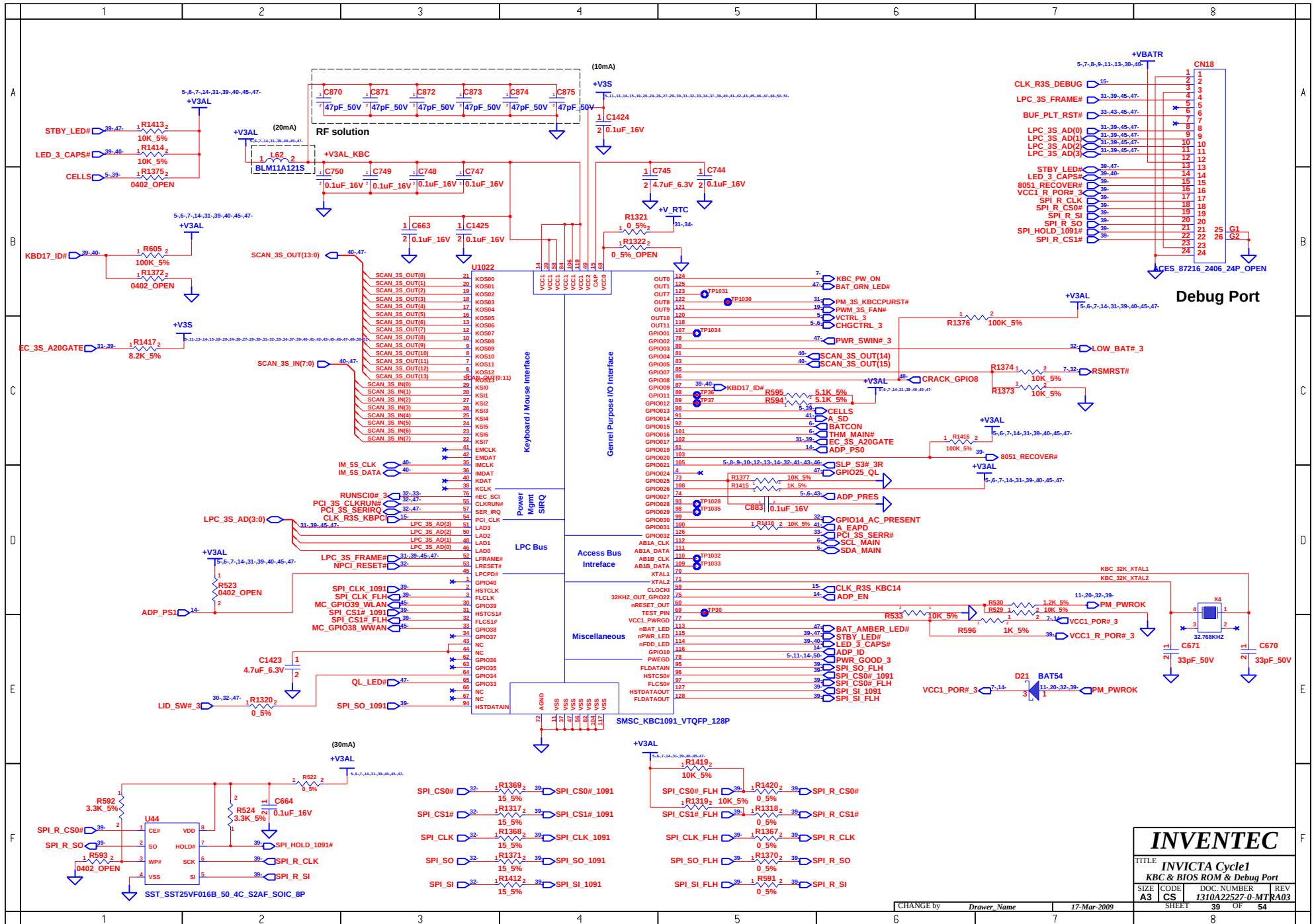
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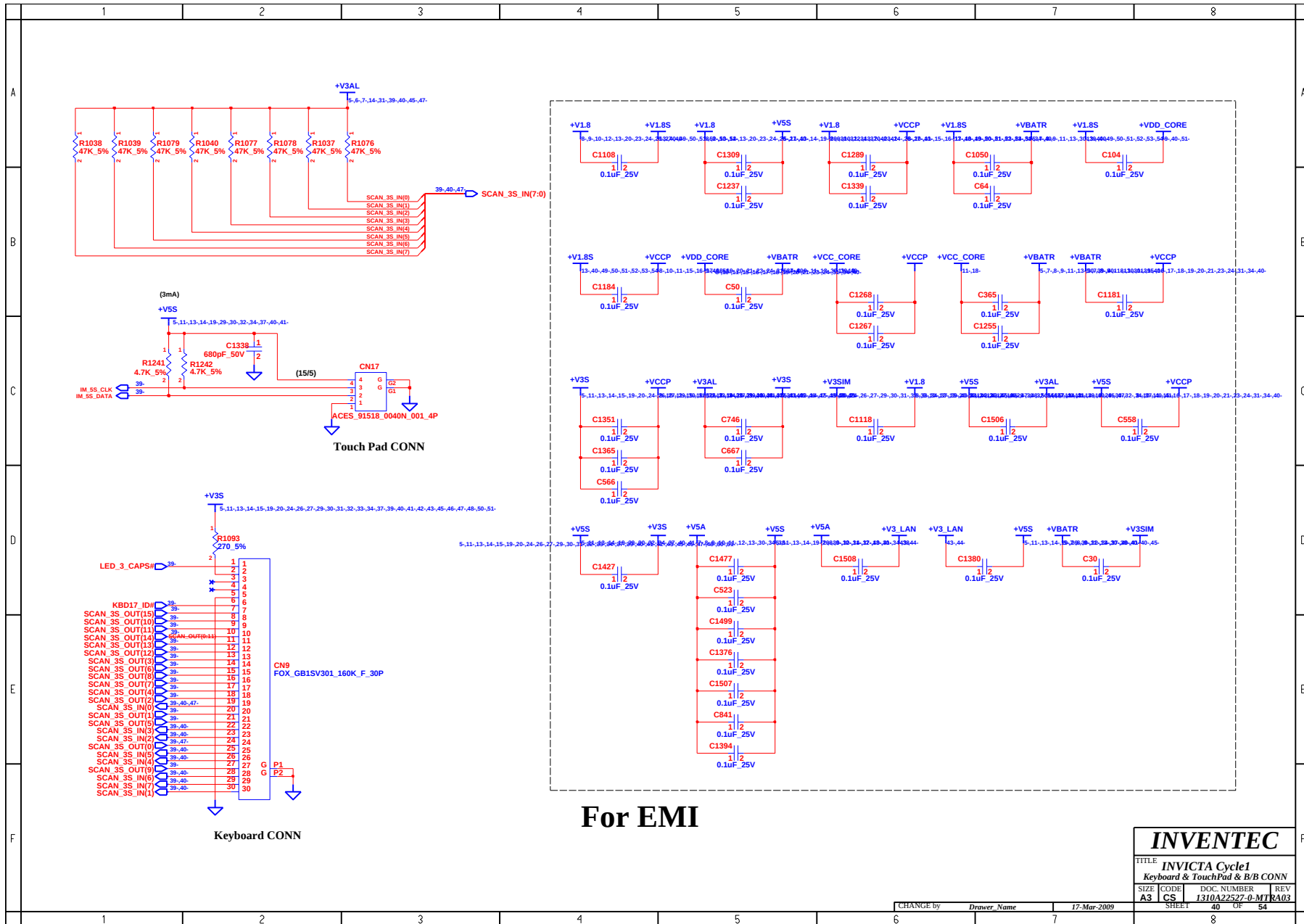


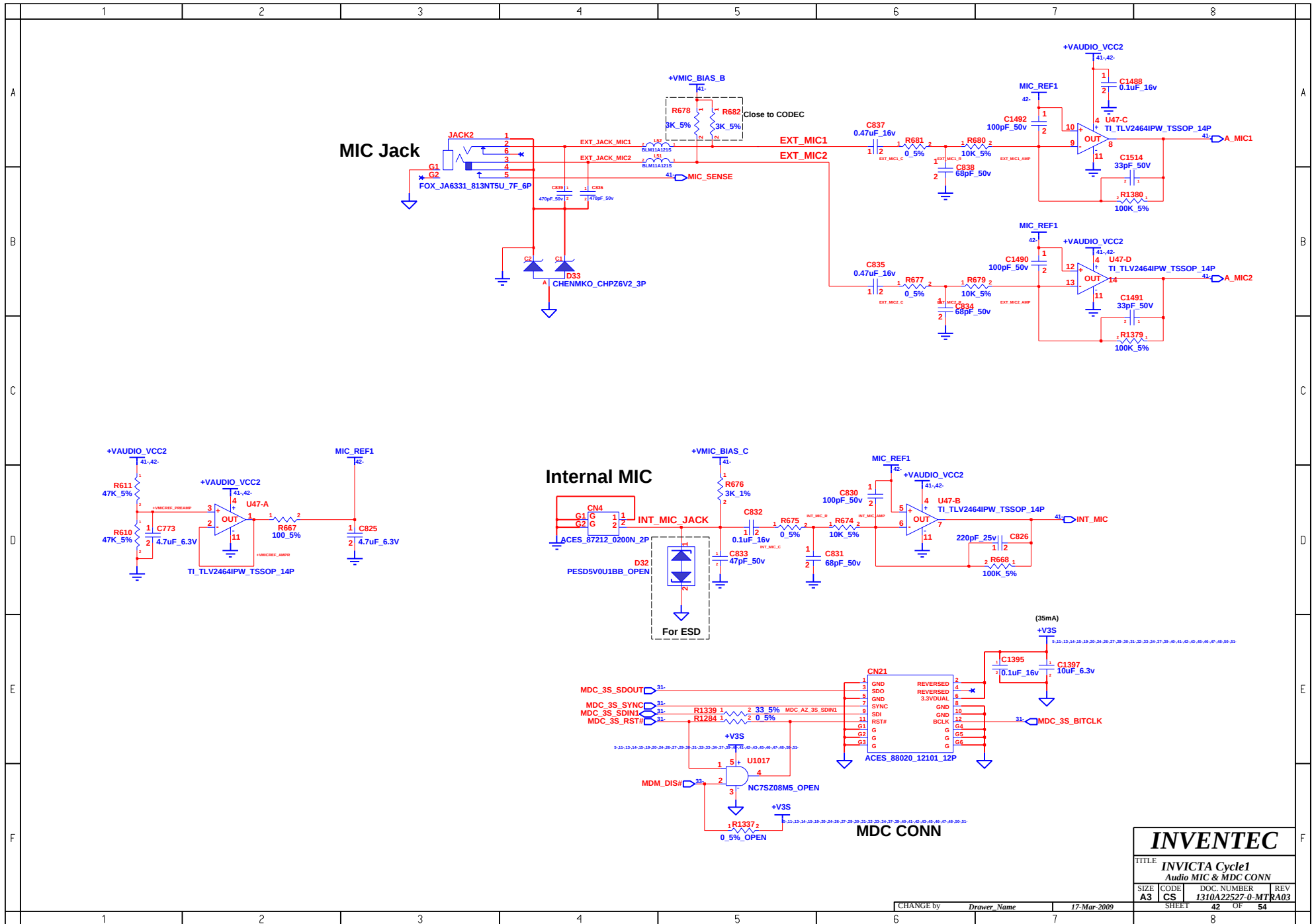


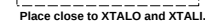
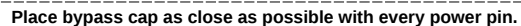


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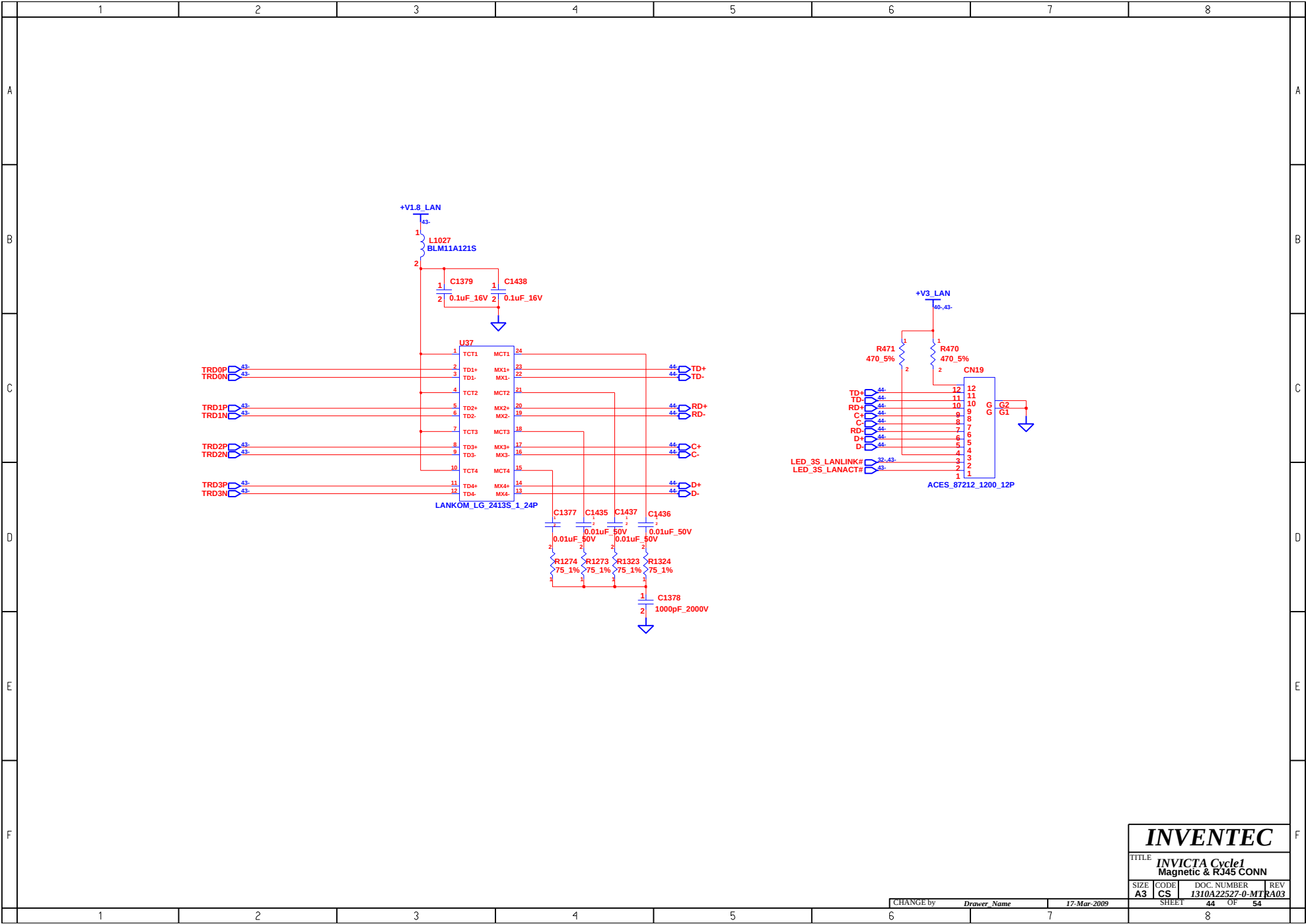








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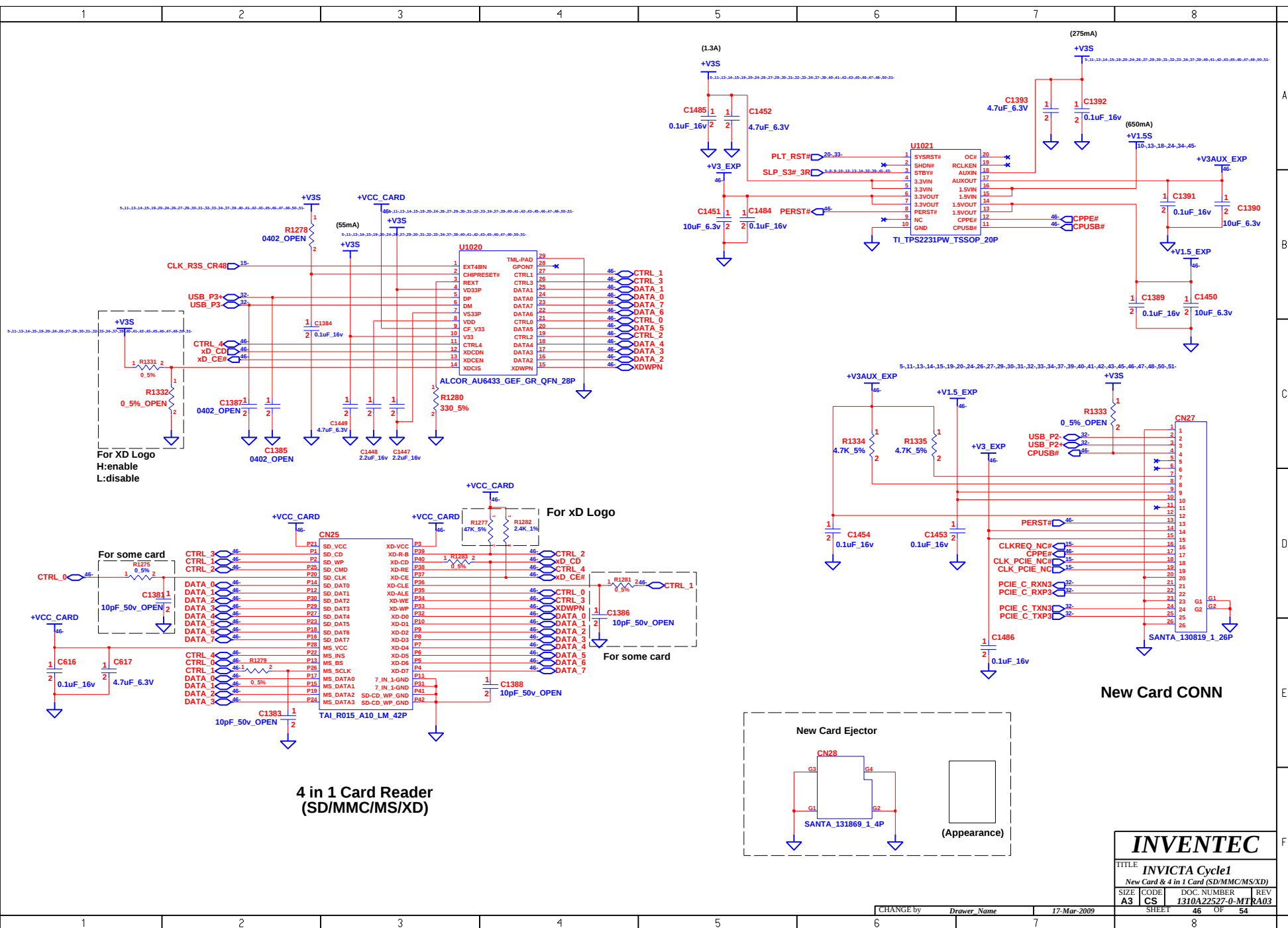
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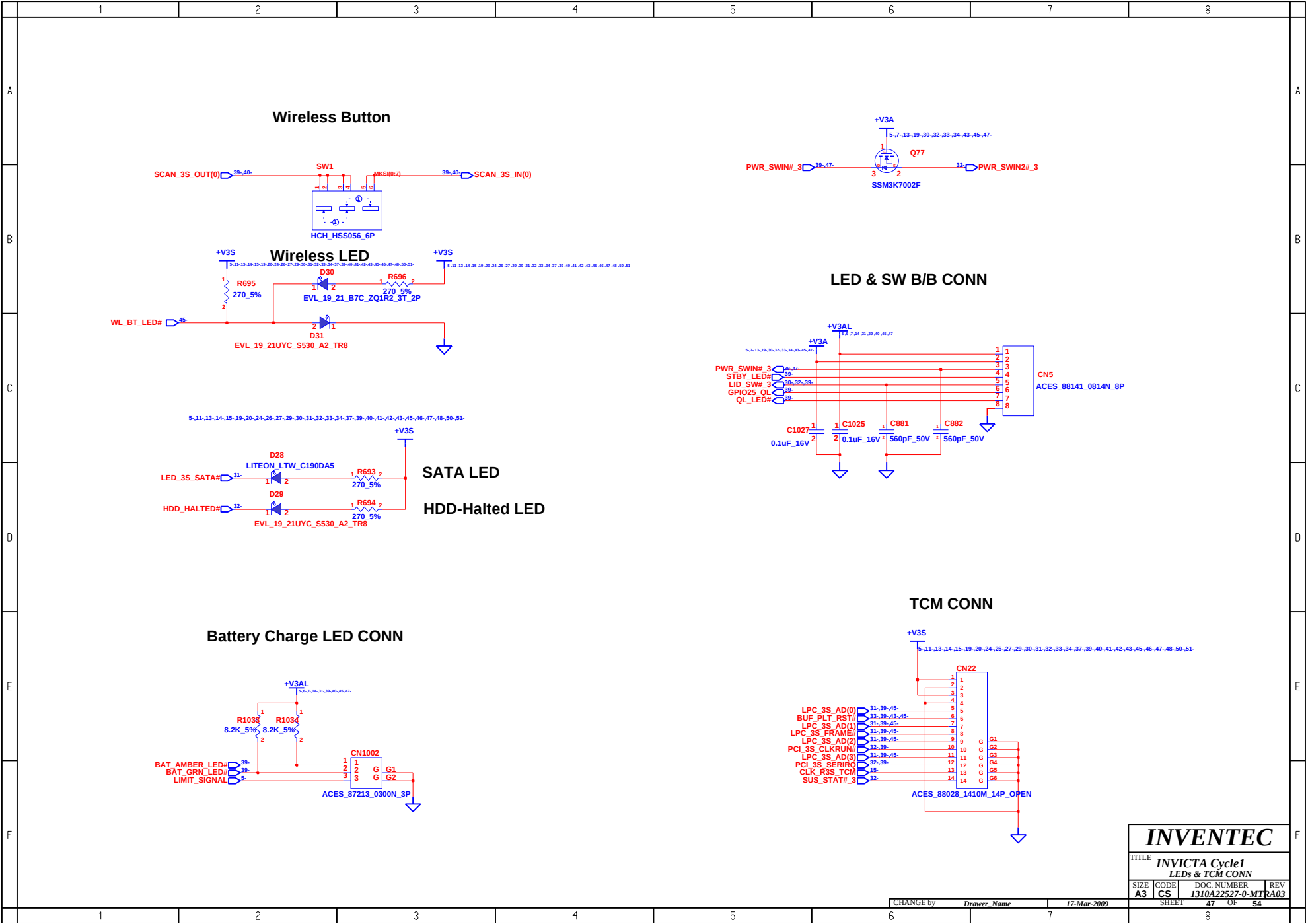
TITLE **INVICTA Cycle1**
Magnetic & RJ45 CONN

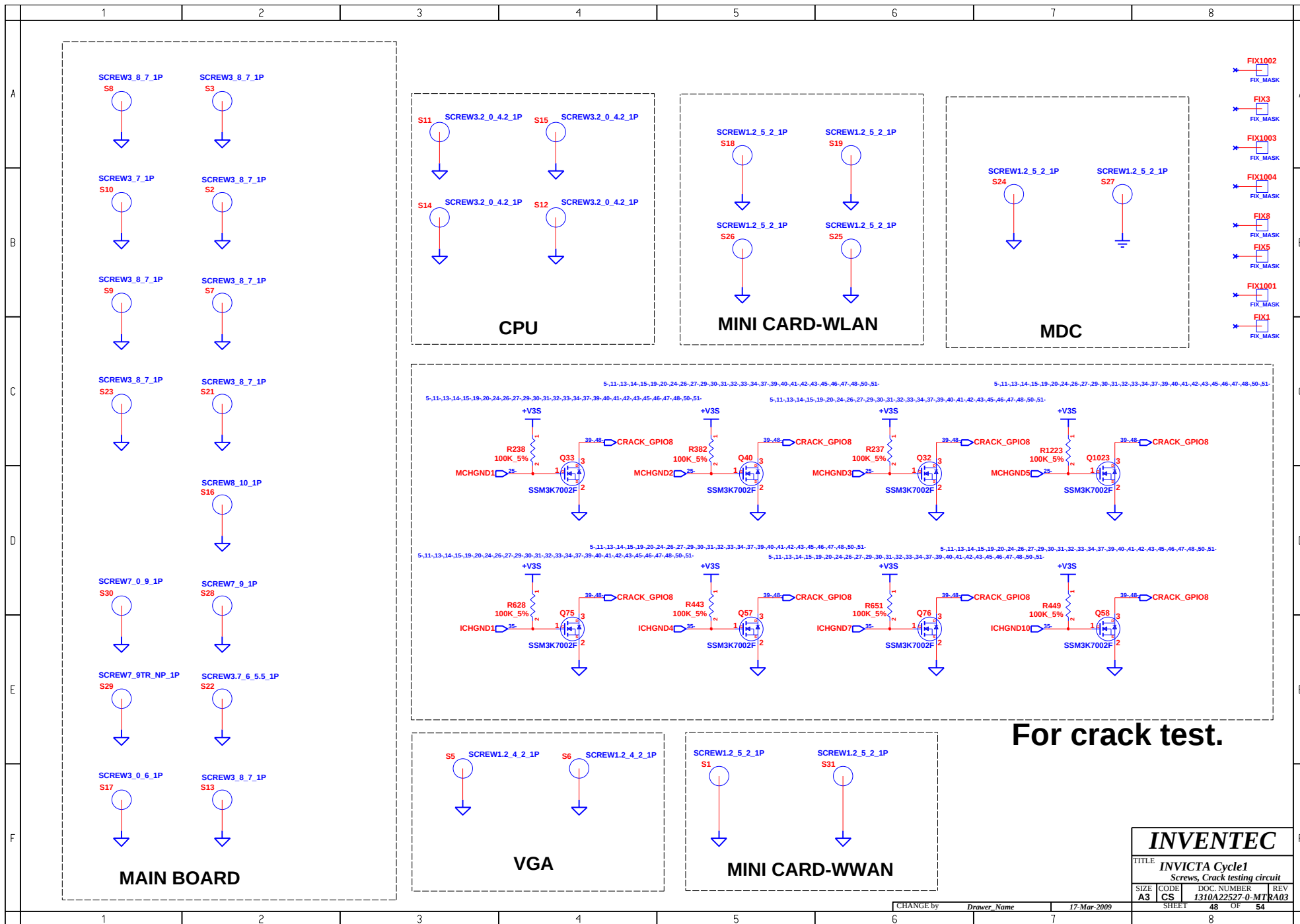
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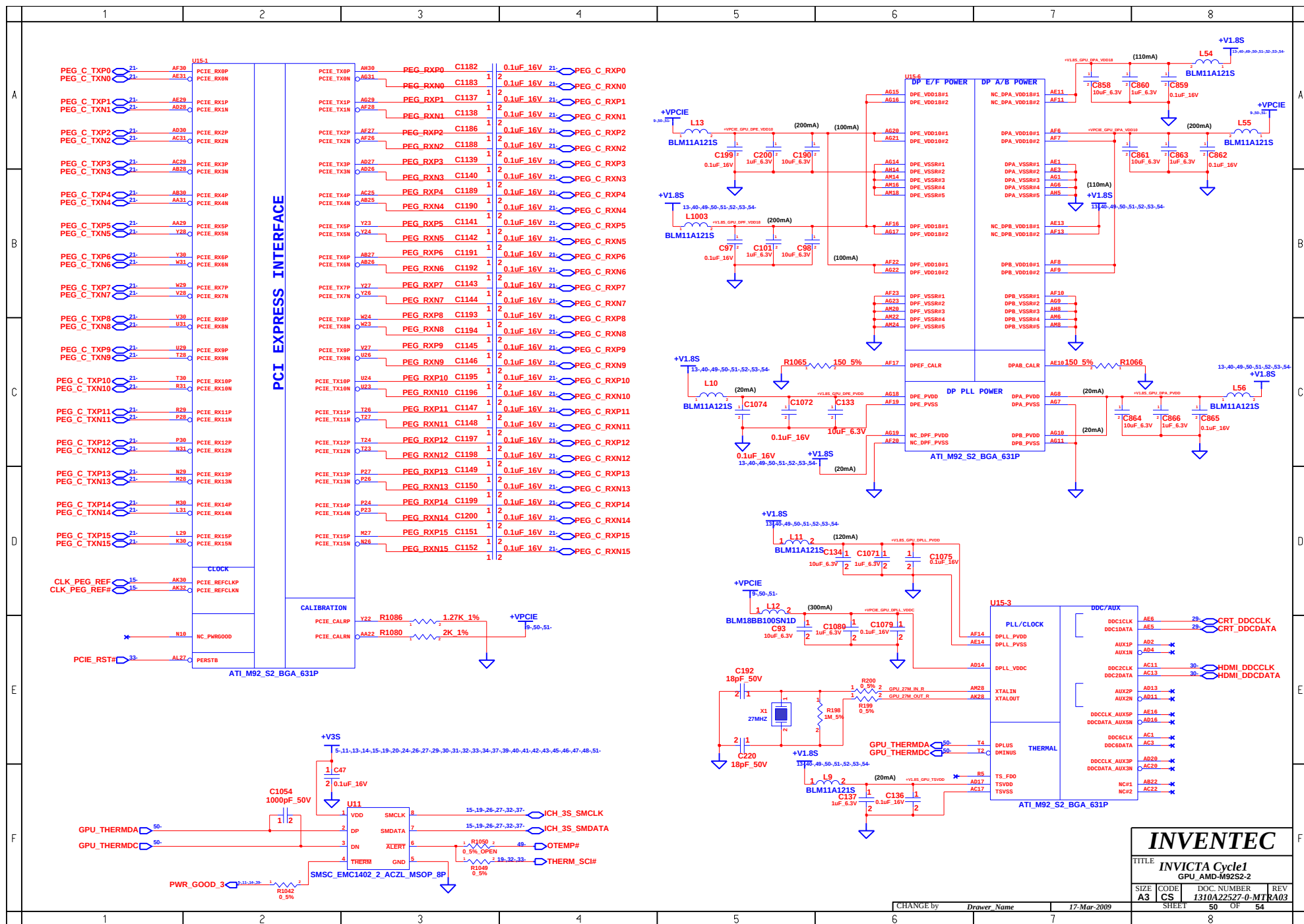
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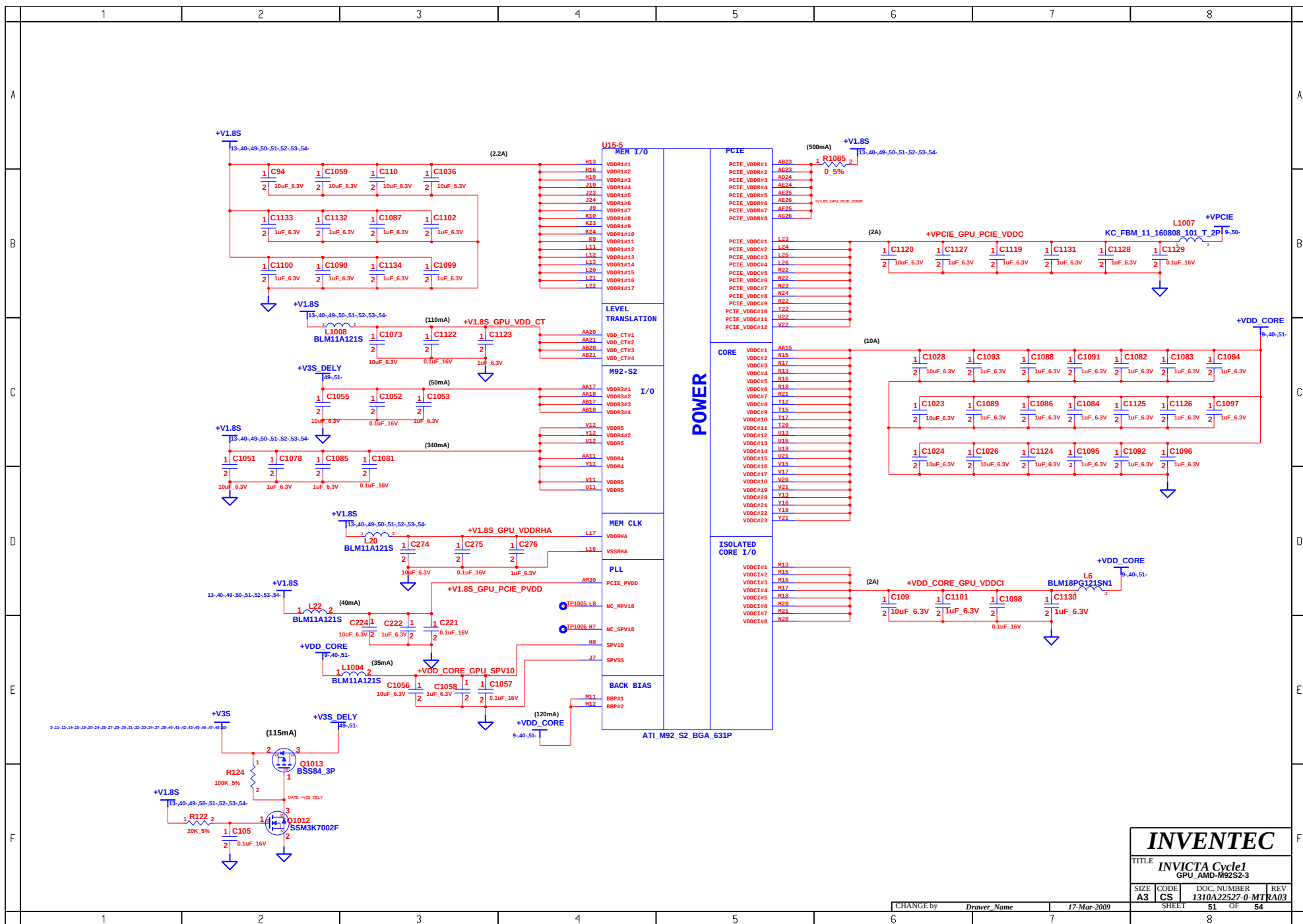


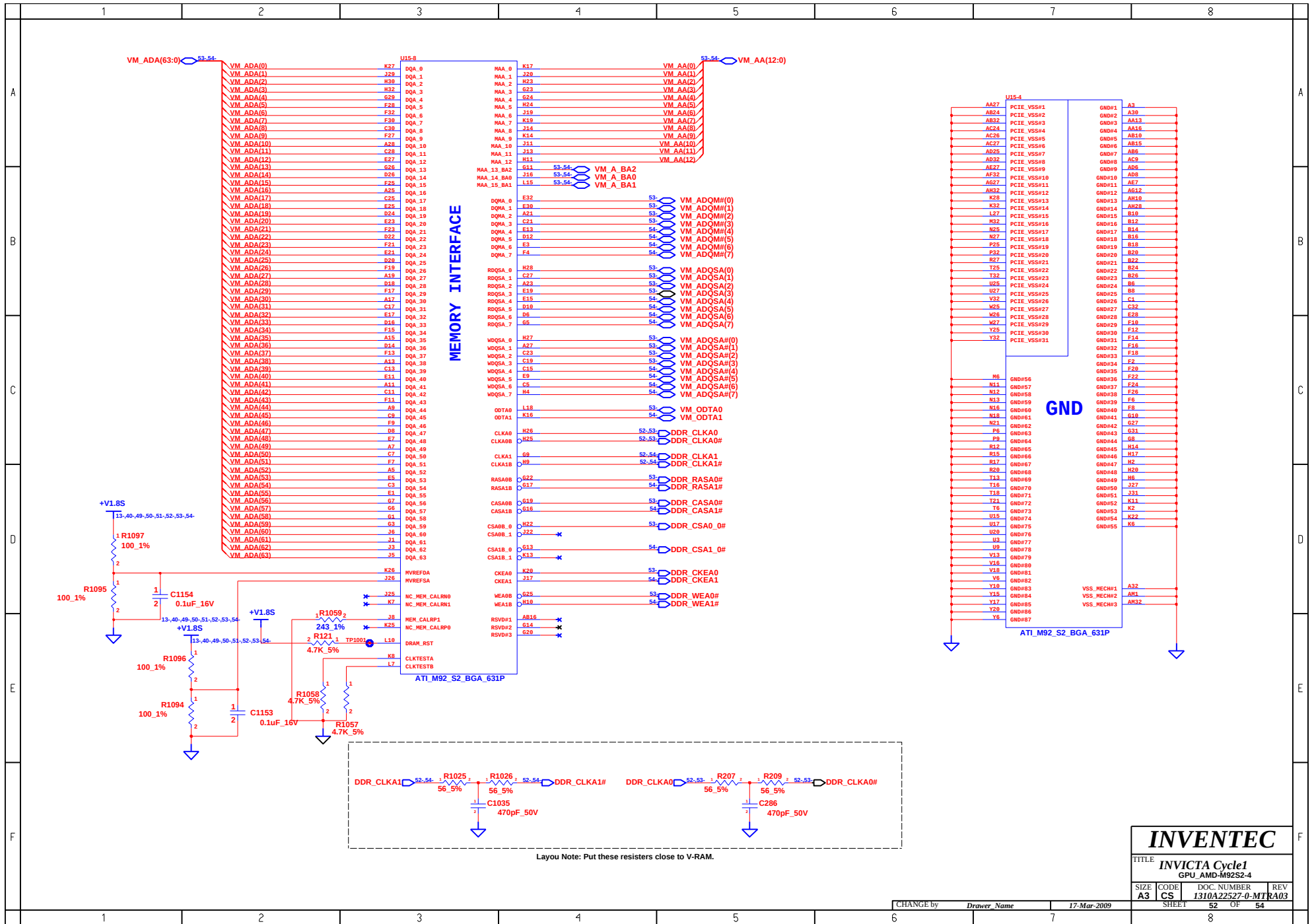


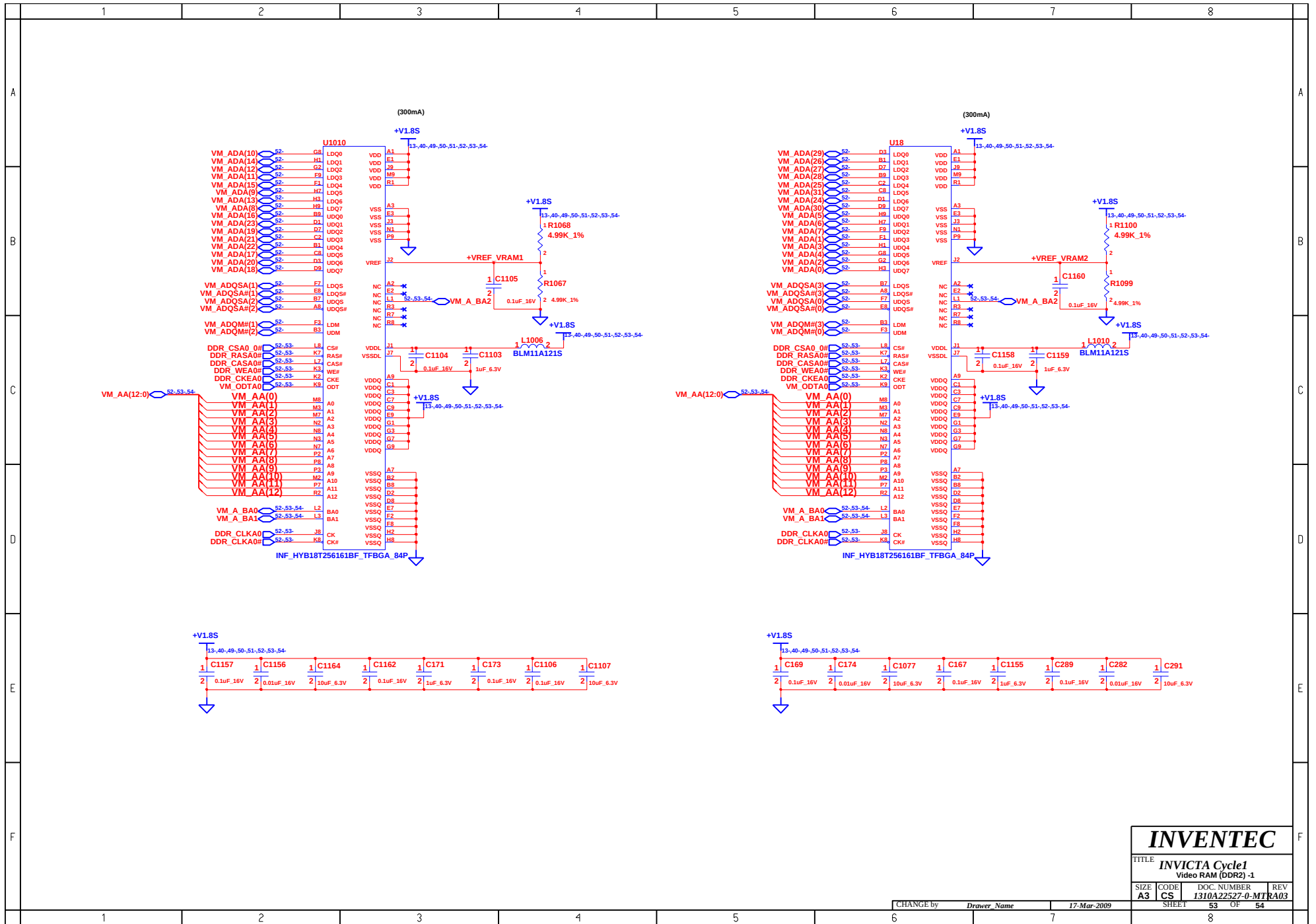


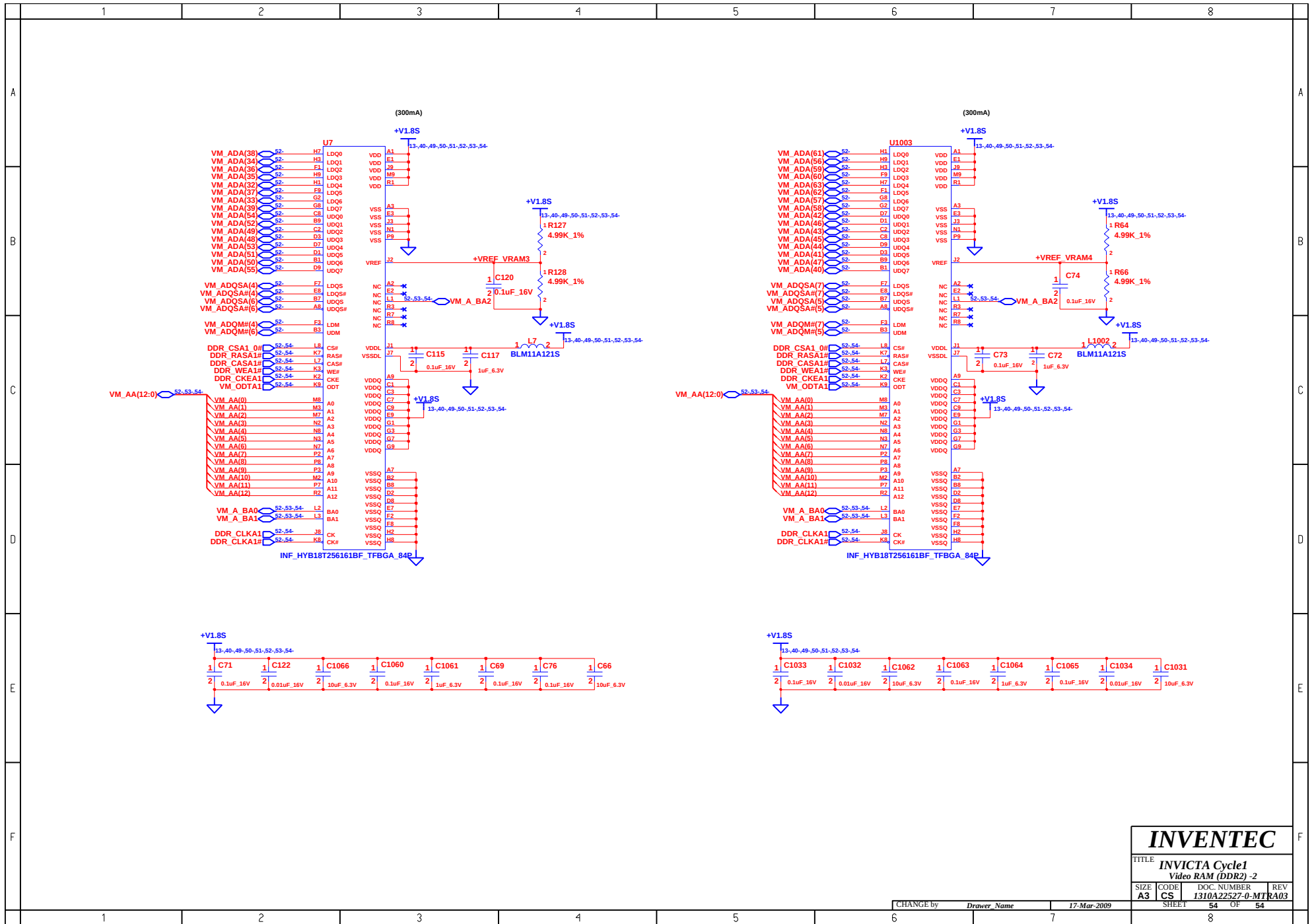
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INVENTEC

TITLE
INVICTA Cycle1
Video RAM (DDR2) -2

SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A22527-0-MT	RA03

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