

01



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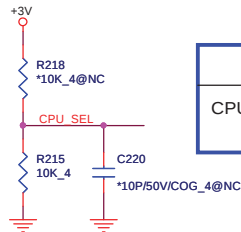
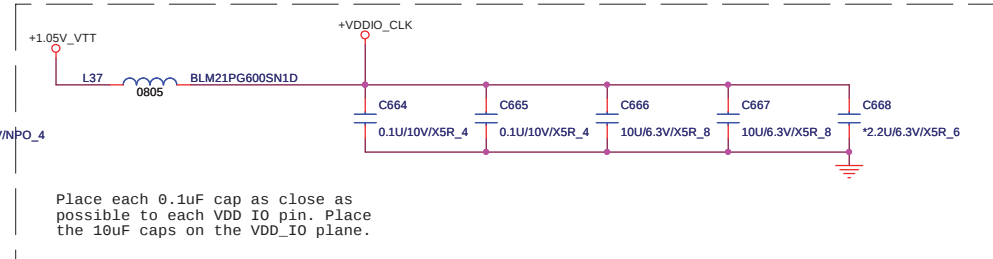
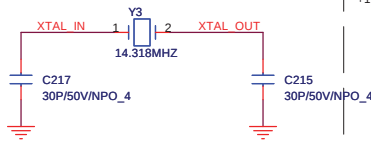
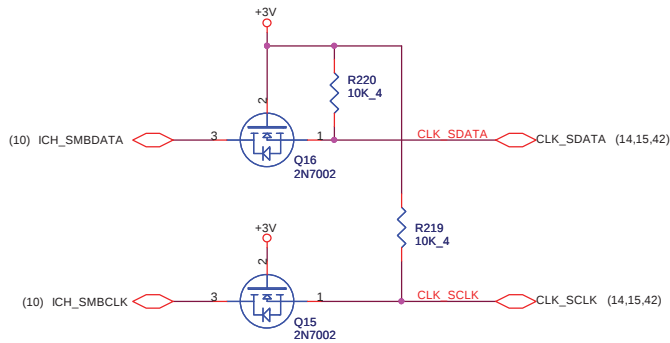
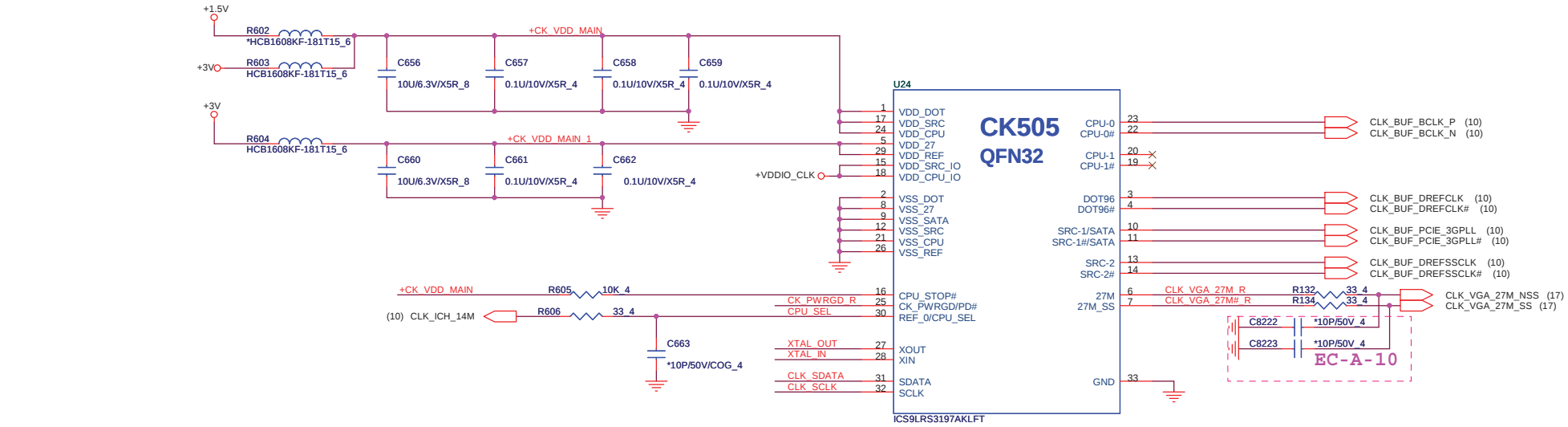
PAGE	DESCRIPTION
01	BLOCK DIAGRAM
02	FRONT PAGE
03	CLOCK GENERATOR
04-07	Auburndale PROCESSER
08-13	Ibex Peak-M
14-15	DDRIII SO-DIMM
16	LCD & LID CON
17	HDMI PORT(PS8101)
18	CRT CONN
19	AUDIO CODEC(ALC269Q)
20	LAN(8111DL)
21	SATA HDD/CD-ROM
22	USB X2/SIM_CARD/LEDs/RF
23	CARD READER/USB/SIM CONN
24	MINI-Card (WWAN)
25	MINI-Card (WLAN)
26	ONFI
27	Express Card
28	K/B, T/P
29	BlueTooth
30	FAN /THERMAL
31	G-SENSOR
32	B TO B CON
33	TPM & RFID EEPROM
34	KBC IT8502E
35	HOLD & SKEW
36	Discharge
37	Charger (ISL88731)
38	DDR3/0.75V(TPS51116REGR)
39	1.05V_VTT (RT8204)
40	3V/5V (ISL6237IRZ-T)
41	CPU (ISL62882)
42	GFX_VCC (MAX17028)
43	XDP & JTAG
44	Power Block Dianram
45	Schematic Value Descript
46	BOM Matrix Table

Power States

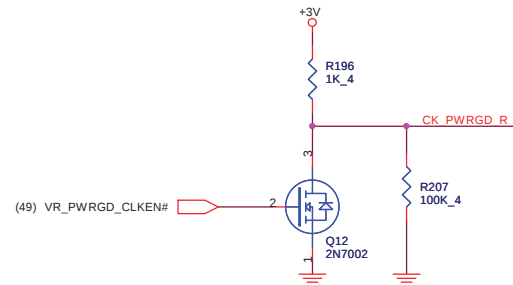
POWER PLANE	VOLTAGE	PAGE	DESCRIPTION	CONTROL SIGNAL	ACTIVE IN
VIN	10V~+20V	16,36,37,38,39,40,41,42	MAIN POWER		S0~S5
+3VRTC	+3.0V~+3.3V	9,12,34	RTC		S0~S5
3VPCU	+3.3V	9,16,20,23,28,32,34,36,37,40,42	ITE8502 POWER	3V5V_EN	S0~S5
5VPCU	+5V	36,37,38,39,40,42	DC/DC POWER IC SOURCE	3V5V_EN	S0~S5
+15V	+15V	16,31,36,38,39,40	LARGE POWER	3V5V_EN	S0~S5
LANVCC	+3.3V	20,36	LAN POWER	LAN_ON	
5V_S5	+5V	12,22,23,36	PCH SUS POWER	S5_ON	S0~S3
3V_S5	+3.3V	8,9,10,11,12,36	Sys Management,PCH Resume Well, Intel HD Audio,USB,WLAN,WiMAX POWER	S5_ON	S0~S3
5VSUS	+5V	16,32,36,41,42	SLP_S4# CTRLD POWER	SUSON	S0~S3
3VSUS	+3.3V	8,23,27,34,36,42	SLP_S4# CTRLD POWER	SUSON	S0~S3
1.5VSUS	+1.5V	4,6,12,14,15,36,38,39	DDR3 SODIMM POWER	SUSON	S0~S3
0.75VSMDDR_VTERM	+0.75V	14,15,36,38	DDR3 SODIMM REFERENCE POWER	MAINON	S0
+5V	+5V	8,12,16,17,18,19,21,28,30,34,36,37	SLP_S3# CTRLD POWER	MAINON	S0
+3V	+3.3V	3,4,8,9,10,11,12,14,15,16,17,18,19,20,21,22,23,24,25,26,27,29,30,31,32,33,34,36,37,38,39,40,41,42,43	SLP_S3# CTRLD POWER	MAINON	S0
+1.8V	+1.8V	6,12,26,36,42	LVDS,NVM POWER	MAINON	S0
+1.5V	+1.5V	12,24,25,27,38,39	Mini PCIe,Express Card POWER	MAIND	S0
+1.05V_VTT	+1.05V	3,4,6,8,10,11,12,36,39,41,43	AuBurndale VTT POWER/PCH CORE POWER	MAINON	S0
+VCC_GFX_CORE		6,36,42	VGA CORE POWER	GFXVR_EN	S0
VCC_CORE		6,36,41	CPU CORE POWER	VRON	S0
LCDVCC	+3.3V	16	LCD Power	ENVDD	S0
+5V_ODD	+5V	21	ODD Power	MAINON#	S0
+5V_HDD	+5V	21	HDD Power	MAINON#	S0
BAT-V	+10V~+17V	37	MAIN BATTERY	CHG_PBATT	S0~S5



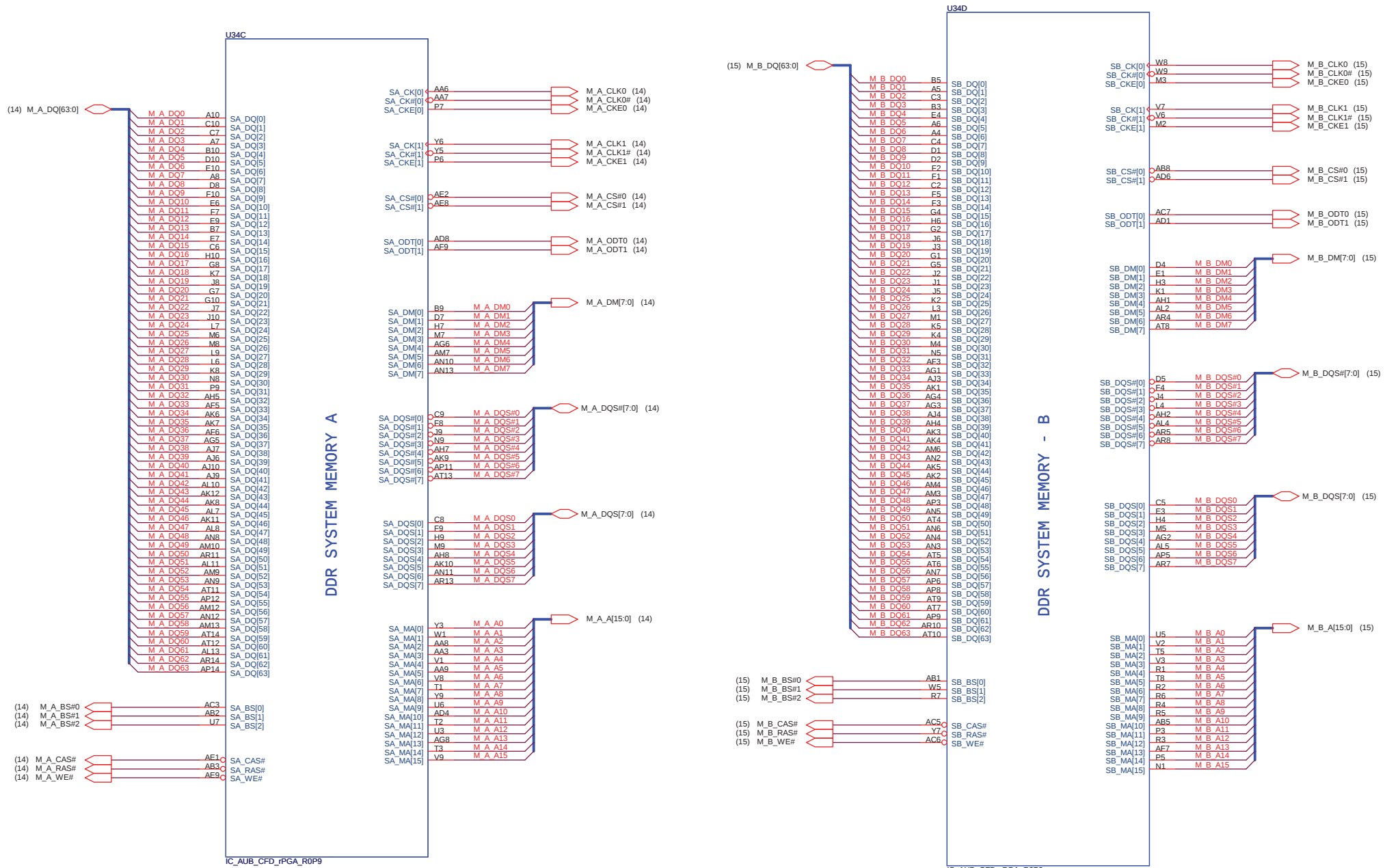
PROJECT: GC9A
Quanta Computer Inc.



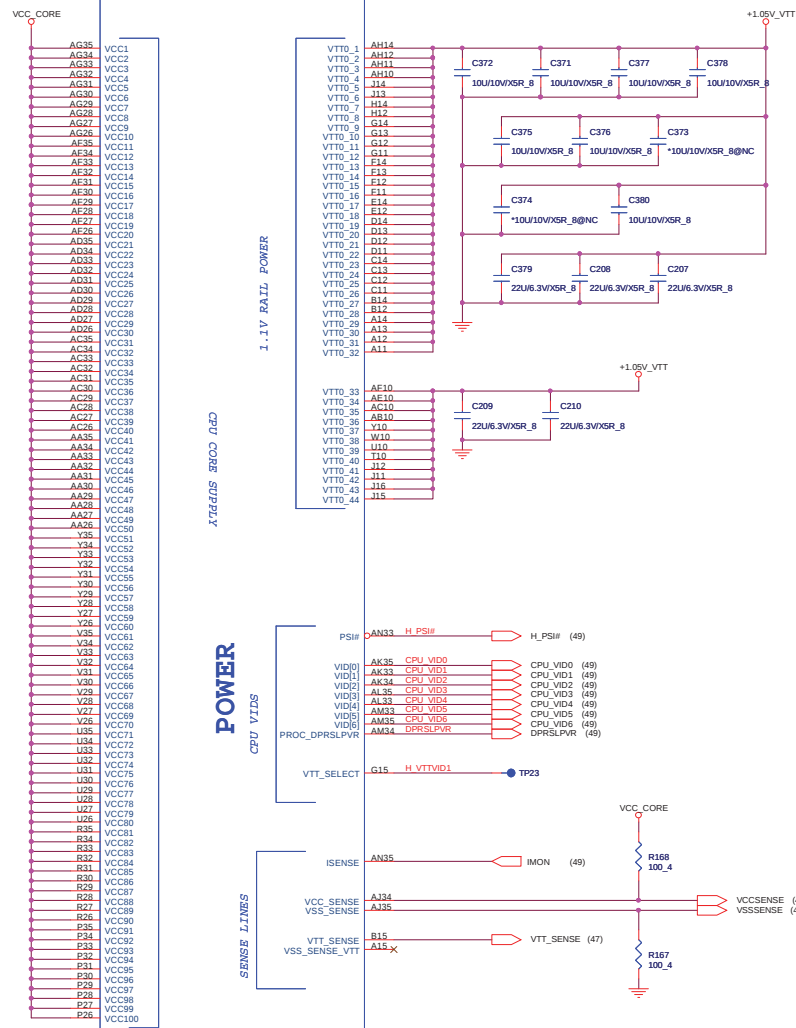
	0	1
CPU_SEL	CPU0/1=133MHz (default)	CPU0/1=100MHz



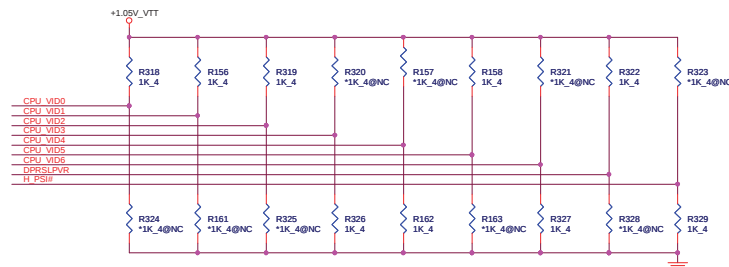
ARRANDALE PROCESSOR (DDR3)



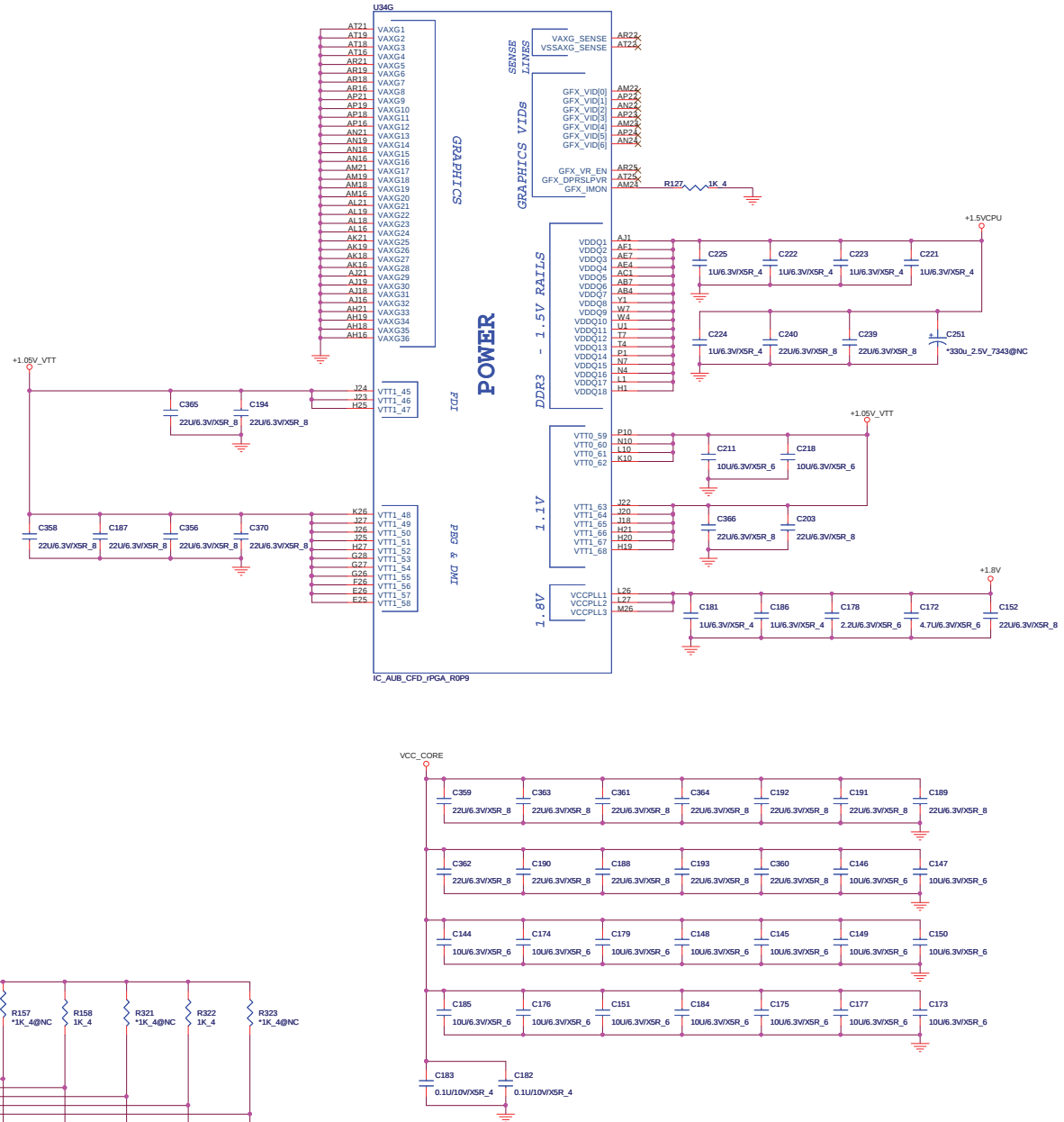
CPU Core Power



AUBURNDALE PROCESSOR (POWER)

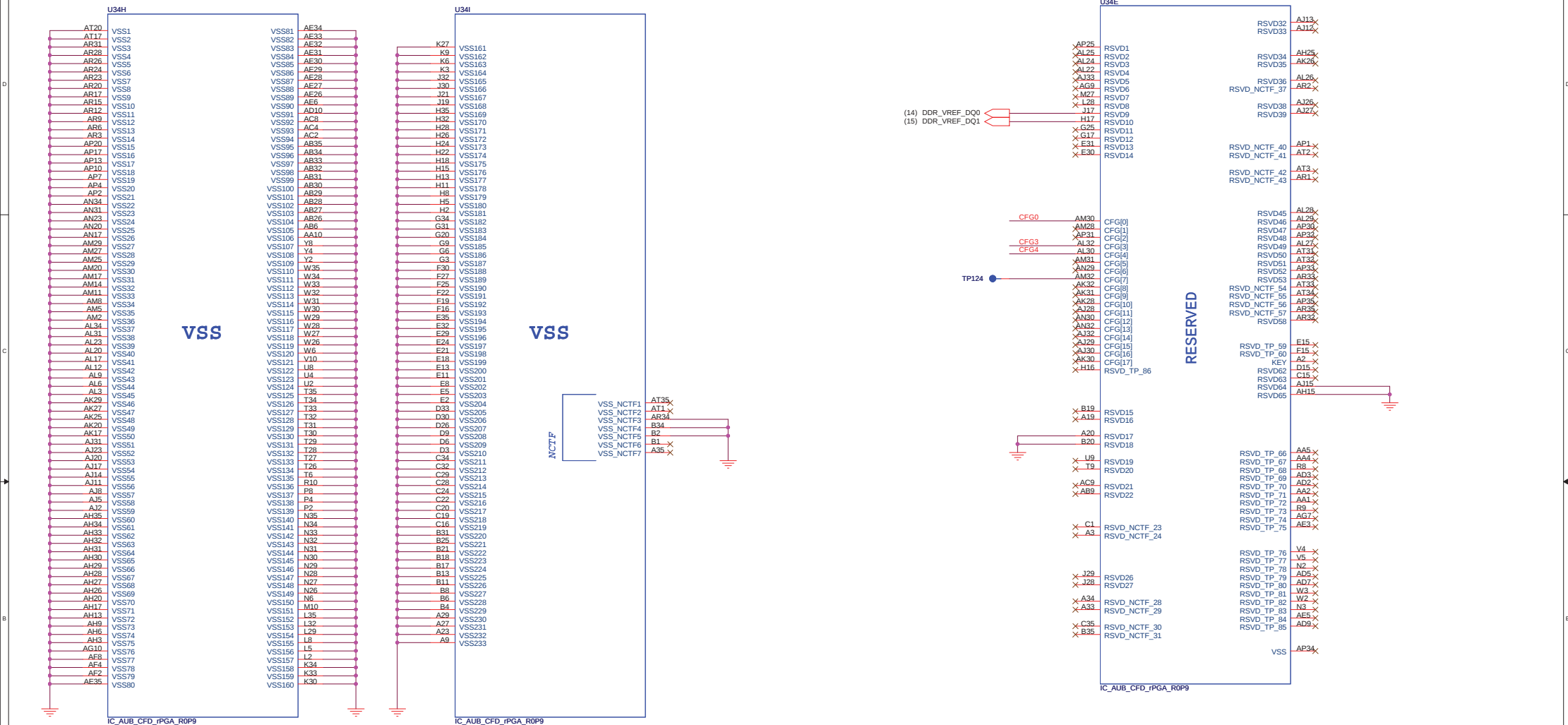


ARRANDALE PROCESSOR (GRAPHICS POWER)



ARRANDALE PROCESSOR (GND)

ARRANDALE PROCESSOR (RESERVED, CFG)

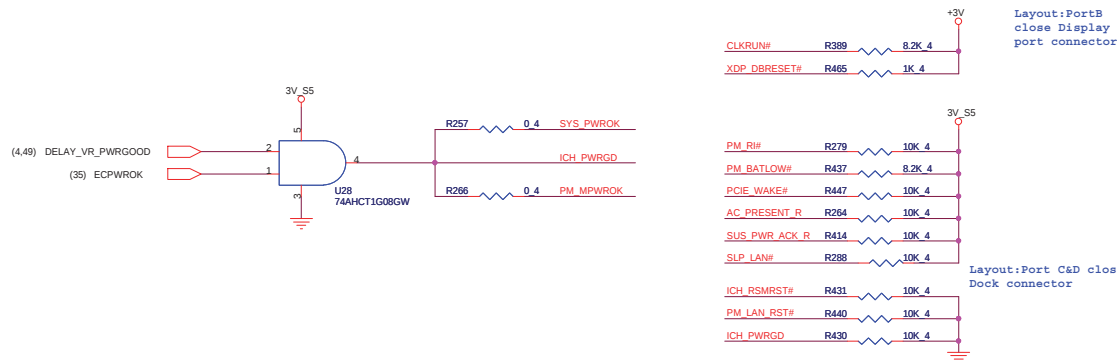
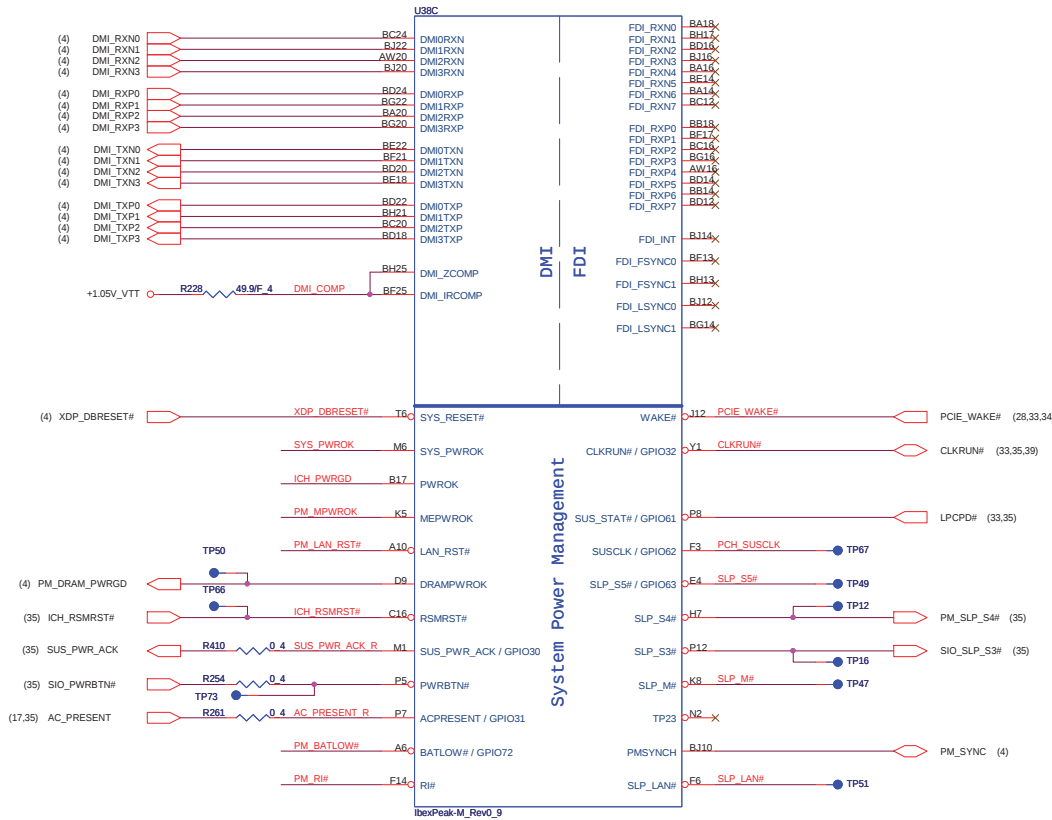


	1	0
CFG4 (Display Port Presence)	Disabled; No Physical Display Port attached to Embedded Display Port	Enabled; An external Display port device is connected to the Embedded Display port
CFG0 (PCI-Epress Configuration Select)	Single PEG	Bifurcation enabled
CFG3 (PCI-Epress Static Lane Reversal)	Normal Operation	Lane Numbers Reversed

IBEX PEAK-M (DMI, FDI, GPIO)

IBEX PEAK-M (LVDS, DDI)

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RTC Circuitry

CMOS Settings	J2
Clear CMOS	1-2
Save CMOS	1-X (Default)

TPM Settings	J3
Clear ME RTC registers	1-2
Save ME RTC registers	1-X (Default)

IBEX PEAK-M (HDA,JTAG,SATA)

INTVRMEN - Integrated SUS 1.1V VRM Enable High - Enable Internal VRs

HDA_DOCK_EN# has a weak internal pull-up. High : the security measures defined in the Flash Descriptor Security will be in effect (default). Low : the Flash Descriptor Security will be overridden.

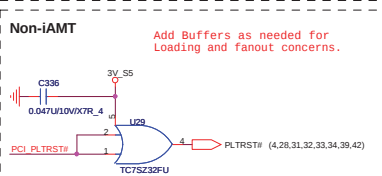
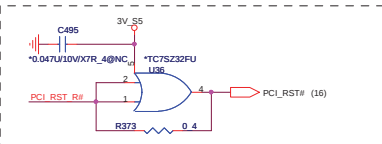
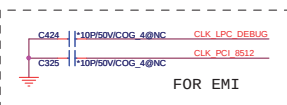
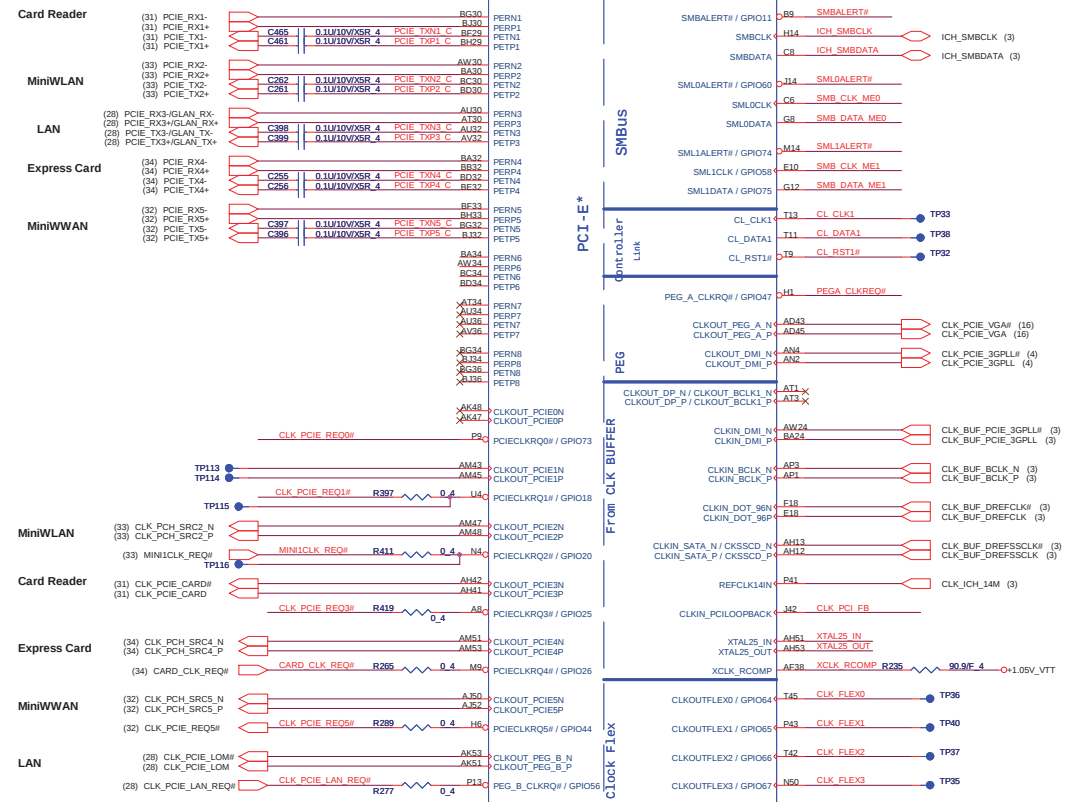
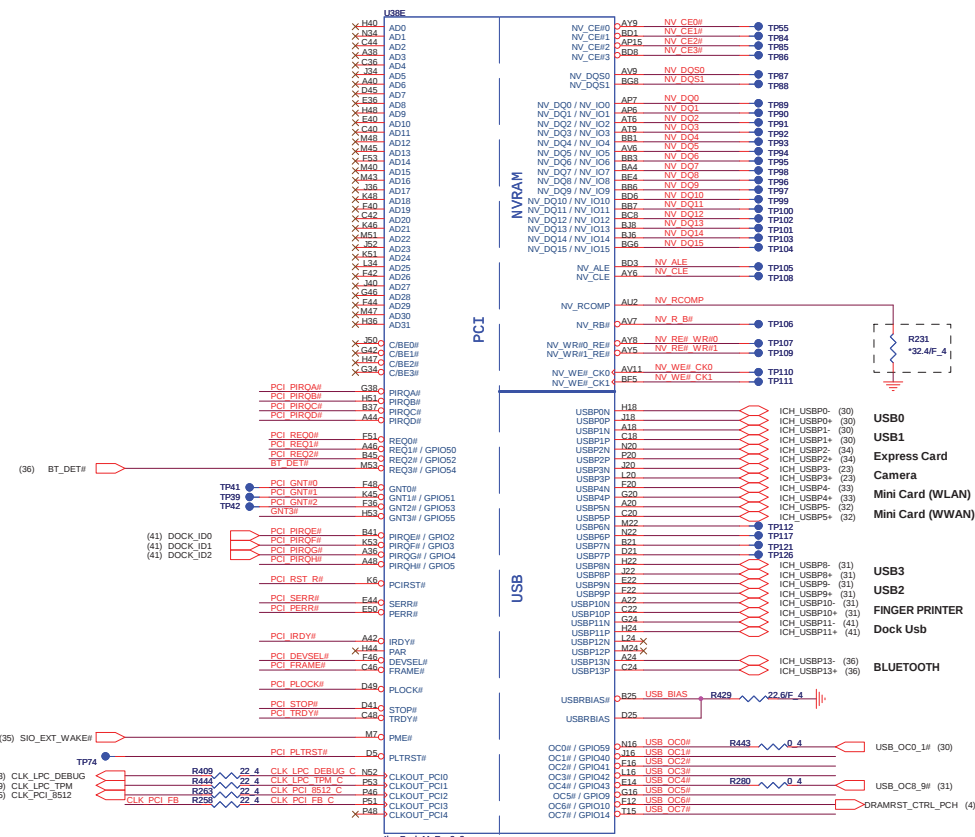
Distance between the PCH and cap on the "P" signal should be identical distance between the PCH and cap on the "N" signal for the same pair.

No Reboot Strap

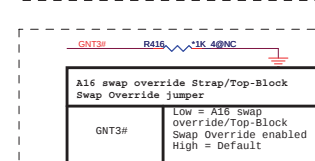
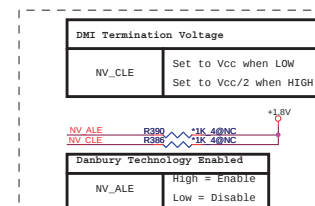
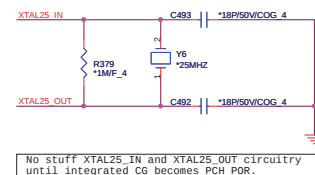
IBEX PEAK-M (PCI,USB,NVRAM)

IBEX PEAK-M (PCI-E,SMBUS,CLK)

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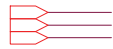


Boot BIOS Strap	
PCI_GNT0#	PCI_GNT#1
0	0
0	1
1	0
1	1

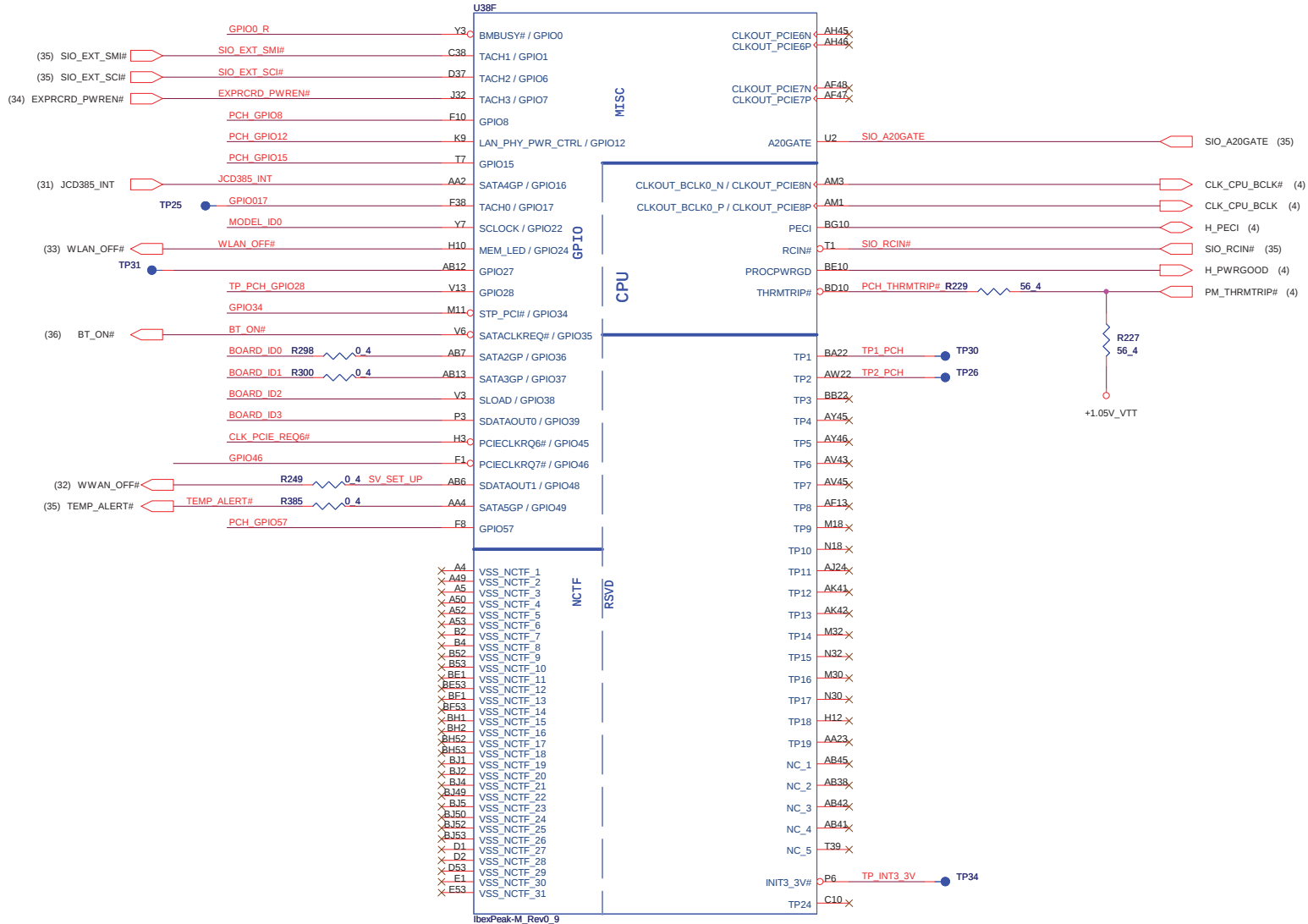


IBEX PEAK-M (GPIO,VSS_NCTF,RSVD)

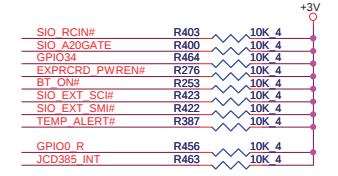
(3,4,8,9,10,12,14,15,17,23,24,25,26,28,29,30,31,32,33,34,35,36,38,39,40,41,42,44,46,47,48,49,51)
(4,8,9,10,12,30,33,42,44)
(3,4,6,8,9,10,12,44,47,49)



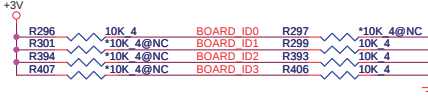
11



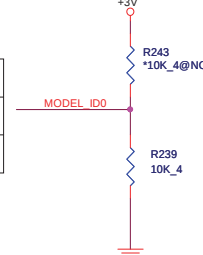
EC-A-02



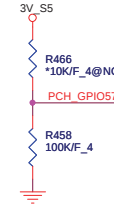
Board ID For Function	ID3 GPIO39	ID2 GPIO38	ID1 GPIO37	ID0 GPIO36
SDV	0	0	0	0
SIV	0	0	0	1
SIT	0	0	1	0
SVT	0	0	1	1
SOVP	0	1	0	0



Model ID	MODEL_ID0
14"	0
15"	1

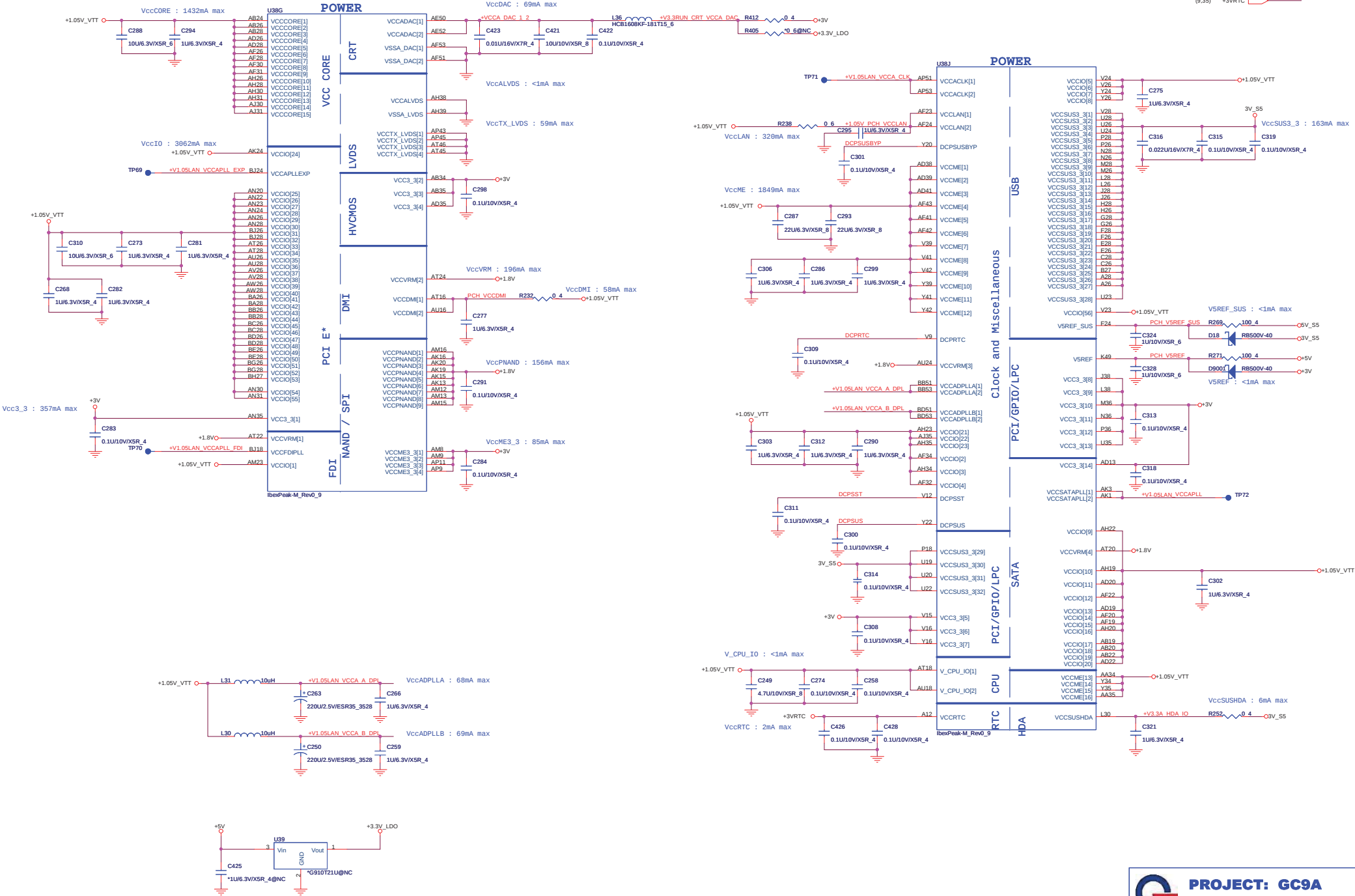


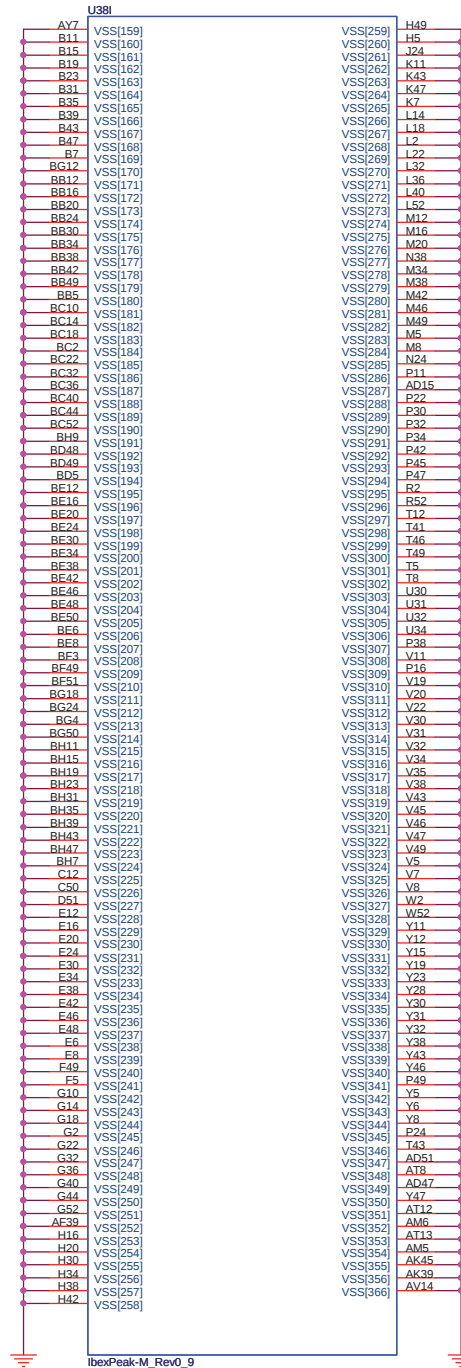
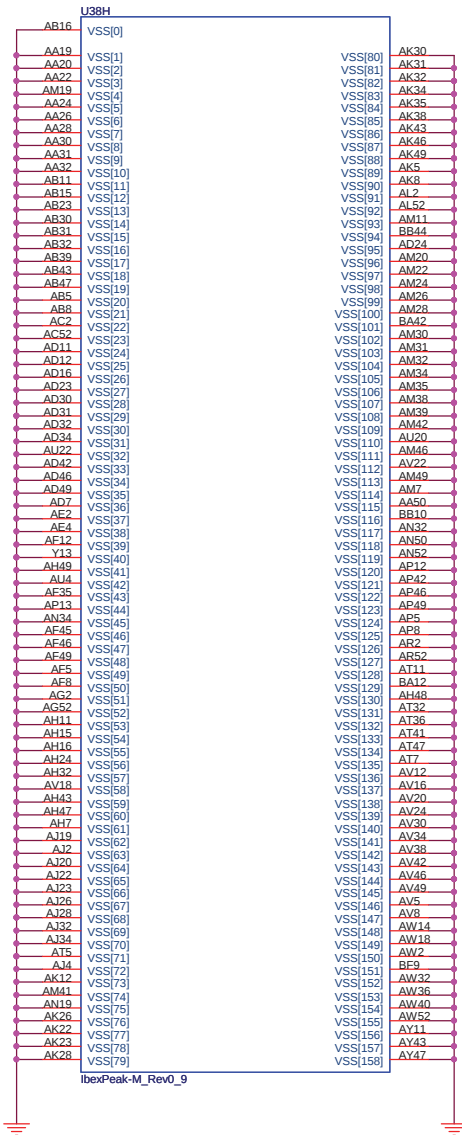
TPM physical presence	
PCH_GPIO57	Low: Default



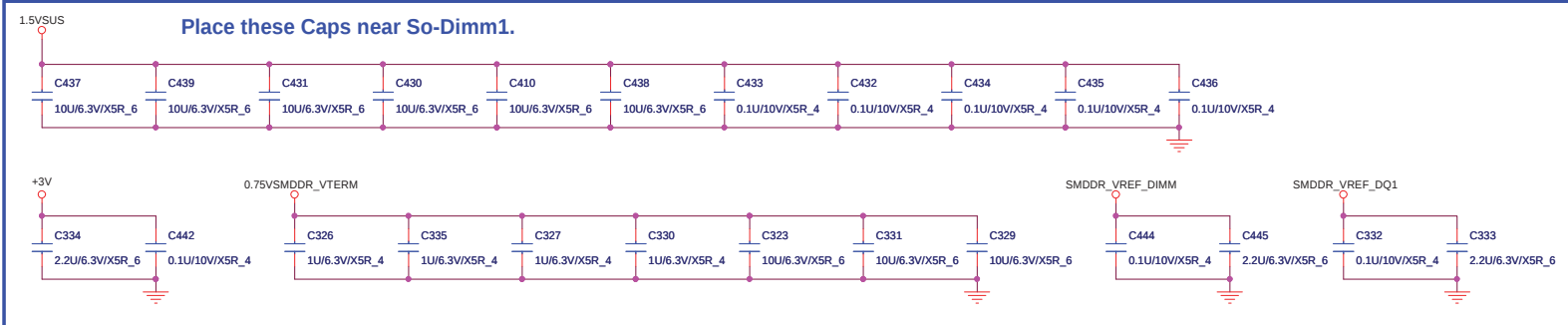
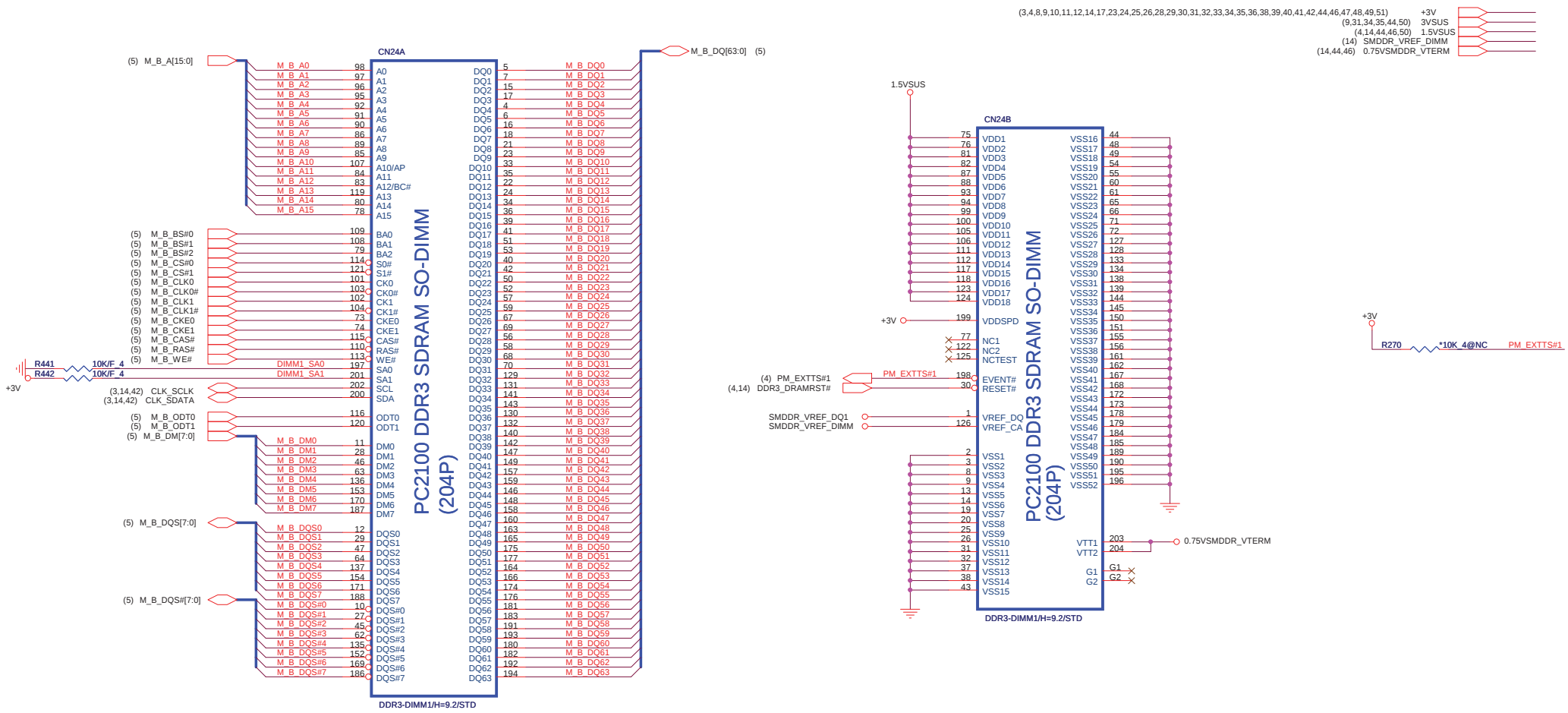
SV_SET_UP	1-X High = Strong (Default)
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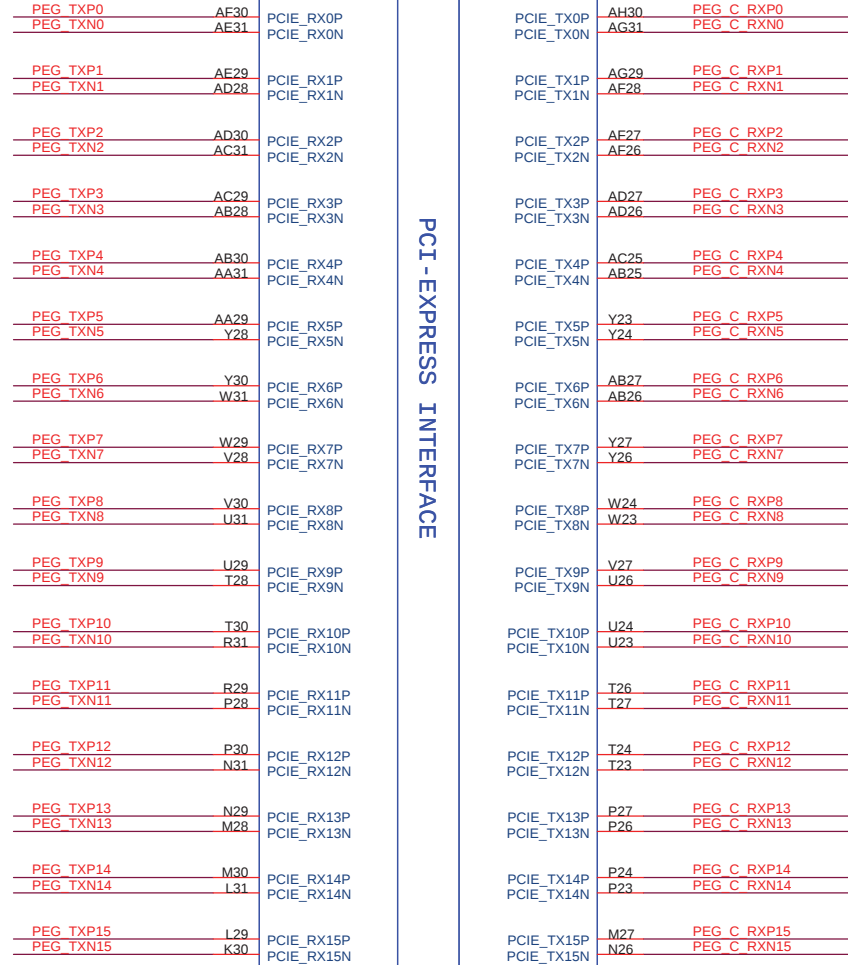
(4) PEG_TXP[0..15]
(4) PEG_TXN[0..15]



U9003A

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PCI-EXPRESS INTERFACE



(10) CLK_PCIE_VGA
(10) CLK_PCIE_VGA#



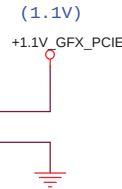
(10) PCI_RST#

100 MHz (+/-300 ppm) input frequency,
0-0.7 V single-ended swing.
clock must be provided less than 400ns
after CLKREQ# is asserted

M92-S2/M92-XT

PCIE_CALRN
PCIE_CALRP

AA22 PCIE_CALRN 2K/F 4
Y22 PCIE_CALRP 1.27K/F 4

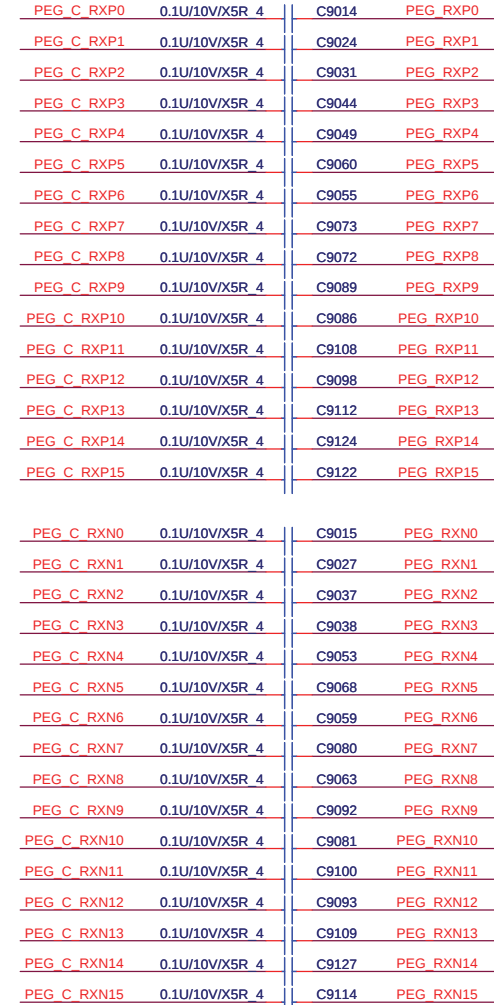


(18,21,22,44,50) +1.1V_GFX_PCIE



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PEG_RXP[0..15] (4)
PEG_RXN[0..15] (4)





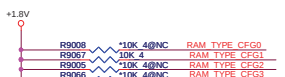
PROJECT: GC9A
Quanta Computer Inc.

Size Custom	Document Number VGA-M92-XT (PCIe) 1/7	Rev 1A
Date: Monday, December 28, 2009		Sheet 16 of 55

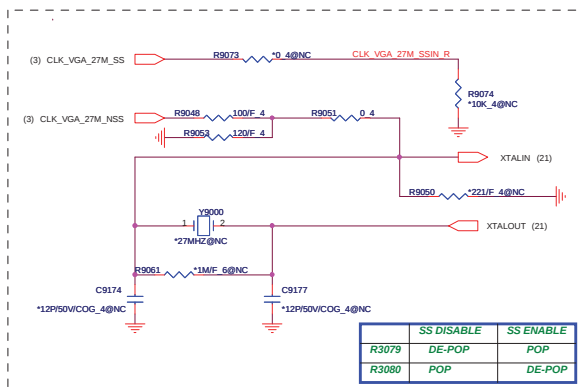
MEMORY APERTURE SIZE SELECT			
MEMORY SIZE	CFG2 GPIO13	CFG1 GPIO12	CFG0 GPIO11
128MB	0	0	0
256MB	0	0	1
64MB	0	1	0
512MB	1	0	0



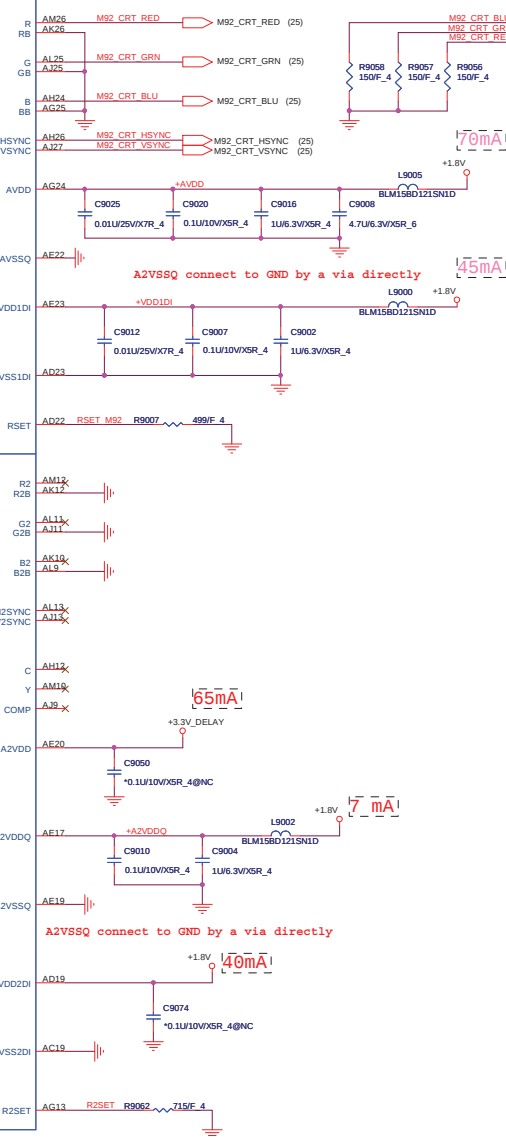
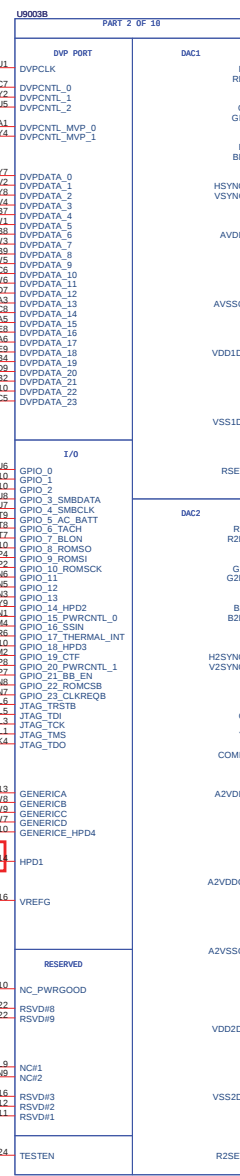
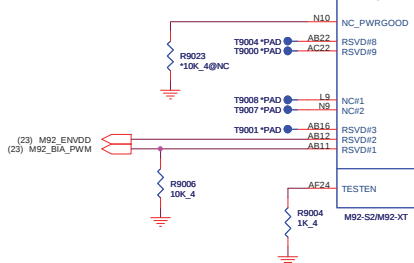
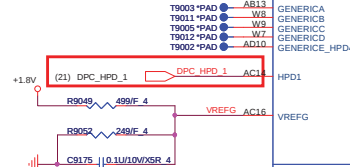
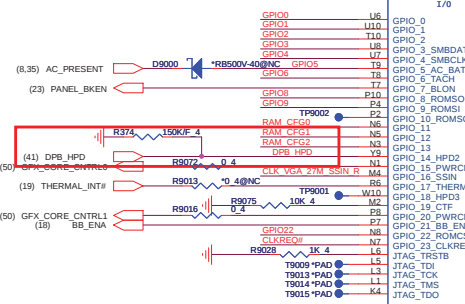
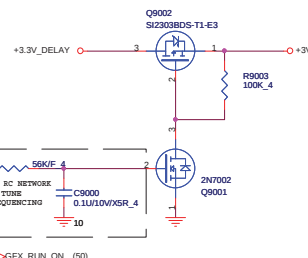
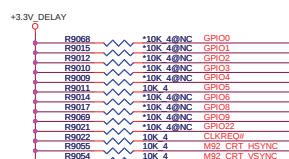
Memory Straps	RAM_TYPE _CFG3	RAM_TYPE _CFG2	RAM_TYPE _CFG1	RAM_TYPE _CFG0
800MHz 512MB(64M*16) Samsung K4W1G1646E-HC12	0	0	0	1
800MHz 512MB(64M*16) Hynix H5TQ1G63BFR-12C	0	0	1	0



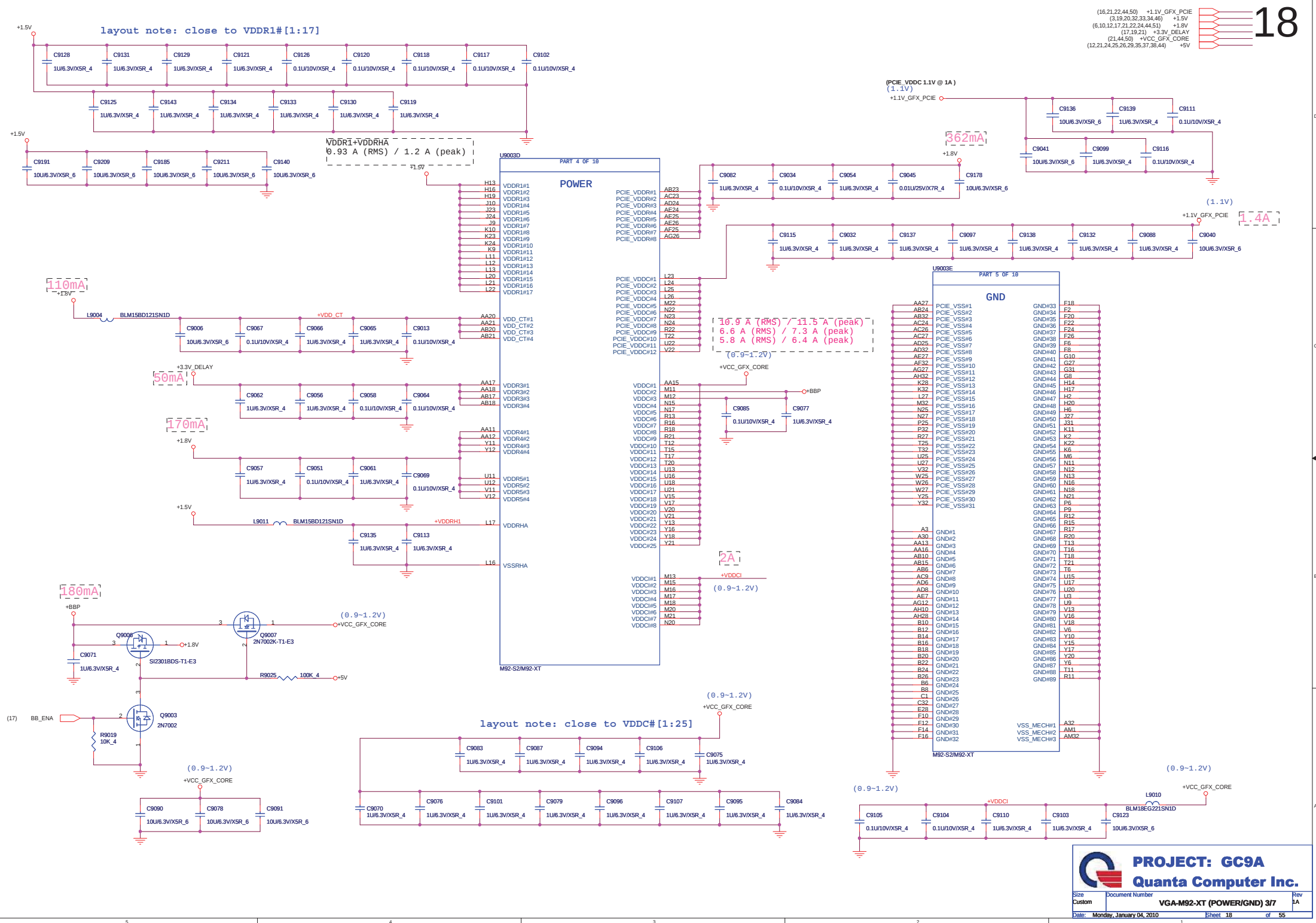
GPIO Straps table	DESCRIPTION OF DEFAULT SETTINGS	G Note Setting
GPI00	GPIO0 - TX_PWREN_EN (Transmitter Power Savings Enable) 0: 50% TX output swing for mobile mode 1: full TX output swing (Default setting for Desktop)	0
GPI01	GPIO1 - TX_DEEMPH_EN (Transmitter De-emphasis Enable) 0: 1x de-emphasis disabled (mobile mode) 1: 1x de-emphasis enabled (Default setting for Desktop)	0
GPI02	GPIO2 - BIF_GEN2_EN (B.G GtIs Enable) 0: Default (Driver Controlled Gen2) 1: Strap Controlled Gen2	0
GPI03	ATI reserved configuration straps.	0
GPI04	ATI reserved configuration straps.	0
GPI05	GPIO_5_AC_BATT 0: Battery saving mode = 0.9 V 1: Performance mode = 2.3 V	1
GPI06	ATI Internal use only	0
GPI08	ATI reserved configuration straps.	0
GPI09	VGA Disable 0 - VGA Controller capacity enabled 1 - The device will not recognize as the system's VGA controller	0
GPI022	Disable external BIOS ROM device 0 - Disable external BIOS ROM device 1 - Enable external BIOS ROM device	0
HSYNC	AUD[0] 00 - No audio function	1
VSYNC	01 - Audio for DisplayPort and HDMI if adapter is detected 10 - Audio for DisplayPort only 11 - Audio for both DisplayPort and HDMI	1



	<i>SS DISABLE</i>	<i>SS ENABLE</i>
<i>R3079</i>	<i>DE-POP</i>	<i>POP</i>
<i>R3080</i>	<i>POP</i>	<i>DE-POP</i>



Layout Note:
Place 150 ohm
termination re-
close to ATI



MEMORY INTERFACE

(3,18,20,32,33,34,46) +1.5V
(17,18,21) +3.3V_DELAY

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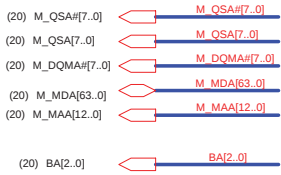
U9003C

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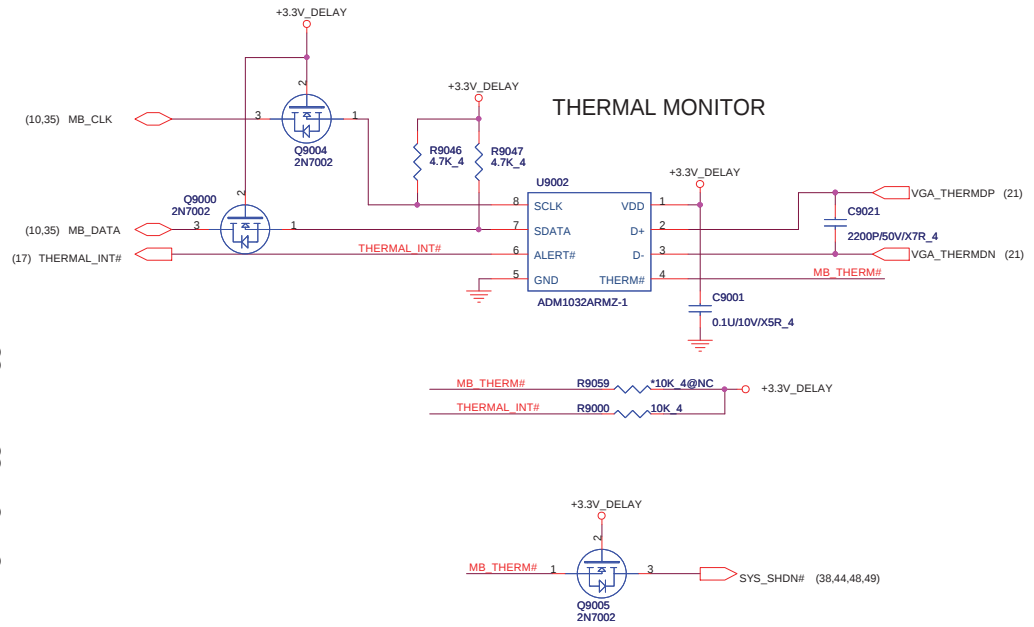
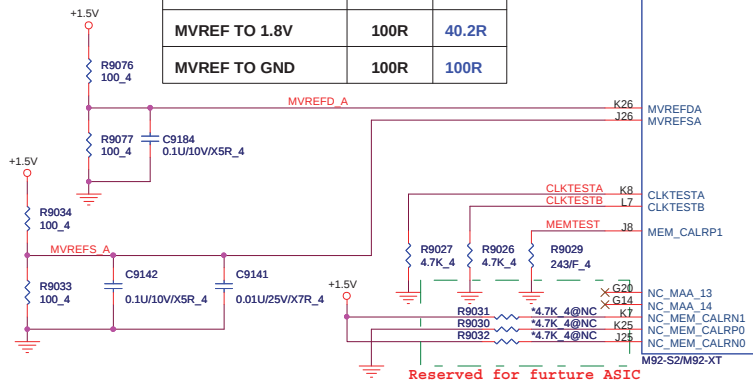
MEMORY INTERFACE

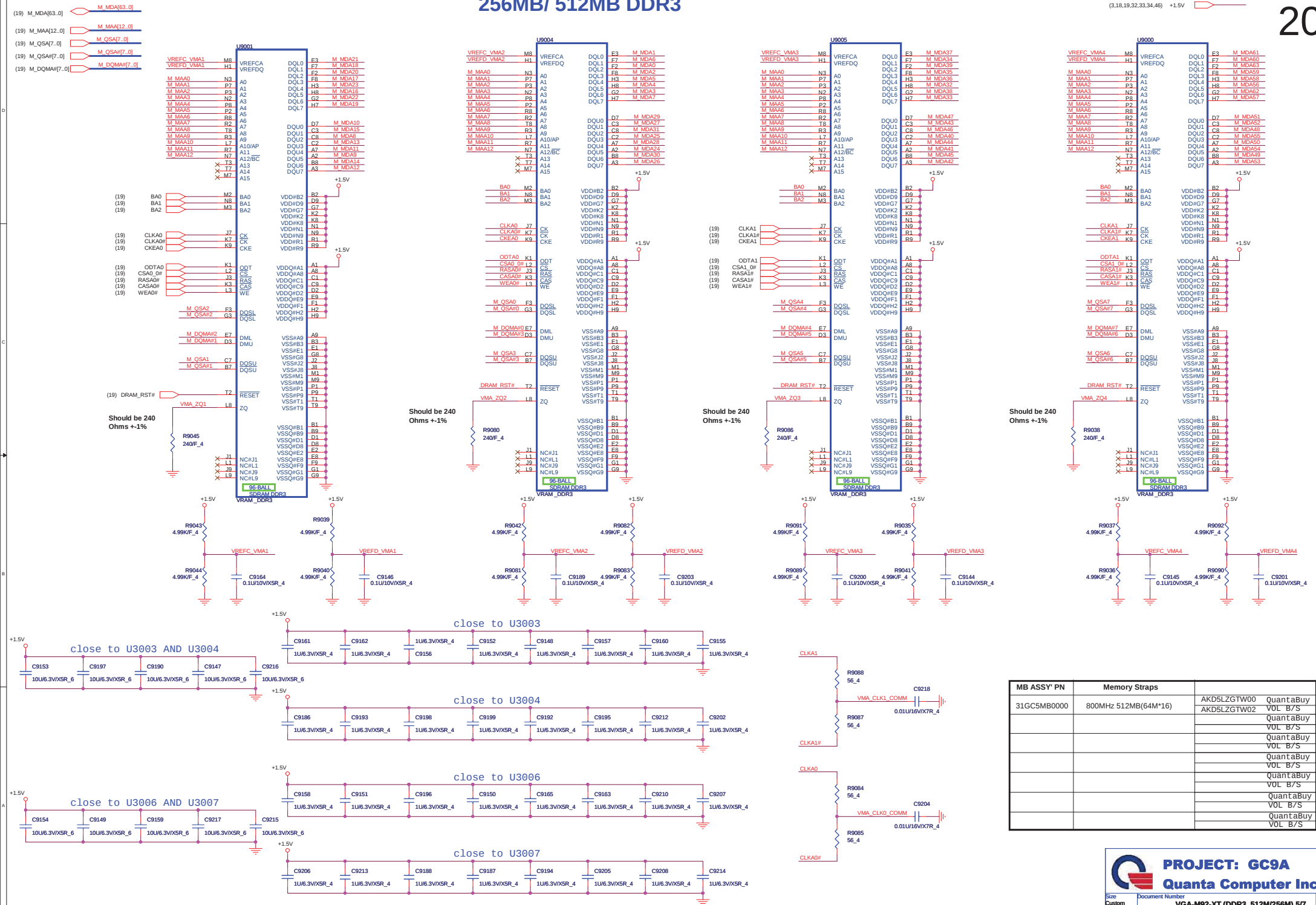
M MDA0 K27	DQA_0	M MAA0 K17	DQA_0
M MDA1 J29	DQA_1	M MAA1 J20	DQA_1
M MDA2 H30	DQA_2	M MAA2 H23	DQA_2
M MDA3 H32	DQA_3	M MAA3 G23	DQA_3
M MDA4 Q29	DQA_4	M MAA4 Q24	DQA_4
M MDA5 F28	DQA_5	M MAA5 H24	DQA_5
M MDA6 F32	DQA_6	M MAA6 J19	DQA_6
M MDA7 F30	DQA_7	M MAA7 K19	DQA_7
M MDA8 C30	DQA_8	M MAA8 J14	DQA_8
M MDA9 F27	DQA_9	M MAA9 K14	DQA_9
M MDA10 A28	DQA_10	M MAA10 J11	DQA_10
M MDA11 C28	DQA_11	M MAA11 J13	DQA_11
M MDA12 E27	DQA_12	M MAA12 L15	DQA_12
M MDA13 G26	DQA_13	M MAA13 L15	DQA_13
M MDA14 D26	DQA_14	M MAA14 H11	DQA_14
M MDA15 F25	DQA_15	M MAA15 G11	DQA_15
M MDA16 A25	DQA_16		
M MDA17 E25	DQA_17		
M MDA18 E25	DQA_18		
M MDA19 D24	DQA_19		
M MDA20 E23	DQA_20		
M MDA21 F23	DQA_21		
M MDA22 D22	DQA_22		
M MDA23 F21	DQA_23		
M MDA24 E21	DQA_24		
M MDA25 D20	DQA_25		
M MDA26 F19	DQA_26		
M MDA27 A19	DQA_27		
M MDA28 D18	DQA_28		
M MDA29 F17	DQA_29		
M MDA30 C17	DQA_30		
M MDA31 E17	DQA_31		
M MDA32 D16	DQA_32		
M MDA33 F15	DQA_33		
M MDA34 A15	DQA_34		
M MDA35 D14	DQA_35		
M MDA36 F13	DQA_36		
M MDA37 A13	DQA_37		
M MDA38 E13	DQA_38		
M MDA39 C13	DQA_39		
M MDA40 E11	DQA_40		
M MDA41 A11	DQA_41		
M MDA42 C11	DQA_42		
M MDA43 F11	DQA_43		
M MDA44 A9	DQA_44		
M MDA45 C9	DQA_45		
M MDA46 F9	DQA_46		
M MDA47 D8	DQA_47		
M MDA48 E7	DQA_48		
M MDA49 A7	DQA_49		
M MDA50 C7	DQA_50		
M MDA51 F7	DQA_51		
M MDA52 A5	DQA_52		
M MDA53 C3	DQA_53		
M MDA54 E1	DQA_54		
M MDA55 G7	DQA_55		
M MDA56 G7	DQA_56		
M MDA57 G8	DQA_57		
M MDA58 G1	DQA_58		
M MDA59 G3	DQA_59		
M MDA60 J6	DQA_60		
M MDA61 J1	DQA_61		
M MDA62 J3	DQA_62		
M MDA63 J5	DQA_63		

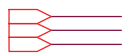
WRITE STROBE READ STROBE



DIVIDER RESISTORS	DDR3	GDDR3
MVREF TO 1.8V	100R	40.2R
MVREF TO GND	100R	100R







120mA

(1.1V)

+1.1V GFX_PCIE

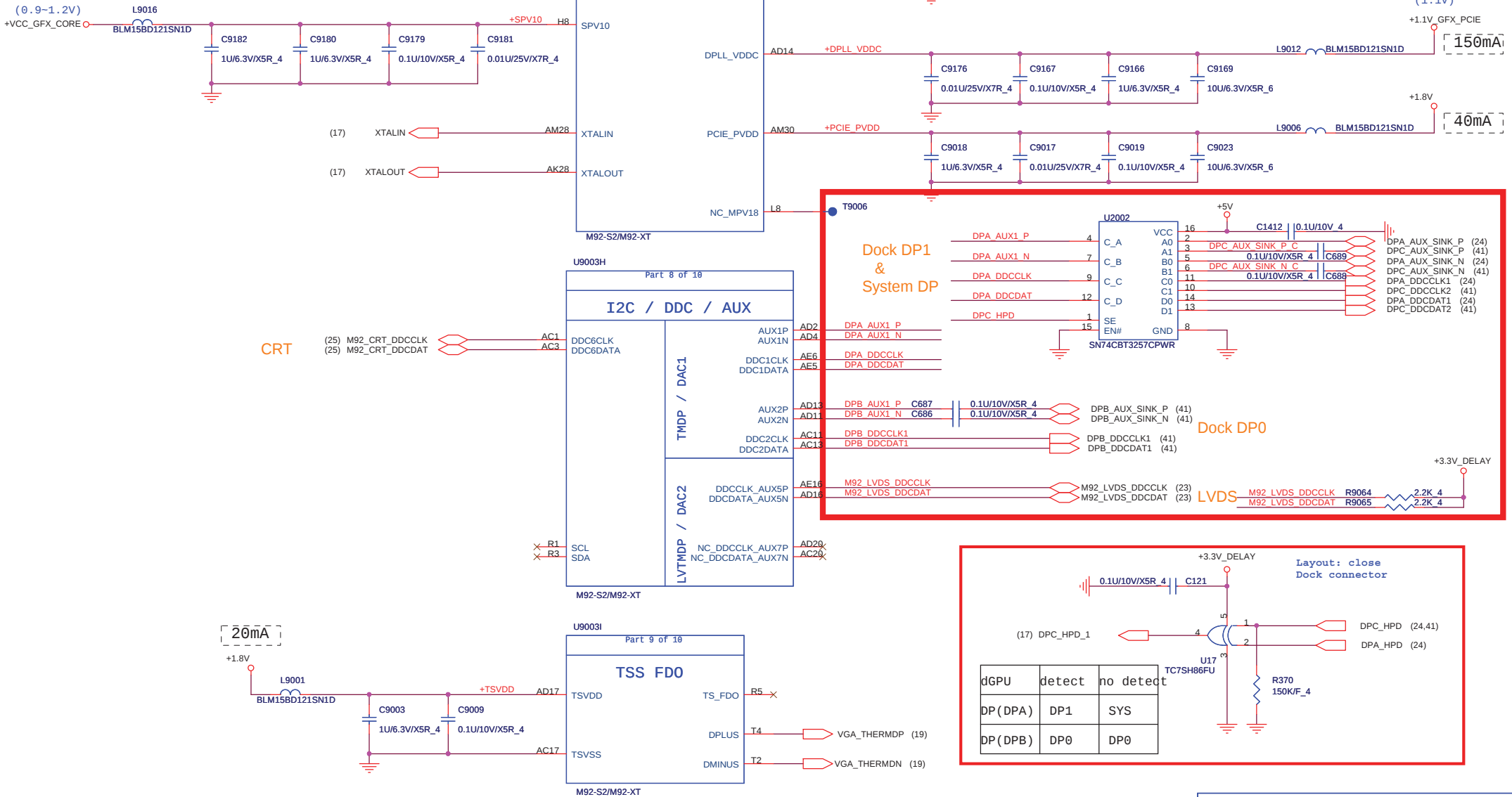
150mA

+1.8V

40mA

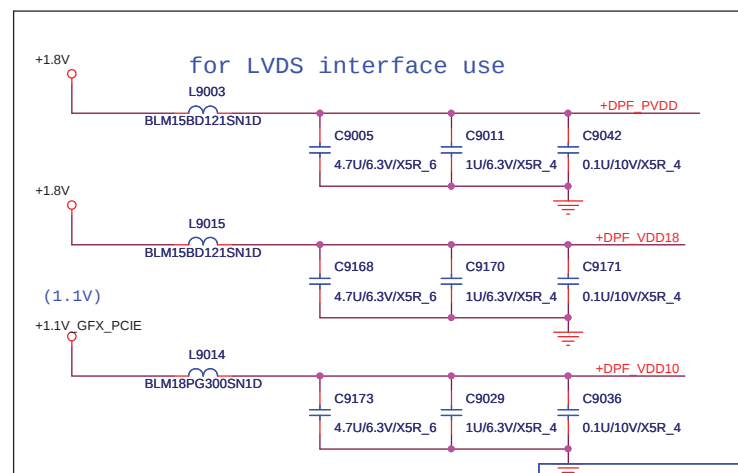
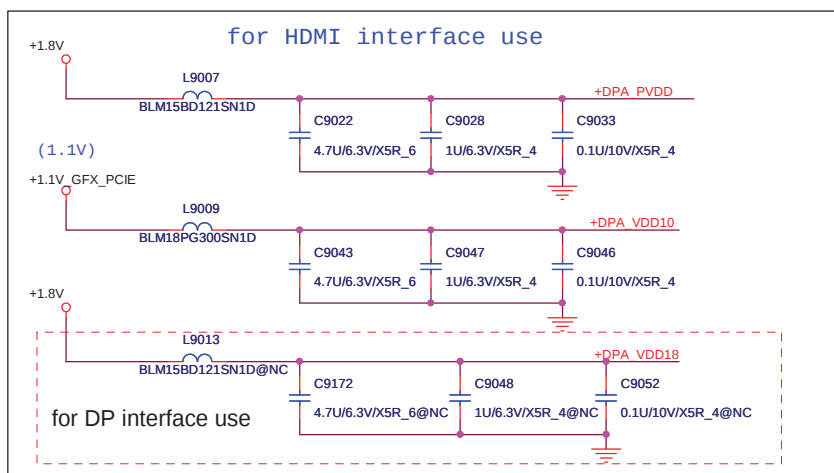
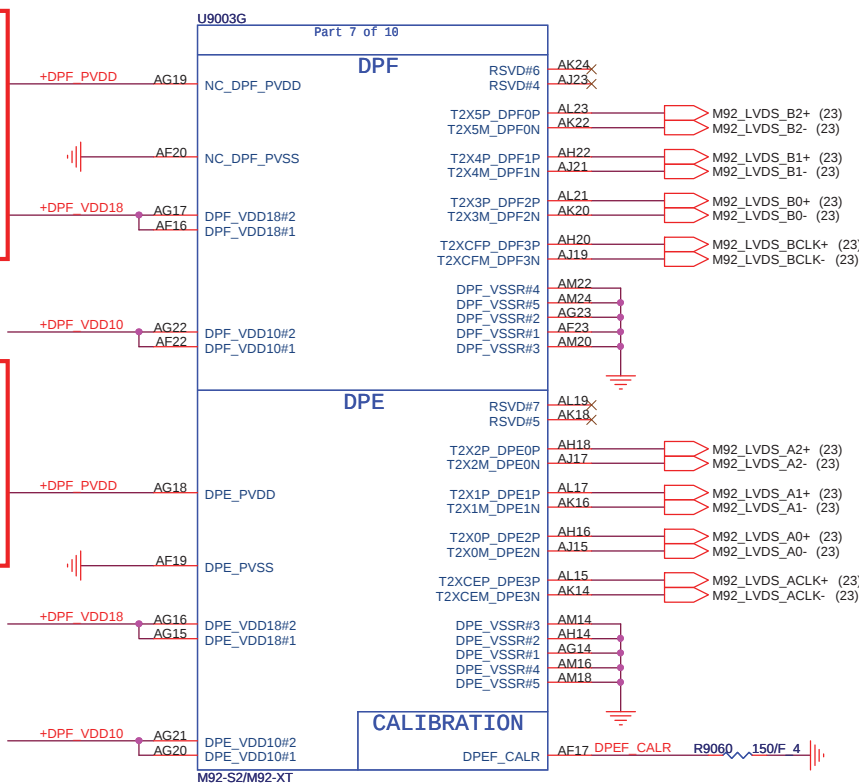
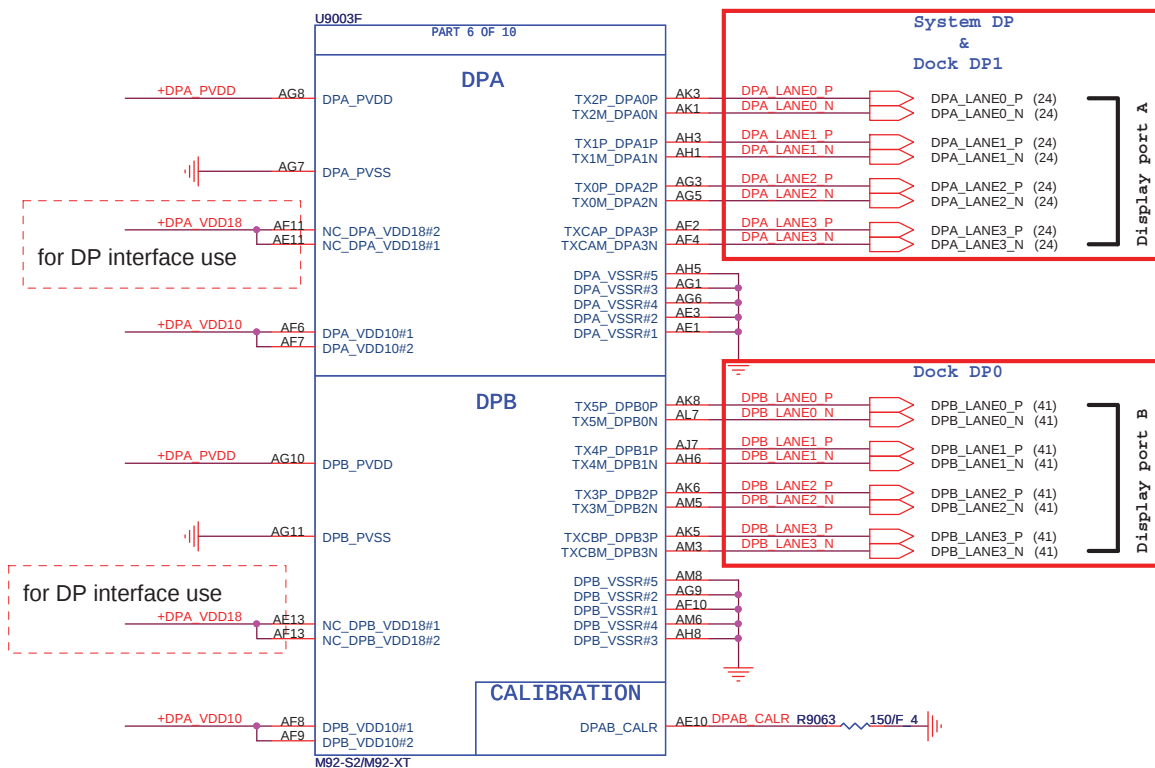
(0.9~1.2V)

+VCC_GFX_CORE



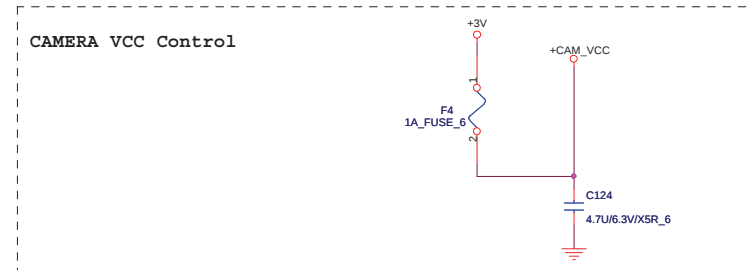
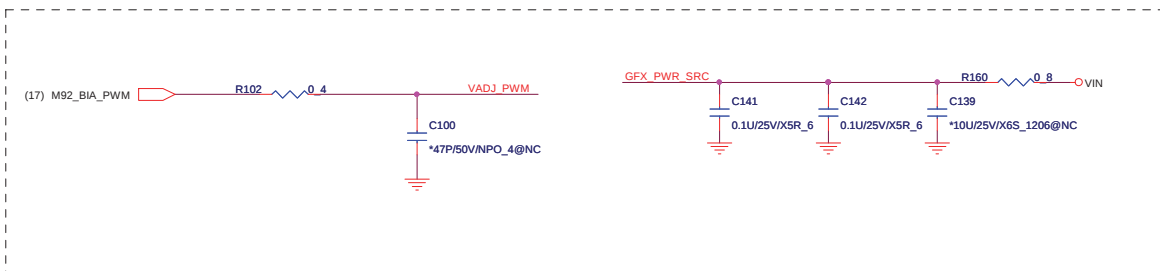
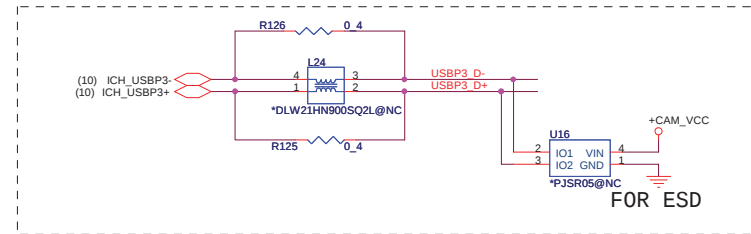
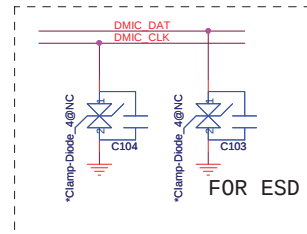
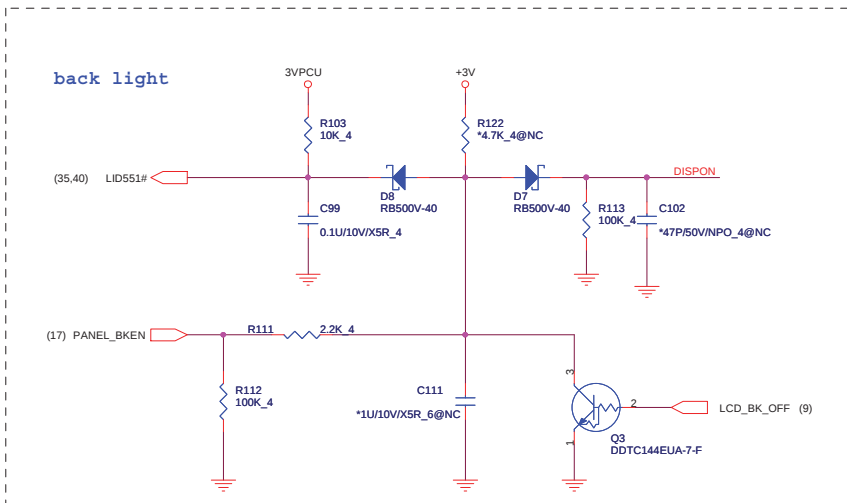
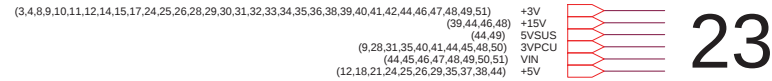
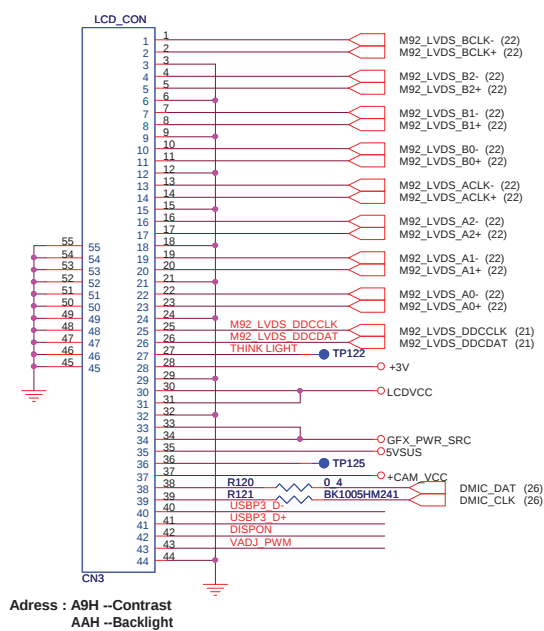
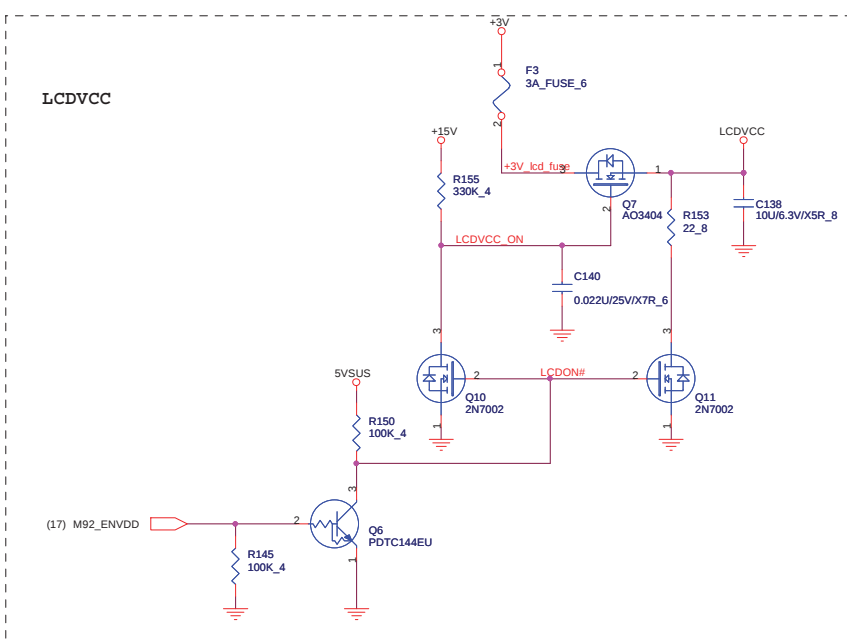
TMDP(HDMI) INTERFACE

LVDS INTERFACE

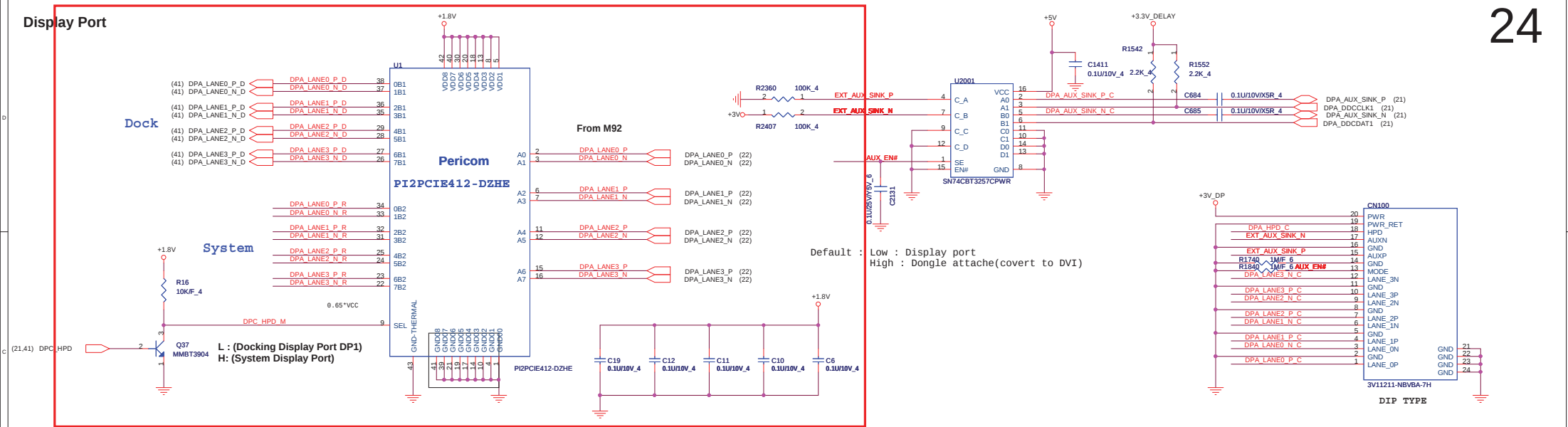


PROJECT: GC9A
Quanta Computer Inc.

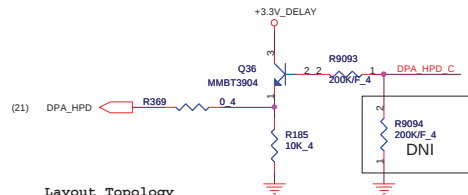
Size Custom	Document Number VGA-M92-XT (TMDP I/F) 7/7	Rev 1A
Date: Monday, December 28, 2009	Sheet 22	of 55



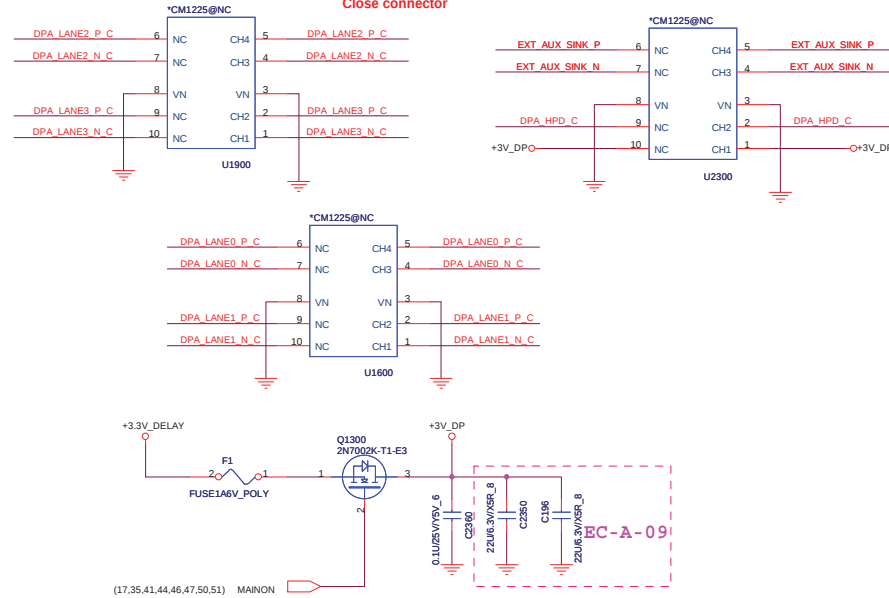
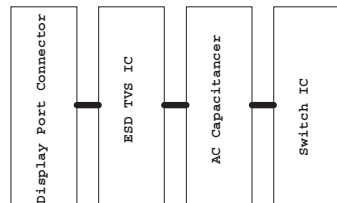
Display Port



Reserve For ESD
Close connector

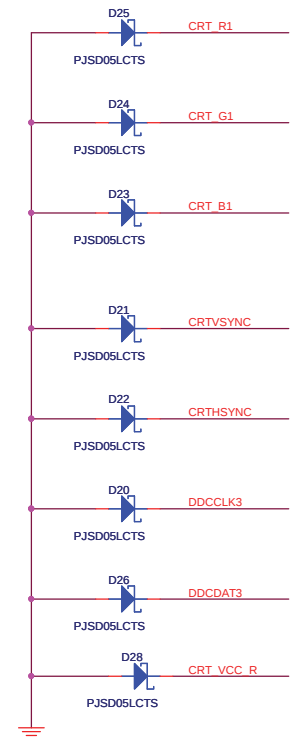


Layout Topology



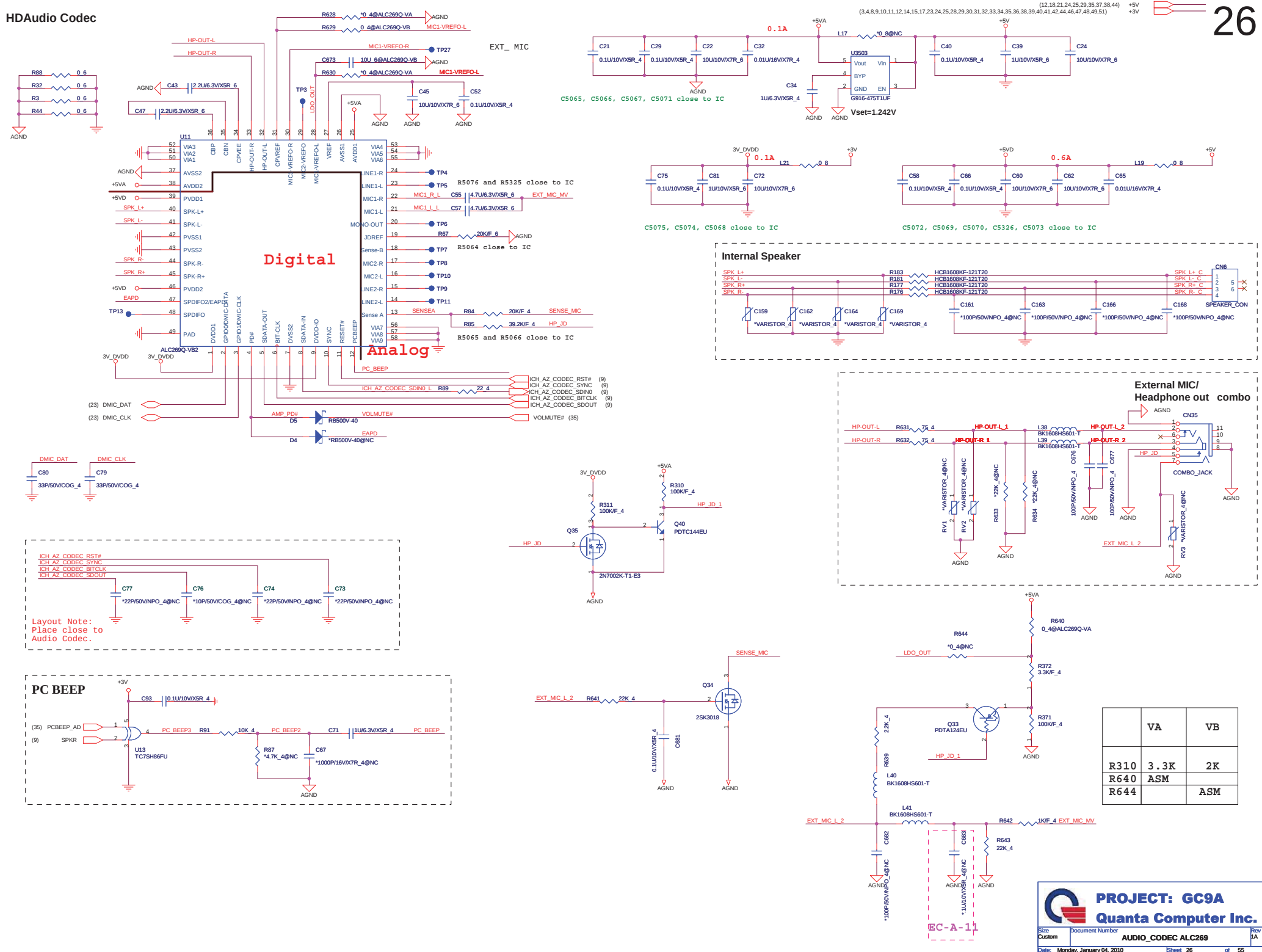
Close to display port connector

Display port sys	Component	Value	Pin
DPA_LANE0_N_R	C245	0.1U/10V/XSR 4	DPA_LANE0_N_C
DPA_LANE0_P_R	C244	0.1U/10V/XSR 4	DPA_LANE0_P_C
DPA_LANE1_N_R	C391	0.1U/10V/XSR 4	DPA_LANE1_N_C
DPA_LANE1_P_R	C390	0.1U/10V/XSR 4	DPA_LANE1_P_C
DPA_LANE2_N_R	C247	0.1U/10V/XSR 4	DPA_LANE2_N_C
DPA_LANE2_P_R	C246	0.1U/10V/XSR 4	DPA_LANE2_P_C
DPA_LANE3_N_R	C254	0.1U/10V/XSR 4	DPA_LANE3_N_C
DPA_LANE3_P_R	C253	0.1U/10V/XSR 4	DPA_LANE3_P_C

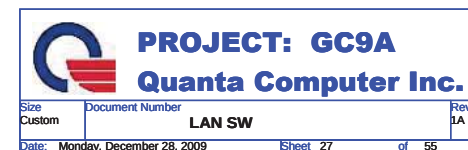


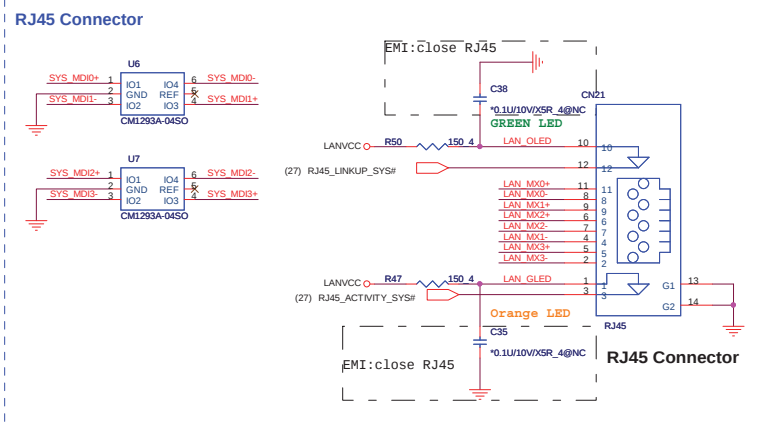
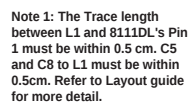
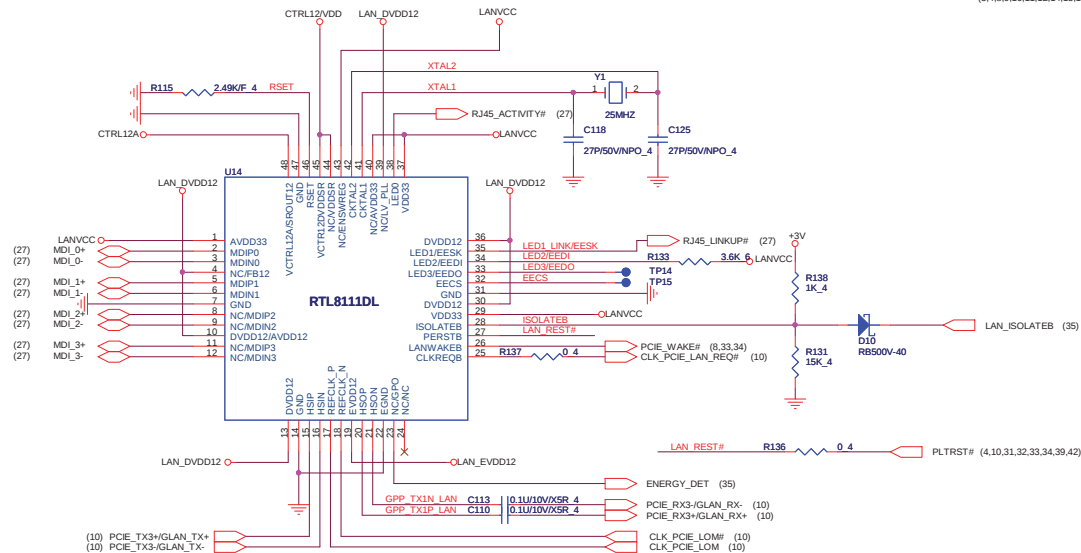
SEL	FUNCTION(COM)
LOW	IN_x0
HIGH	IN_x1

HDAudio Codec



	VA	VB
R310	3.3K	2K
R640	ASM	
R644		ASM



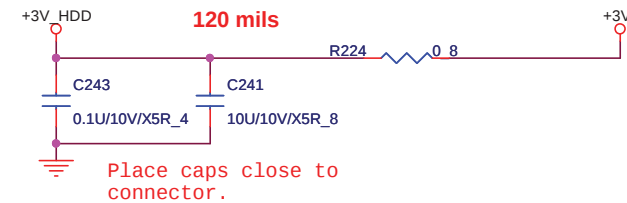
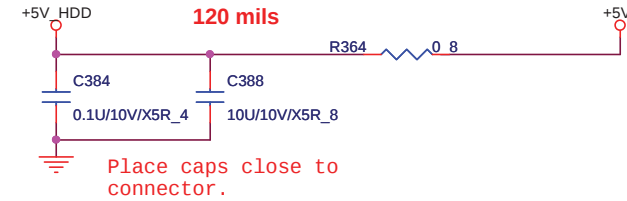
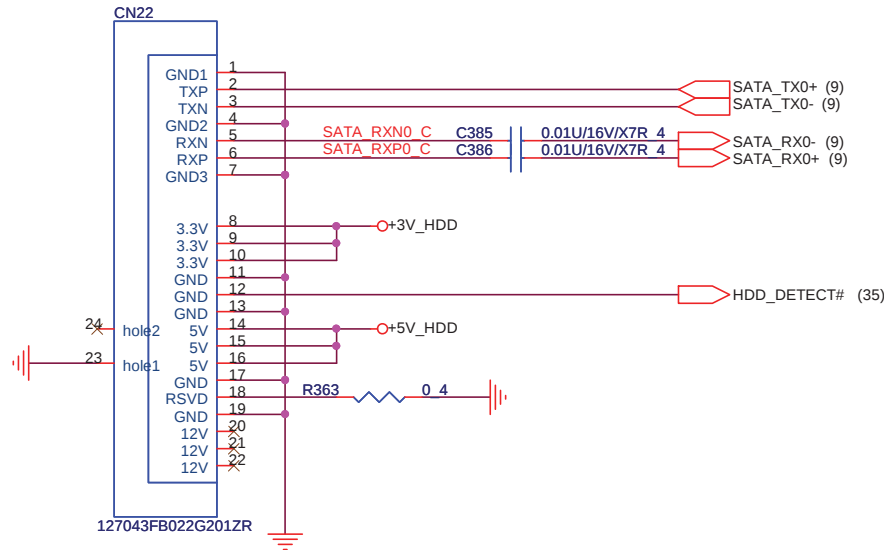


SATA Connector.

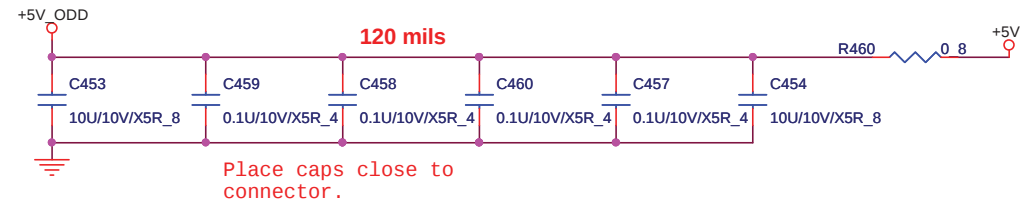
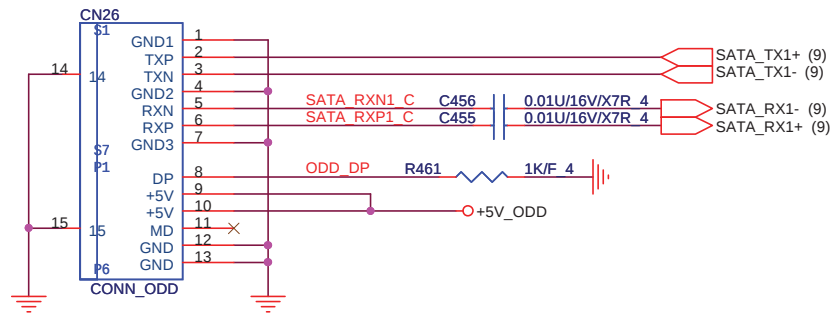
(3,4,8,9,10,11,12,14,15,17,23,24,25,26,28,30,31,32,33,34,35,36,38,39,40,41,42,44,46,47,48,49,51) +5V
+3V



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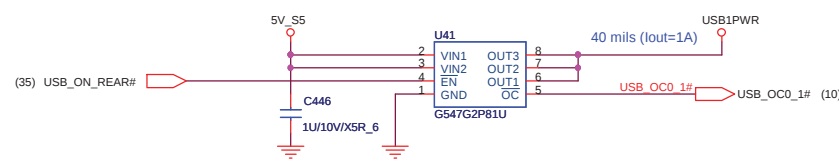


ODD Connector



PROJECT: GC9A Quanta Computer Inc.		Size Custom	Document Number SATA (HDD&CD_ROM)	Rev 1A
		Date: Tuesday, January 05, 2010		Sheet 29 of 55

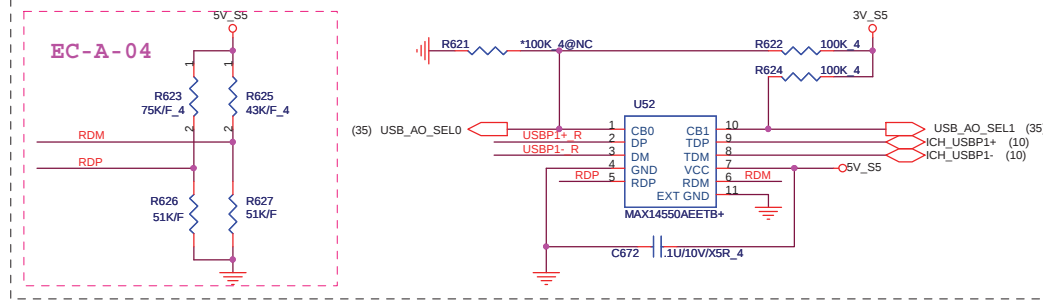
USBX1



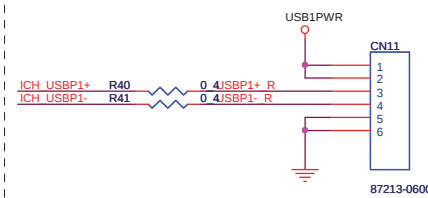
(3,4,8,9,10,11,12,14,15,17,23,24,25,26,28,29,31,32,33,34,35,36,38,39,40,41,42,44,46,47,48,49,51) (12,31,44) 5V_S5 +3V

30

Support Black-berry function



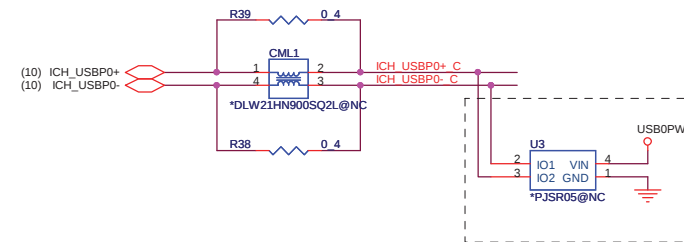
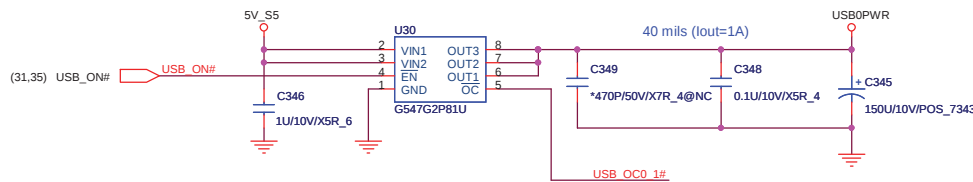
USB X1----> Wire to board conn



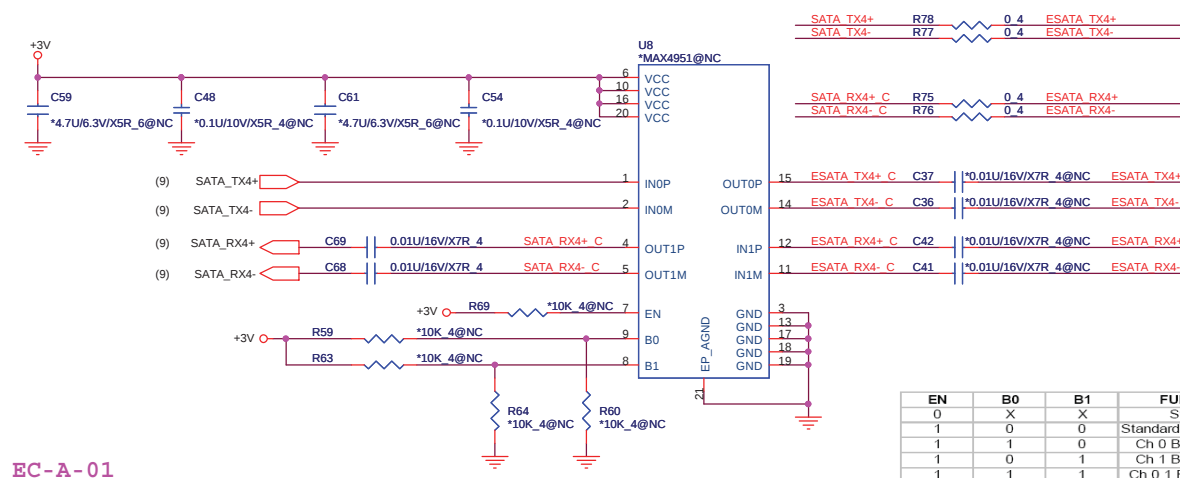
USB 1

	w/ AOU3	w/o AOU3
R40	NO ASM	ASM
R41	NO ASM	ASM
U52	ASM	NO ASM
R622	ASM	NO ASM
R624	ASM	NO ASM
R626	ASM	NO ASM
C676	ASM	NO ASM

USB + E-SATA

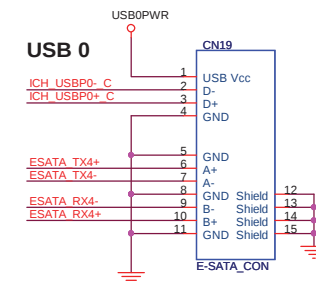
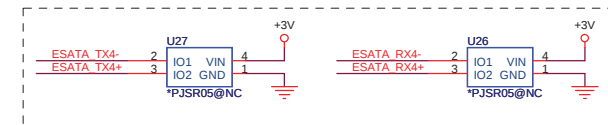


E-SATA RE-DRIVER



EC-A-01

EN	B0	B1	FUNCTION
0	X	X	Standby
1	0	0	Standard SATA Output
1	1	0	Ch 0 Boost Output
1	0	1	Ch 1 Boost Output
1	1	1	Ch 0,1 Boost Output

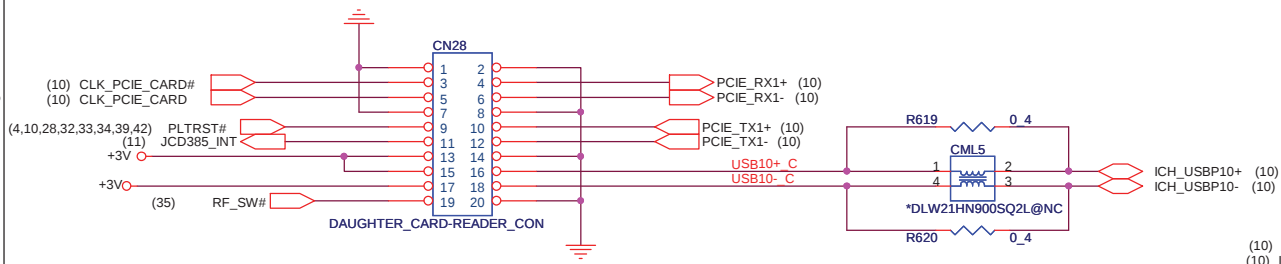
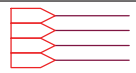


PROJECT: GC9A
Quanta Computer Inc.

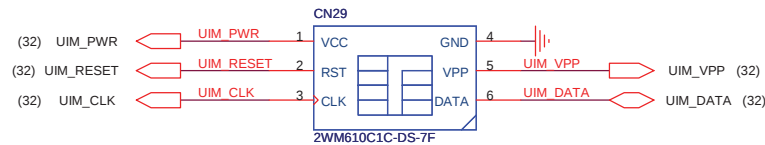
Size Custom Document Number **USB X1/USB+ESATA** Rev 1A

Date: Monday, December 28, 2009 Sheet 30 of 55

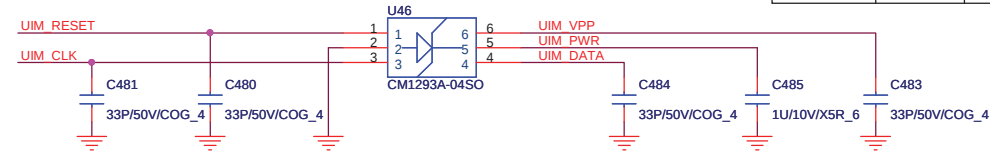
(3,4,8,9,10,11,12,14,15,17,23,24,25,26,28,29,30,32,33,34,35,36,38,39,40,41,42,44,46,47,48,49,51)
 (9,23,28,35,40,41,44,45,48,50)
 (12,30,44)
 5V_S5
 +3V
 3VPCU
 3VSUS
 (9,34,35,44,50)



SIM Card CONN

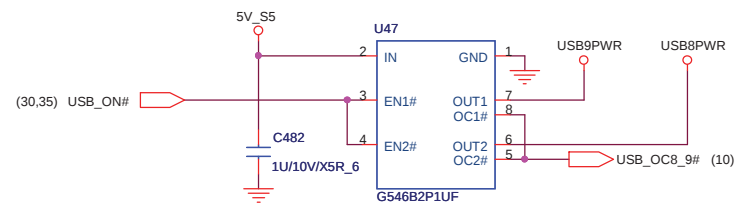
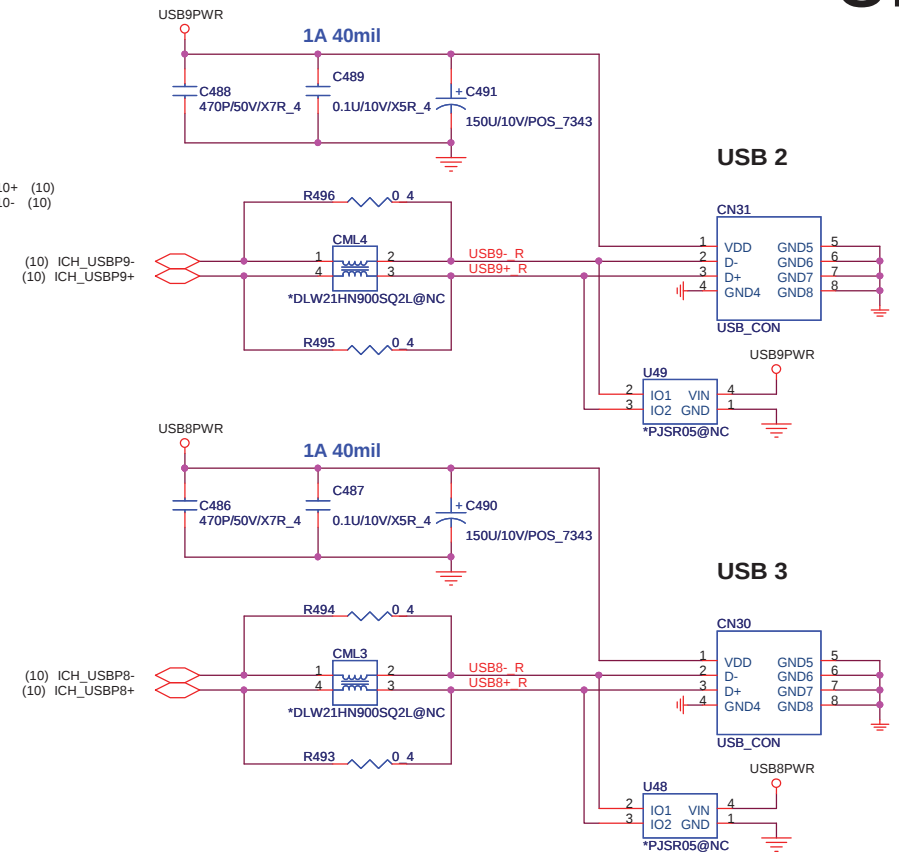
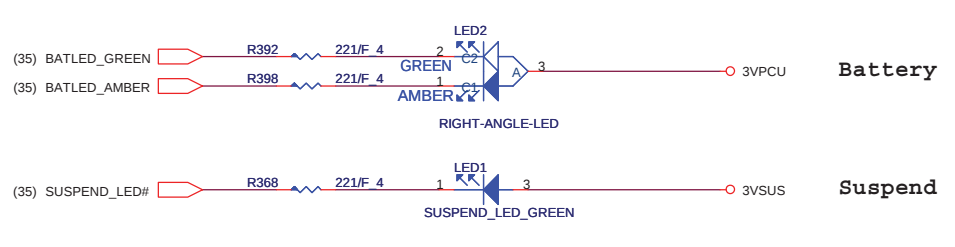


Layout Note:
 UIM_RESET, UIM_CLK, UIM_DATA routing as short as possible

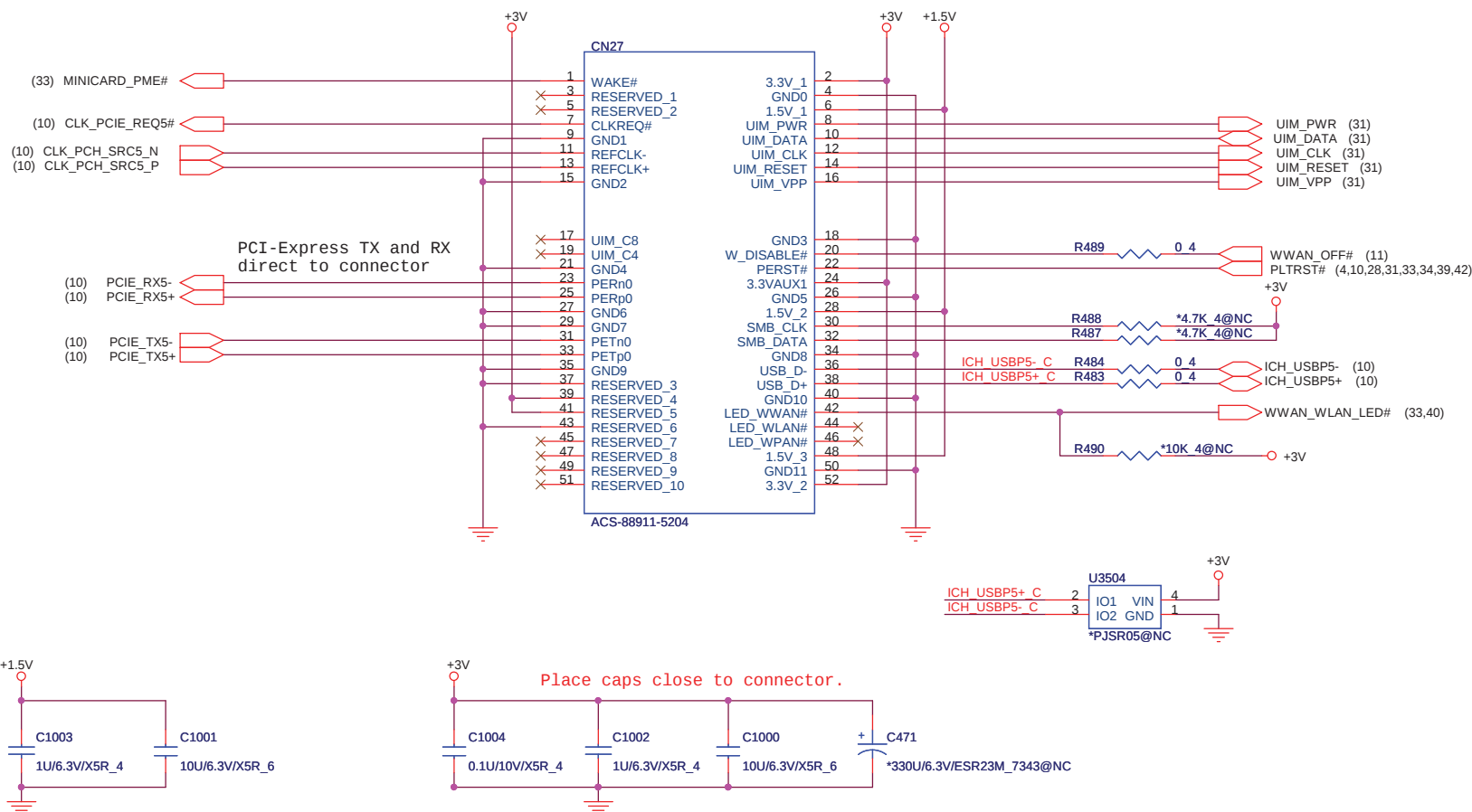


	w/ WWAN	w/o WWAN
CN29	ASM	NO ASM
U46	ASM	NO ASM
C480 - C481	ASM	NO ASM
C483 - C485	ASM	NO ASM

FRONT LEDs



MiniCard WWAN connector



	w/ WWAN	w/o WWAN
CN27	ASM	NO ASM
R489	ASM	NO ASM
R484	ASM	NO ASM
R483	ASM	NO ASM
C1000~C1004	ASM	NO ASM

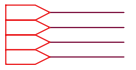
PROJECT: GC9A
Quanta Computer Inc.

Size: Custom
 Document Number: MINI-Card (UWB, WWAN)
 Date: Monday, December 28, 2009
 Sheet 32 of 55
 Rev 1A

MiniCard WLAN/WiMAX connector

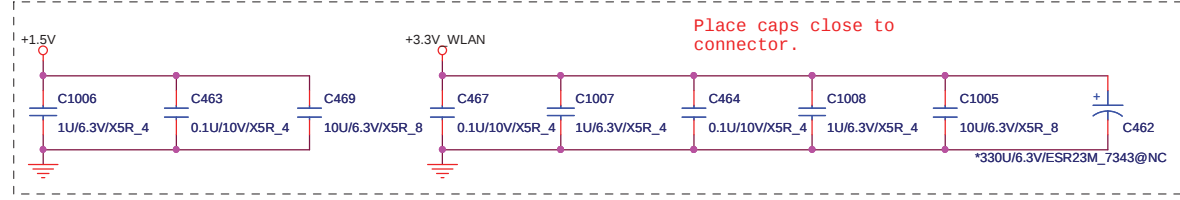
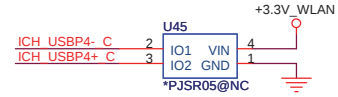
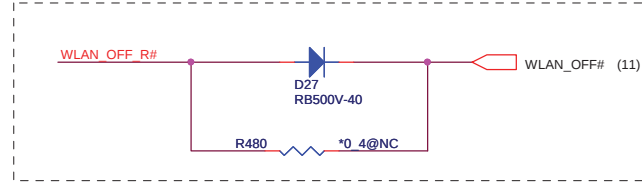
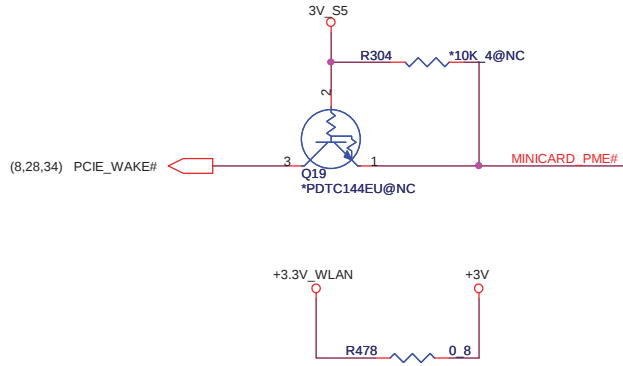
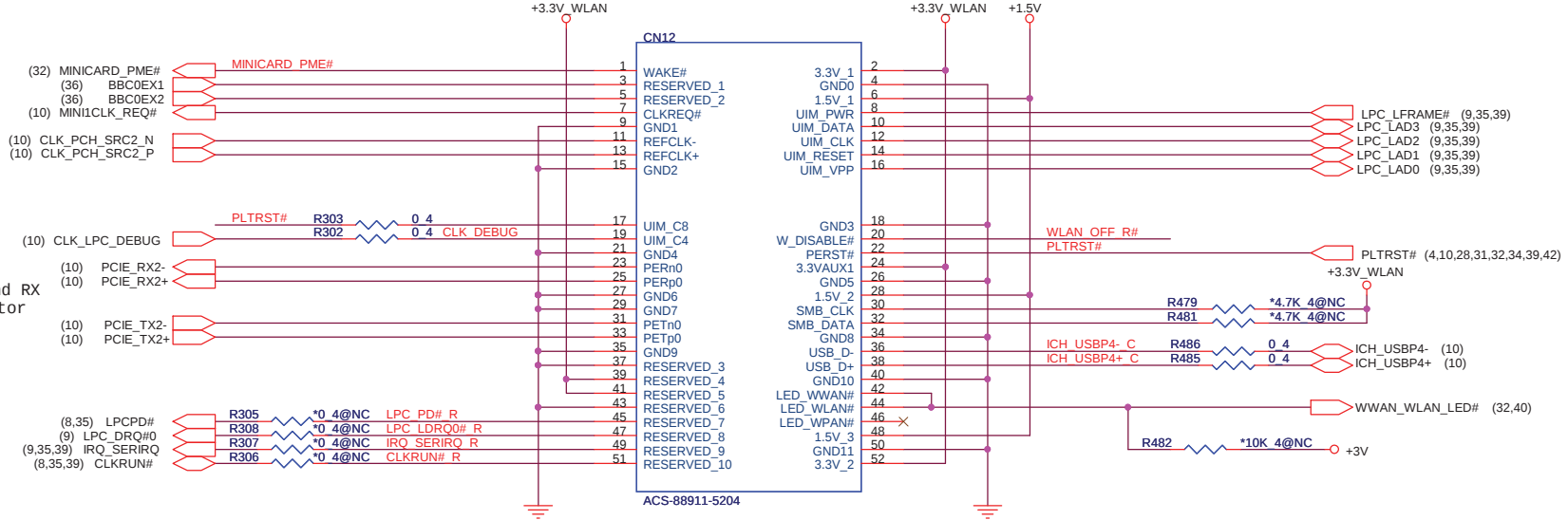
(3,4,8,9,10,11,12,14,15,17,23,24,25,26,28,29,30,31,32,34,35,36,38,39,40,41,42,44,46,47,48,49,51)
(3,18,19,20,32,34,46)
(9,23,28,31,35,40,41,44,45,48,50)
(23,44,45,46,47,48,49,50,51)

+3V
+1.5V
3VPCU
VIN



33

PCI-Express TX and RX
direct to connector

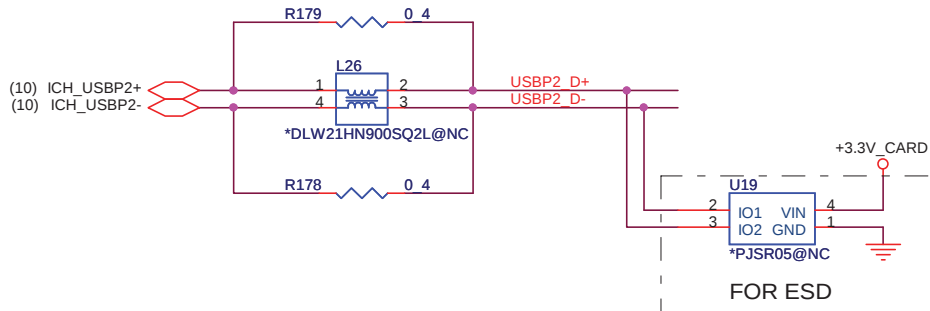


Express Card

(3,4,8,9,10,11,12,14,15,17,23,24,25,26,28,29,30,31,32,33,35,36,38,39,40,41,42,44,46,47,48,49,51)
 (3,18,19,20,32,33,46)
 (9,31,35,44,50)

+3V
 +1.5V
 3VSUS

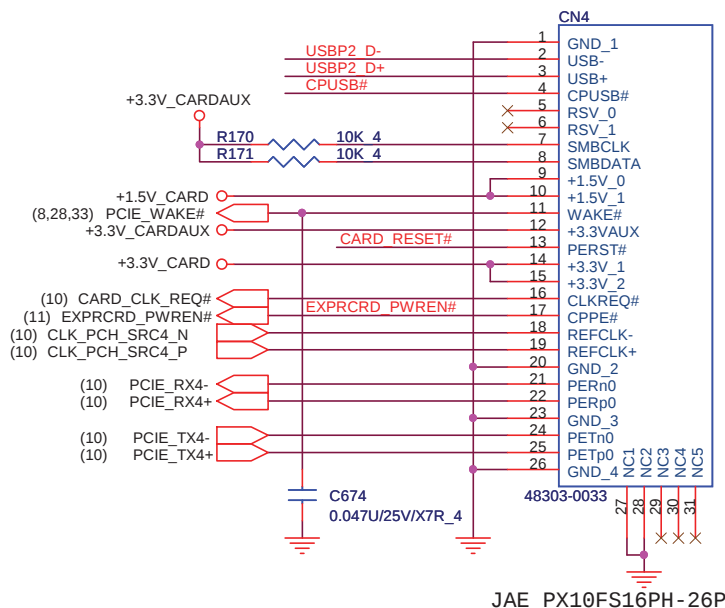
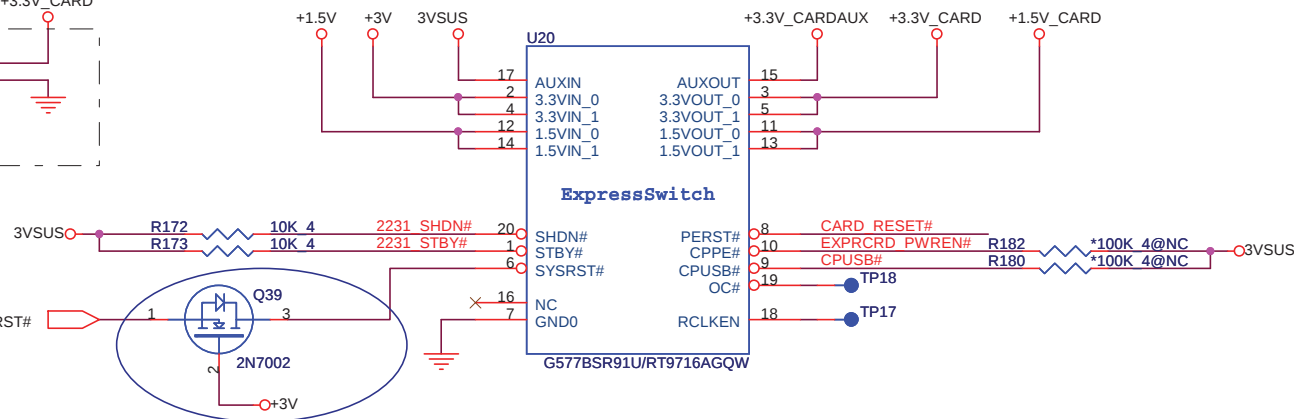
34



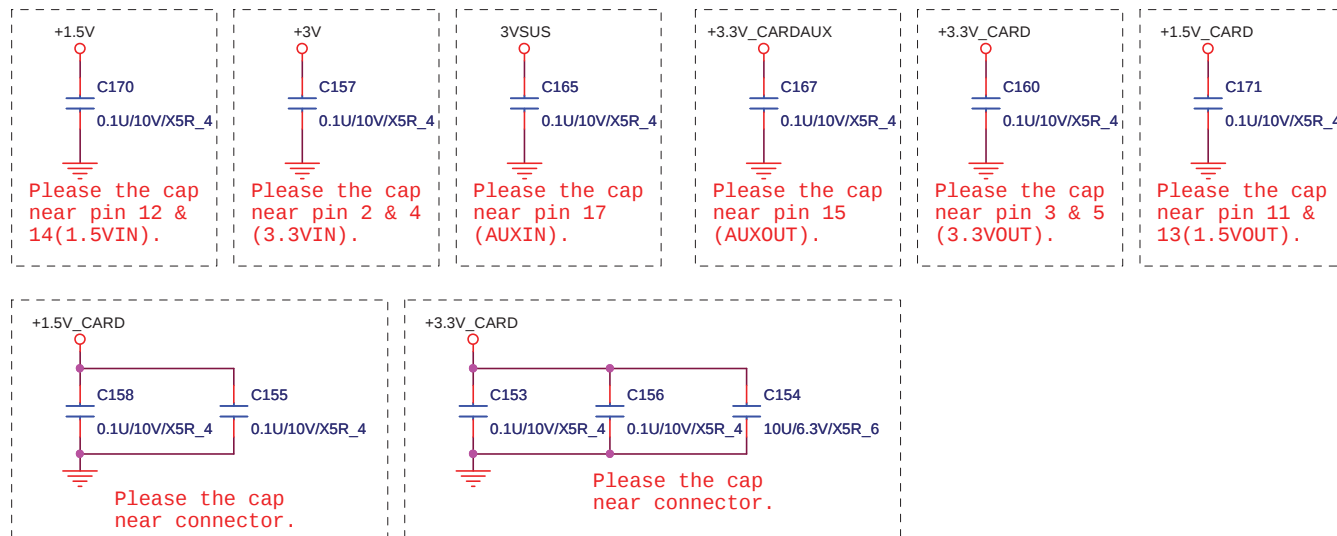
+1.5V_CARD Max. 650mA, Average 500mA.
 +3V_CARD Max. 1300mA, Average 1000mA.

20090928
 avoid current leakage

(4,10,28,31,32,33,39,42) PLTRST#



JAE PX10FS16PH-26P



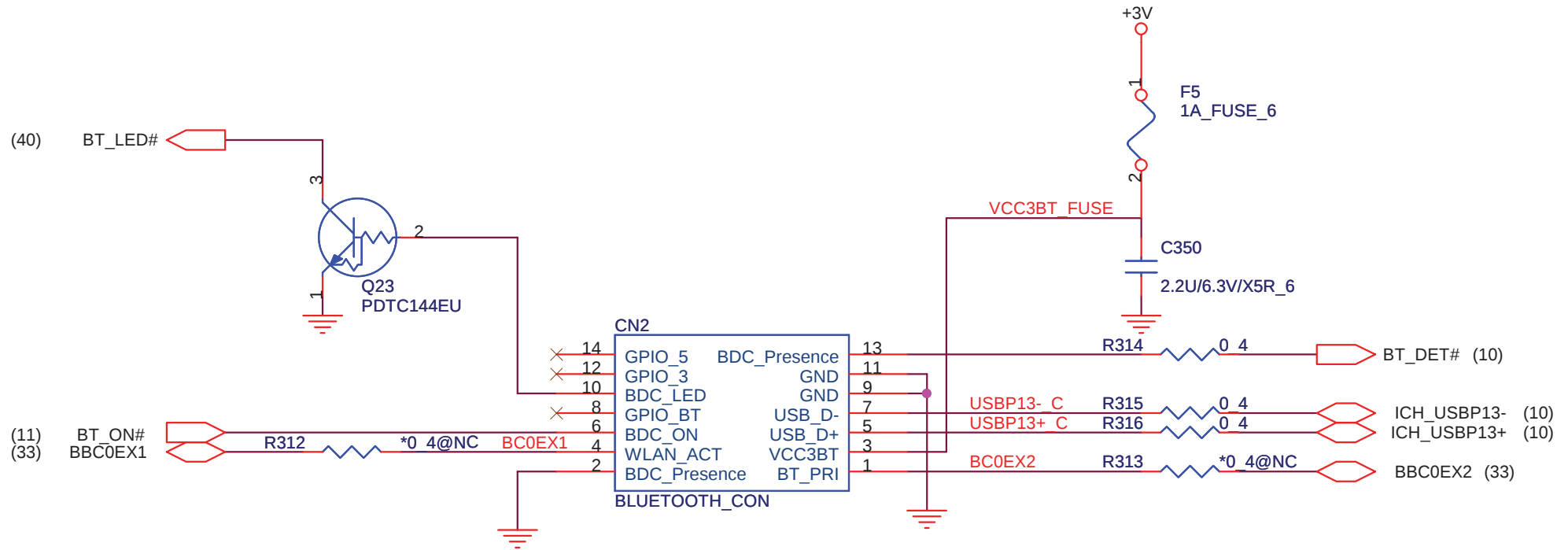


BLUETOOTH

(3,4,8,9,10,11,12,14,15,17,23,24,25,26,28,29,30,31,32,33,34,35,38,39,40,41,42,44,46,47,48,49,51)

+3V

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PROJECT: GC9A
Quanta Computer Inc.

Size
Custom

Document Number

B/T

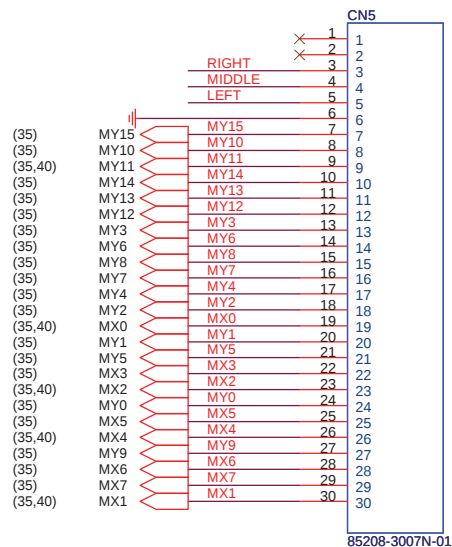
Rev
1A

Date: Monday, December 28, 2009

Sheet 36 of 55

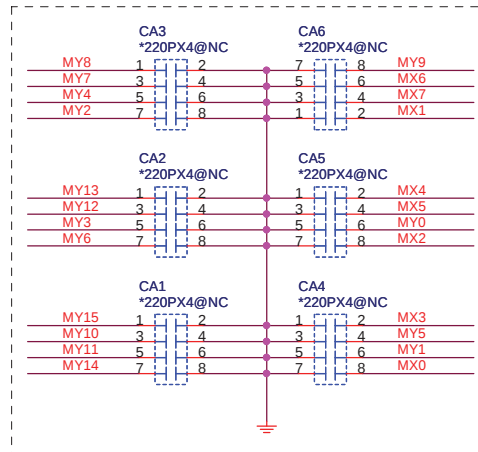
KEYBOARD

KEYBOARD connector



(12,18,21,24,25,26,29,35,38,44)
(9,23,28,31,35,40,41,44,45,48,50)

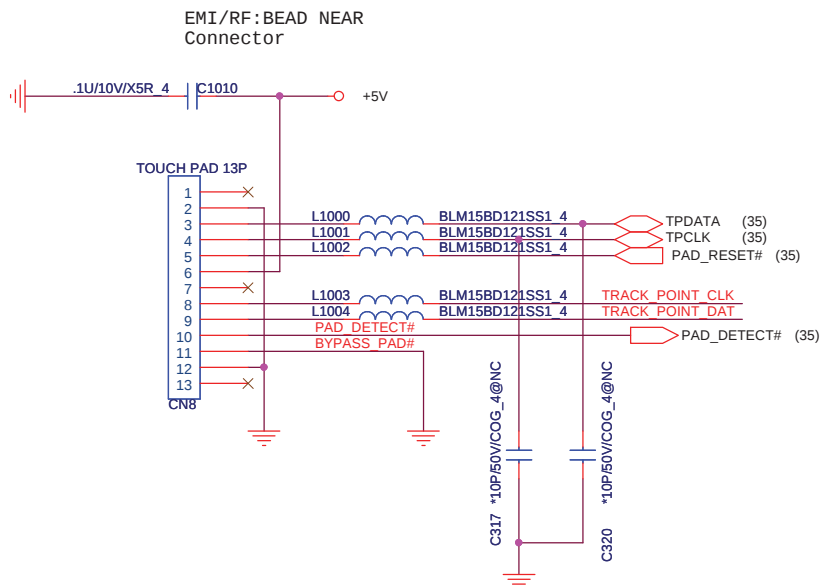
+5V
3VPCU



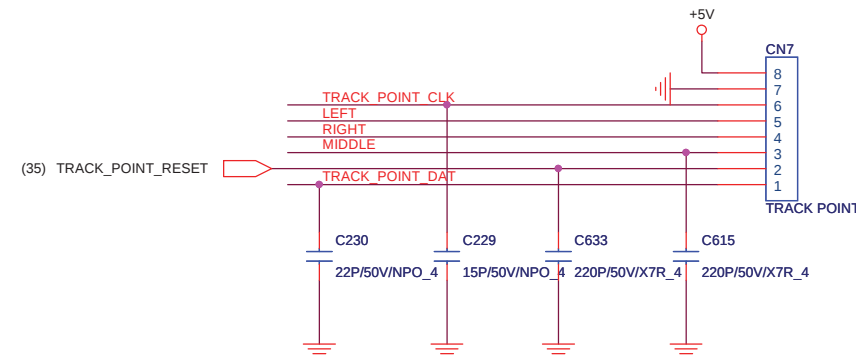
For EMI request

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Touch pad



TRACK POINT



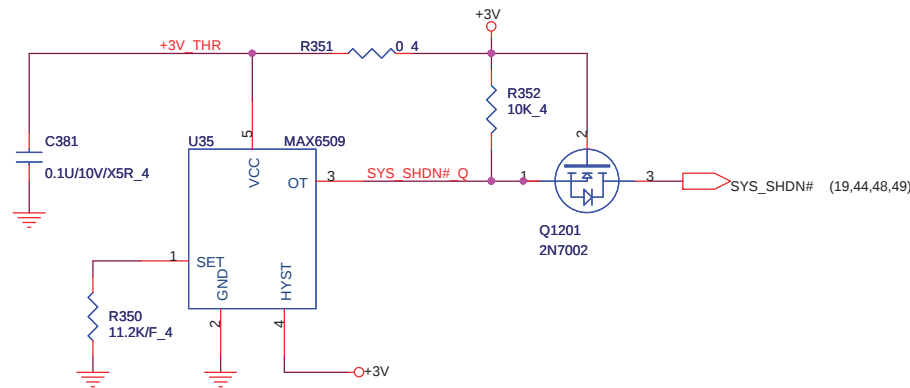
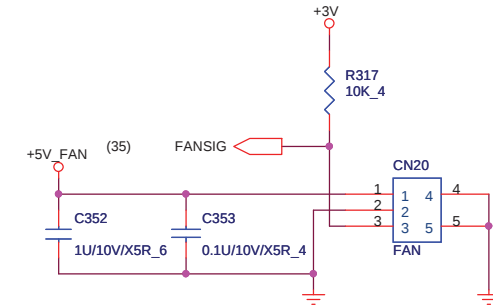
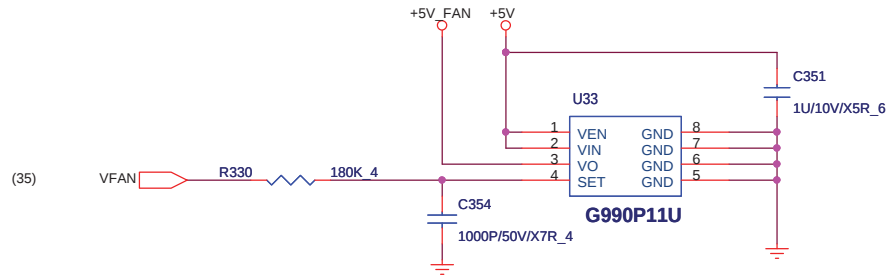
FAN CONTROL

(3,4,8,9,10,11,12,14,15,17,23,24,25,26,28,29,30,31,32,33,34,35,36,39,40,41,42,44,46,47,48,49,51)
(12,18,21,24,25,26,29,35,37,44)

+3V
+5V

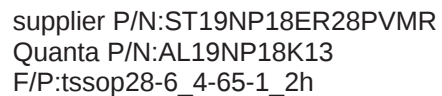


38



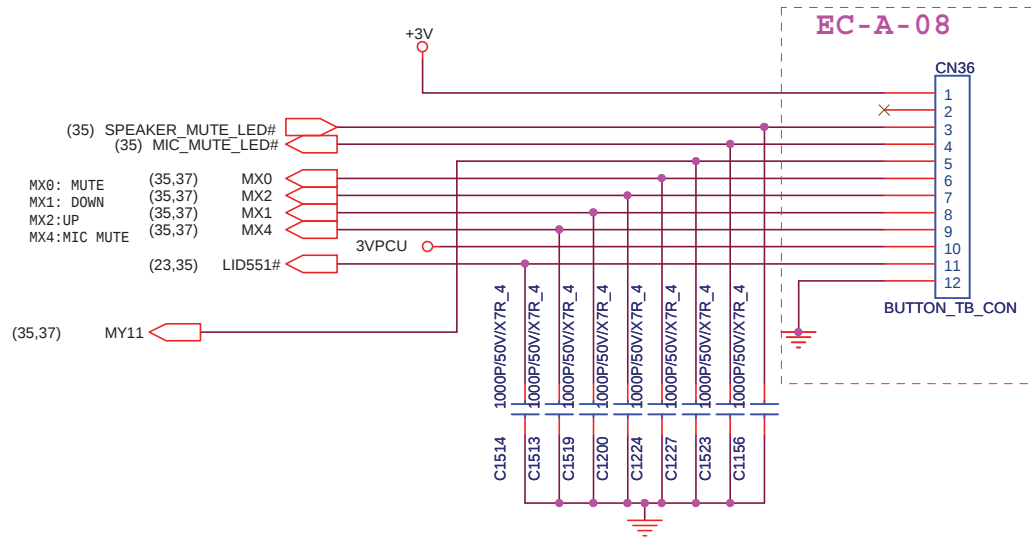


STNP18
TPM

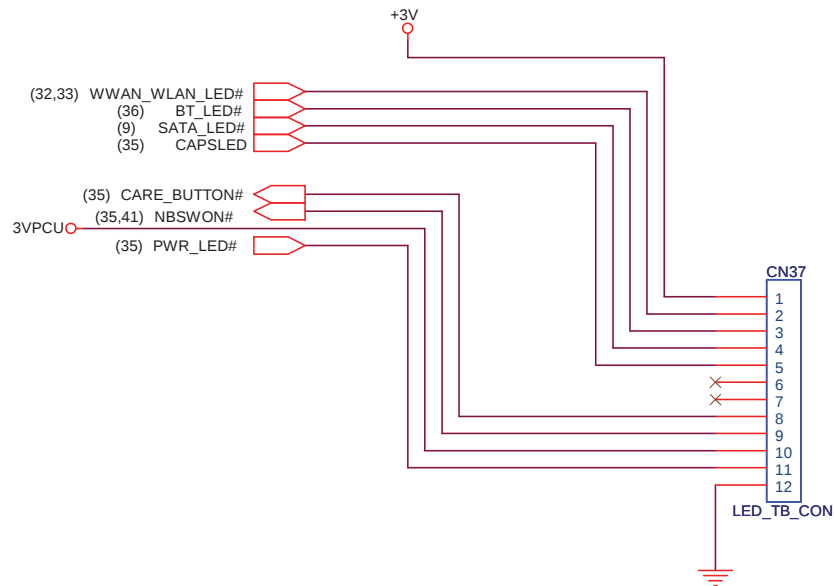


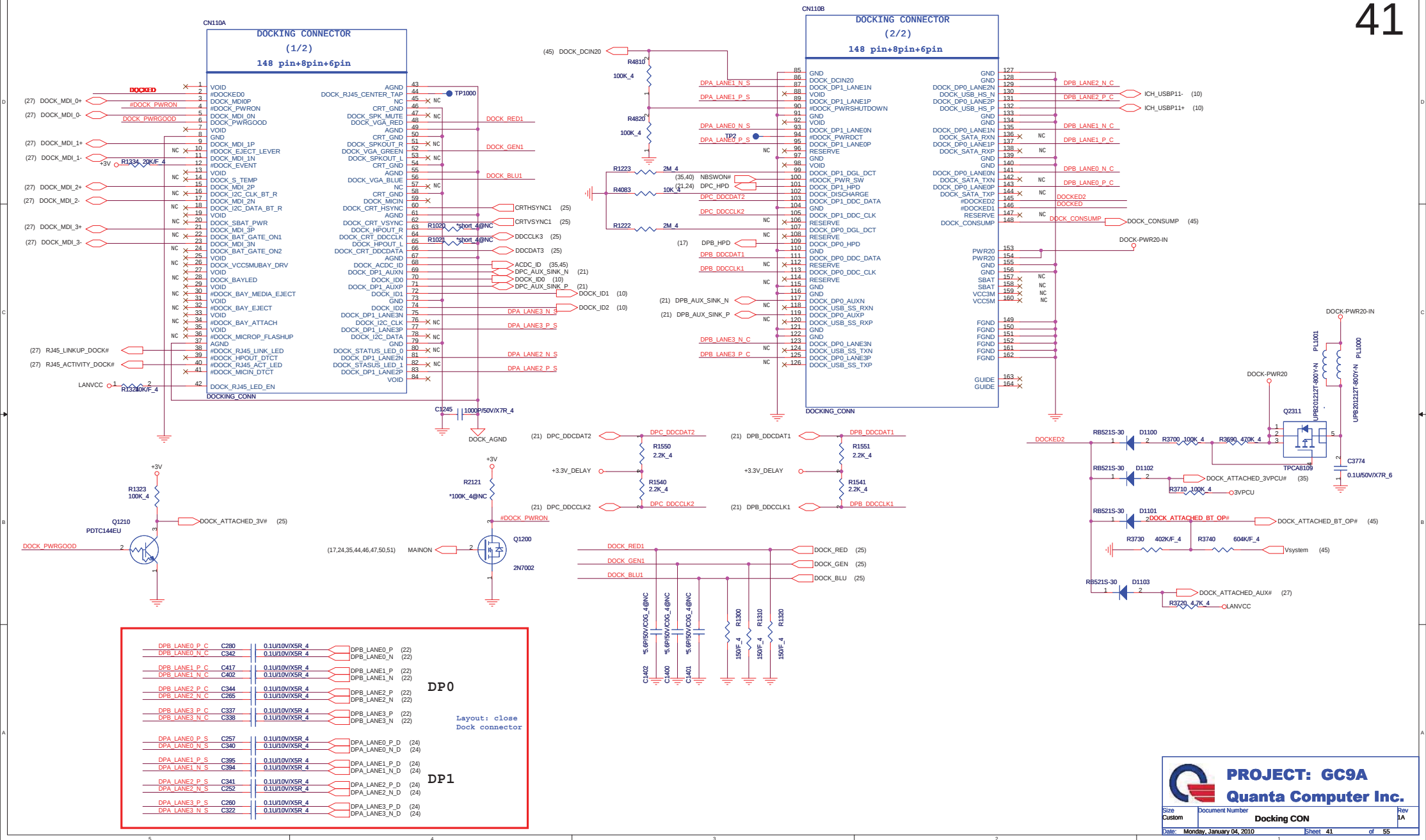
Daughter Boards for LEDs & Ports

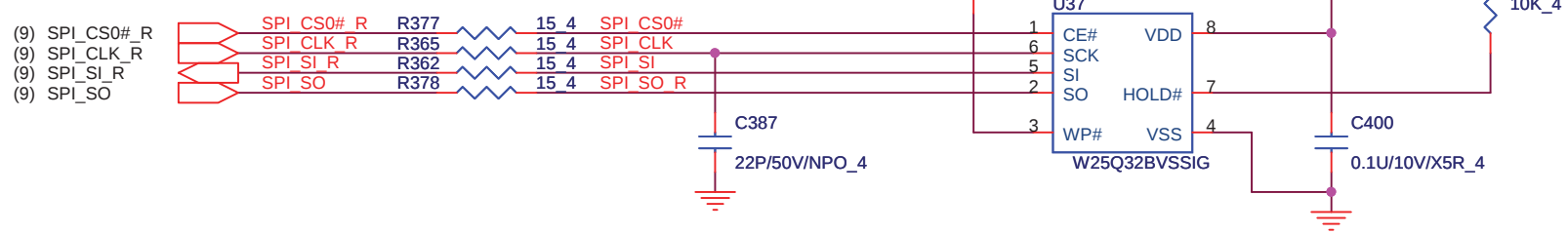
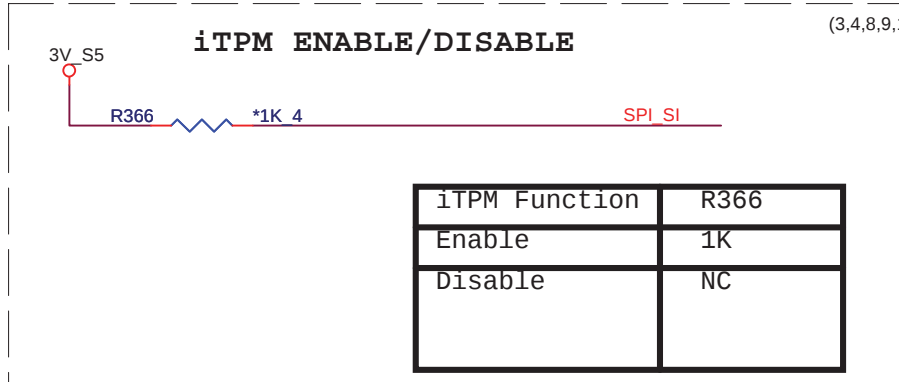
FFC TO KBD LEFT SIDE CONNECTOR



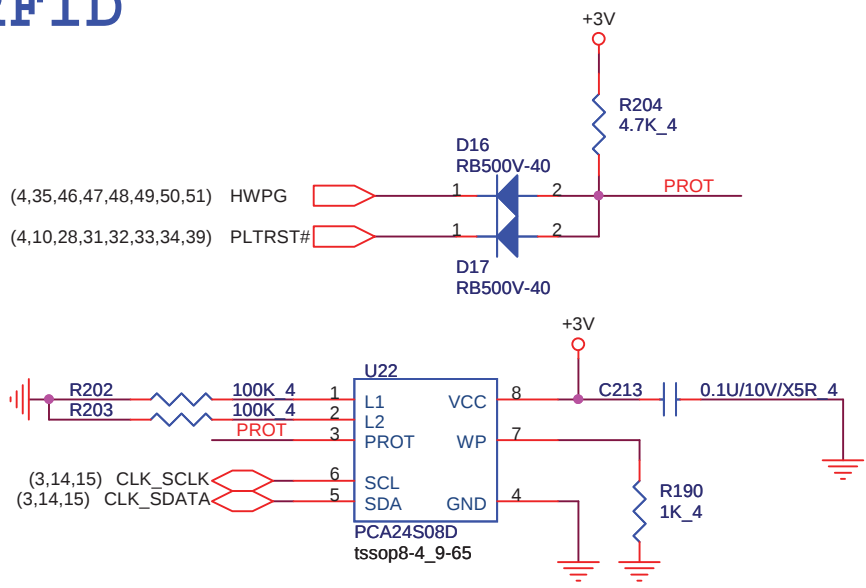
FFC TO LED RIGHT SIDE CONNECTOR







RFID

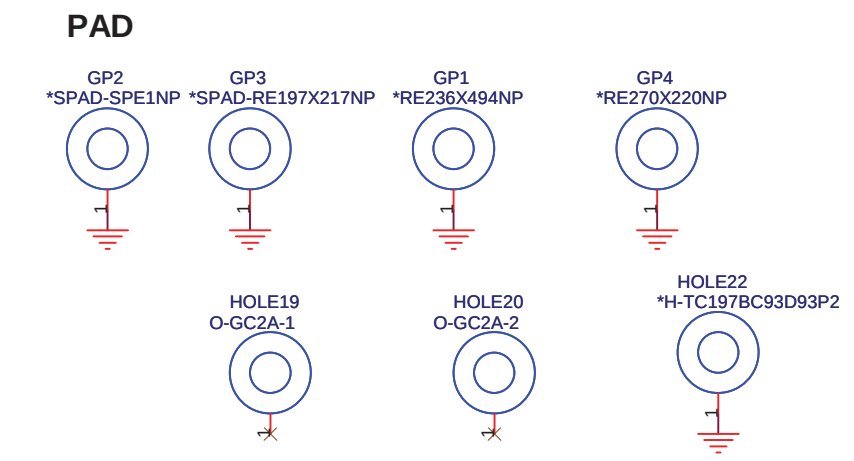
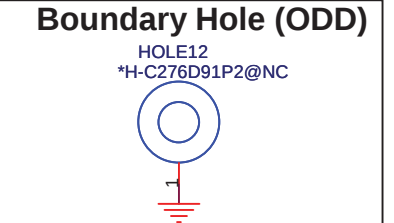
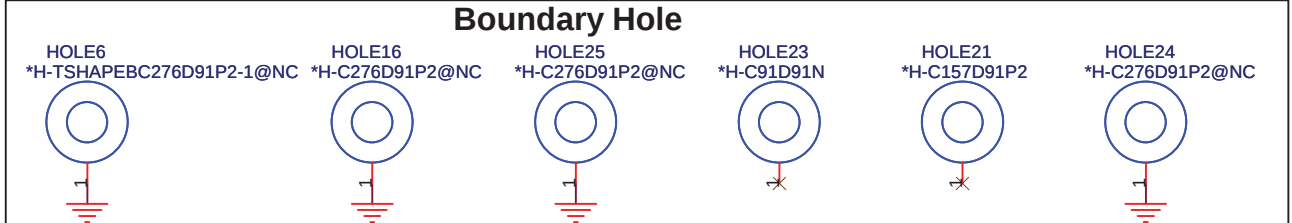
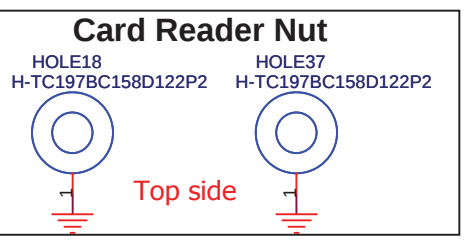
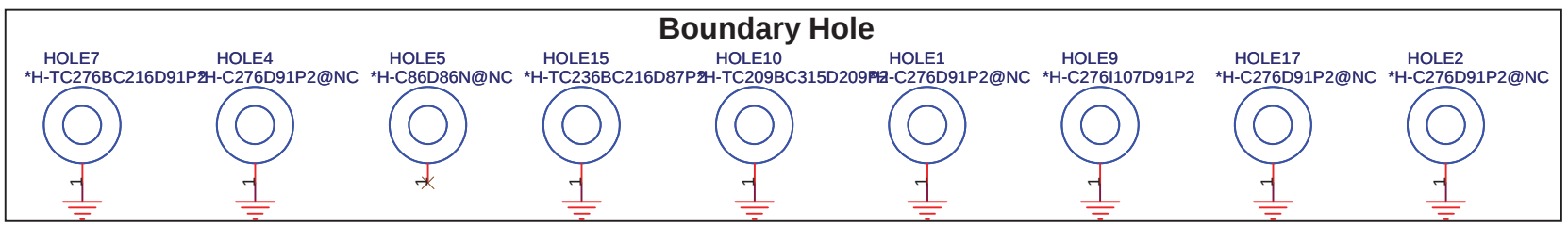
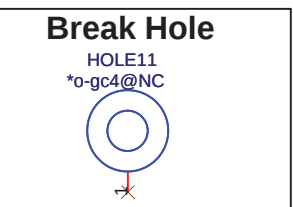
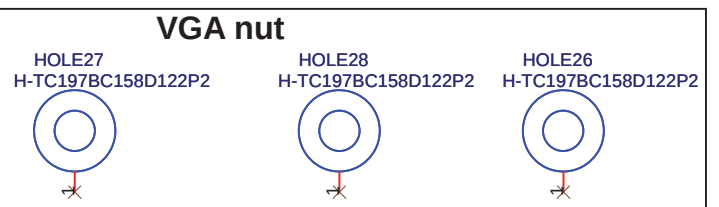
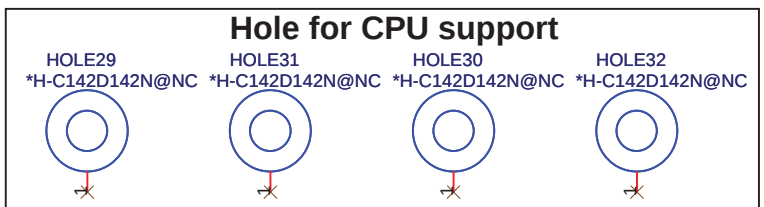
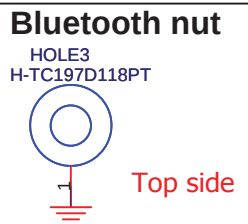
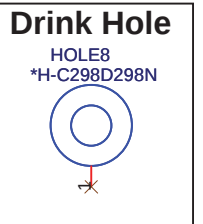
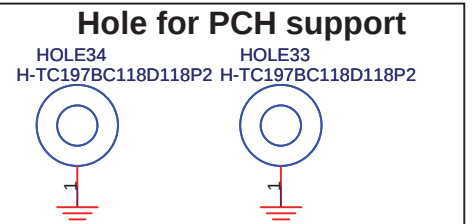
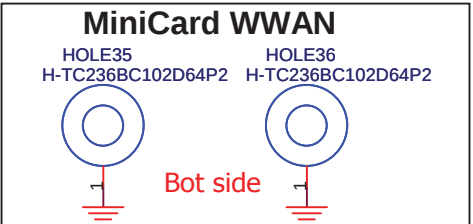
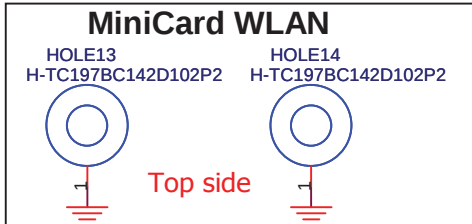




PROJECT: GC9A

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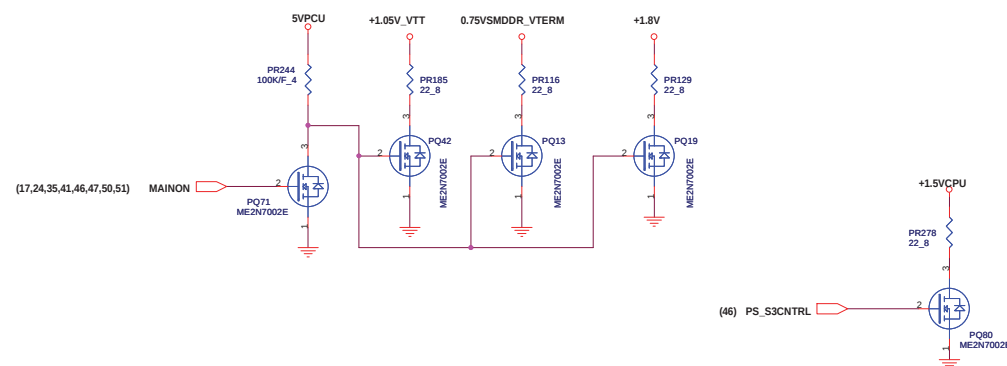
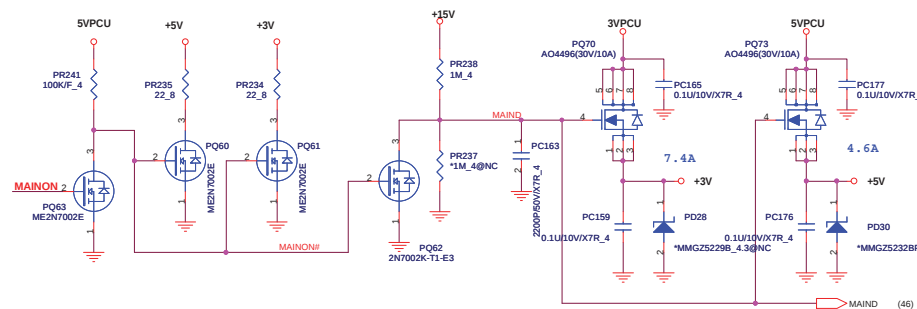


PROJECT: GC9A

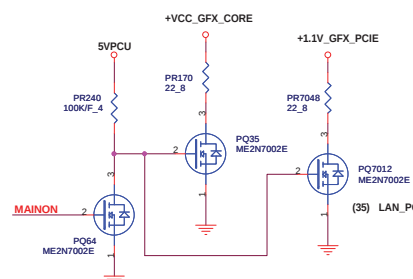
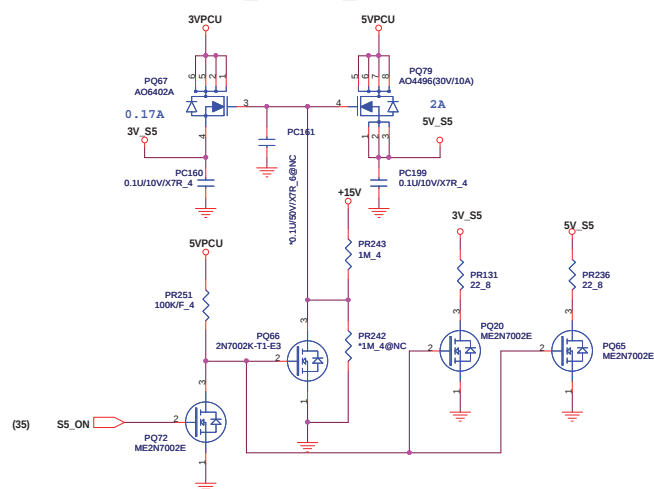
Quanta Computer Inc.

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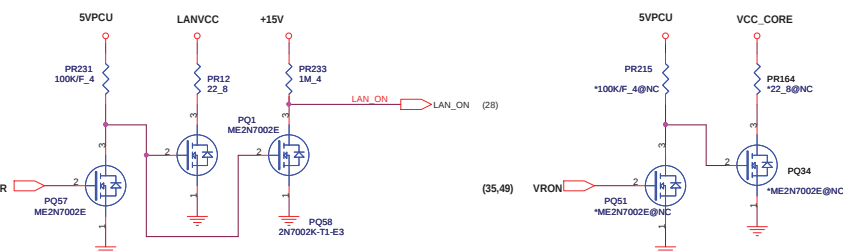
+3.3V, +5V



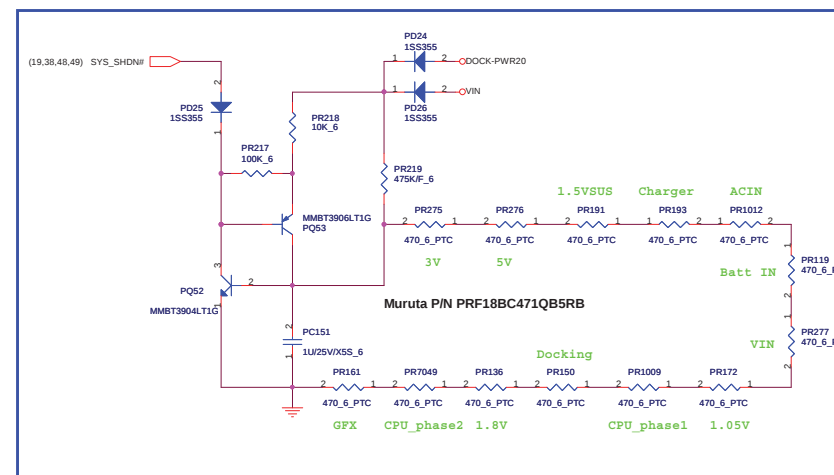
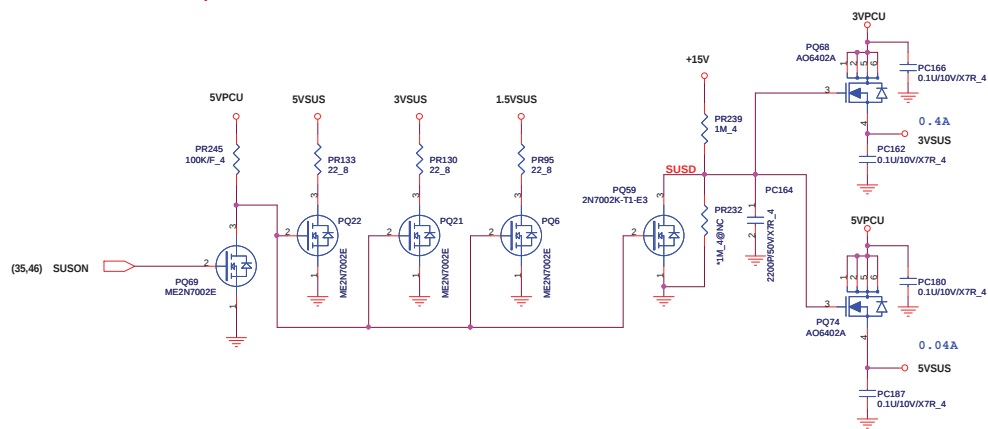
3V_S5, 5V_S5

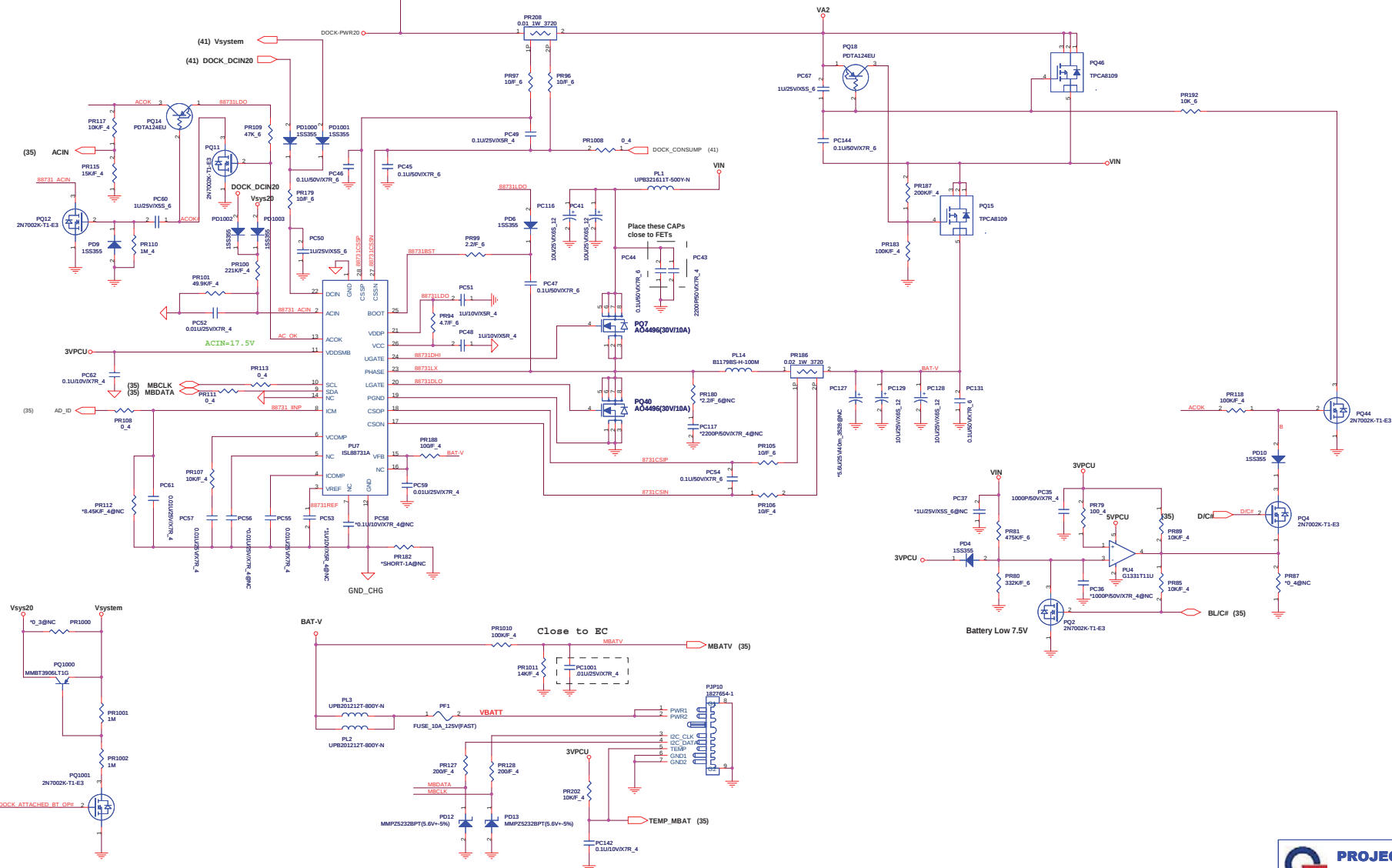


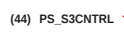
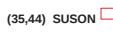
LANVCC

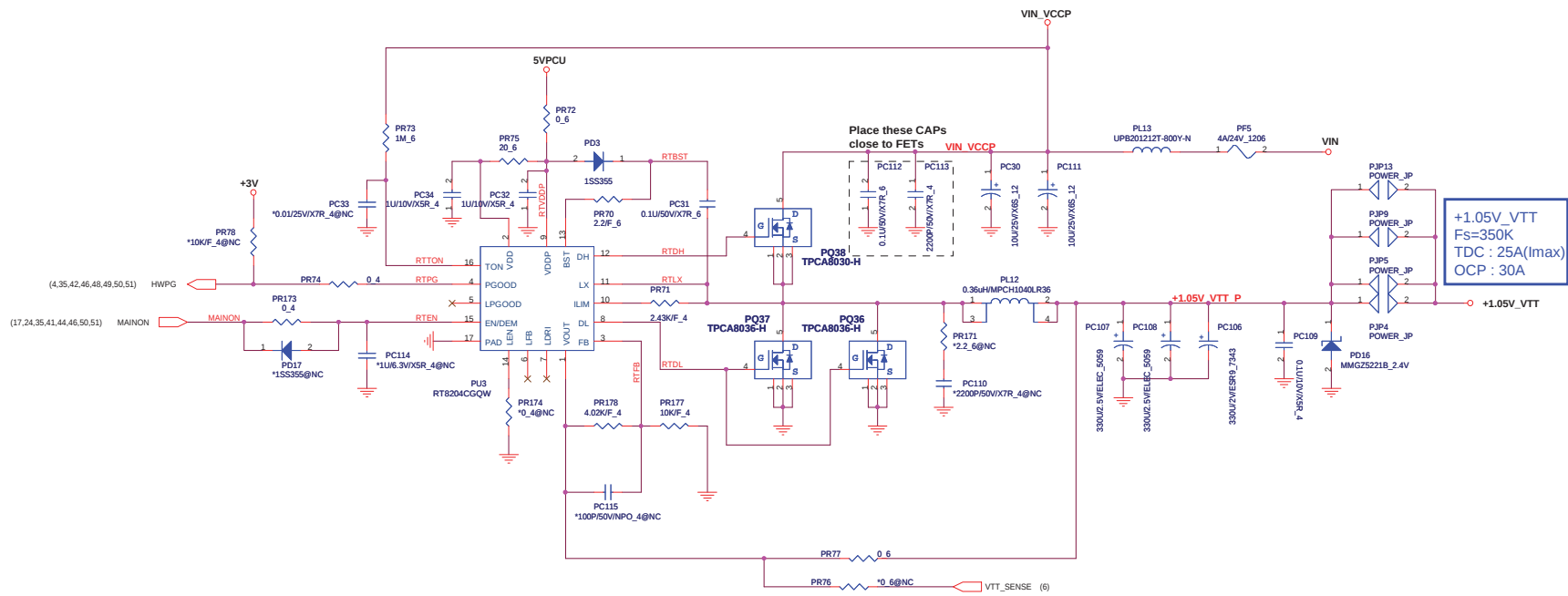


3VSUS, 5VSUS

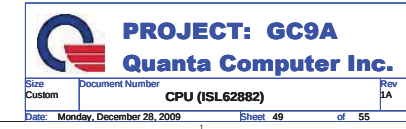


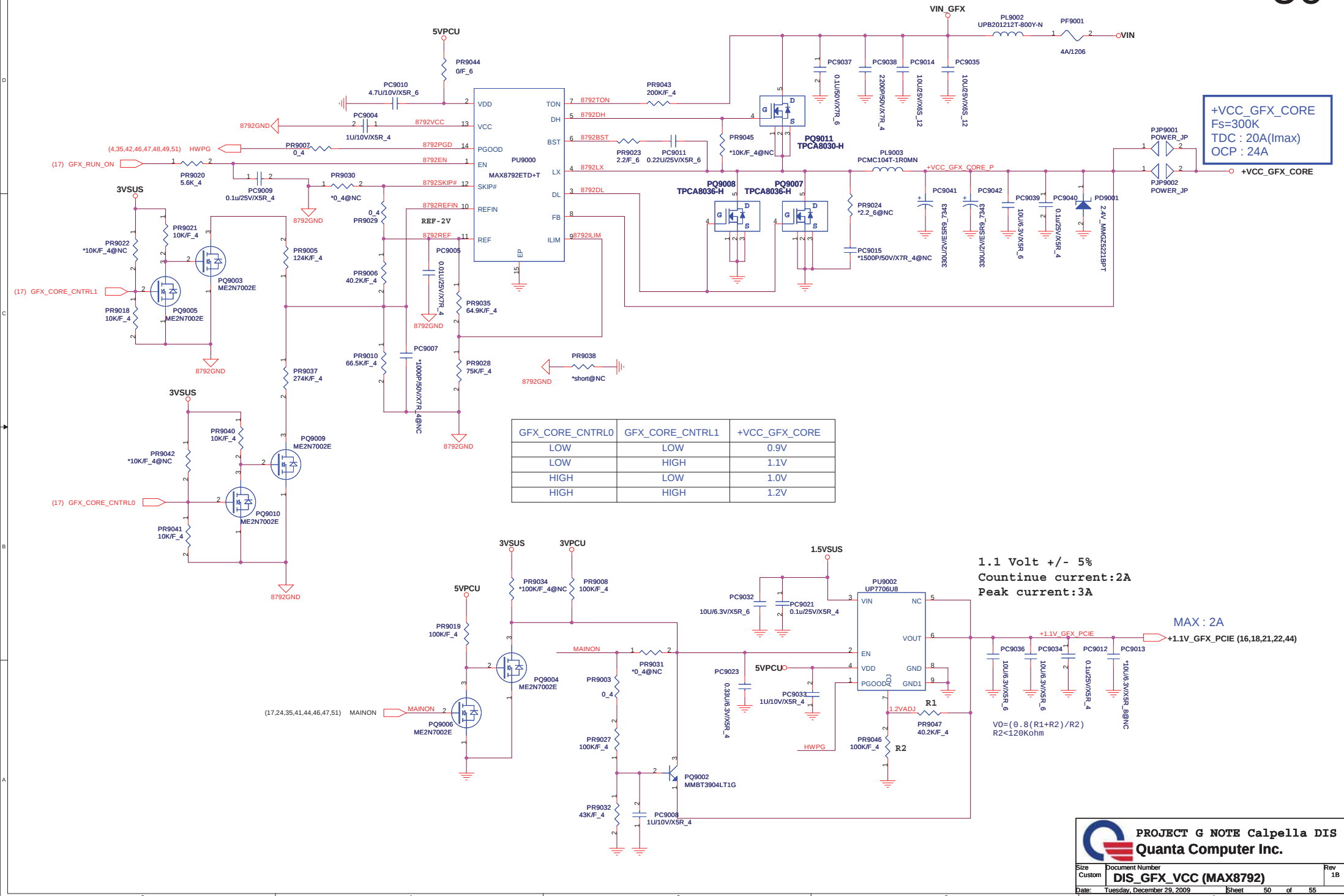


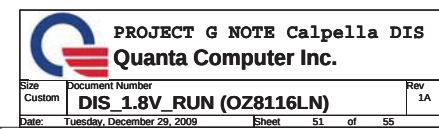












Revision History

Revision	Date	Phase	Change List	Release Schematic Date	Release Gerber File Date
1A		DV	Initial release		

Schematic Value Explanation Description :

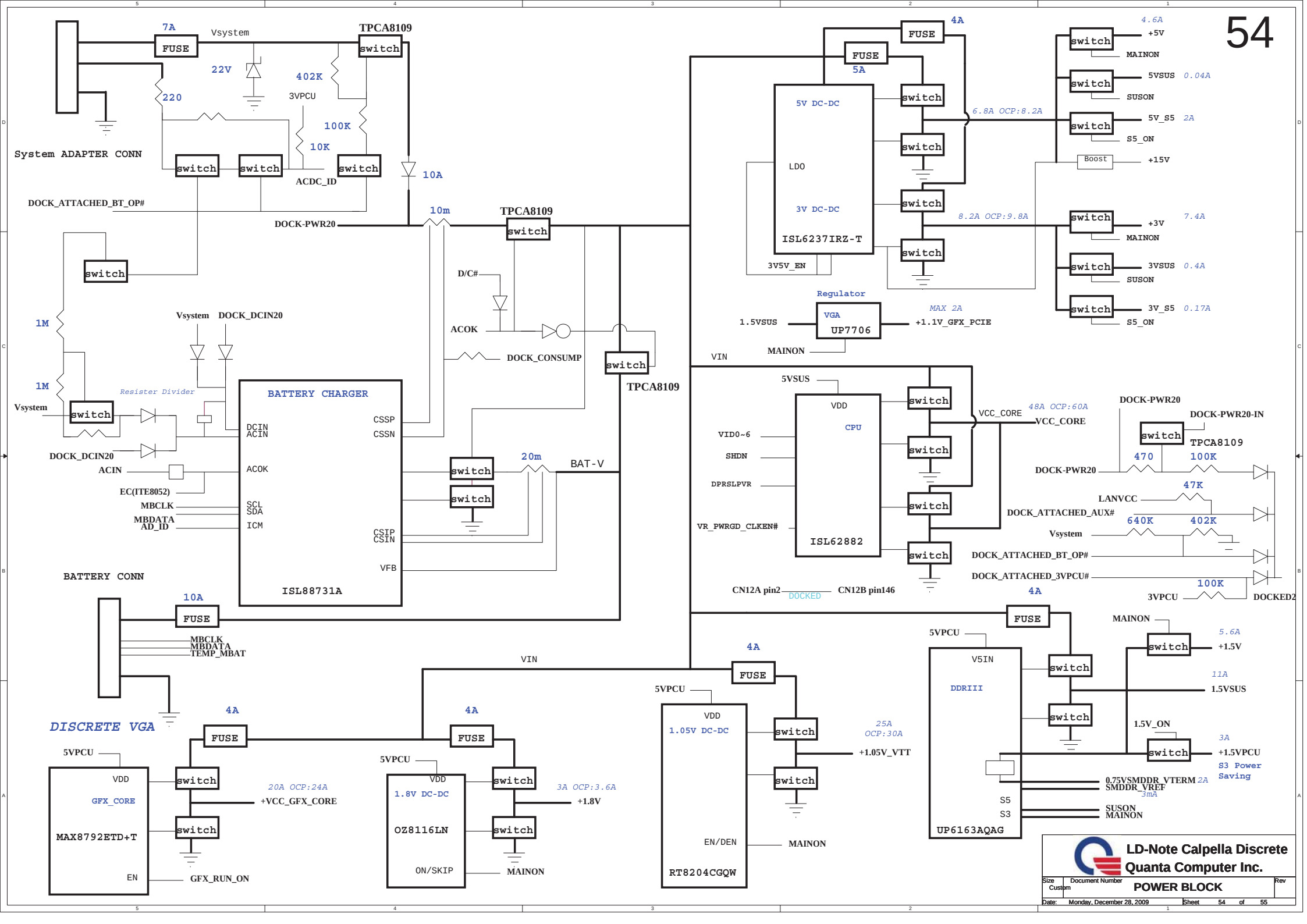
RESISTOR

Value	F	4	6	8	12	1210	*	Description
*1K/F_4	1%	0402 (1005)					DE POP	1K ohm 1% SMD 0402 package and DE POP
1K_6	5%		0603 (1608)				POP	1K ohm 5% SMD 0603 package and POP
1K_8	5%			0805 (2125)			POP	1K ohm 5% SMD 0805 package and POP
1K_12	5%				1206 (3216)		POP	1K ohm 5% SMD 1206 package and POP
1K_1210	5%					1210 (3225)	POP	1K ohm 5% SMD 1210 package and POP

CAPACITOR

Value	Voltage	Material	6				*	Description
*0.1U/10V/X5R_4	10V	X5R	0402 (1005)				DE POP	0.1UF 10V X5R SMD 0402 package DE POP
1U/25V/X7R_6	25V	X7R	0603 (1608)				POP	0.1UF 25V X7R SMD 0603 package POP

[illegible]



2009

A stage

EC NO.	PG.	DATE	PART REFERENCE	DESCRIPTION
EC-A-01	30	12/22	R36,R37,R42,R43	Delete R36,R37,R41,R42 (redundant optional resistor) and change connection of E-sata.
EC-A-02	11	12/22	R285	No POP R285 or delete.We have R466 for TPM physical presence
EC-A-03	35	12/22	R101	EC use SPI type
EC-A-04	30	12/22	R623,R625,R626, R627	AUO3 use ext power
EC-A-05	9	12/22	R449,R450	GPIO19,21 should have pull up 10K to +3V due to no internal PU/PD
EC-A-06	39	12/22		Cut LPCPD# signal from TPM#28. due to PCH bug(SUSSTAT# signal chatteringwhen assert)
EC-A-07	35	12/23	R262,C382	Add Pull high for CARE_BUTTON#
EC-A-08	40	12/23	R79,R80,R81, R82,R83,R86, R104,R105,R108	Delete these resistor to save space for layout.
EC-A-09	24	12/28	C2350,C196	Change CAP value per customer request.
EC-A-10	03	12/28	C8222,C8223	Add decoupling cap per RF engineer requested.
EC-A-11	26	12/29	C683	De-pop C683 to prevent efect high frequency of THD+N.



G-Note Montevina
Quanta Computer Inc.

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ERROR: syntaxerror
OFFENDING COMMAND: --nostringval--

STACK:

```
/Title  
(  
/Subject  
(D:20100106114247+08'00')  
/ModDate  
(  
/Keywords  
(PDFCreator Version 0.9.5)  
/Creator  
(D:20100106114247+08'00')  
/CreationDate  
(93121305)  
/Author  
-mark-
```