

Compal Confidential

Intel Haswell rPGA Processor with Lynx Point-H

Afterburn MXM

LA-9371P

2012-09-28

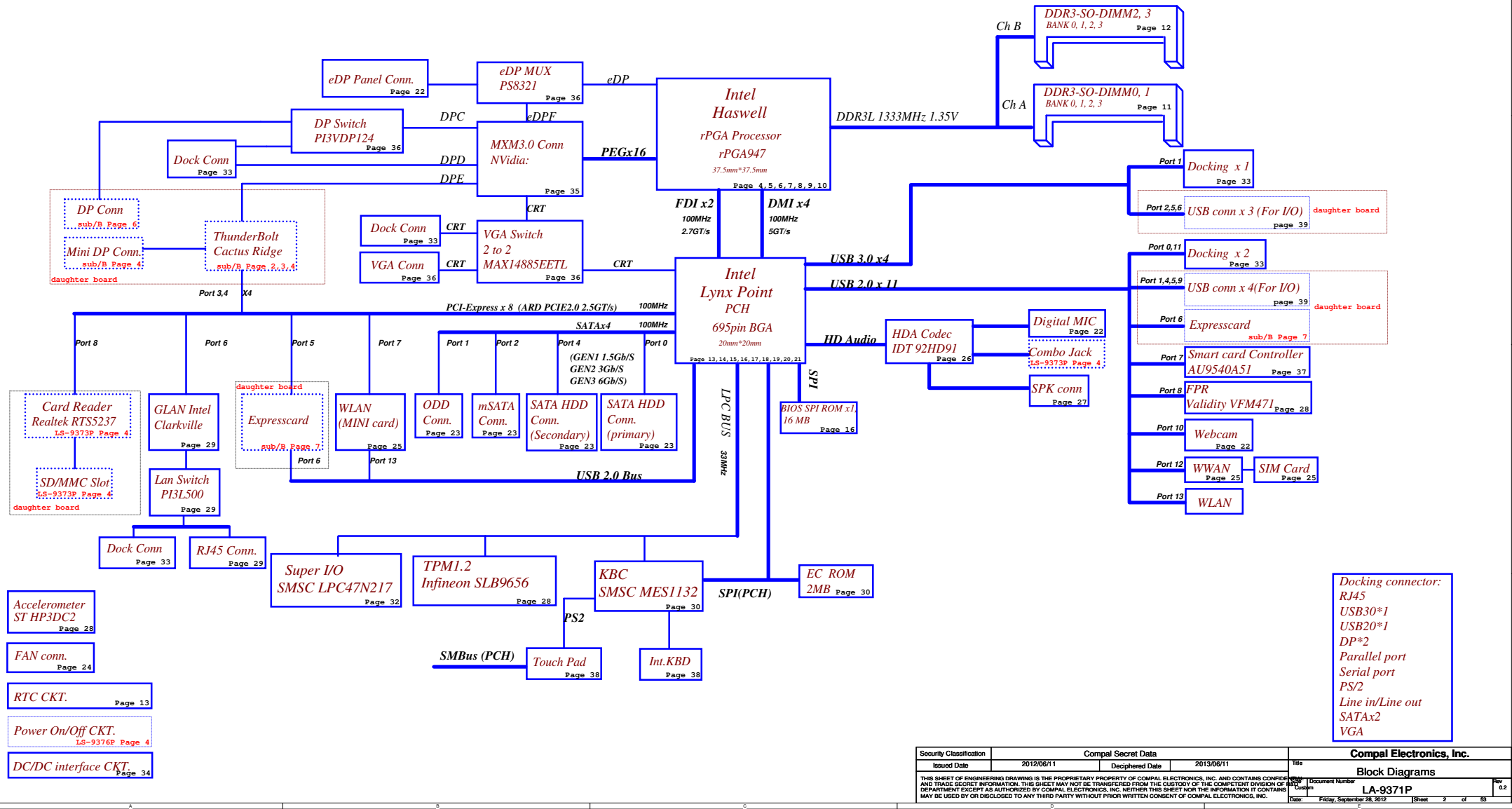
REV : 0.2

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Model Name : Afterburn

File Name : LA-9371P



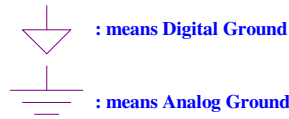
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Block Diagrams				LA-9371P	
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Voltage Rails

(O MEANS ON X MEANS OFF)

power plane State	+RTCVCC	B+	+5VDS +3VDS	+1.35V +0.675VS	+5VS +3VS +1.5VS +VCC_CORE +1.05VS +1.05VM	
S0	O	O	O	O	O	
S1	O	O	O	O	O	
S3	O	O	O	O	X	
S5 S4/AC	O	O	O	X	X	
S5 S4/ Battery only	O	O	X	X	X	
S5 S4/AC & Battery don't exist	O	X	X	X	X	

Symbol Note :



@ : means just reserve , no build

AMT@ : means just install for support iAMT

CONN@ : means ME part.

Layout Notes

07/24 update

: Question Area Mark.(Wait check)

Install below 45 level BOM structure for ver. 0.1

45@ : means just put it in the BOM of 45 level.

Install below 43 level BOM structure for ver. 0.1

DEBUG@ : means just build when PCIE port 80 CARD function enable. *Remove before MP*

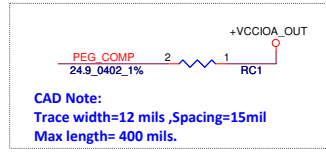
SMBUS Control Table

	SOURCE	BATT	2nd BATT	XDP	SODIMM	G-SENSOR	TP	NIC	NFC	EC	MXM
I2C_MAIN_CLK I2C_MAIN_DAT	SMSC1126	V	X	X	X	X	X	X	X	X	X
I2C_BAY_CLK I2C_BAY_DAT	SMSC1126	X	V	X	X	X	X	X	X	X	X
MEM_SMBCLK MEM_SMBDATA	Haswell	X	X	V	V	V	V	X	X	X	X
LAN_SMBCLK LAN_SMBDATA	Haswell	X	X	X	X	X	X	V	V	X	X
SML1_SMBCLK SML1_SMBDATA	Haswell	X	X	X	X	X	X	X	X	V	V

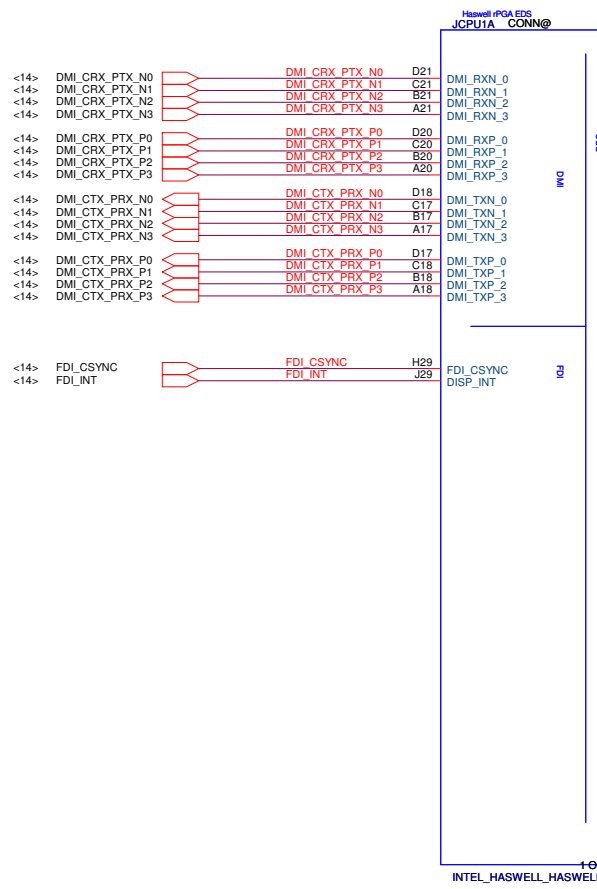
Stapping Options Flash

GPIO 51 Bit 1	GPIO 19 Bit 0	Boot BIOS Destination
0	0	Reserved
0	1	RSVD
1	0	SPI
1	1	LPC

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CAD Note:
Trace width=12 mils ,Spacing=15mil
Max length= 400 mils.

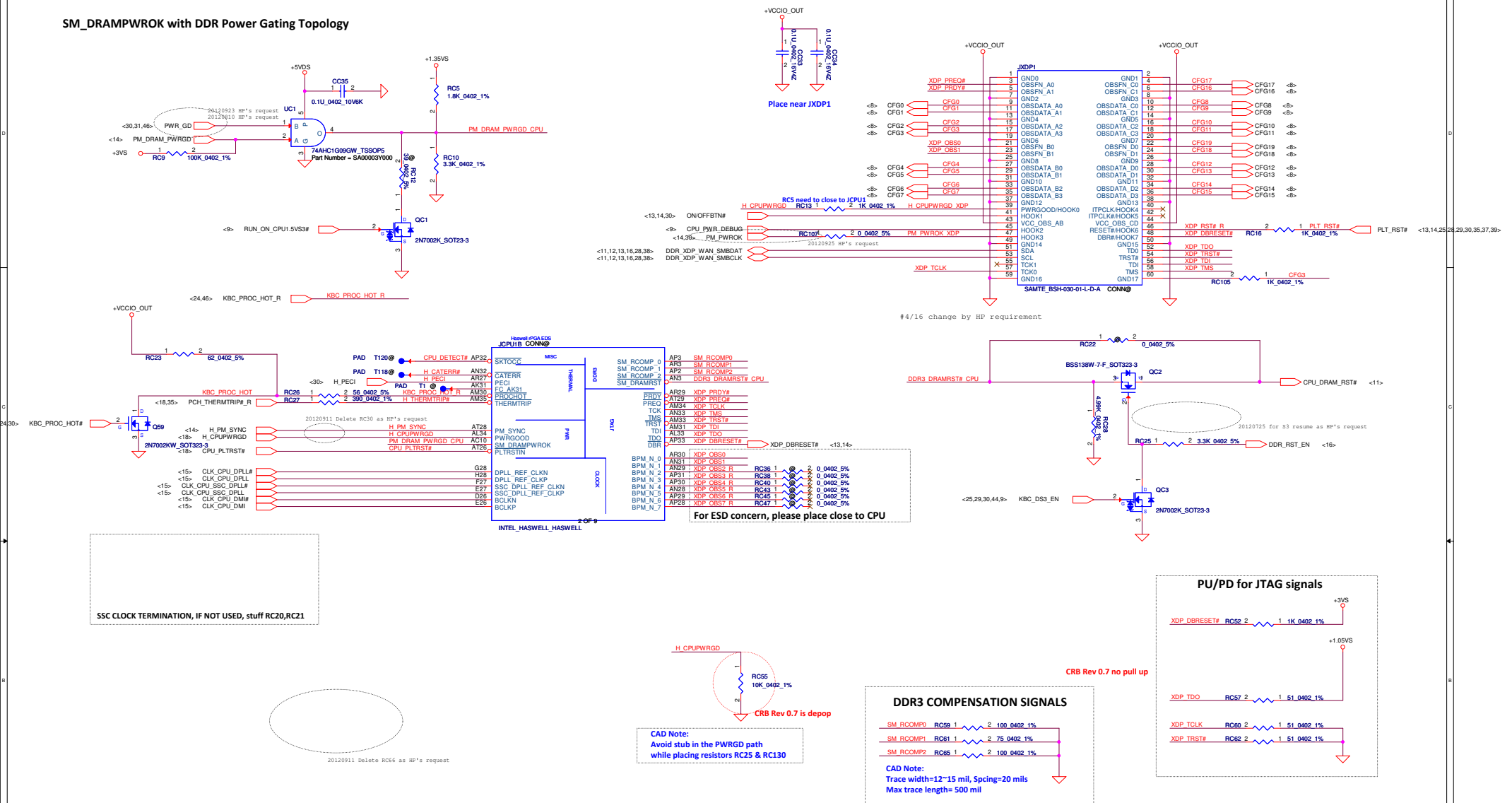


- PEG_RCOMP E23 PEG_COMP
- PEG_RXN_0 M29 PEG_CRX_GTX_N0
- PEG_RXN_1 K28 PEG_CRX_GTX_N1
- PEG_RXN_2 M31 PEG_CRX_GTX_N2
- PEG_RXN_3 L30 PEG_CRX_GTX_N3
- PEG_RXN_4 M33 PEG_CRX_GTX_N4
- PEG_RXN_5 L32 PEG_CRX_GTX_N5
- PEG_RXN_6 M35 PEG_CRX_GTX_N6
- PEG_RXN_7 L34 PEG_CRX_GTX_N7
- PEG_RXN_8 E29 PEG_CRX_GTX_N8
- PEG_RXN_9 D28 PEG_CRX_GTX_N9
- PEG_RXN_10 E31 PEG_CRX_GTX_N10
- PEG_RXN_11 D30 PEG_CRX_GTX_N11
- PEG_RXN_12 E35 PEG_CRX_GTX_N12
- PEG_RXN_13 D34 PEG_CRX_GTX_N13
- PEG_RXN_14 E33 PEG_CRX_GTX_N14
- PEG_RXN_15 E32 PEG_CRX_GTX_N15
- PEG_RXP_0 L29 PEG_CRX_GTX_P0
- PEG_RXP_1 L28 PEG_CRX_GTX_P1
- PEG_RXP_2 L31 PEG_CRX_GTX_P2
- PEG_RXP_3 K30 PEG_CRX_GTX_P3
- PEG_RXP_4 L33 PEG_CRX_GTX_P4
- PEG_RXP_5 K32 PEG_CRX_GTX_P5
- PEG_RXP_6 L35 PEG_CRX_GTX_P6
- PEG_RXP_7 K34 PEG_CRX_GTX_P7
- PEG_RXP_8 F29 PEG_CRX_GTX_P8
- PEG_RXP_9 E28 PEG_CRX_GTX_P9
- PEG_RXP_10 F31 PEG_CRX_GTX_P10
- PEG_RXP_11 E30 PEG_CRX_GTX_P11
- PEG_RXP_12 F35 PEG_CRX_GTX_P12
- PEG_RXP_13 E34 PEG_CRX_GTX_P13
- PEG_RXP_14 F33 PEG_CRX_GTX_P14
- PEG_RXP_15 D32 PEG_CRX_GTX_P15
- PEG_TXN_0 H35 PEG_CTX_GRX_C_N0
- PEG_TXN_1 H34 PEG_CTX_GRX_C_N1
- PEG_TXN_2 J33 PEG_CTX_GRX_C_N2
- PEG_TXN_3 H32 PEG_CTX_GRX_C_N3
- PEG_TXN_4 J31 PEG_CTX_GRX_C_N4
- PEG_TXN_5 G30 PEG_CTX_GRX_C_N5
- PEG_TXN_6 C33 PEG_CTX_GRX_C_N6
- PEG_TXN_7 B32 PEG_CTX_GRX_C_N7
- PEG_TXN_8 B31 PEG_CTX_GRX_C_N8
- PEG_TXN_9 A30 PEG_CTX_GRX_C_N9
- PEG_TXN_10 B29 PEG_CTX_GRX_C_N10
- PEG_TXN_11 A28 PEG_CTX_GRX_C_N11
- PEG_TXN_12 B27 PEG_CTX_GRX_C_N12
- PEG_TXN_13 A26 PEG_CTX_GRX_C_N13
- PEG_TXN_14 B25 PEG_CTX_GRX_C_N14
- PEG_TXN_15 A24 PEG_CTX_GRX_C_N15
- PEG_TXP_0 J35 PEG_CTX_GRX_C_P0
- PEG_TXP_1 G34 PEG_CTX_GRX_C_P1
- PEG_TXP_2 H33 PEG_CTX_GRX_C_P2
- PEG_TXP_3 G32 PEG_CTX_GRX_C_P3
- PEG_TXP_4 H31 PEG_CTX_GRX_C_P4
- PEG_TXP_5 H30 PEG_CTX_GRX_C_P5
- PEG_TXP_6 B33 PEG_CTX_GRX_C_P6
- PEG_TXP_7 A32 PEG_CTX_GRX_C_P7
- PEG_TXP_8 C31 PEG_CTX_GRX_C_P8
- PEG_TXP_9 B30 PEG_CTX_GRX_C_P9
- PEG_TXP_10 C29 PEG_CTX_GRX_C_P10
- PEG_TXP_11 B28 PEG_CTX_GRX_C_P11
- PEG_TXP_12 C27 PEG_CTX_GRX_C_P12
- PEG_TXP_13 B26 PEG_CTX_GRX_C_P13
- PEG_TXP_14 C25 PEG_CTX_GRX_C_P14
- PEG_TXP_15 B24 PEG_CTX_GRX_C_P15

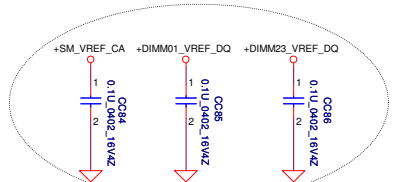
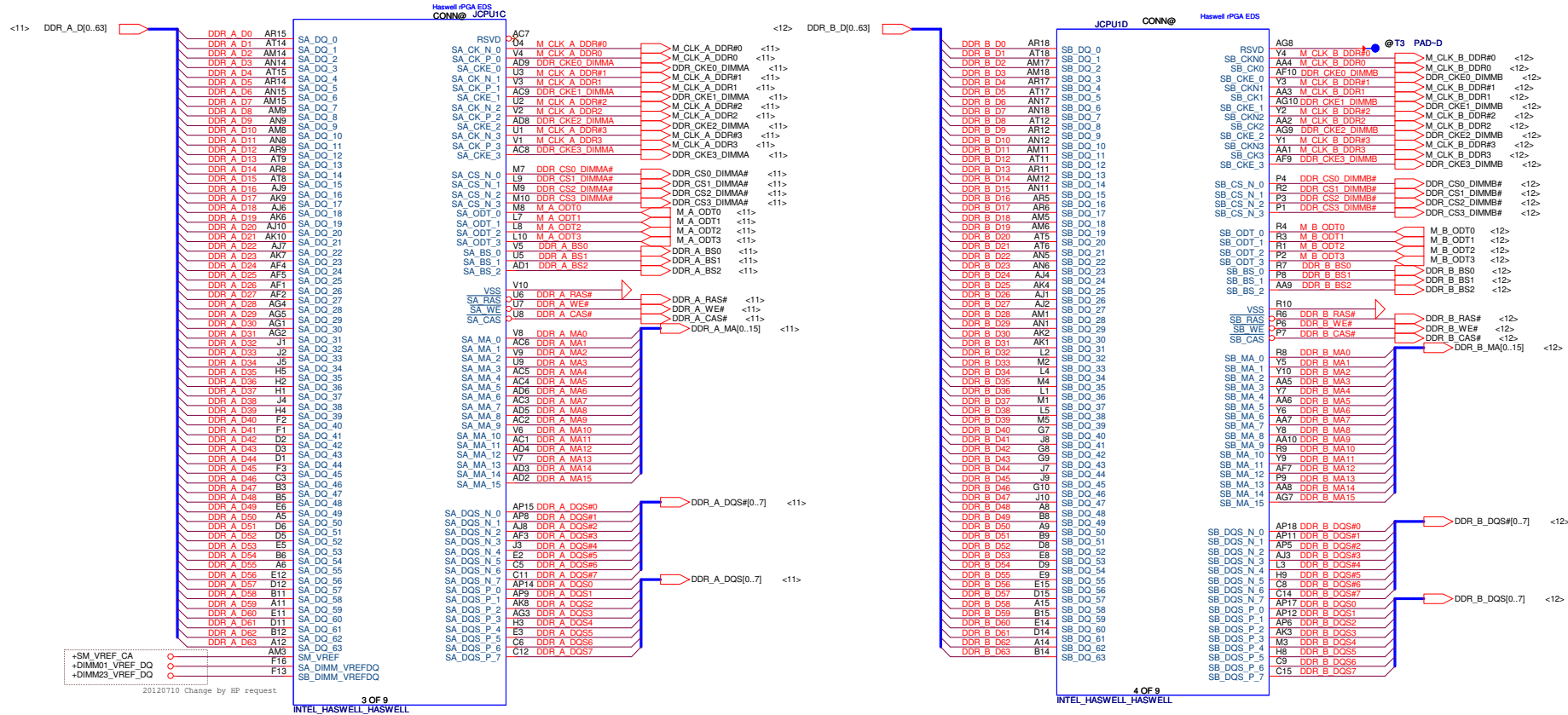
PEG_CRX_GTX_P[0..15]	PEG_CRX_GTX_P[0..15]	<35>
PEG_CRX_GTX_N[0..15]	PEG_CRX_GTX_N[0..15]	<35>
PEG_CTX_GRX_P[0..15]	PEG_CTX_GRX_P[0..15]	<35>
PEG_CTX_GRX_N[0..15]	PEG_CTX_GRX_N[0..15]	<35>

PEG_CTX_GRX_C_P0	CC1	2	1	0.22U	0402	6.3V6K	PEG_CTX_GRX_P0
PEG_CTX_GRX_C_N0	CC2	2	1	0.22U	0402	6.3V6K	PEG_CTX_GRX_N0
PEG_CTX_GRX_C_P1	CC3	2	1	0.22U	0402	6.3V6K	PEG_CTX_GRX_P1
PEG_CTX_GRX_C_N1	CC4	2	1	0.22U	0402	6.3V6K	PEG_CTX_GRX_N1
PEG_CTX_GRX_C_P2	CC5	2	1	0.22U	0402	6.3V6K	PEG_CTX_GRX_P2
PEG_CTX_GRX_C_N2	CC6	2	1	0.22U	0402	6.3V6K	PEG_CTX_GRX_N2
PEG_CTX_GRX_C_P3	CC7	2	1	0.22U	0402	6.3V6K	PEG_CTX_GRX_P3
PEG_CTX_GRX_C_N3	CC8	2	1	0.22U	0402	6.3V6K	PEG_CTX_GRX_N3
PEG_CTX_GRX_C_P4	CC9	2	1	0.22U	0402	6.3V6K	PEG_CTX_GRX_P4
PEG_CTX_GRX_C_N4	CC10	2	1	0.22U	0402	6.3V6K	PEG_CTX_GRX_N4
PEG_CTX_GRX_C_P5	CC11	2	1	0.22U	0402	6.3V6K	PEG_CTX_GRX_P5
PEG_CTX_GRX_C_N5	CC12	2	1	0.22U	0402	6.3V6K	PEG_CTX_GRX_N5
PEG_CTX_GRX_C_P6	CC13	2	1	0.22U	0402	6.3V6K	PEG_CTX_GRX_P6
PEG_CTX_GRX_C_N6	CC14	2	1	0.22U	0402	6.3V6K	PEG_CTX_GRX_N6
PEG_CTX_GRX_C_P7	CC15	2	1	0.22U	0402	6.3V6K	PEG_CTX_GRX_P7
PEG_CTX_GRX_C_N7	CC16	2	1	0.22U	0402	6.3V6K	PEG_CTX_GRX_N7
PEG_CTX_GRX_C_P8	CC17	1	2	0.22U	0402	6.3V6K	PEG_CTX_GRX_P8
PEG_CTX_GRX_C_N8	CC18	1	2	0.22U	0402	6.3V6K	PEG_CTX_GRX_N8
PEG_CTX_GRX_C_P9	CC19	1	2	0.22U	0402	6.3V6K	PEG_CTX_GRX_P9
PEG_CTX_GRX_C_N9	CC20	1	2	0.22U	0402	6.3V6K	PEG_CTX_GRX_N9
PEG_CTX_GRX_C_P10	CC21	1	2	0.22U	0402	6.3V6K	PEG_CTX_GRX_P10
PEG_CTX_GRX_C_N10	CC22	1	2	0.22U	0402	6.3V6K	PEG_CTX_GRX_N10
PEG_CTX_GRX_C_P11	CC23	1	2	0.22U	0402	6.3V6K	PEG_CTX_GRX_P11
PEG_CTX_GRX_C_N11	CC24	1	2	0.22U	0402	6.3V6K	PEG_CTX_GRX_N11
PEG_CTX_GRX_C_P12	CC25	1	2	0.22U	0402	6.3V6K	PEG_CTX_GRX_P12
PEG_CTX_GRX_C_N12	CC26	1	2	0.22U	0402	6.3V6K	PEG_CTX_GRX_N12
PEG_CTX_GRX_C_P13	CC27	1	2	0.22U	0402	6.3V6K	PEG_CTX_GRX_P13
PEG_CTX_GRX_C_N13	CC28	1	2	0.22U	0402	6.3V6K	PEG_CTX_GRX_N13
PEG_CTX_GRX_C_P14	CC29	1	2	0.22U	0402	6.3V6K	PEG_CTX_GRX_P14
PEG_CTX_GRX_C_N14	CC30	1	2	0.22U	0402	6.3V6K	PEG_CTX_GRX_N14
PEG_CTX_GRX_C_P15	CC31	1	2	0.22U	0402	6.3V6K	PEG_CTX_GRX_P15
PEG_CTX_GRX_C_N15	CC32	1	2	0.22U	0402	6.3V6K	PEG_CTX_GRX_N15

SM_DRAMPWROK with DDR Power Gating Topology



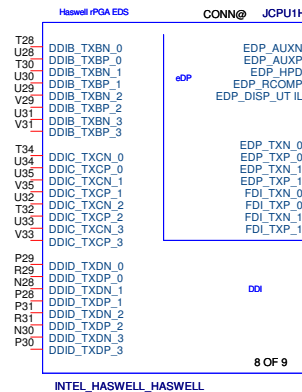
Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2012/06/11	Deciphered Date	2013/06/11	Title	PM_XDP_CLK	
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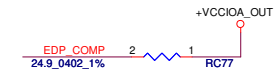
Layout Notes

Place C84,C85,C86 close to JCPU1

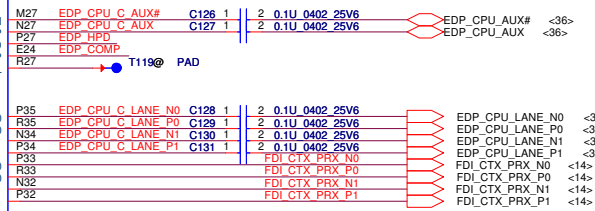
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								DDRIII	
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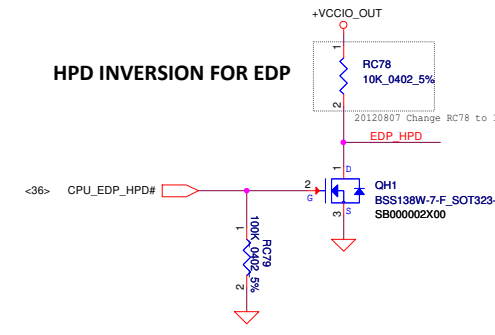
COMPENSATION PU FOR eDP



CAD Note: Trace width=20 mils, Spacing=25mil, Max length=100 mils.

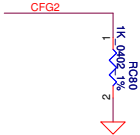


HPD INVERSION FOR EDP

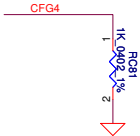


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Issued Date				2012/06/11				Title			
				Deciphered Date				CPU-FDI,eDP,DDI			
				2013/06/11				Document Number			
								LA-9371P			
								Rev			
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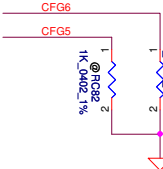
CFG STRAPS for CPU



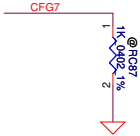
PEG Static Lane Reversal - CFG2 is for the 16x	
CFG2	1:(Default) Normal Operation; Lane # definition matches socket pin map definition 0:Lane Reversed



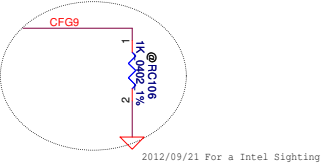
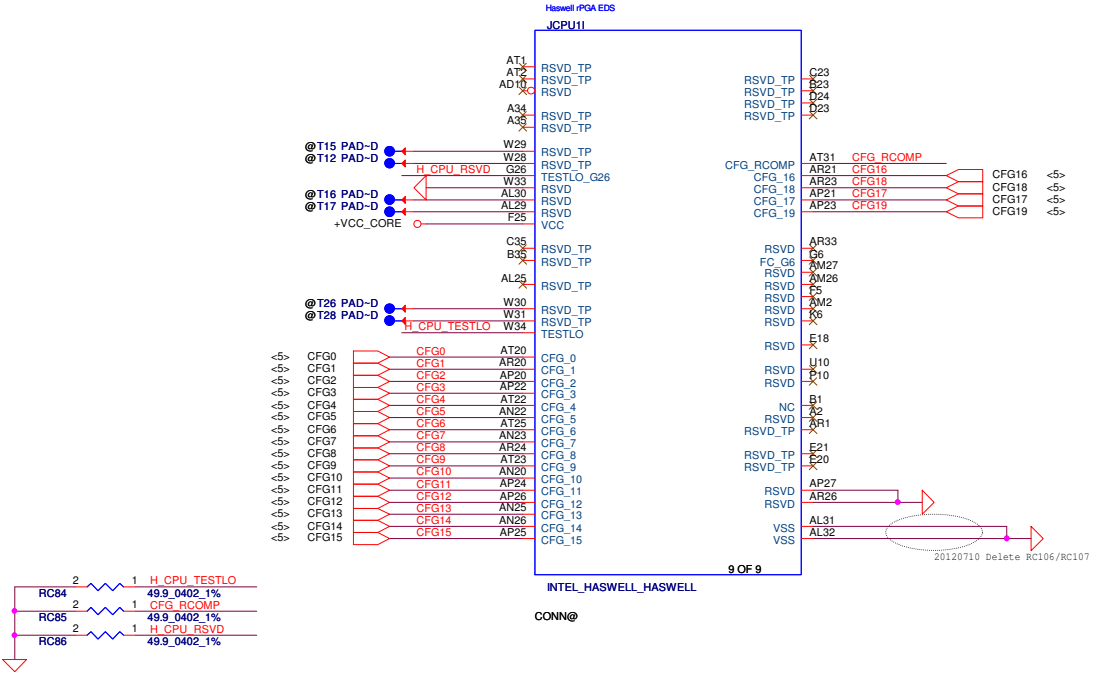
Display Port Presence Strap	
CFG4	1 : Disabled; No Physical Display Port attached to Embedded Display Port 0 : Enabled; An external Display Port device is connected to the Embedded Display Port



PCIe Port Bifurcation Straps	
CFG[6:5]	11: (Default) x16 - Device 1 functions 1 and 2 disabled 10: x8, x8 - Device 1 function 1 enabled ; function 2 disabled 01: Reserved - (Device 1 function 1 disabled ; function 2 enabled) 00: x8,x4,x4 - Device 1 functions 1 and 2 enabled

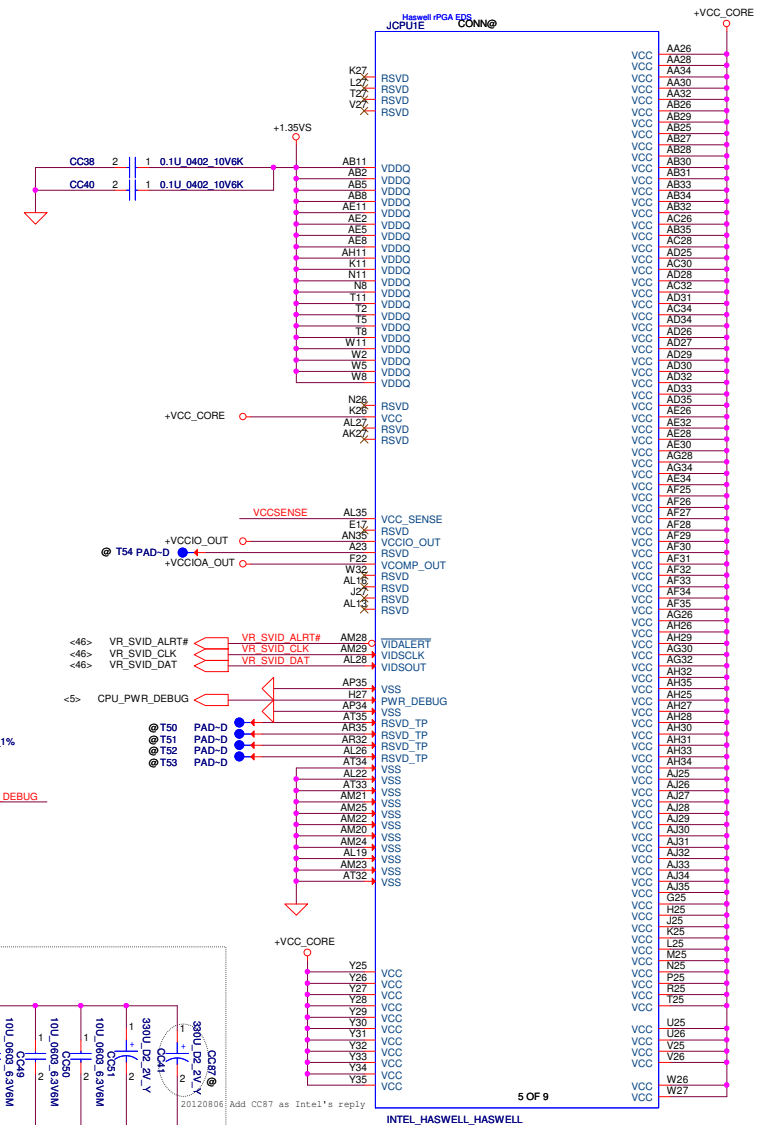
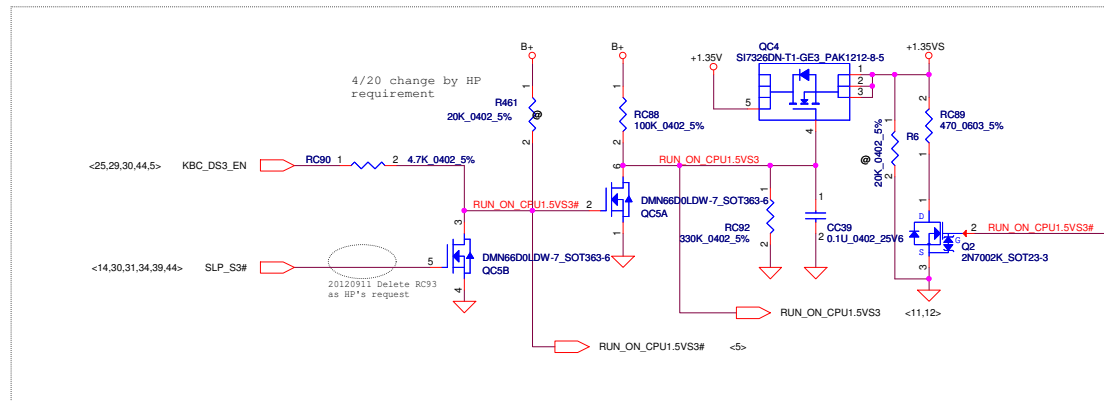


PEG DEFER TRAINING	
CFG7	1: (Default) PEG Train immediately following xxRESETB de assertion 0: PEG Wait for BIOS for training



2012/09/21 For a Intel Sighting

+1.35VS Source

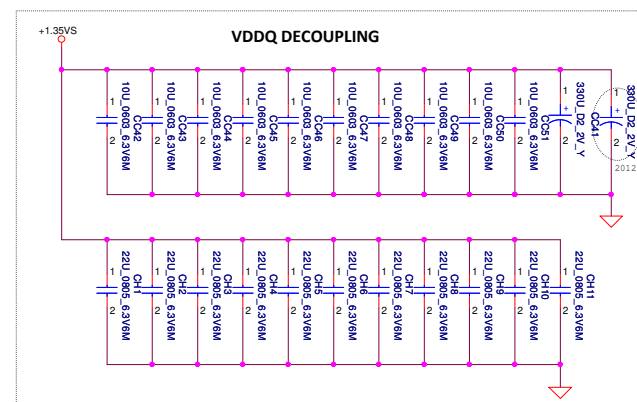
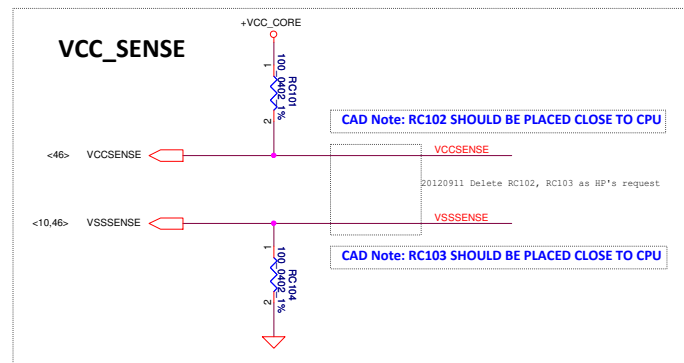


SVID ALERT

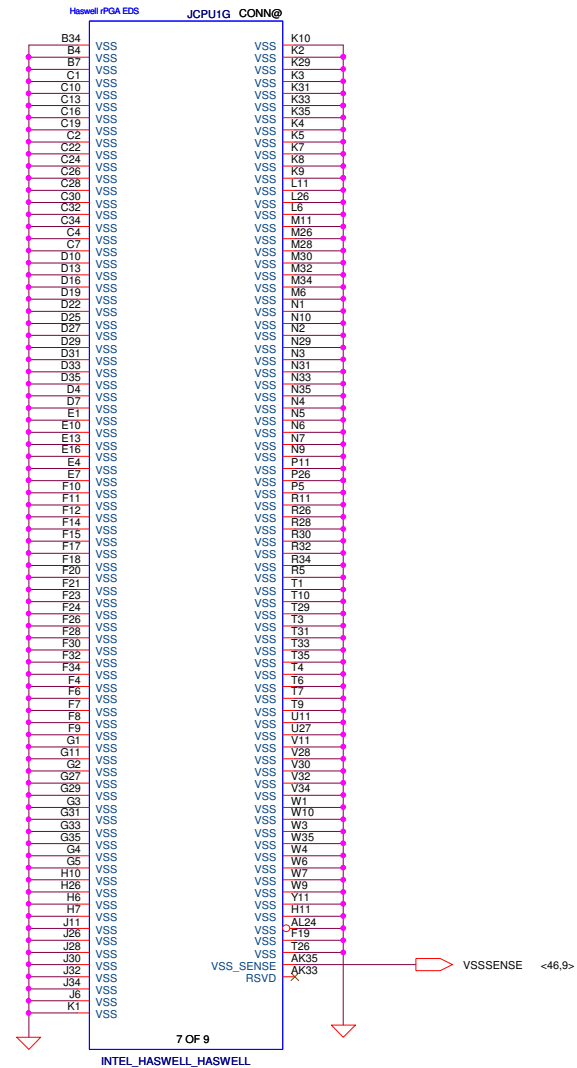
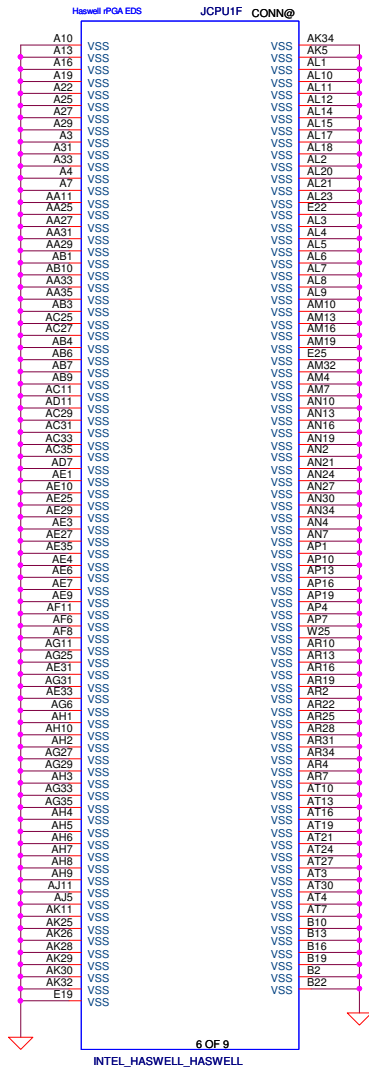
**CAD Note: Place the PU resistors close to CPU
RC60 close to CPU 300 - 1500mils**

SVID DATA

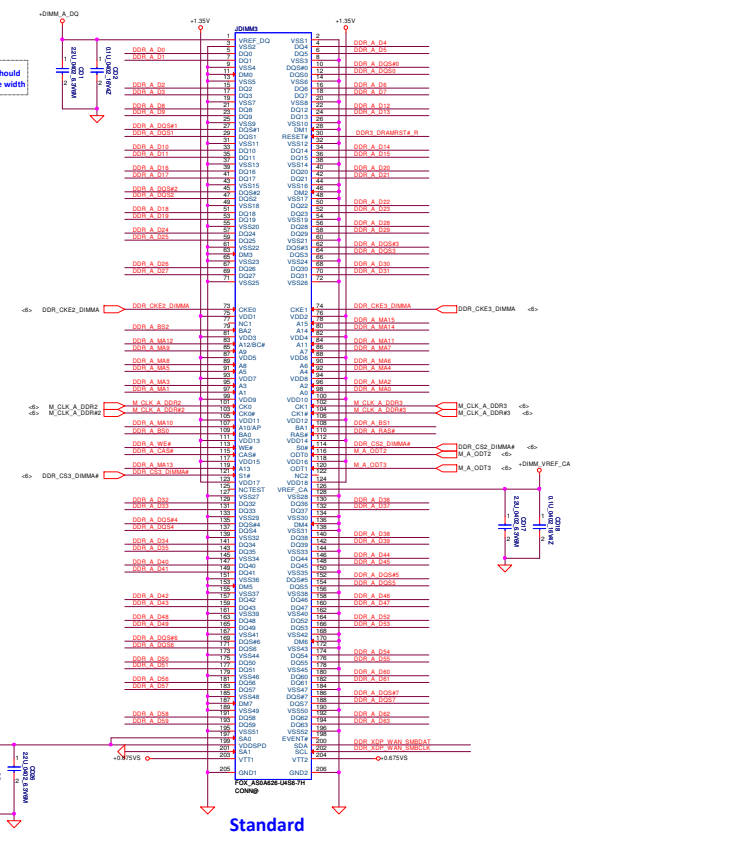
**CAD Note: Place the PU resistors close to CPU
RC63 close to CPU 300 - 1500mils**



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JDIMM3 H=4.0mm BOT

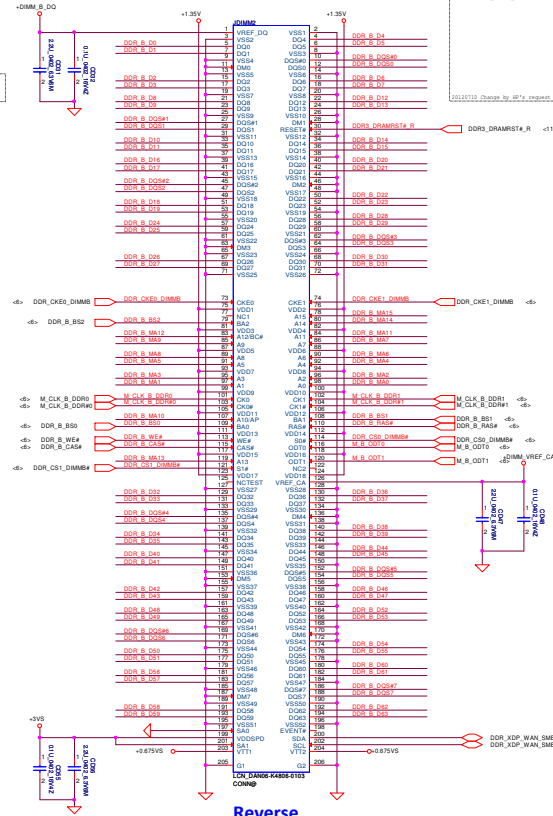


Standard

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			001 001	DDRIII DIMM1&2 LA-937P1
			Date 001	001 001

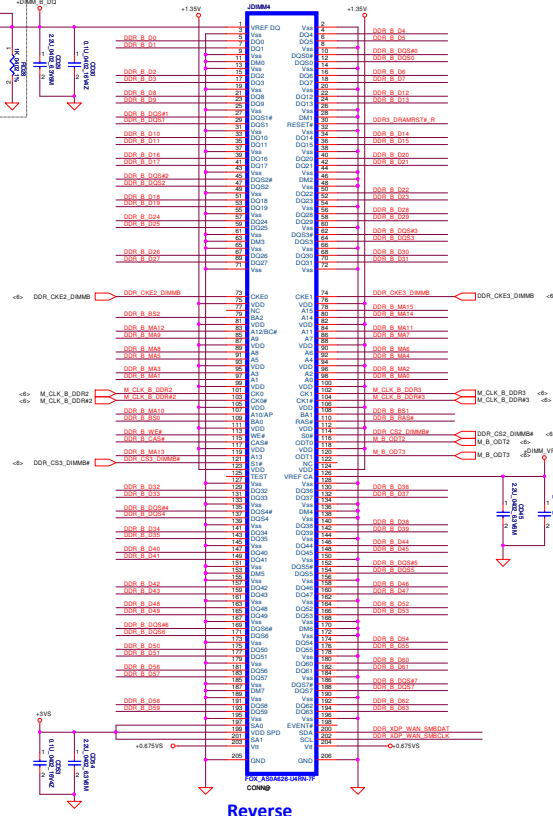
Populate RD4, De-Populate RD8 for Intel DDR3 VREFDQ multiple methods M1
Populate RD8, De-Populate RD4 for Intel DDR3 VREFDQ multiple methods M3

JDIMM2 H=9.2mm TOP



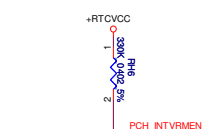
Reverse

JDIMM4 H=4.0mm BOT

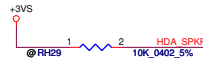


Reverse

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Page	13	Page	13	Page	13



INTVRMEN - INTEGRATED SUS 1.05V VRM
ENABLE
High - Enable Internal VRs
Low - Enable External VRs



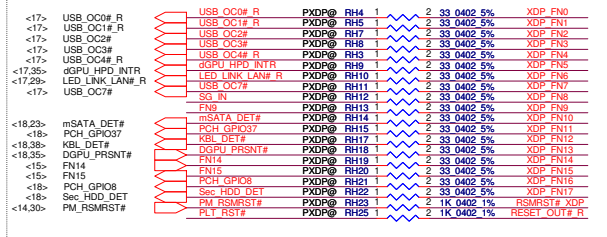
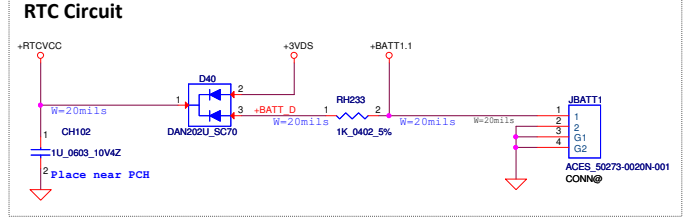
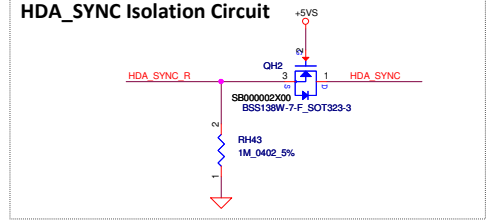
NO REBOOT STRAP
DISABLED WHEN LOW (DEFAULT)
ENABLED WHEN HIGH



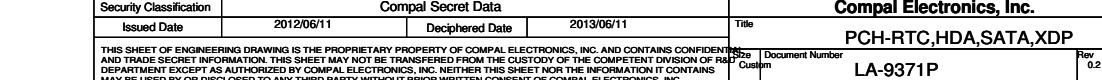
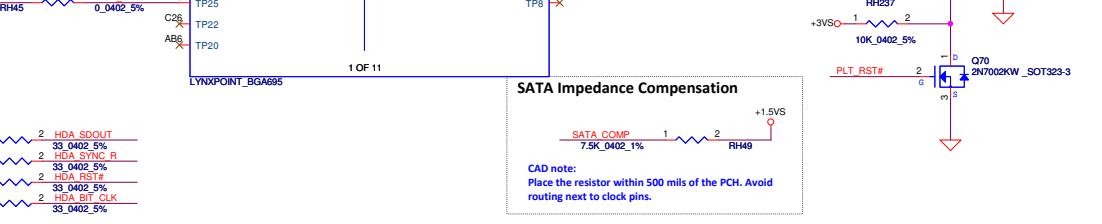
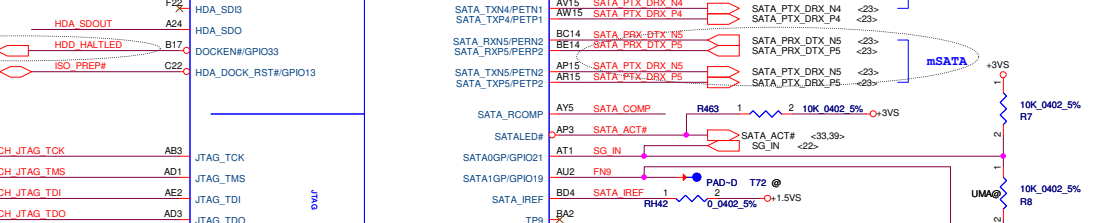
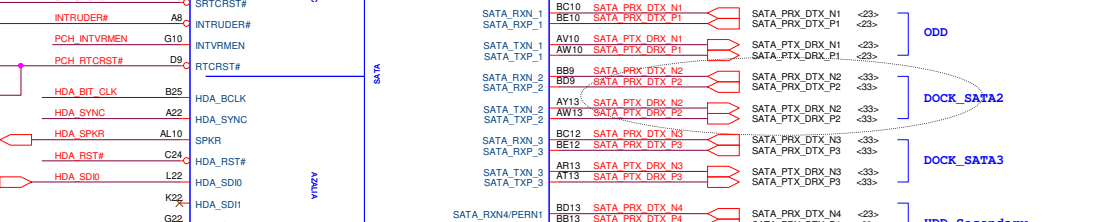
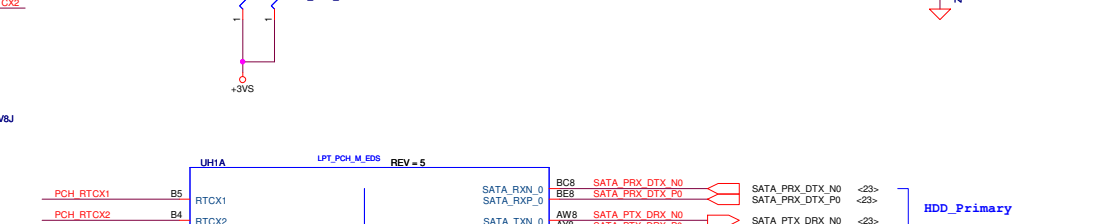
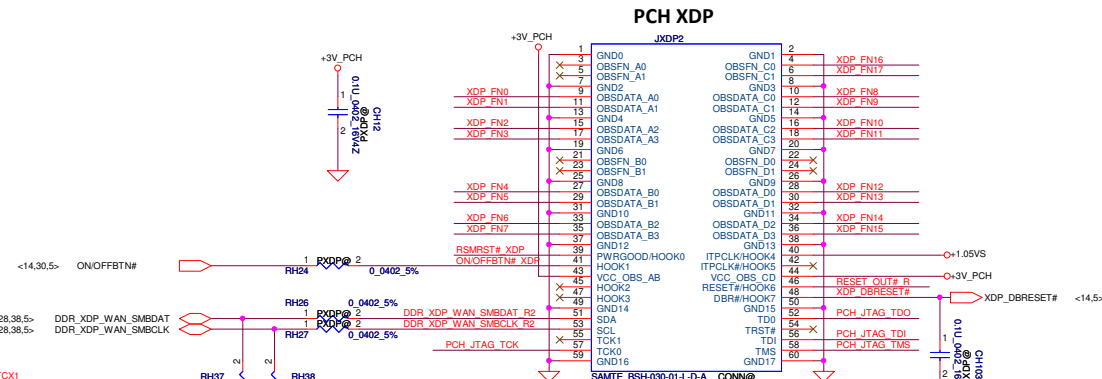
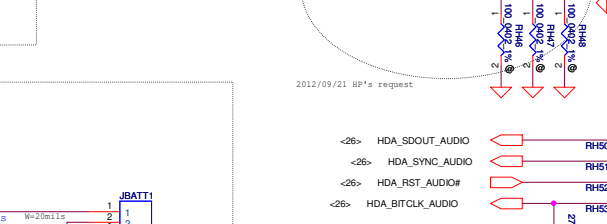
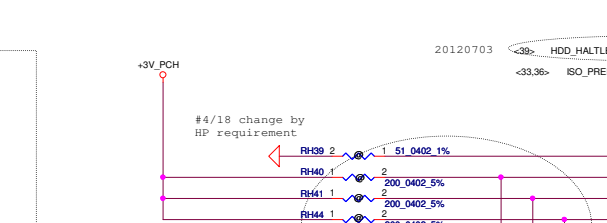
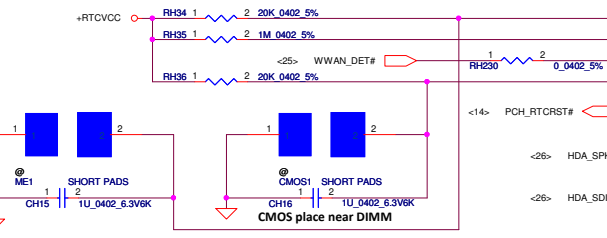
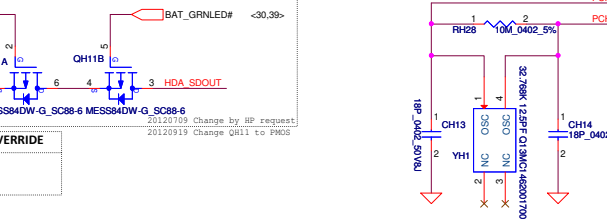
20120802 HP's request

CMOS_CLR1	CMOS setting
Shunt	Clear CMOS
Open	Keep CMOS

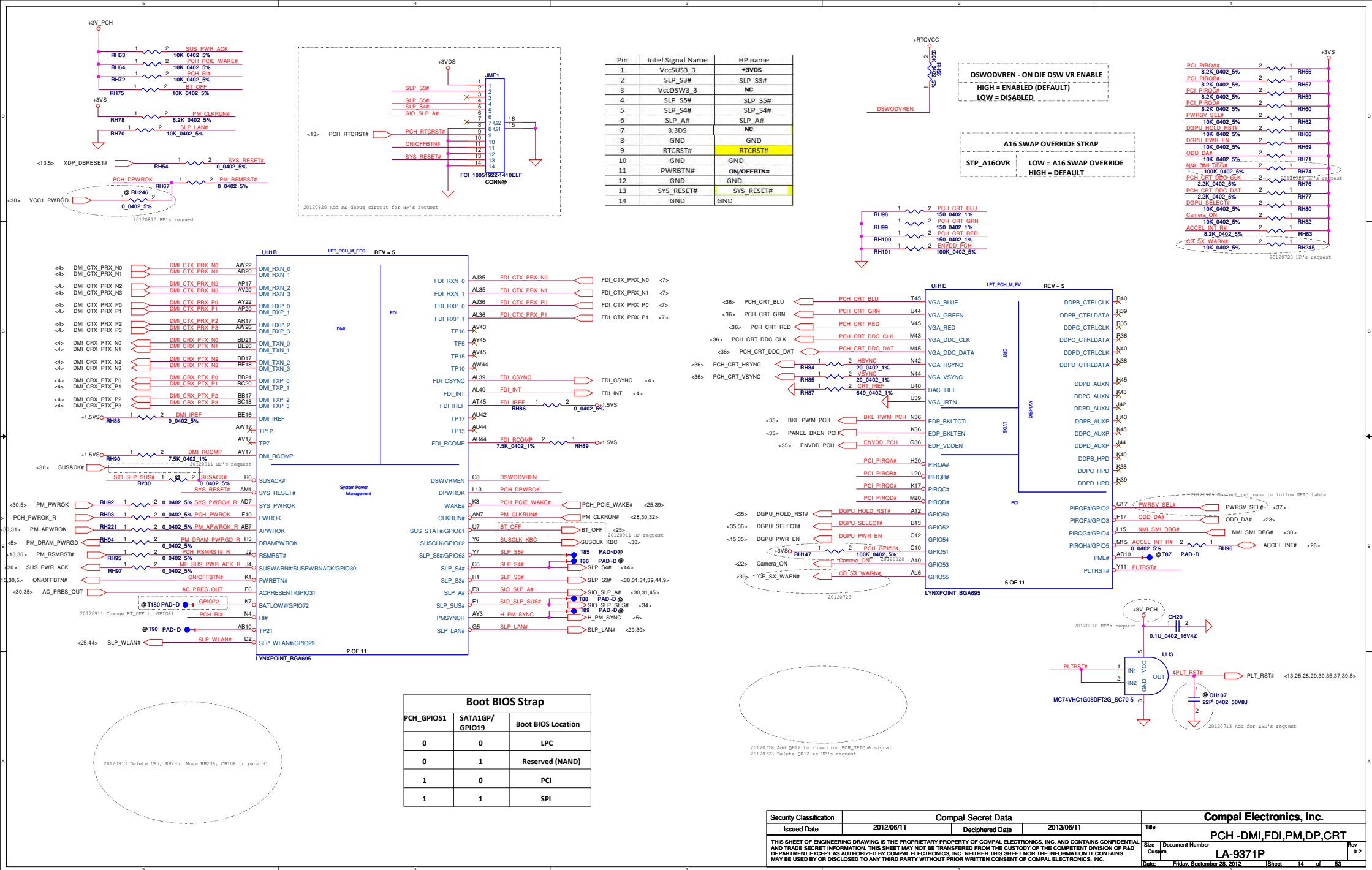
ME_CLR1	TPM setting
Shunt	Clear ME RTC Registers
Open	Keep ME RTC Registers



#4/18 change by HP requirement

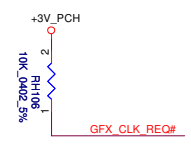
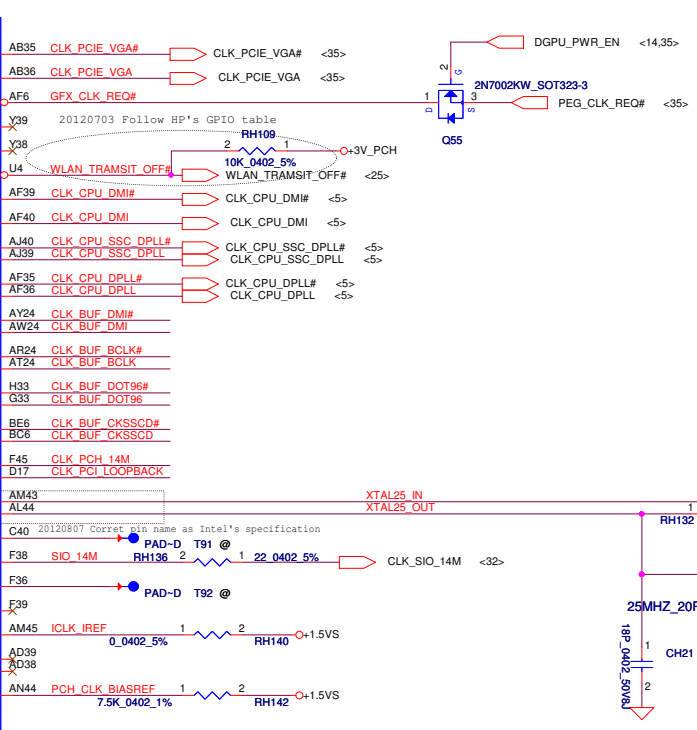
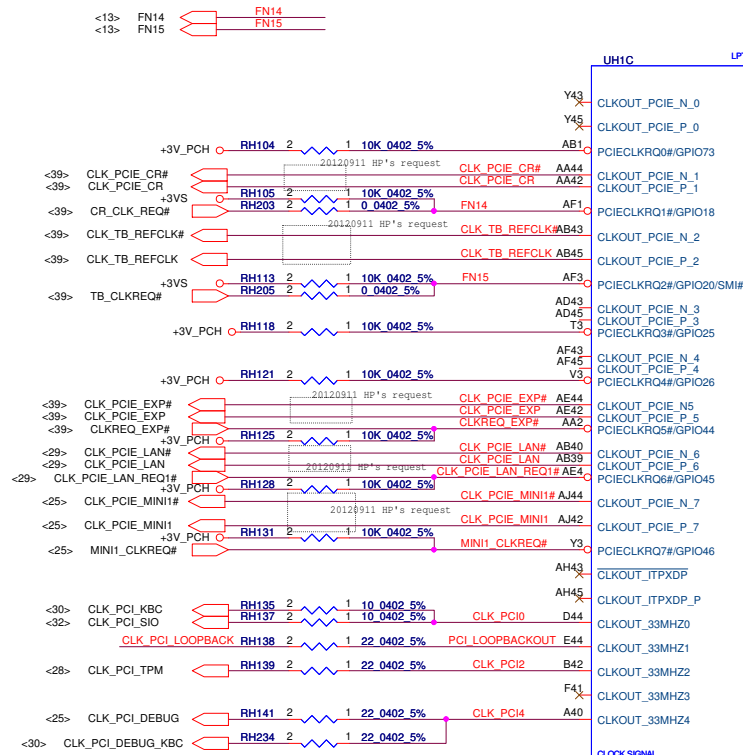


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		Rev	0.2
		Date	Friday, September 28, 2012
		Sheet	13 of 53

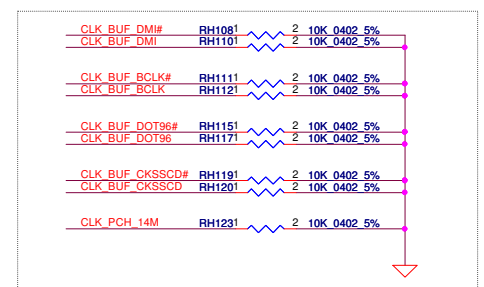
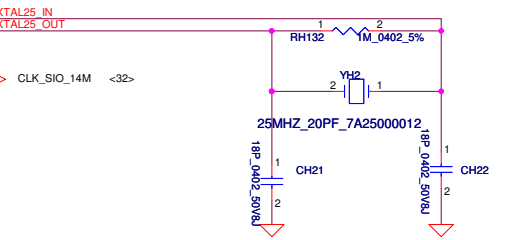


Boot BIOS Strap		
PCH_GPIOS1	SATA1GP/ GPIO19	Boot BIOS Location
0	0	LPC
0	1	Reserved (NAND)
1	0	PCI
1	1	SPI

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				Custom	ment
				LA-9371P	Rev. 0.2
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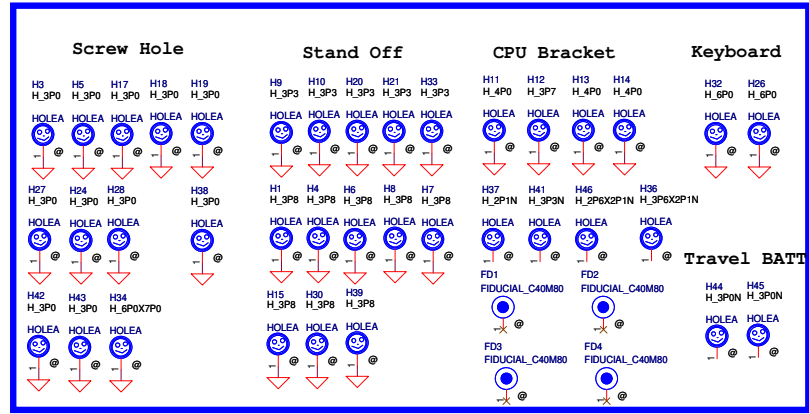
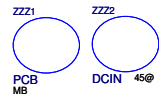
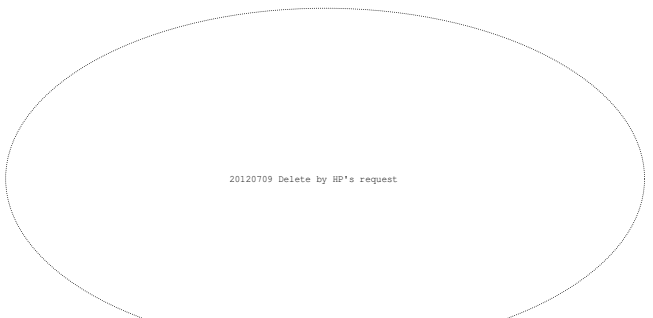
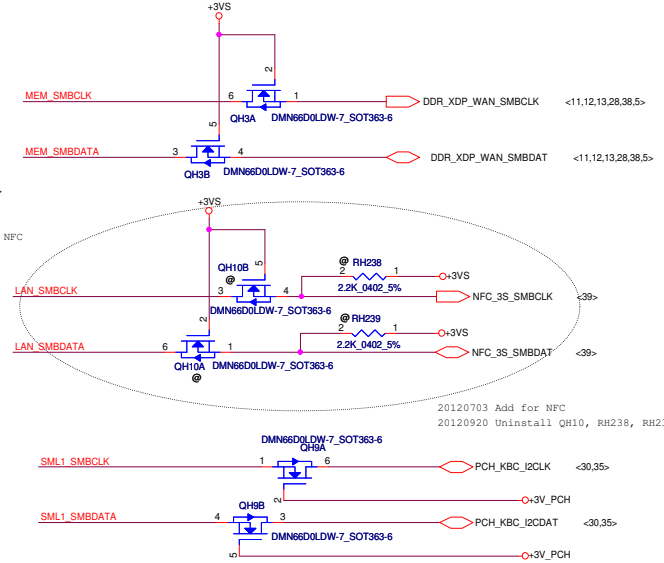
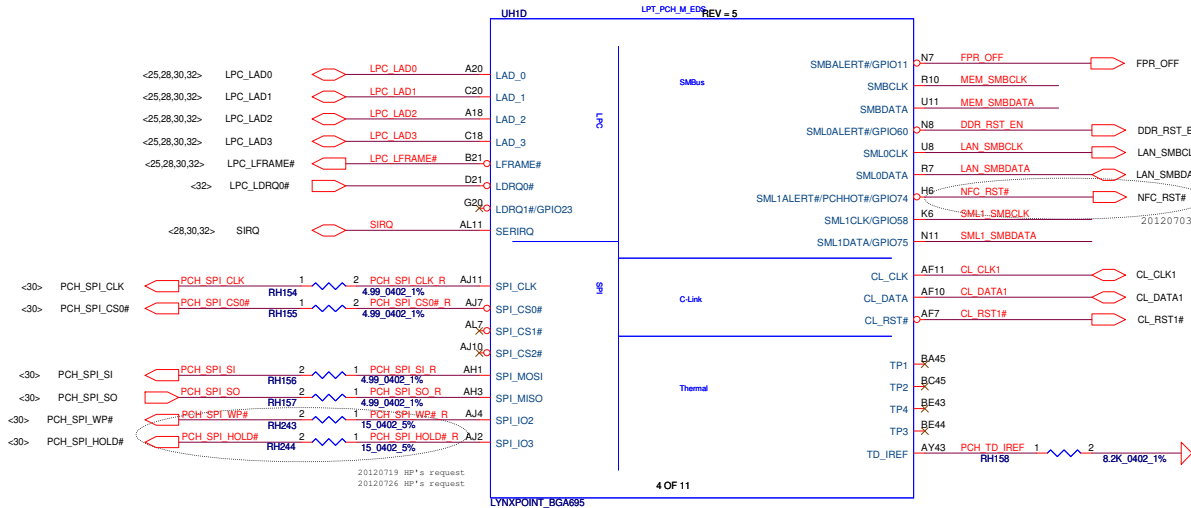
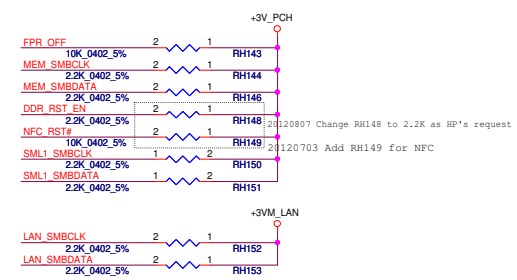
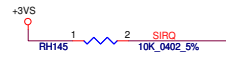


PCIECLK REQ Pull UP Power Rail:
SUS Rail : 0 3 4 5 6 7
Core Rail: 1 2



CLOCK TERMINATION for FCIM and need close to PCH

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Size		Document Number		Rev	
Cuskin		LA-9371P		0.2	
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		53			

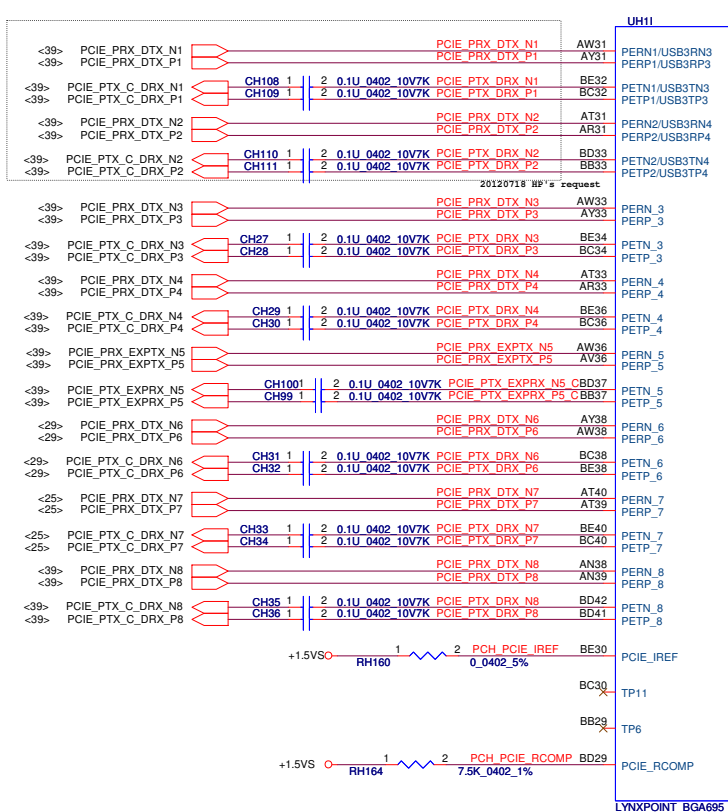
ThunderBolt

Express card slot

GIGA LAN

WLAN

Card Reader



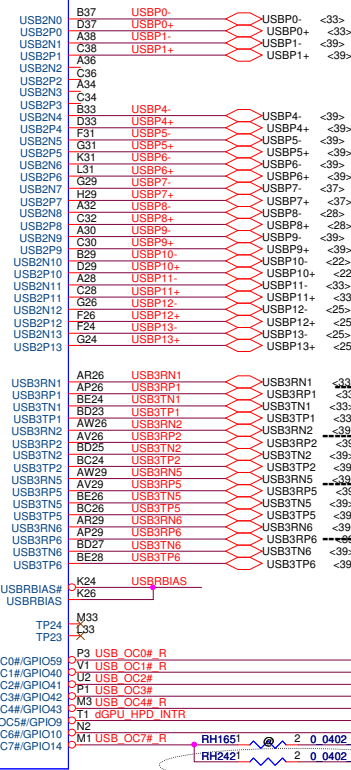
LPT_PCH_M_EDS REV = 5

PCH

SD

9 OF 11

LNXPPOINT_BGA695



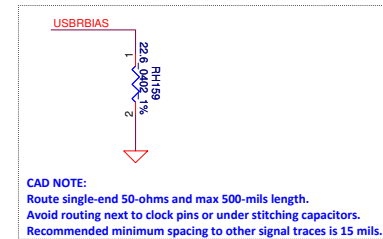
----->Docking USB 3.0
----->USB 3.0 Walkup port 1
----->NA
----->NA
----->USB 3.0 Walkup port 2
----->USB 3.0 Walkup port 3
----->Express card slot
----->Smart card reader
----->Finger Print Reader
----->Walkup USB 2.0/ USB Charging port
----->USB Camera
----->Docking USB 2.0 port
----->WWAN
----->BT/WLAN Combo

----->Docking USB 3.0

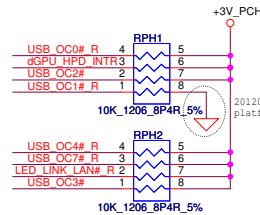
----->USB 3.0 Walkup port 2

----->USB 3.0 Walkup port 5

----->USB 3.0 Walkup port 6



CAD NOTE:
Route single-end 50-ohms and max 500-mils length.
Avoid routing next to clock pins or under stitching capacitors.
Recommended minimum spacing to other signal traces is 15 mils.

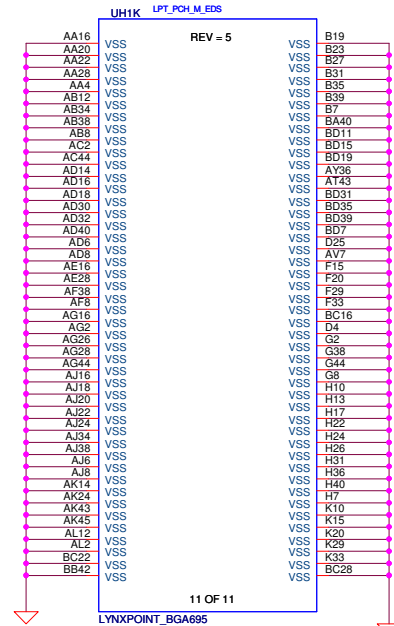
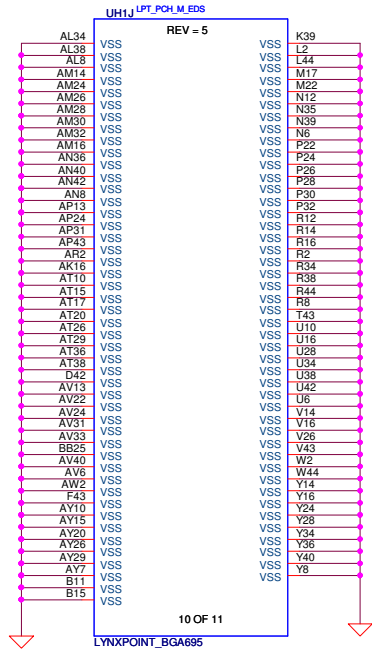


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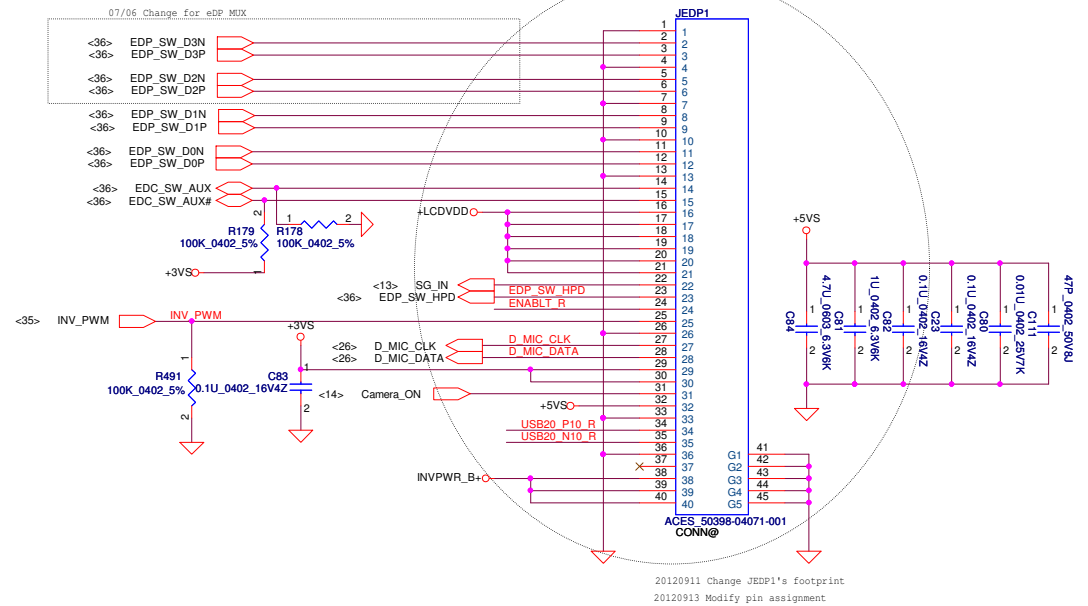
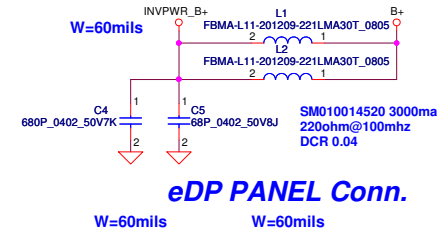
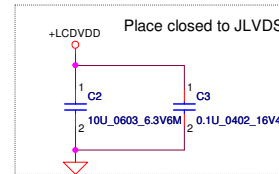
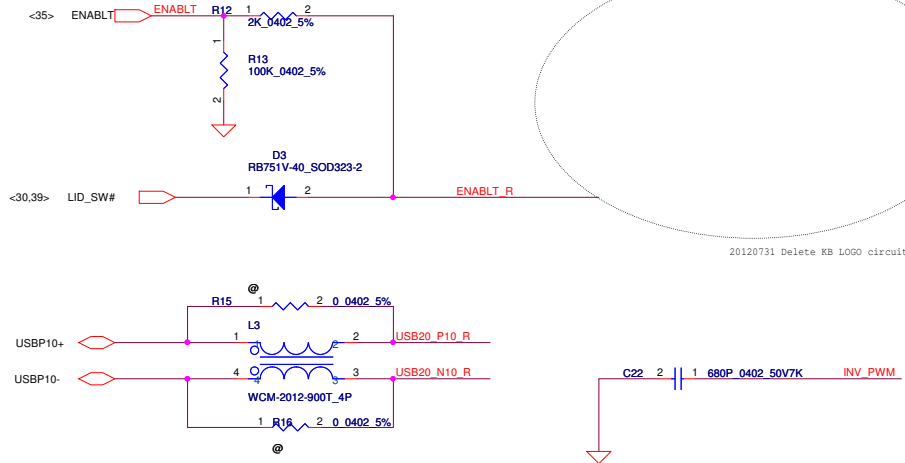
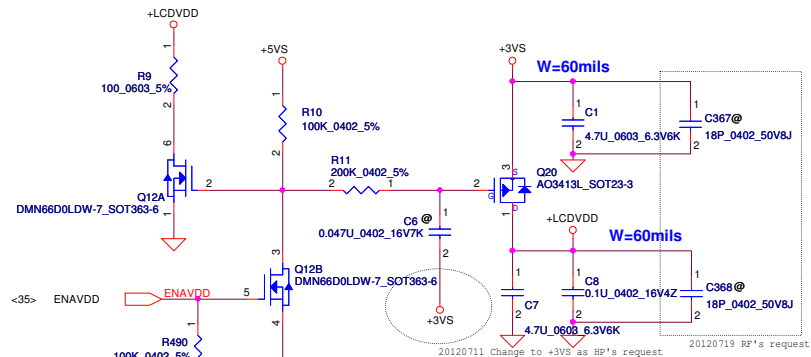
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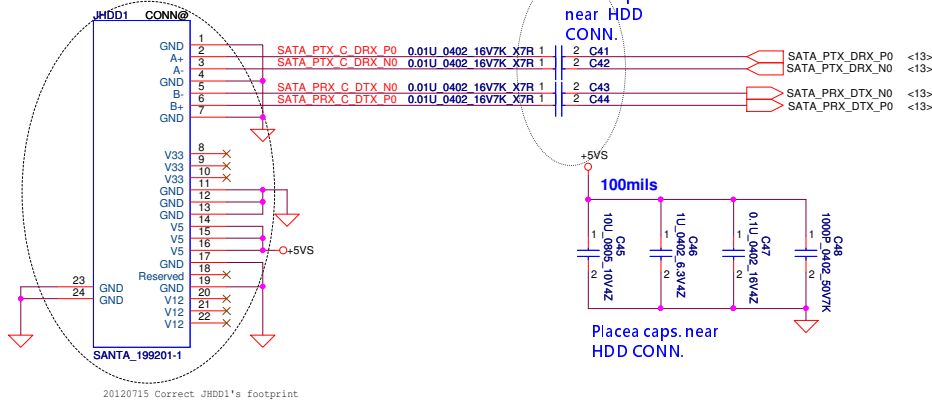


LCD POWER CIRCUIT

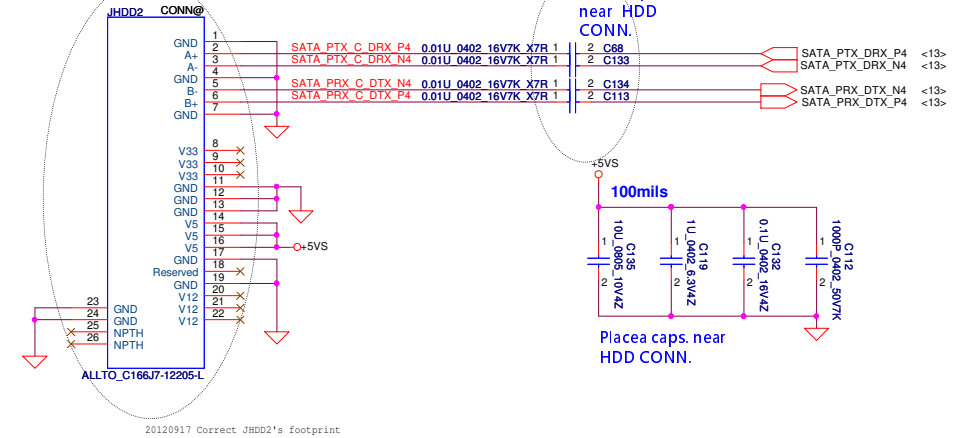


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				Date: Friday, September 28, 2012		Sheet 22 of 53

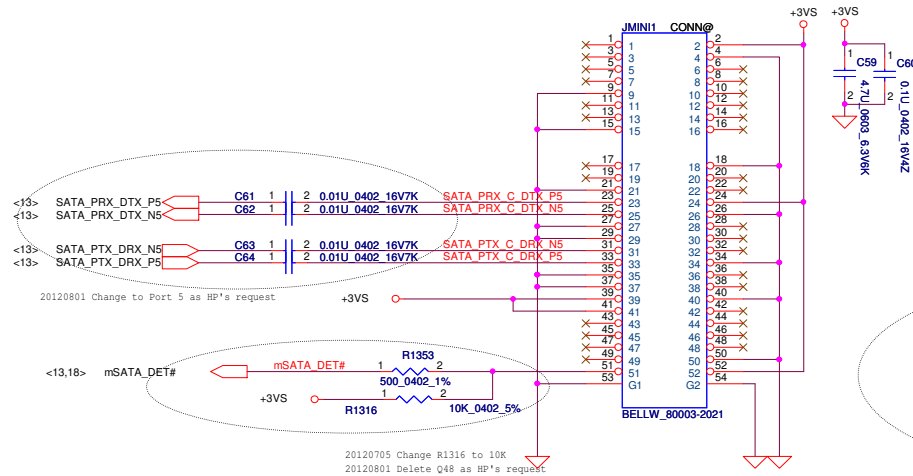
SATA Primary HDD CONN.



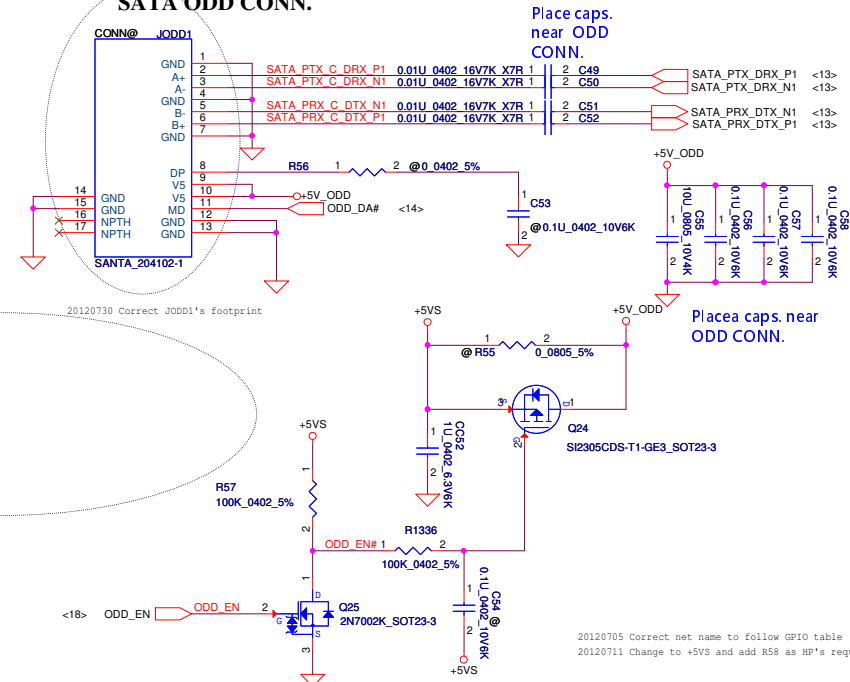
SATA Second HDD CONN.



mSATA Conn.

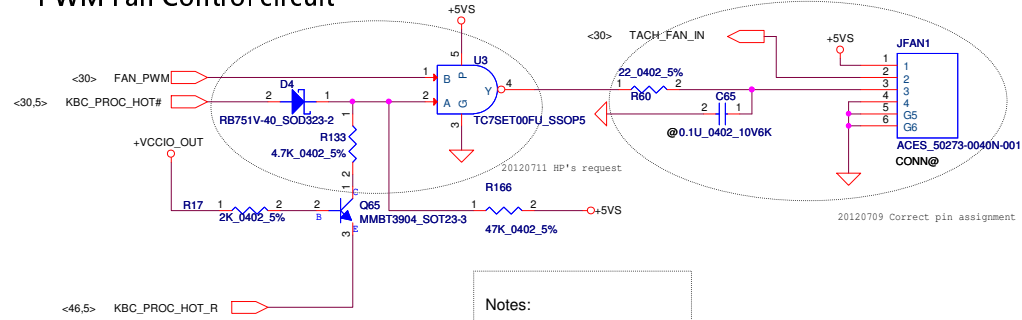


SATA ODD CONN.

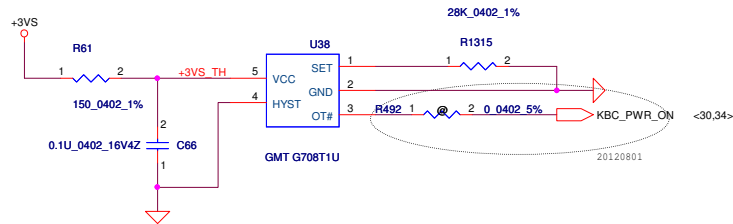


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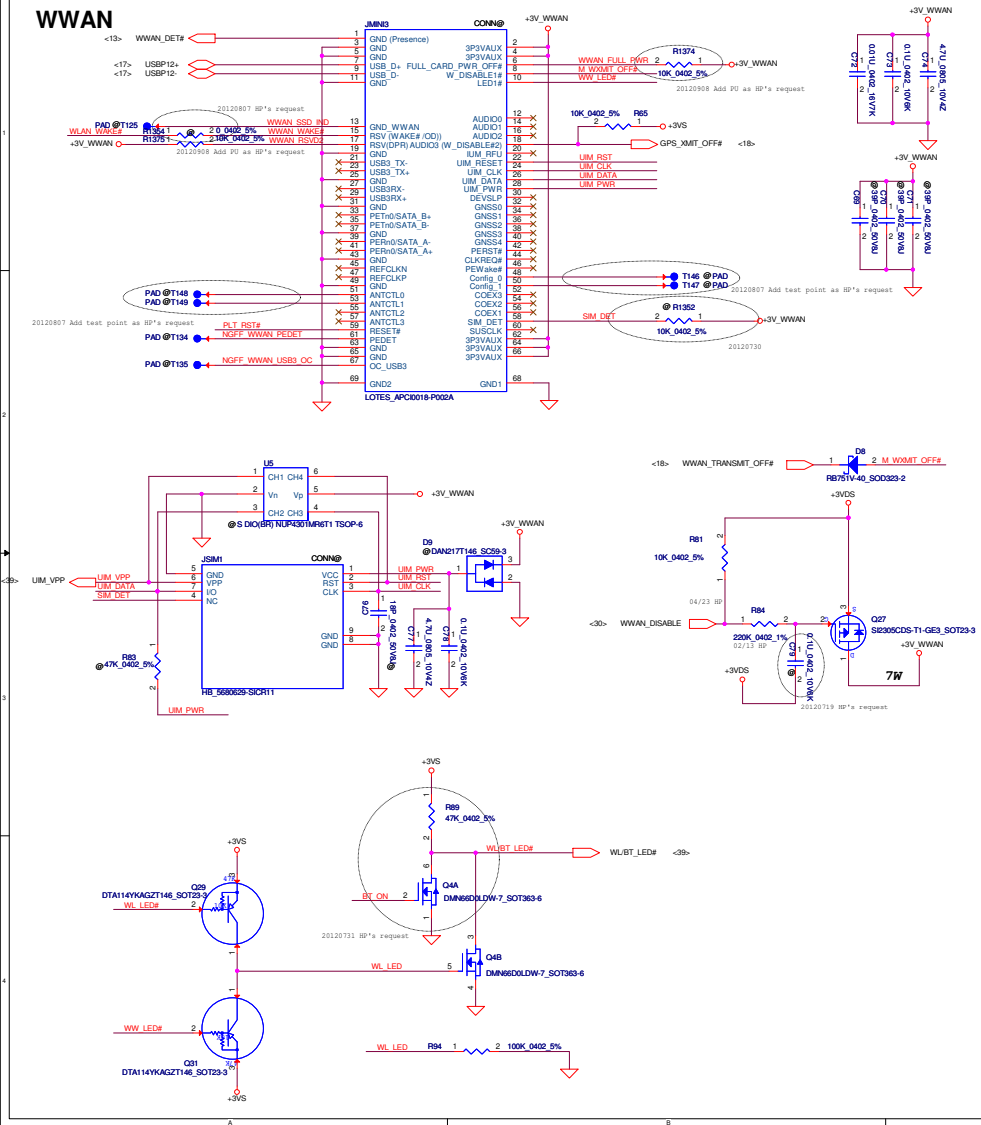
PWM Fan Control circuit



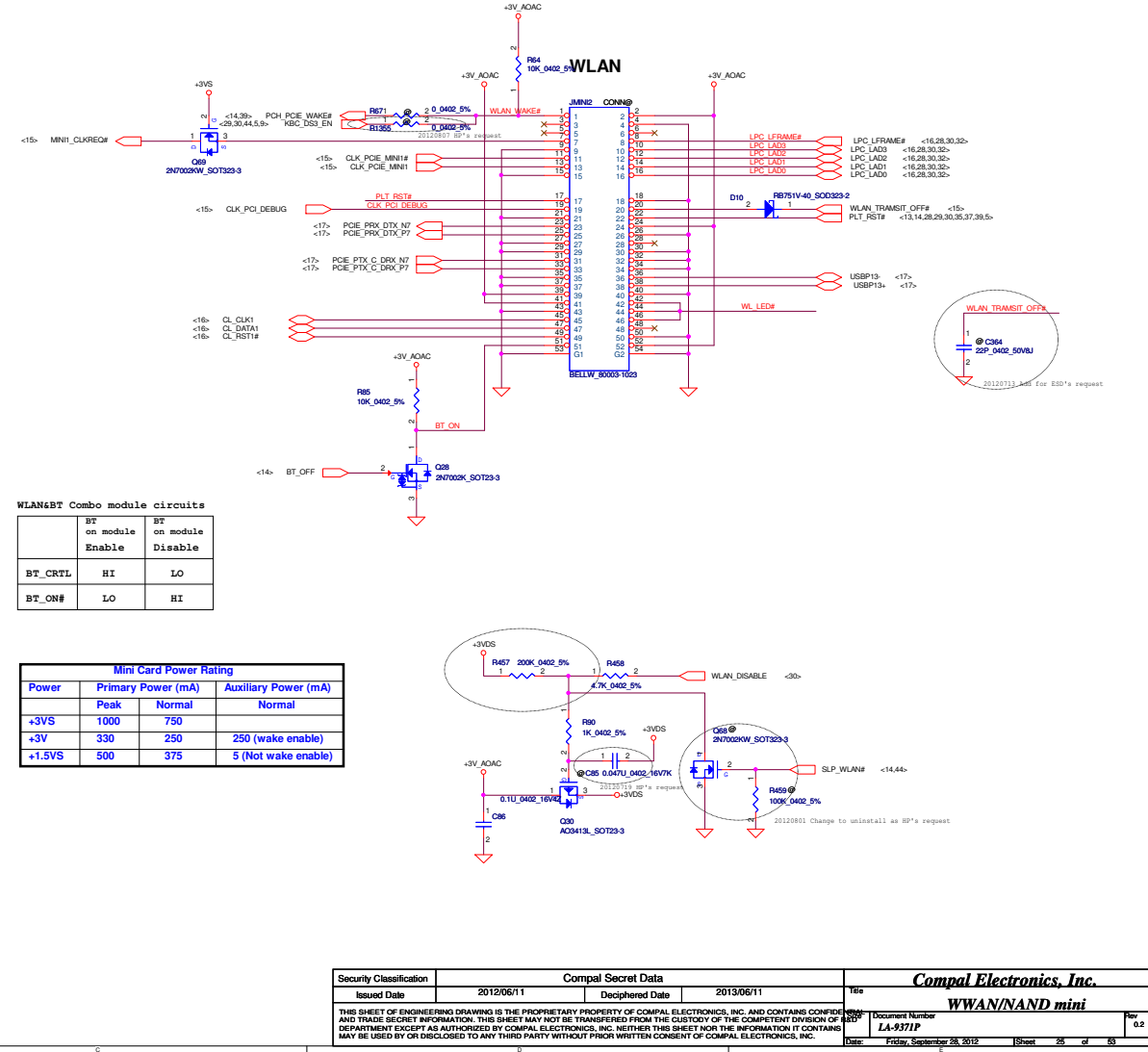
Notes:
Place Q65 close CPU side



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WWAN

WLAN

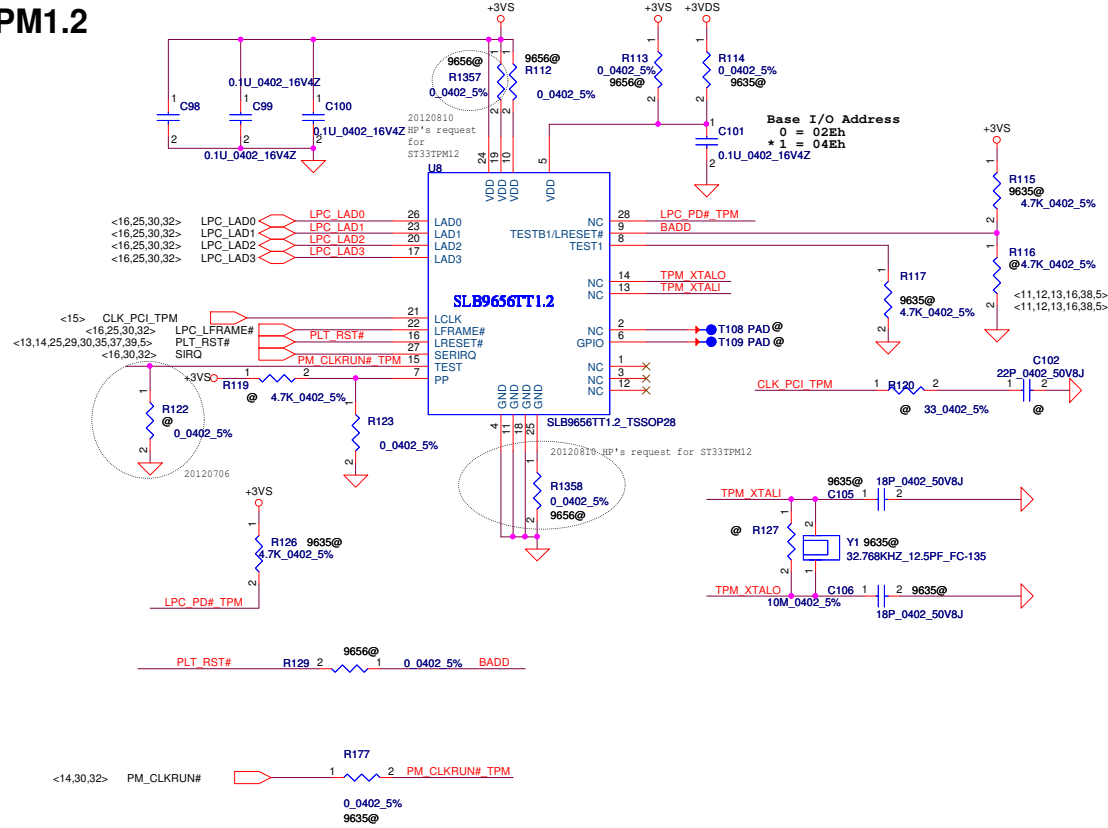


Keep PVDD supply and speaker traces routed on the DGND plane.
Keep away from AGND and other analog signals

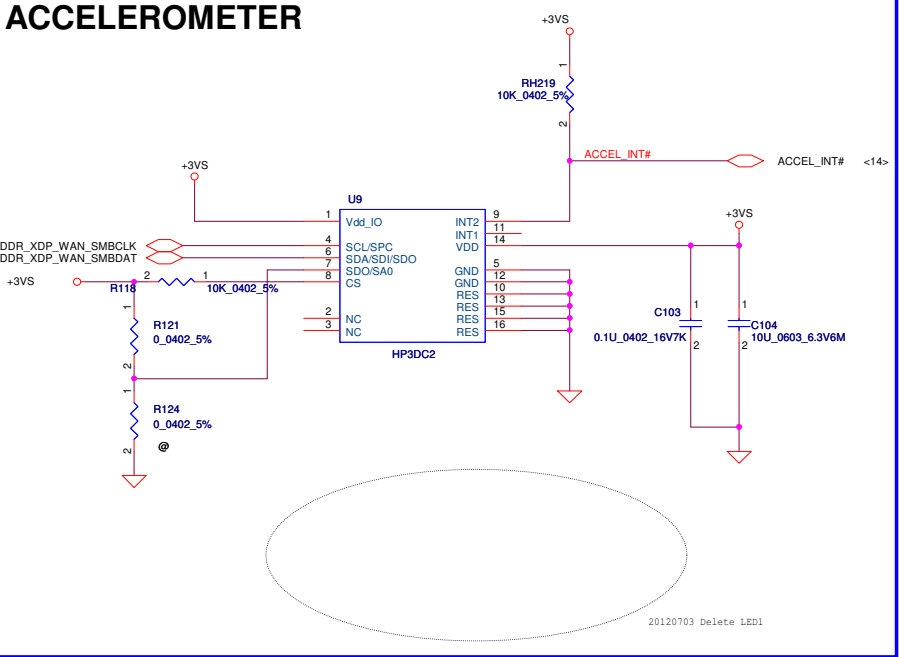


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				Custom	LA-XXXXP	0.2
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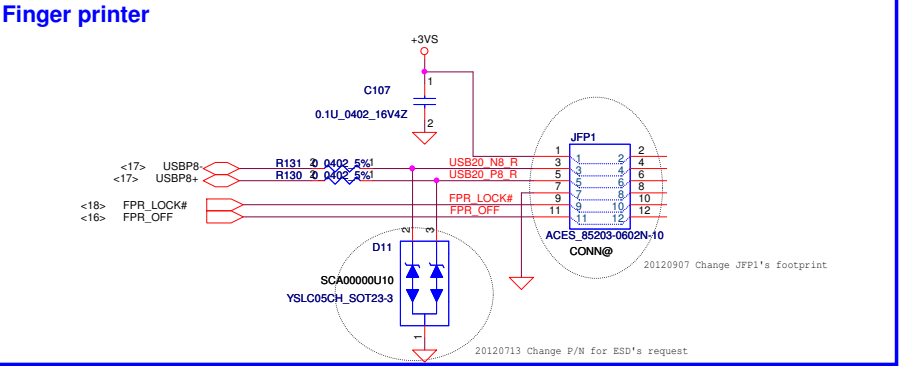
TPM1.2

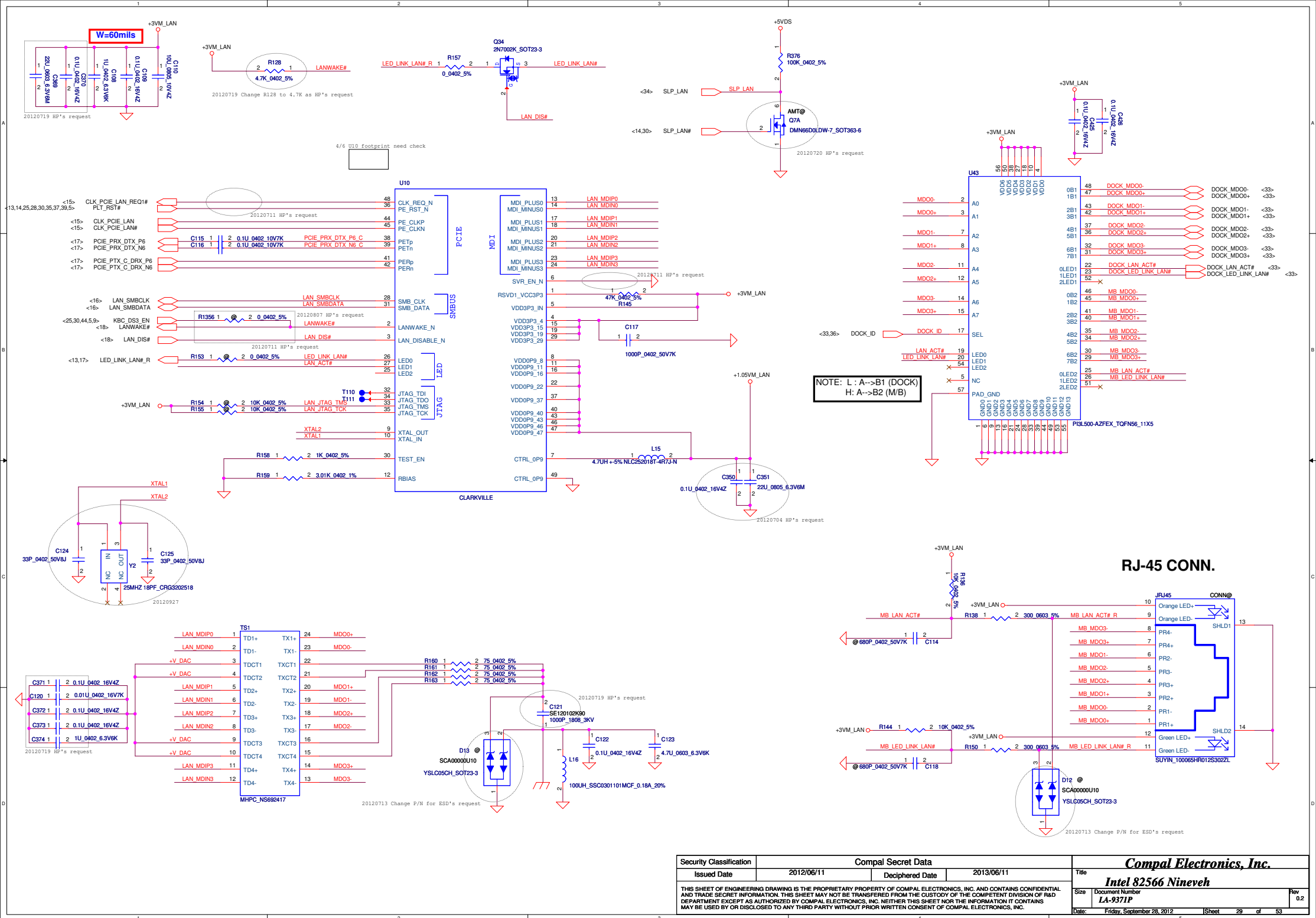


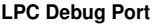
ACCELEROMETER



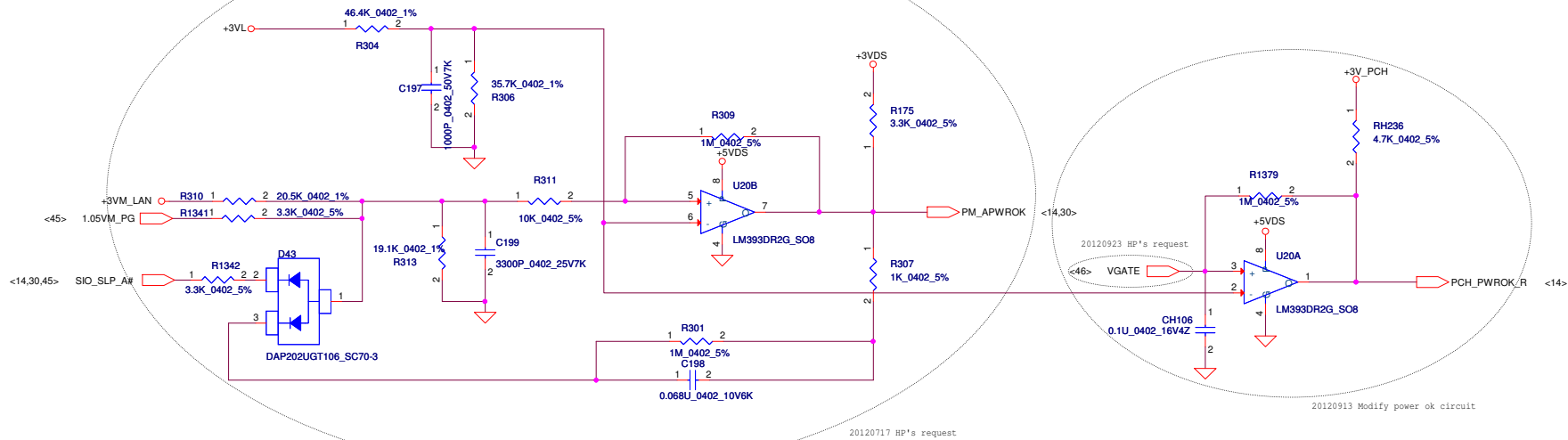
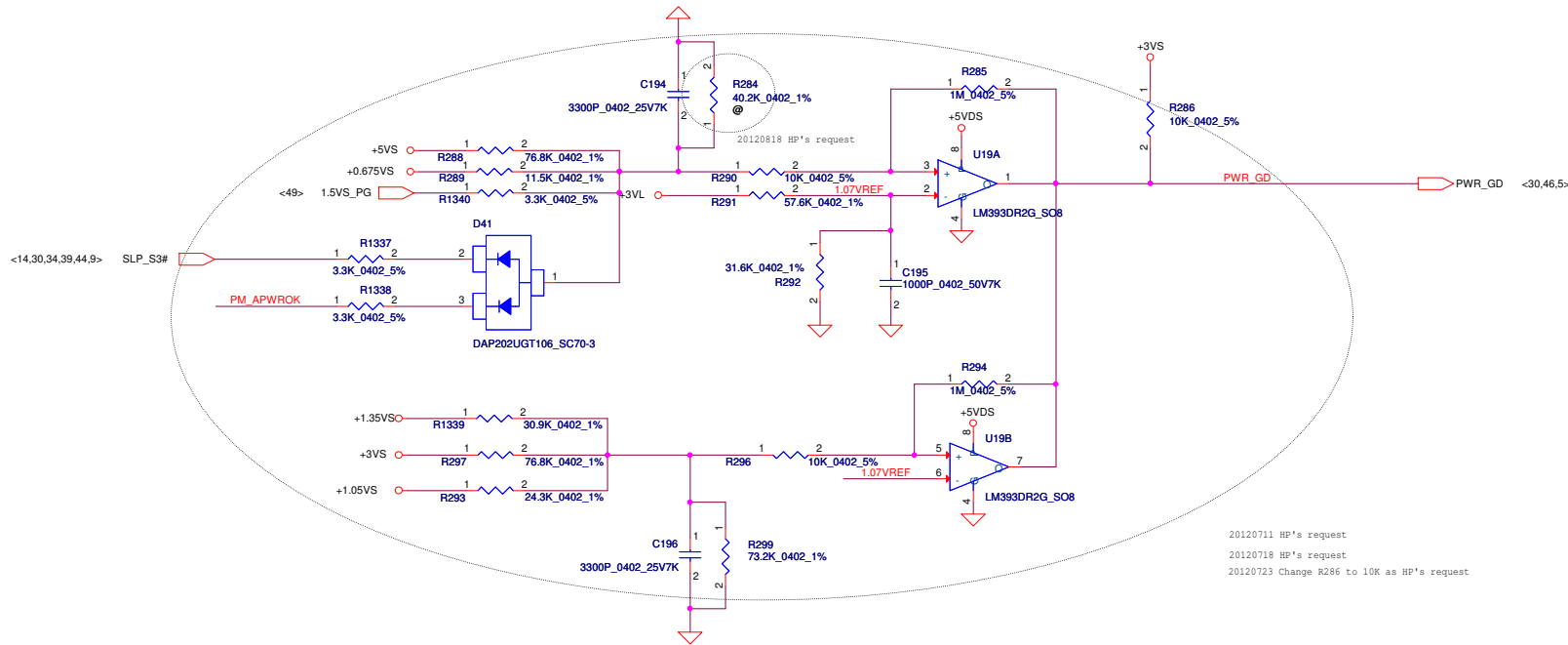
Finger printer







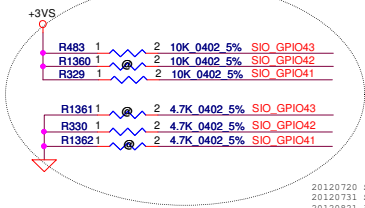
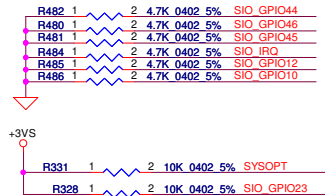
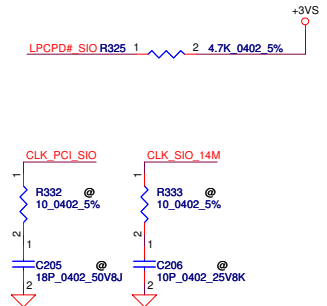
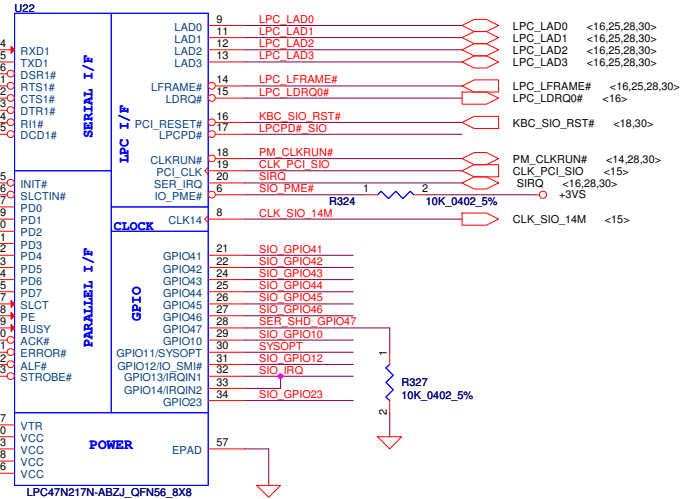
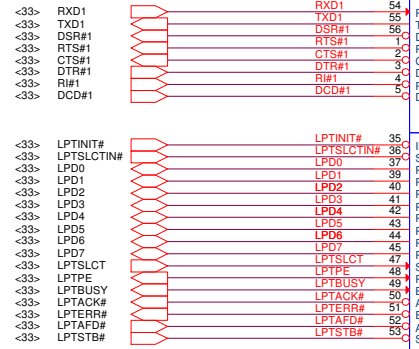
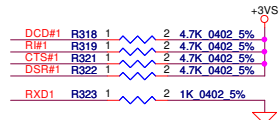
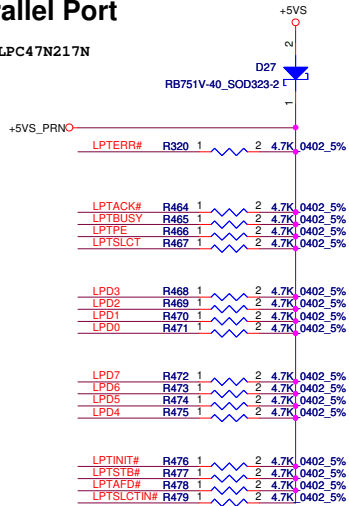
SECRET	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608/11	201608
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Security Classification		Compal Secret Data		Compal Electronics, Inc.	
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				LA-9371P	0.2
Date:				Friday, September 28, 2012	Sheet 31 of 53

Parallel Port

TO LPC47N217N



20120720 System ID for HP's request
20120731 System ID for HP's request
20120821 Add R for 2019M & 4019M option

Platform IDs (Super I/O GPIOs)

PLT_ID1	PLT_ID2	PLT_ID3	SYSTEM ID	Series	CPU	Dock	COMMON BIOS
GPIO43	GPIO42	GPIO41					
1	0	1	Afterburner 17" 4 DIMM	W	Haswell	Common	8
1	1	1	Afterburner 17" 2 DIMM	W	Haswell	Common	8

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				LA-9371P	0.2
Date: Friday, September 28, 2012				Sheet	32 of 53

DOCKING CONNECT

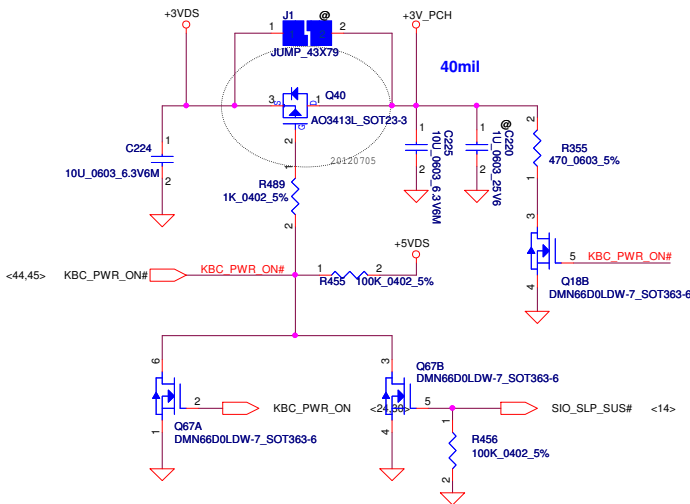


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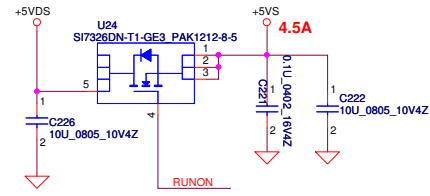
Compal Electronics, Inc.			
Title			
DOCK CONN			
Size Custom	Document Number LA-9371P		Rev 0.2
Date:	Friday, September 28, 2012	Sheet 33 of 53	

+3VALW TO +3VALW(PCH AUX Power)

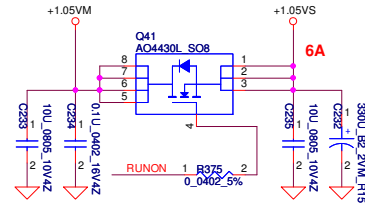
Short J1 for PCH VCCSUS3.3



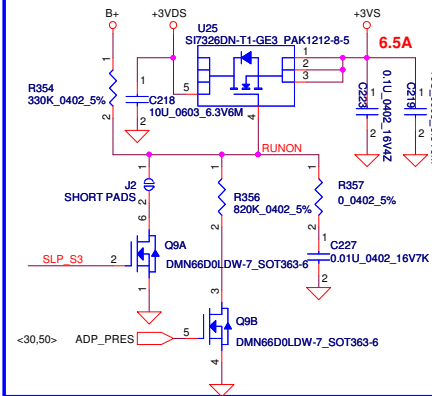
+5VALW to +5VS Transfer



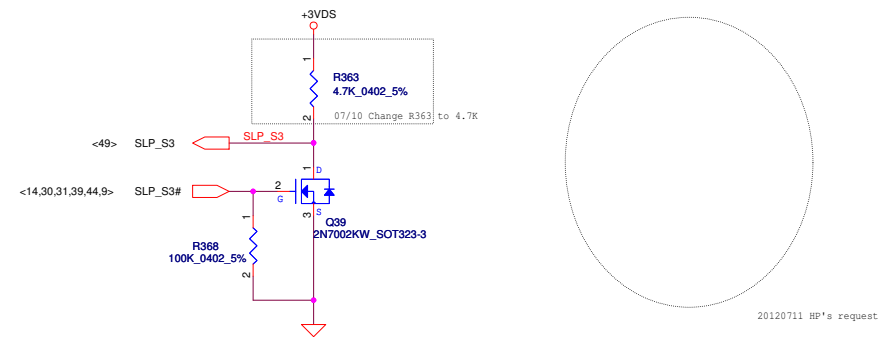
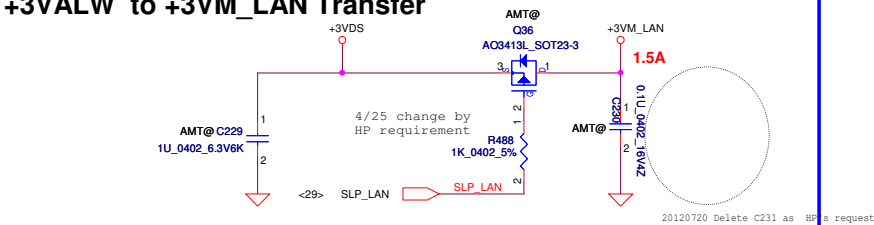
+1.05VM to +1.05VS Transfer



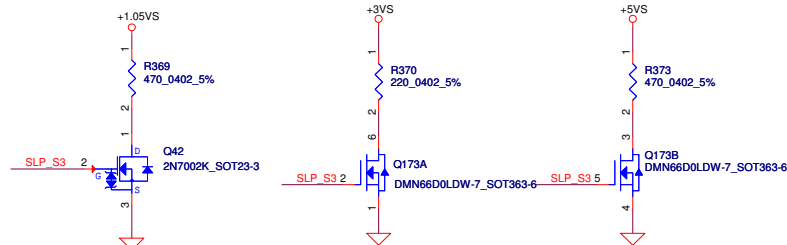
+3VALW to +3VS Transfer



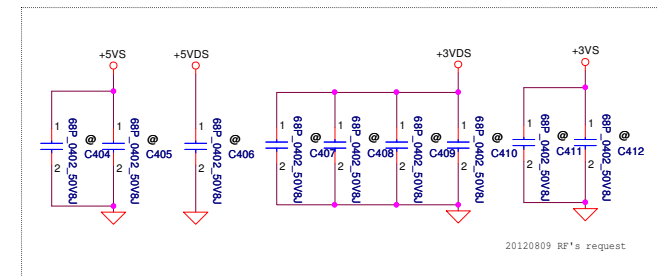
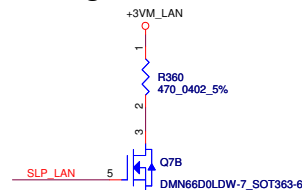
+3VALW to +3VM_LAN Transfer

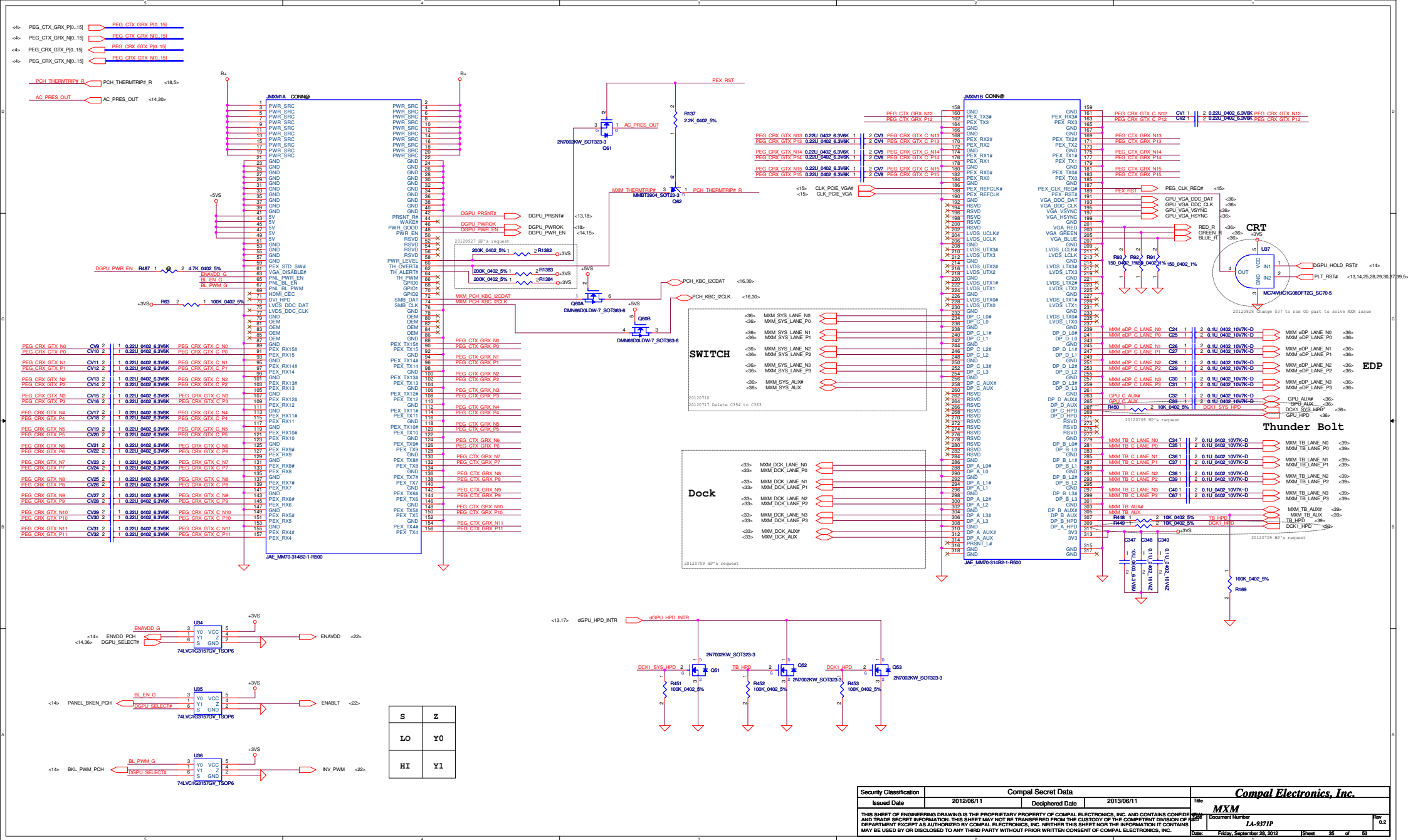


Discharge circuit-1



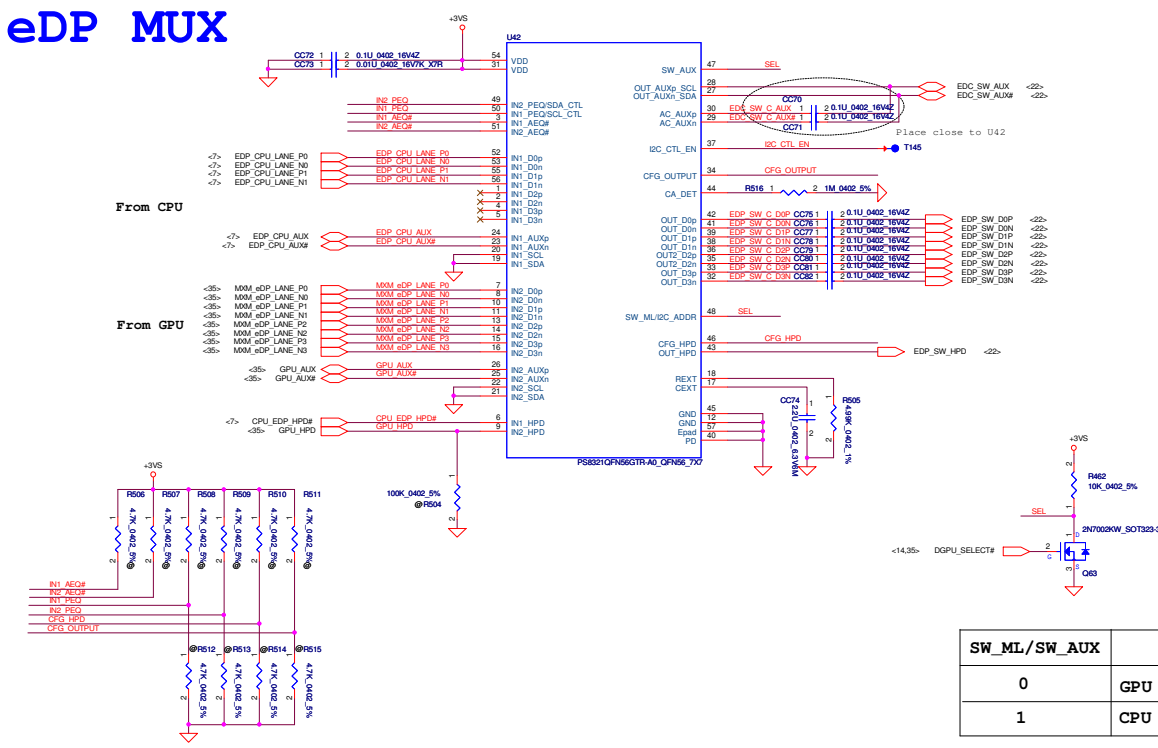
Discharge circuit-2 for V-M



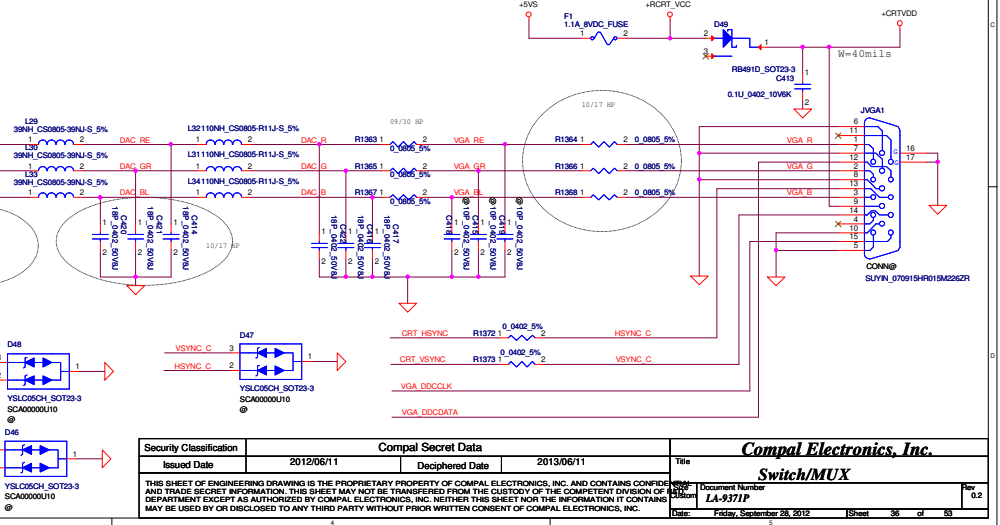
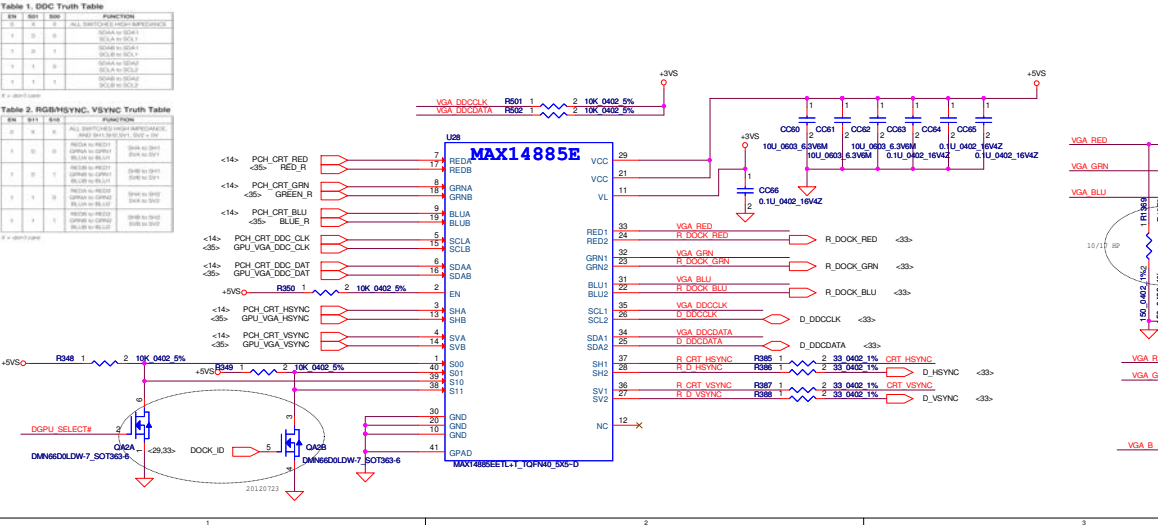
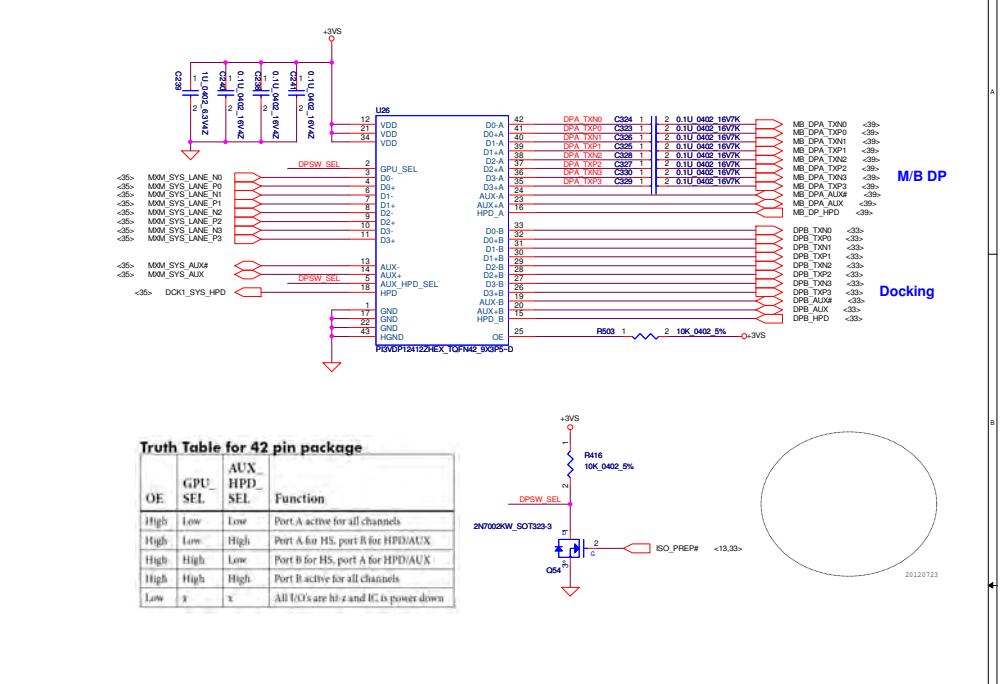


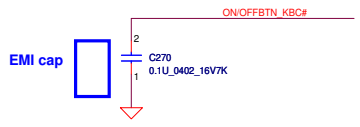
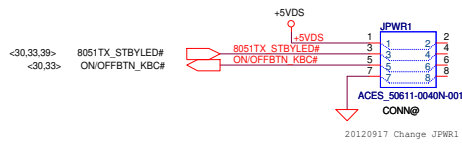
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				Project Number		
				LA-9371P		
Date:	Fri Jun, September 28, 2012	Sheet	35	of	53	Pov 0.2

eDP MUX

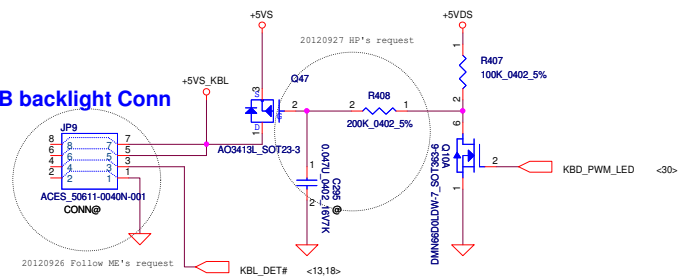


DP Switch

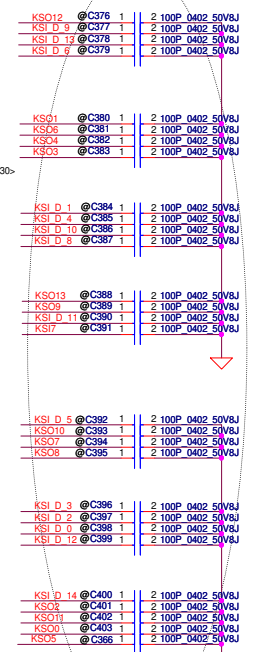
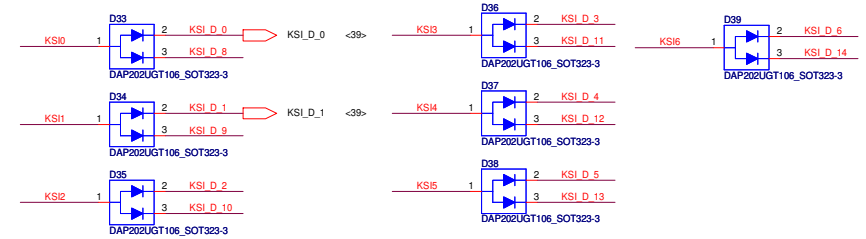
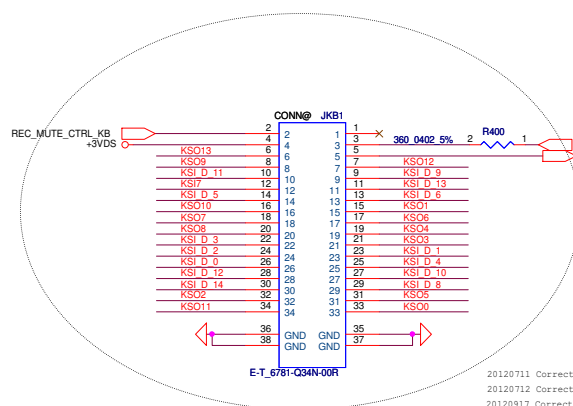
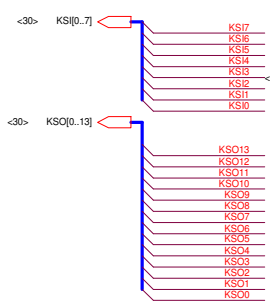
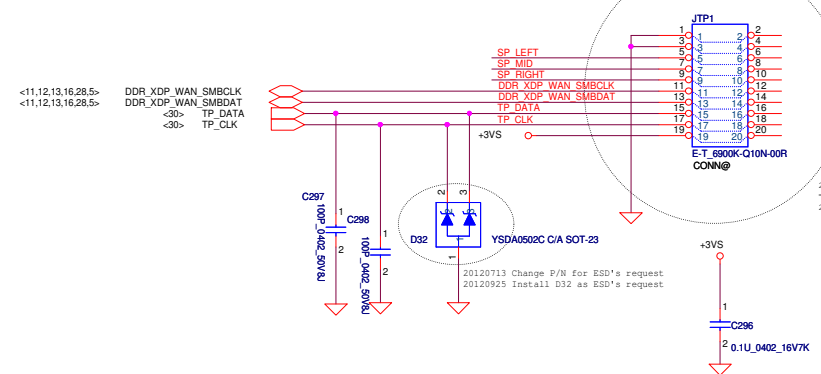




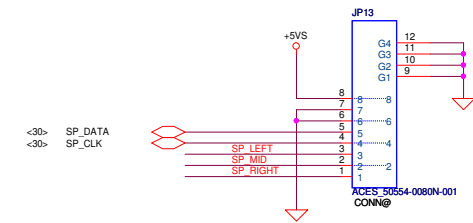
KB backlight Conn



TP/B TO M/B

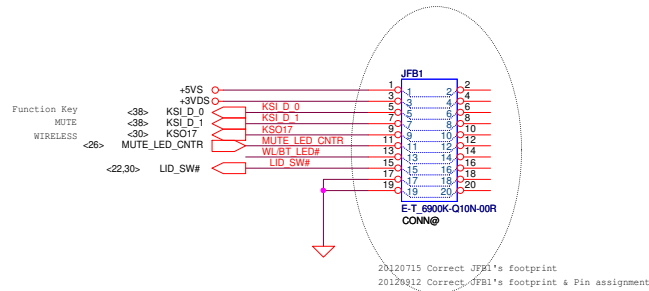


Stick Point CONN

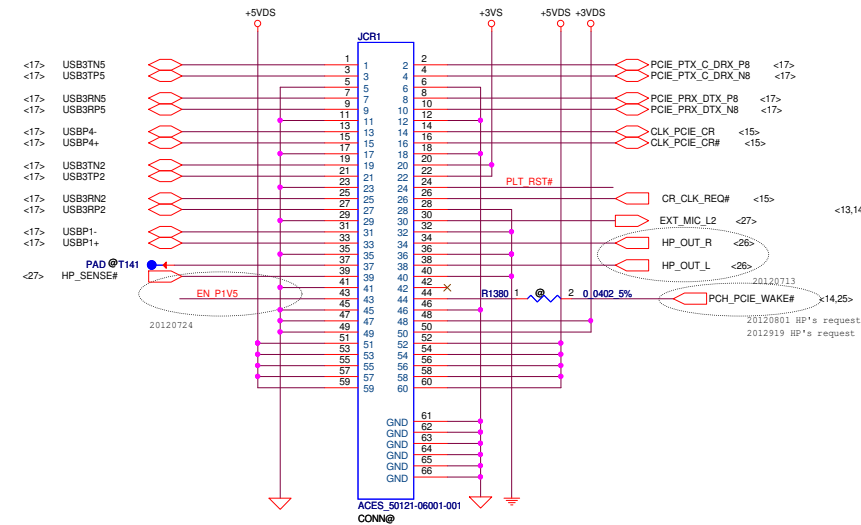


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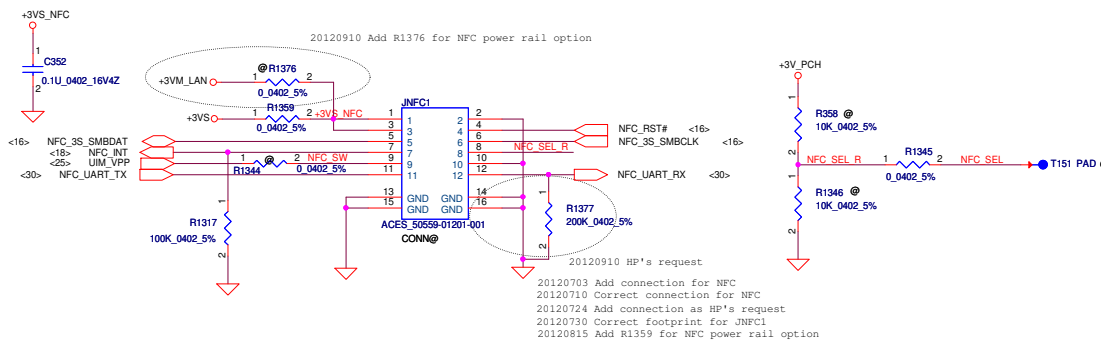
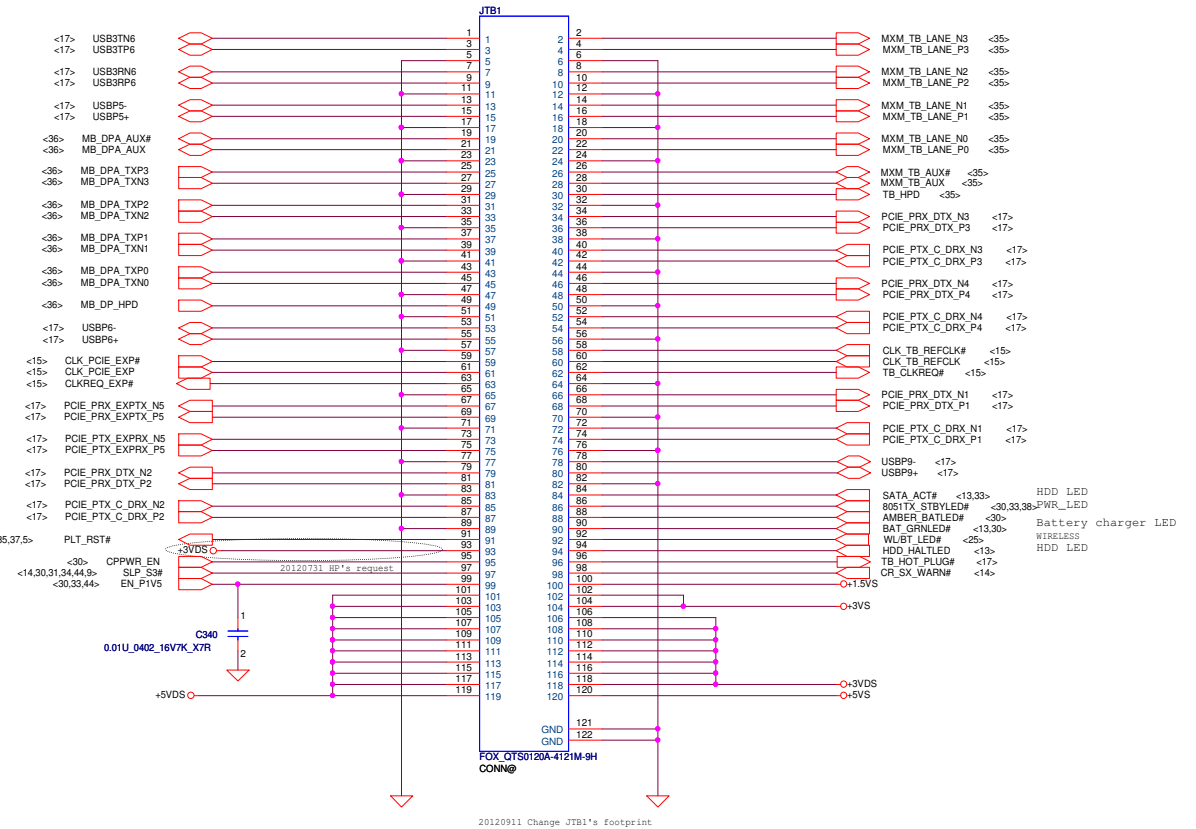
Function Board



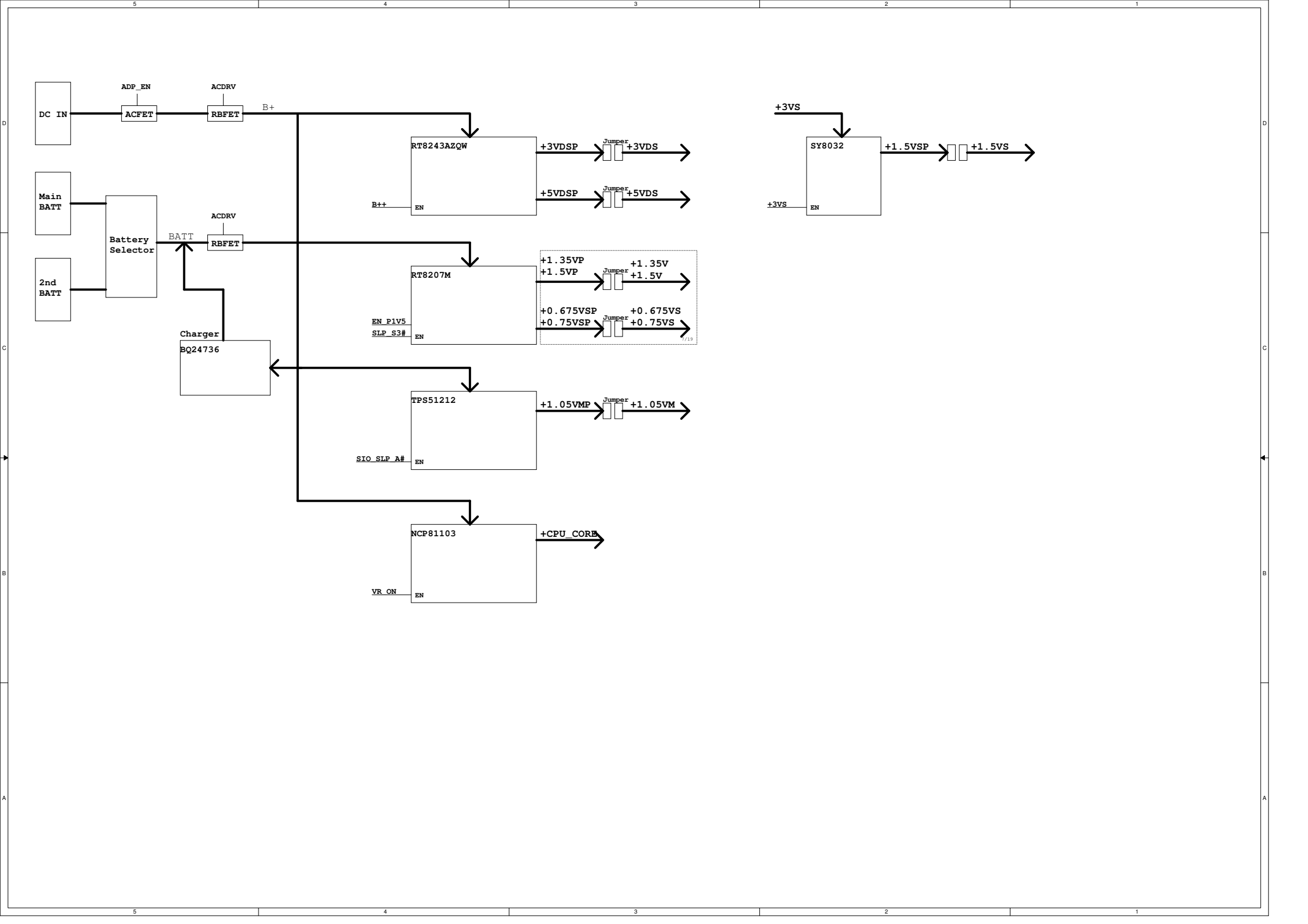
USB & Card Reader Board

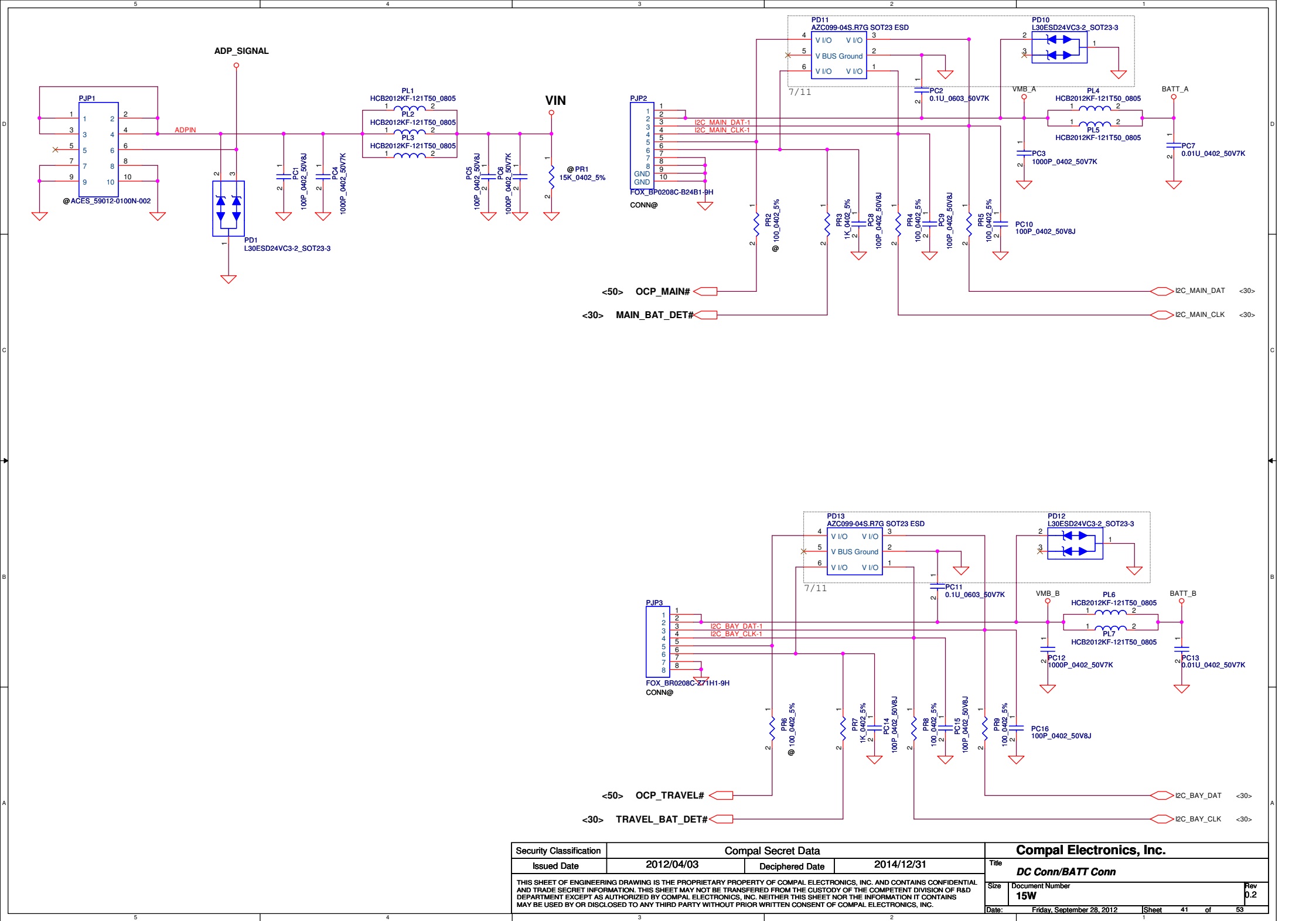


Thunderbolt

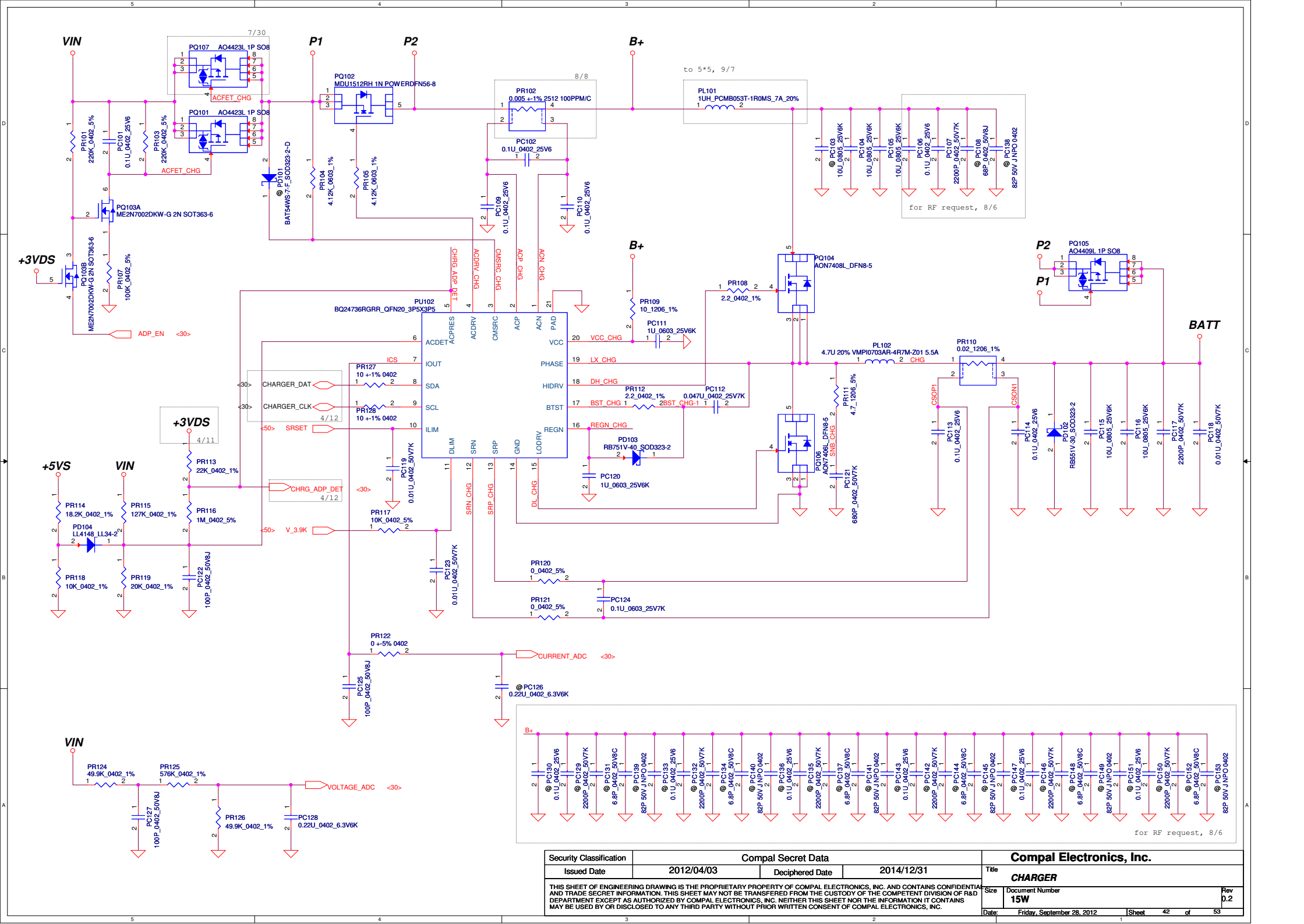


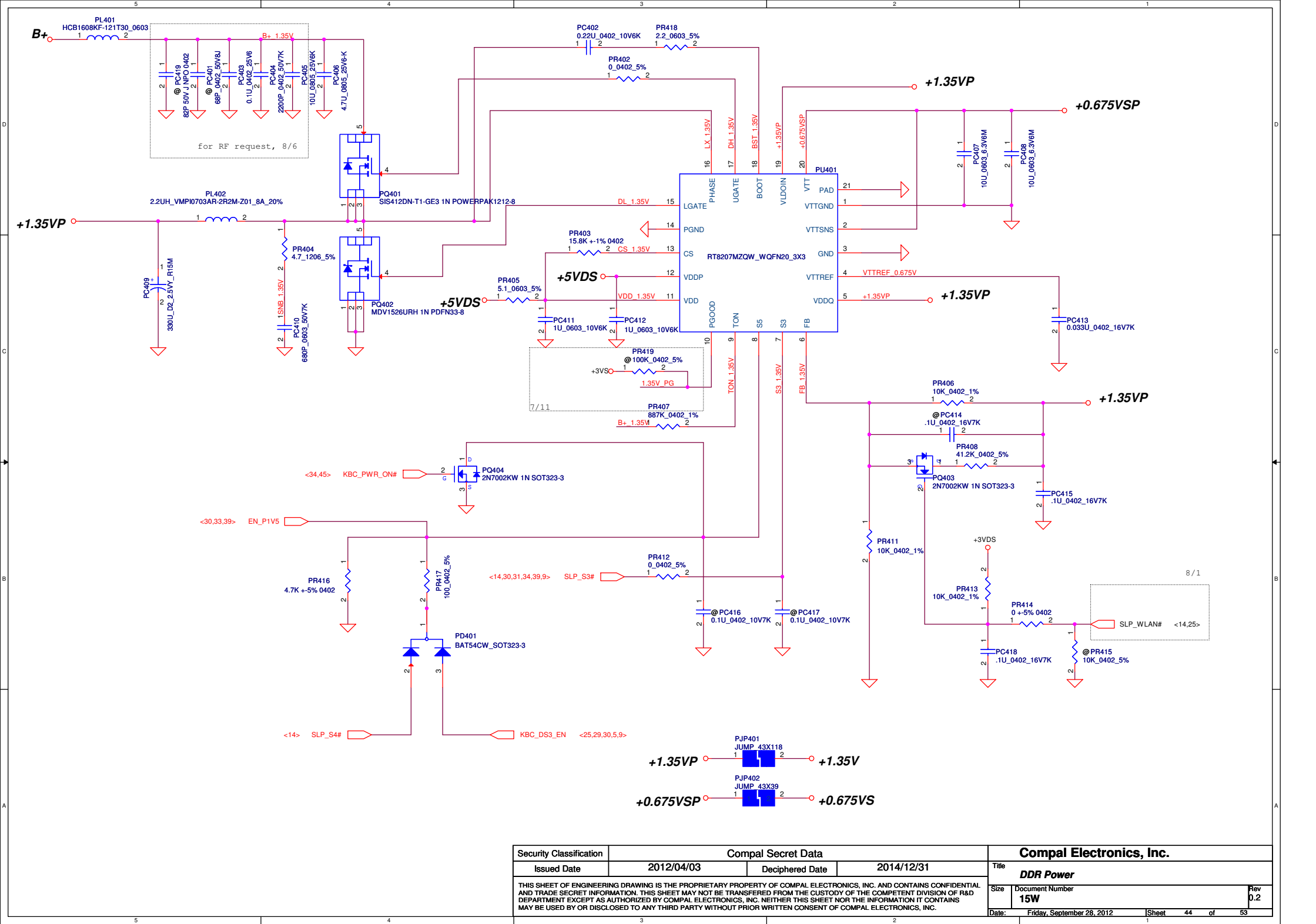
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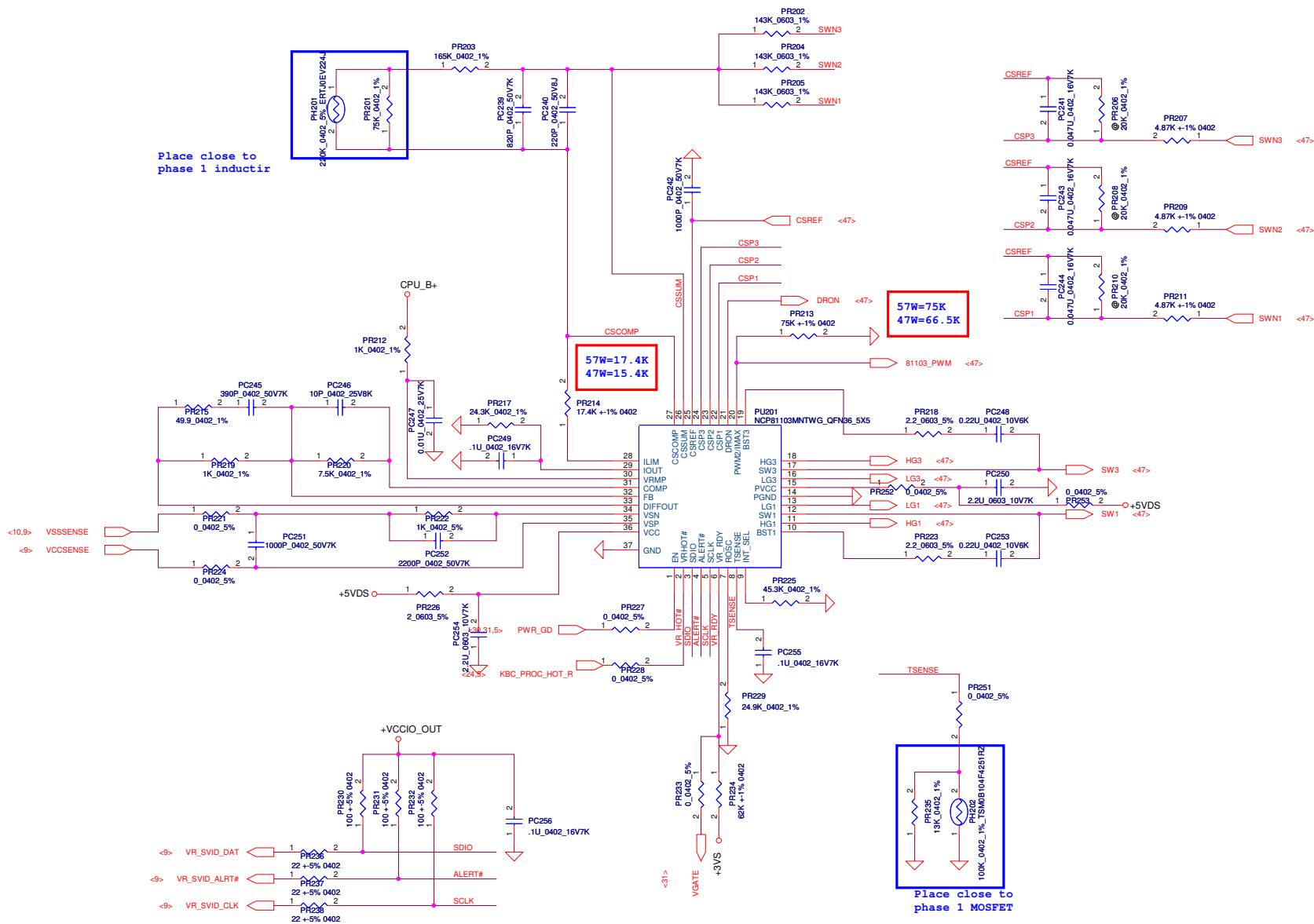




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				53	

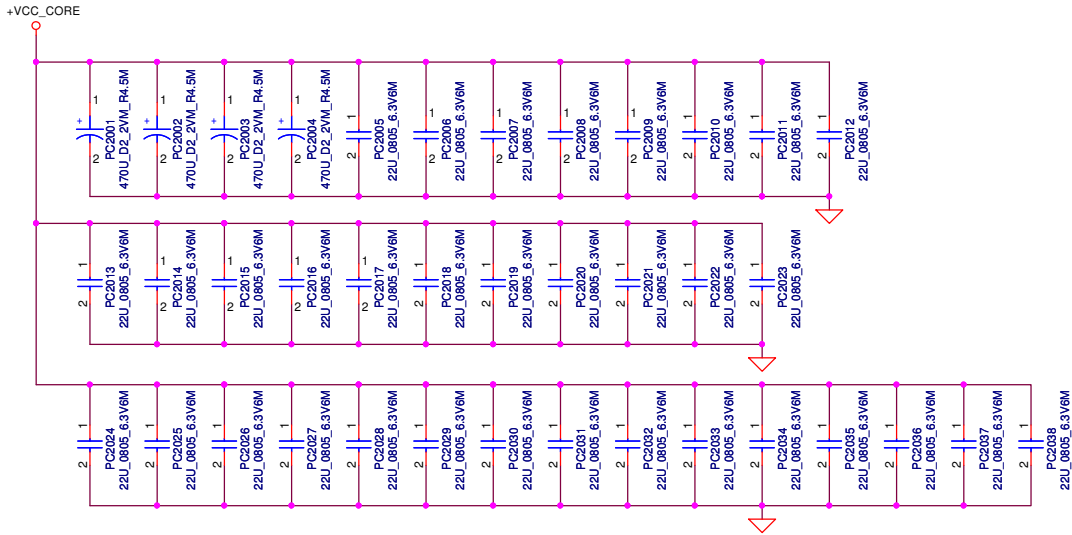




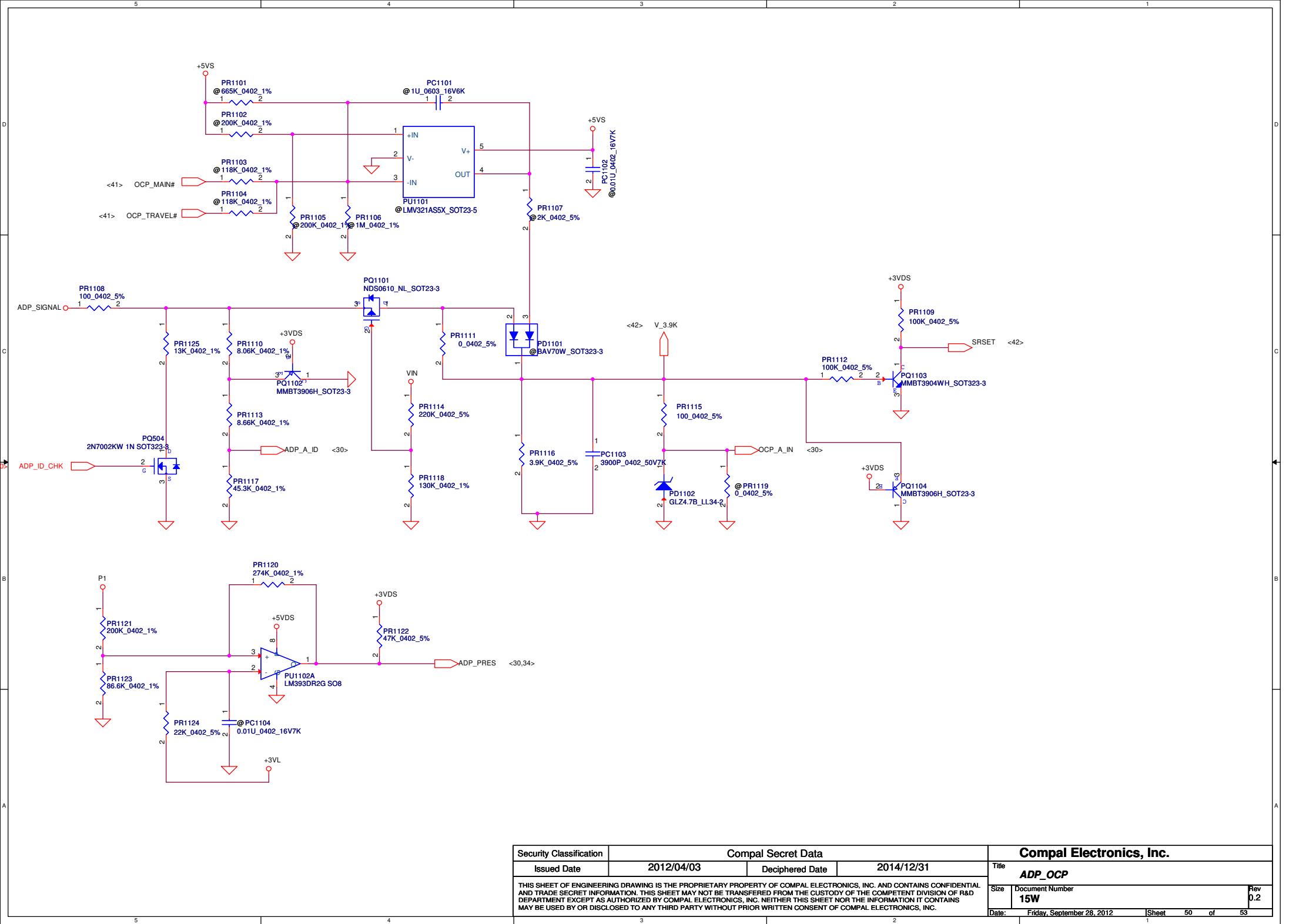


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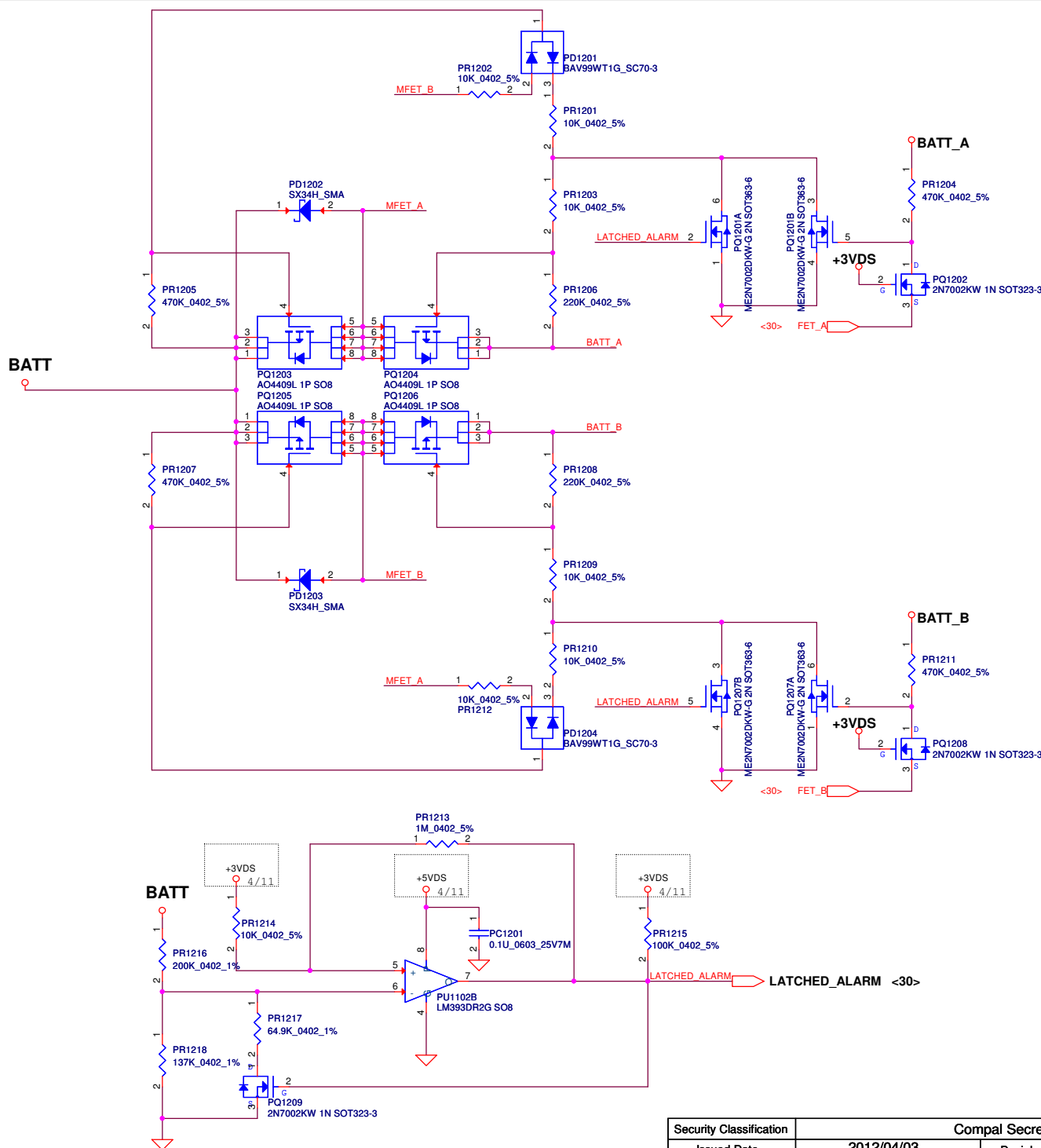
+VCC_CORE 4 X 470u/4m
34X 22u/0805



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					Battery selector
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Version Change List (P. I. R. List) for HW Circuit

Item	Page #	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	30	KBC	08/18	HP	The quad-mode does not require any GPIO from KBC	Delete R1348 & R1349 and Add R541 and R542	0.2
2	31	PWR_OK	08/18	HP	Makes the power good circuit working.	Non-install R284	0.2
3	26	MXM	08/28	Compal	MXM Card can't be recognized in OS.	Change U37 to MC74VHC1G08DFT2G (Non-OD type)	0.2
4	36	Switch/MUX	09/07	HP	Move D-Sub connector on M/B	Add VGA filter circuit and connector	0.2
5	30	KBC	09/08	HP	SUSCLK is off during DS3 condition	Add Y4, C423, C424 ; Delete R258, R264	0.2
6	30	KBC	09/08	HP	Simplify the KBC solution for MEC1322	Delete R262, R266, R219, R216, R241, R242, R271, R253	0.2
7	25	NGFF	09/08	HP		Add PU resistor for WWAN_FULL_PWR/WWAN_RSVD2	0.2
8	39	NFC	09/10	HP	KBC detection of NFC module	Add 200K (R1377) PD resistor to NFC_VART_RX	0.2
9	39	NFC	09/10	HP	Power rail option for NFC module	Add +3VM_LAN (R1376) for NFC module	0.2
10	9	CPU	09/11	HP		Remove RC102 and RC103	0.2
11	20	PCH	09/11	HP	Connect PCH's pin AD12 to +3V_PCH directly	Remove RH226	0.2
12	5	CPU	09/11	HP	Connect CPU_AT26 pin to CPU_PLTRST# directly	Remove RC66	0.2
13	5	CPU	09/11	HP	Connect CPU_AL34 pin to H_CPUPWARGD directly	Remove RC30	0.2
14	9	CPU	09/11	HP	Connect SLP_S3# to QC5.5 directly.	Remove RC93	0.2
15	33	DOCKING	09/11	HP	Connect EN_P1V5 to JDOCK1.140 directly.	Remove R351	0.2
16	33	DOCKING	09/11	HP	Connect ON/OFFBTN_KBC# to Docking directly.	Remove R336	0.2
17	15	PCH	09/11	HP	Simplify PCH solution for PCIE CLK Group	Remove RH107, RH103, RH114, RH116, RH122 RH124, RH126, RH127, RH129, RH130	0.2
18	14	PCH	09/11	HP	Reassignment GPIO for "BT_OFF"	Change it from GPIO72 to GPIO61	0.2
19	30	KBC	09/11	HP	Reserve for SUSACK# signal	Uninstall R436 and add R1378	0.2
20	18	PCH	09/11	HP	Reassigned BRD_ID4 to GPIO40 for CP support and update table for DB1-R	Connect RH180.1 to +3VS & RPH1.8 to GND	0.2
21	30	KBC	09/12	HP	Simplify the KBC solution for MEC1322	Delete R249, R251, R256, R277	0.2
22	36	Switch/MUX	09/12	HP	eDP trace length over Intel's specification	Change solution to PS8321	0.2

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Version Change List (P. I. R. List) for HW Circuit

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