

UT12 SYSTEM DIAGRAM



01

PCB STACK UP

LAYER 1 : TOP
LAYER 2 : IN1
LAYER 3 : IN2
LAYER 4 : VCC
LAYER 5 : IN3
LAYER 6 : BOT

Cable Docking

VGA
RJ-45
CIR/Pwr btn
SPDIF Out
Stereo MIC
Headphone Jack
USB Port
VOL Cntr

PAGE 37

SYSTEM CHARGER(ISL6251A)
PAGE 44

SYSTEM POWER ISL6236IRZA-T
PAGE 38

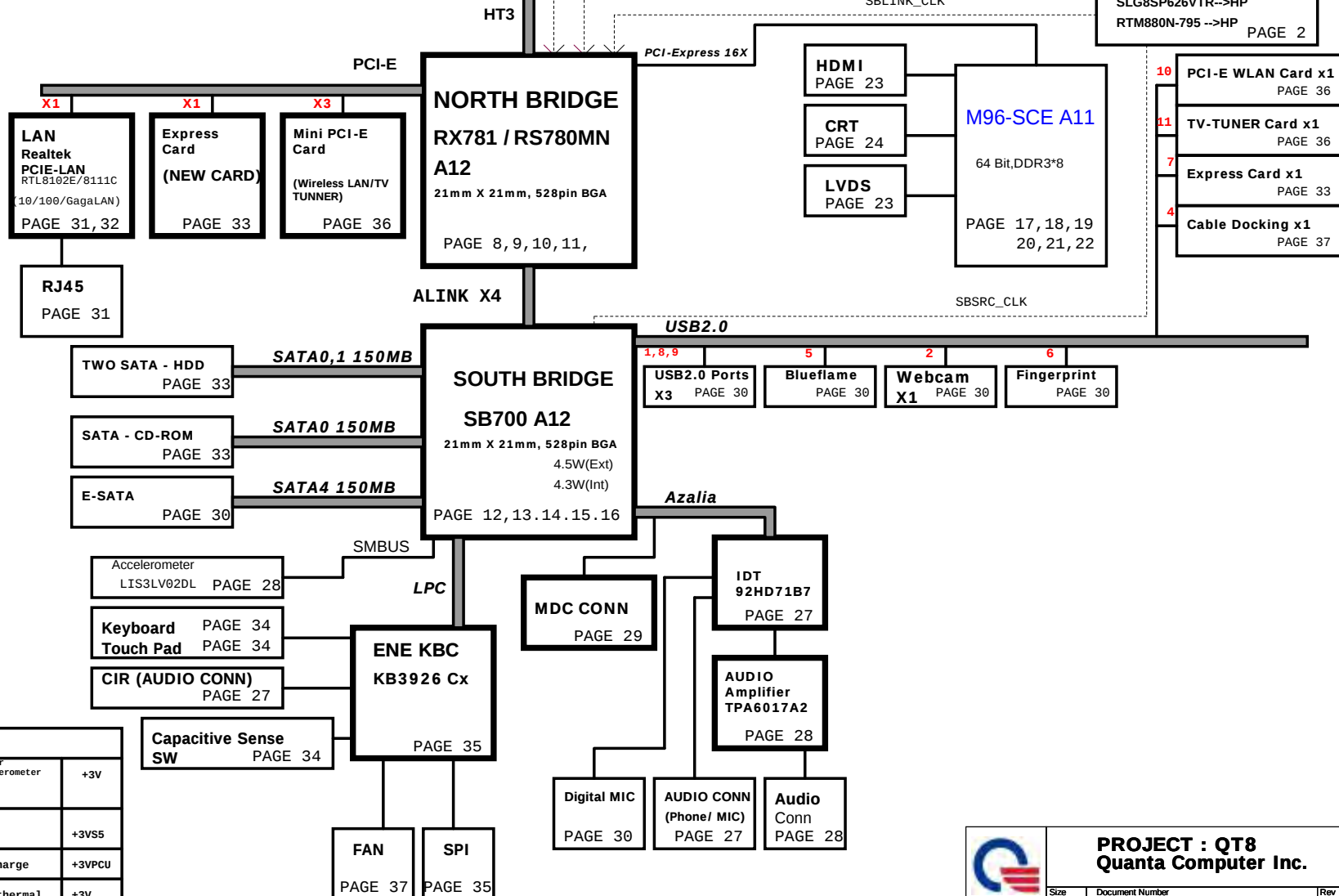
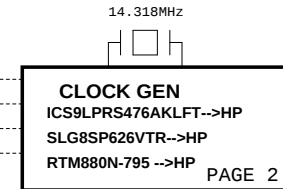
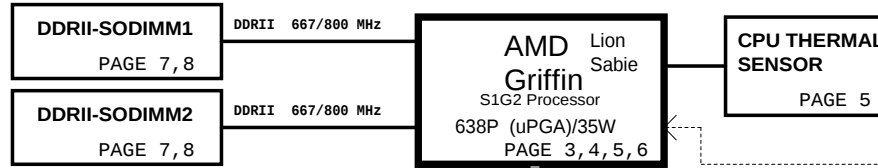
DDR II SMD DR_VTERM
L8V/1.8VSUS(TPS51116REG)
PAGE 41

VCCP +1.1V AND +1.2V(MAX8717)
PAGE 39

VGACORE(1.1V-1.2V)Oz8118
PAGE 42

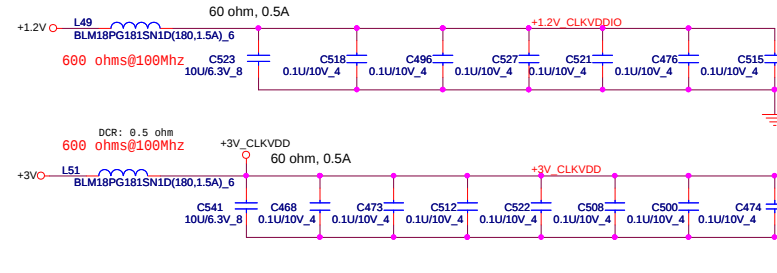
CPU CORE ISL6265A
PAGE 40

SMBUS TABLE		
SB--SCL0/SD0	Clock gen/Robson/TV tuner /DDR2/DDR2 thermal/Accelerometer	+3V
	epress card Wlan Card	+3VS5
EC --SCL/SD	Battery charge/discharge	+3VPCU
EC--SCL2/SD2	VGA thermal/system thermal	+3V

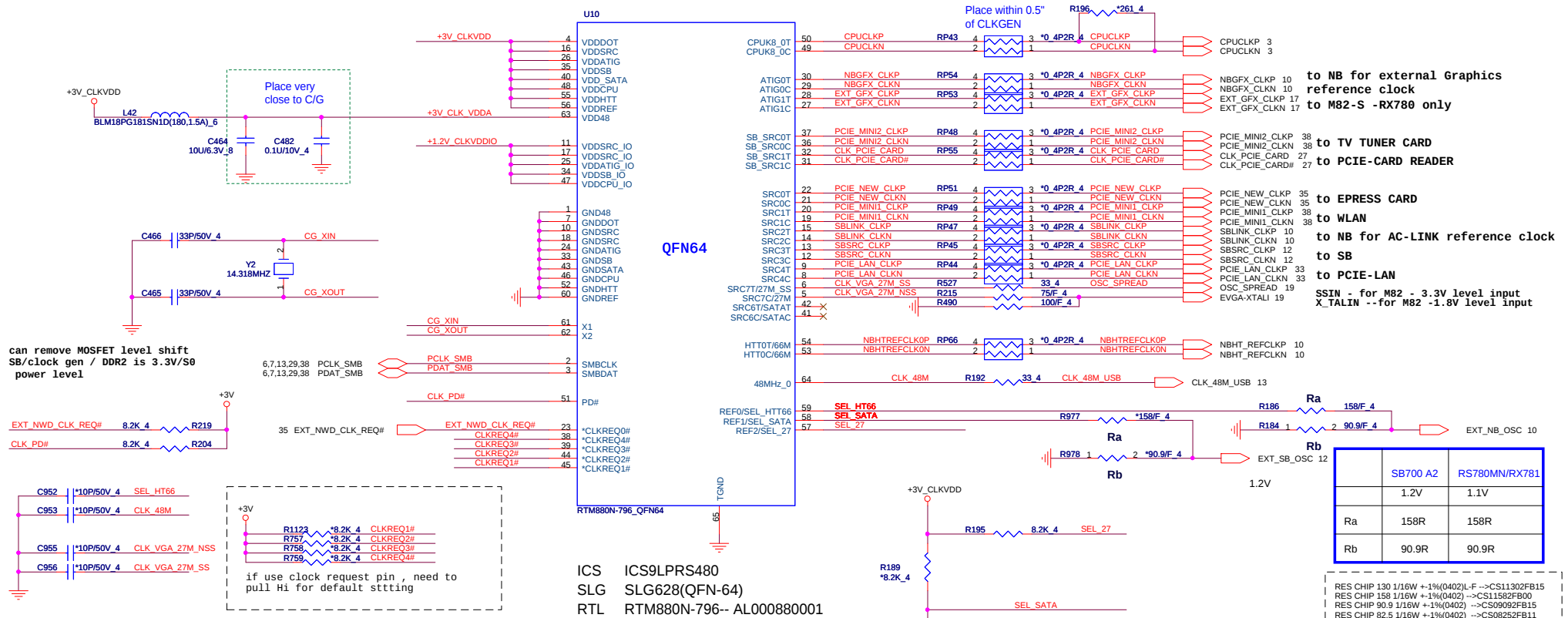


PROJECT : QT8
Quanta Computer Inc.

Size Custom	Document Number Block Diagram	Rev 1A
Date: Thursday, October 16, 2008	Sheet 1 of 48	



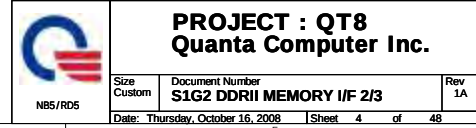
Clock chip has internal serial terminations for differential pairs, external resistors are reserved for debug purpose.



ICS ICS9LPRS480
SLG SLG628(QFN-64)
RTL RTM880N-796-- AL000880001

* default

SEL_HTT66	1	66 MHz 3.3V single ended HTT clock
	0*	100 MHz differential HTT clock
SEL_SATA	1	100 MHz non-spreading differential SRC clock
	0*	100 MHz spreading differential SRC clock
SEL_27	1*	27MHz non-spreading singled clock
	0	100 MHz spreading differential SRC clock





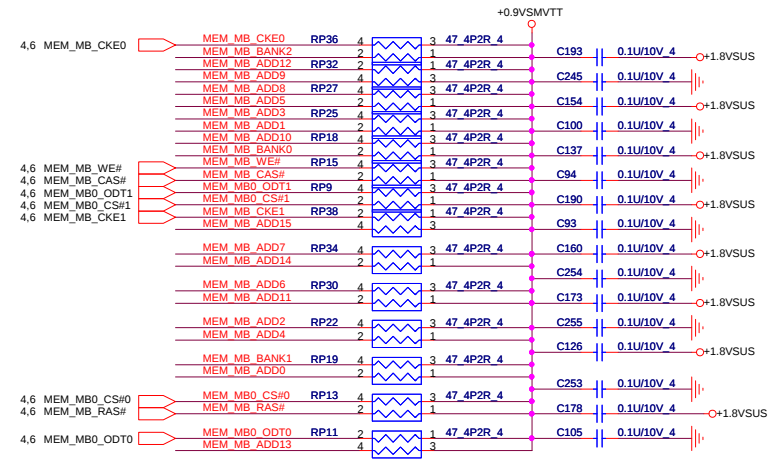
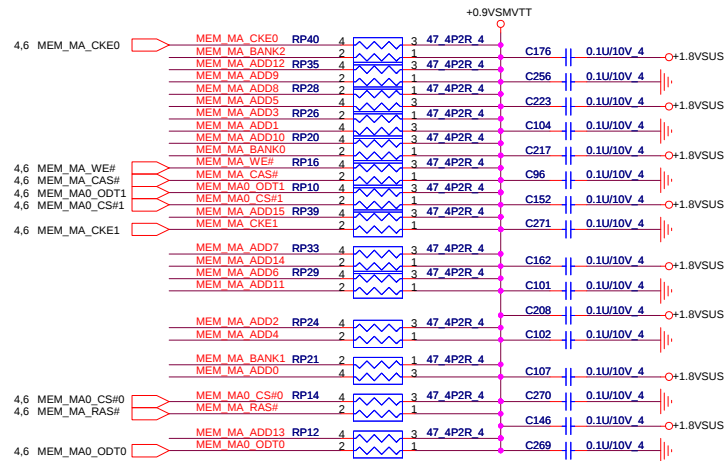
PROJECT : QT8
Quanta Computer Inc.

Size Custom	Document Number S1G2 PWR & GND 3/3
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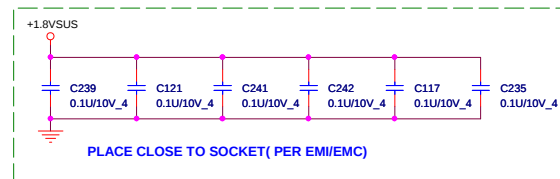


4,6 MEM_MA_ADD[0..15]  MEM_MA_ADD[0..15]
4,6 MEM_MA_BANK[0..2]  MEM_MA_BANK[0..2]

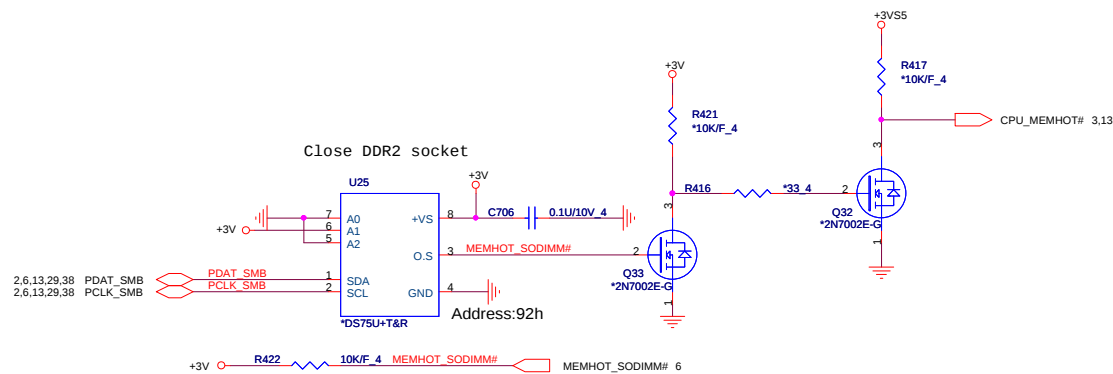
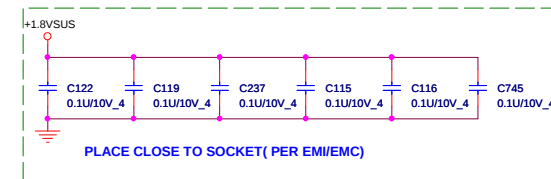
4,6 MEM_MB_ADD[0..15]  MEM_MB_ADD[0..15]
4,6 MEM_MB_BANK[0..2]  MEM_MB_BANK[0..2]



PLACE CLOSE TO PROCESSOR
WITHIN 1.5 INCH

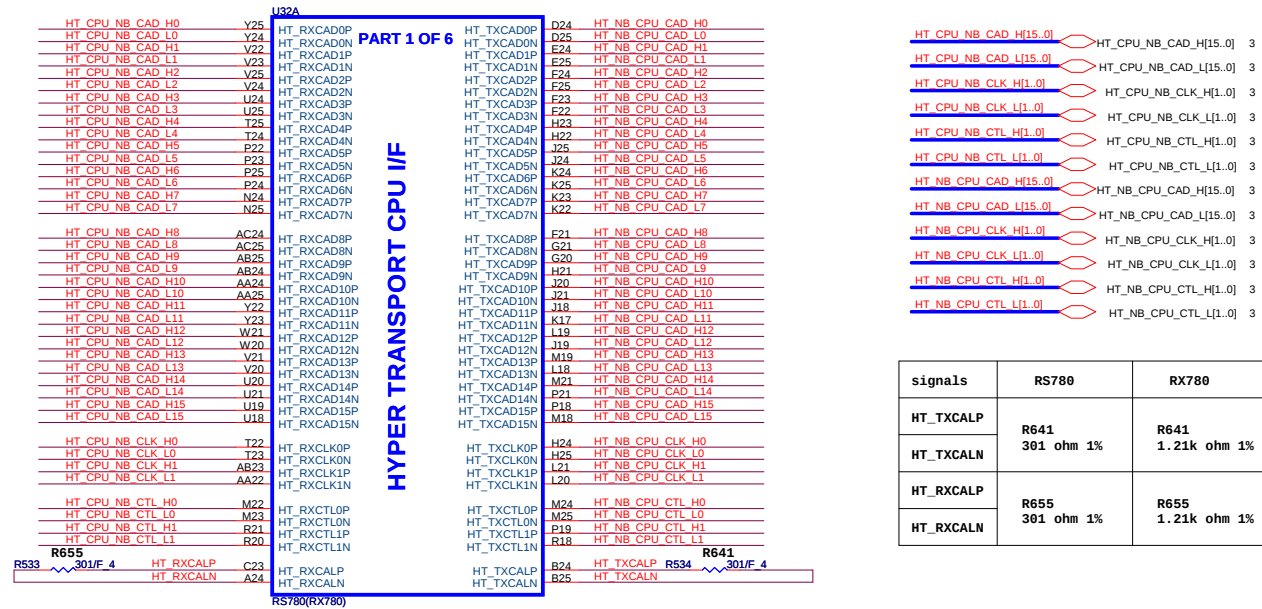


PLACE CLOSE TO PROCESSOR
WITHIN 1.5 INCH

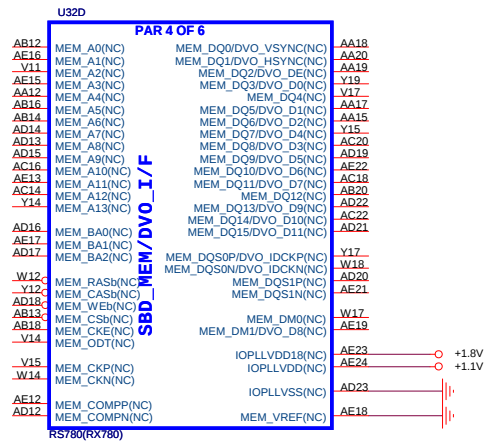


PROJECT : QT8
Quanta Computer Inc.

Size Custom	Document Number DDR2 SODIMMS TERMINATIONS	Rev 1A
Date: Thursday, October 16, 2008	Sheet 7 of 48	



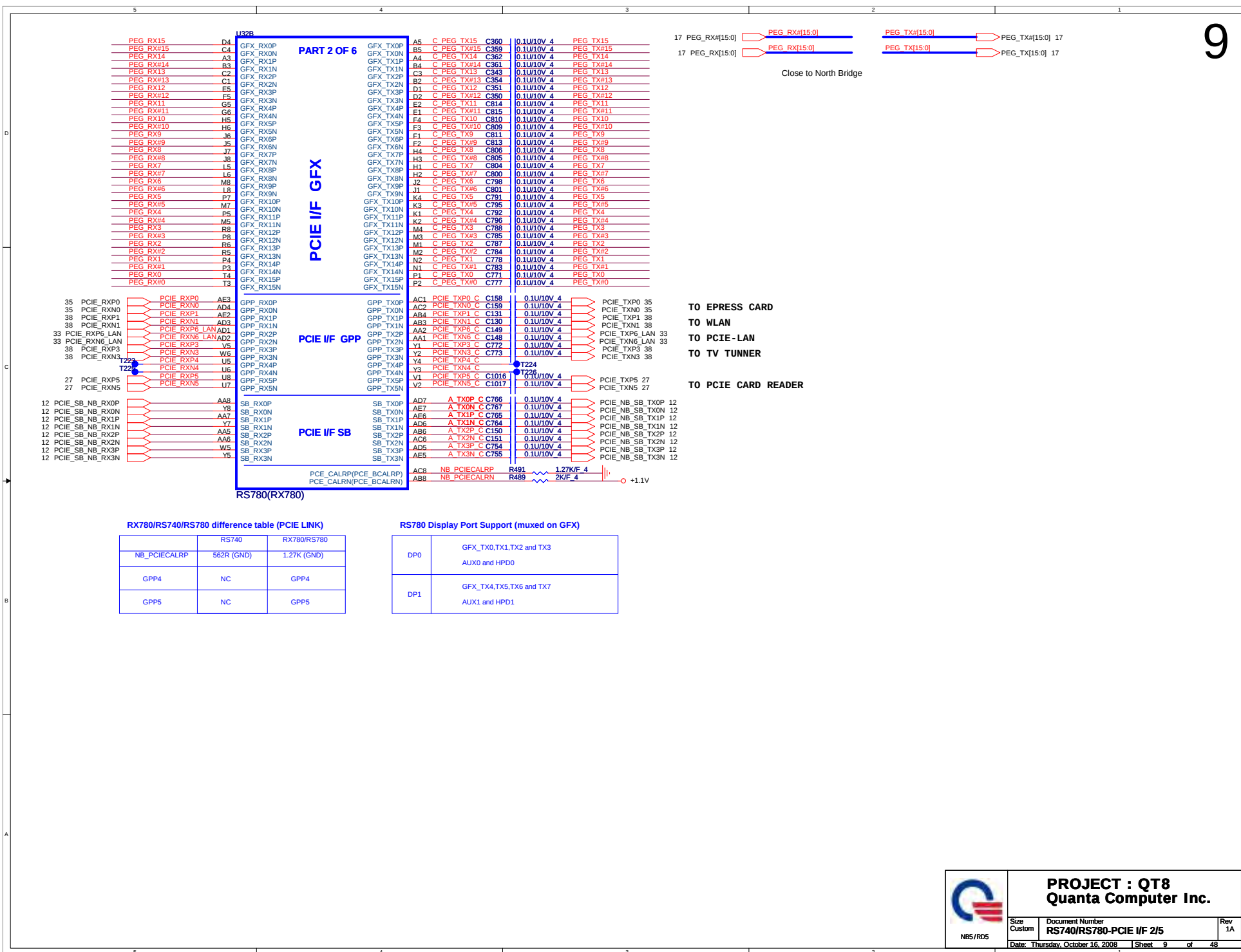
This block is for UMA RS780 only , RX780 can remove all component



PROJECT : QT8
Quanta Computer Inc.

Size Custom Document Number
RS740/RS780-HT LINK I/F 1/5
Date: Thursday, October 16, 2008 Sheet 8 of 48

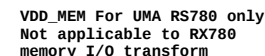
Rev 1A

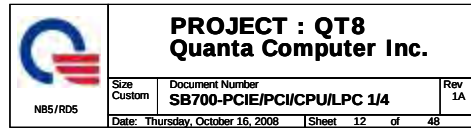
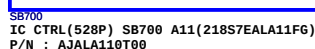




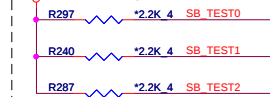
RS780
+1.1V
+3.3V
+1.8V
+1.8V
+1.1V
+1.8V
+1.8V
+1.8V
+1.8V
+1.8V
NC

PIN NAME	RX780	RS780	PIN NAME	RX780	RS780
VDDHT	+1.1V	+1.1V	IOPLLVD	NC	+1.1V
VDDHTRX	+1.1V	+1.1V	AVDD	NC	+3.3V
VDDHTTX	+1.2V	+1.2V	AVDDDI	NC	+1.8V
VDDA18PCIE	+1.8V	+1.8V	AVDDQ	NC	+1.8V
VDDG18	+1.8V	+1.8V	PLLVD	NC	+1.1V
VDD18_MEM	NC	+1.8V	PLLVD18	NC	+1.8V
VDDPCIE	+1.1V	+1.1V	VDDA18PCIEPLL	+1.8V	+1.8V
VDDC	+1.1V	+1.1V	VDDA18HTPLL	+1.8V	+1.8V
VDD_MEM	NC	+1.8V/1.5V	VDDLTP18	NC	+1.8V
VDDG33	NC	+3.3V	VDDL18	NC	+1.8V
IOPLLVD18	NC	+1.8V	VDDL18	NC	NC

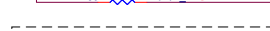




+3VSUS NC only ,Can't be install



+3VSUS



+3V SCL0/SDATA0 is 3V tolerance

AMD datasheet define it



+3VS5 SCL1/SDATA1 is 3V/5S tolerance

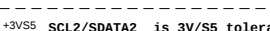
AMD datasheet define it



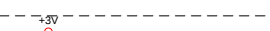
remove pull hi (chip internal have pull hi)

+3VS5 SCL2/SDATA2 is 3V/5S tolerance

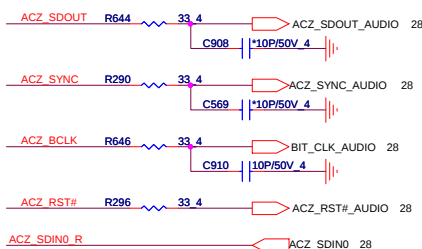
AMD datasheet define it



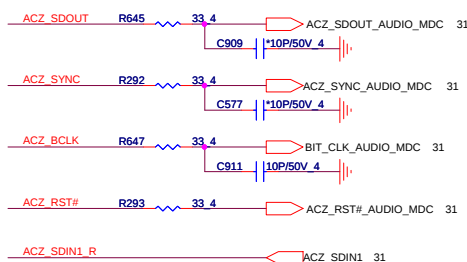
+3V



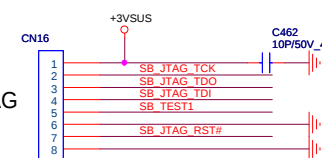
To Azalia



To Modem Board



SB JTAG



*SW JTAG DEBUG

35 NEWCARD_DETECT

NEWCARD_DETECT

R283 0.4 NEWCARD_DETECT R

T211 SLP_S2

T99 SUS#

R271 0.4/S SUSB#

R191 0.4/S SUSC#

R233 0.4/S DNBSWON#

37 DNBSWON#

16 SB_PWRGD_IN

10 SUS_STAT#

37 GATEA20

37 RCIN#

37 KCSI#

37 KBSMI#

37 T109

PCIE_WAKE#

37 SW#

3 CPU_THERMTRIP#

10,16 WD_PWRGD

37 RSMRST#

33,38

PCIE_WAKE#

R307 0.4 PCIE_WAKE# R

R211 0.4/S SW#

R786 0.4 SB_THERMTRIP#

R284 0.4/S RSMRST#

T63

T194

33,37 LAN_DISABLE#

R217 0.4 LAN_DISABLE# SB

T222

R302 0.4/S

28,29 ACZ_SPKR

2,6,7,29,38 PCLK_SMB

2,6,7,29,38 PDAT_SMB

T69

T75

37 PM_BATLOW#

T72

D3E_SCI#

3,7 CPU_MEMHOT#

5 PM_THERM#

+3VS5

R275 0.4 CPU_MEMHOT# IN

R279 10K/F 4 SB_SMBALERT# 1

35 NEWCARD_WAKE#

R285 0.4 NEWCARD_WAKE# R

SB_JTAG_TDO

SB_JTAG_TCK

SB_JTAG_TDI

SB_JTAG_RST#

ACZ_BCLK

ACZ_SDOUT

ACZ_SDI0_R

ACZ_SYNC

ACZ_RST#

ACZ_SDI0_R

ACZ_SDI0_R

ACZ_SDI0_R

U37D

SB700

Part 4 of 5

USBCLK/14M_25M_48M_OSC

USB_RCOMP

USB_FSD13P

USB_FSD13N

USB_FSD12P

USB_FSD12N

USB_HSD11P

USB_HSD11N

USB_HSD10P

USB_HSD10N

USB_HSD9P

USB_HSD9N

USB_HSD8P

USB_HSD8N

USB_HSD7P

USB_HSD7N

USB_HSD6P

USB_HSD6N

USB_HSD5P

USB_HSD5N

USB_HSD4P

USB_HSD4N

USB_HSD3P

USB_HSD3N

USB_HSD2P

USB_HSD2N

USB_HSD1P

USB_HSD1N

USB_HSD0P

USB_HSD0N

IMC_GPIO8

IMC_GPIO9

IMC_GPIO10

IMC_GPIO11

IMC_GPIO12

IMC_GPIO13

IMC_GPIO14

IMC_GPIO15

IMC_GPIO16

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IMC_GPIO162

IMC_GPIO163

IMC_GPIO164

IMC_GPIO165

IMC_GPIO166

IMC_GPIO167

SATA PORT 0, 1, 2, 3
can support AHCI
mode

PLACE SATA AC COUPLING
CAPS CLOSE TO SB600

SATA1

SATA ODD

E-SATA

SATA HDD2

SATA PORT 4, 5
are only
support IDE
mode



PLACE SATA_CAL
RES VERY CLOSE
TO BALL OF SB700

NOTE:
R361 IS 1K 1% FOR 25MHz
XTAL, 4.99K 1% FOR 100MHz
INTERNAL CLOCK

PLVDD_SATA--
SATA PLL
POWER

XTLVDD_SATA-- SATA
crystal power

SERIAL ATA

SATA PWR

HW MONITOR

SB700
Part 2 of 5

ATA 66/100/133

SPI ROM

HW MONITOR

SB700
Part 2 of 5

ATA 66/100/133

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SB700
Part 2 of 5

ATA 66/100/133

SPI ROM

HW MONITOR

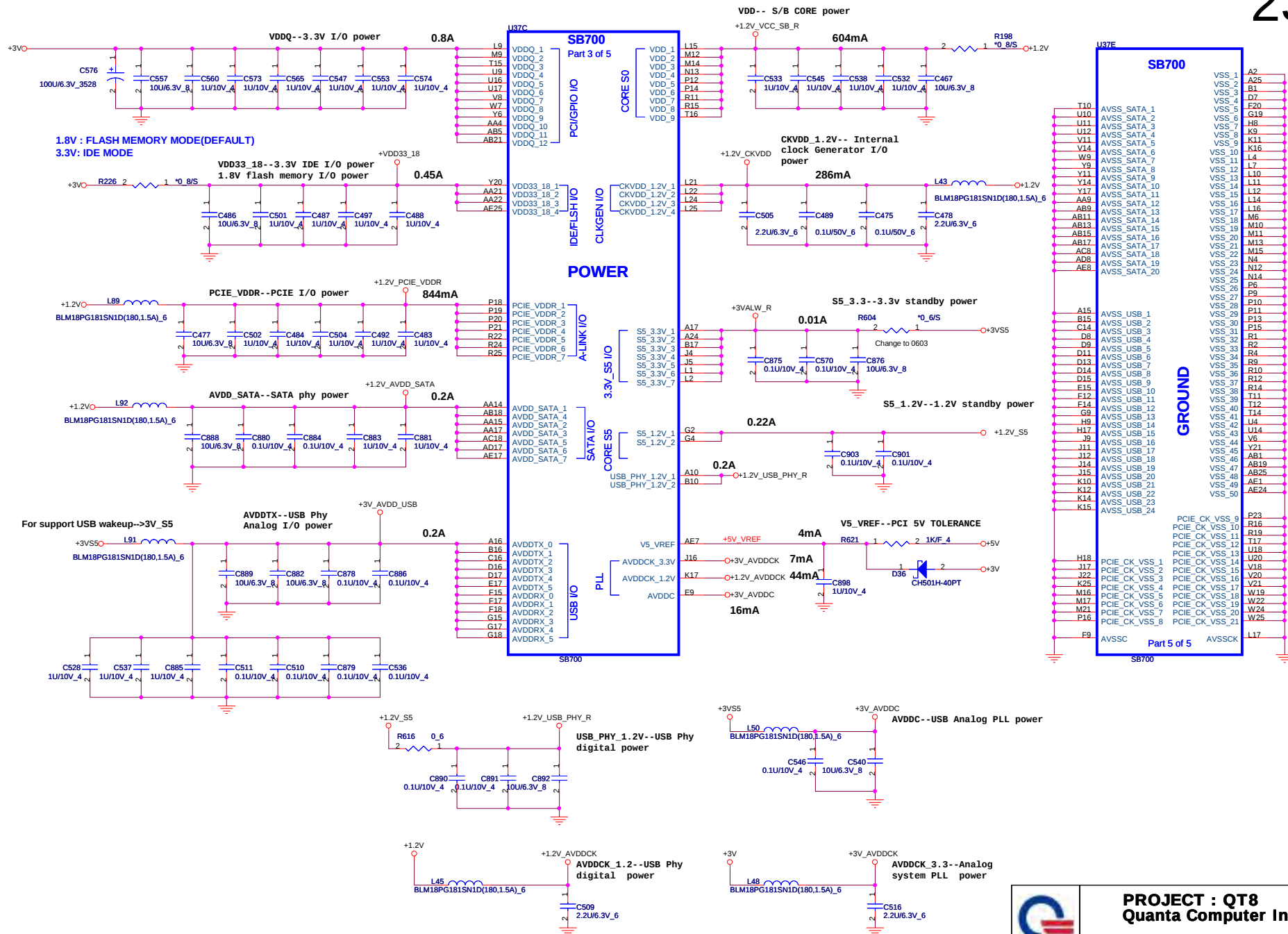
SB700
Part 2 of 5

ATA 66/100/133

SPI ROM

HW MONITOR

PLACE ALL THE DECOUPLING CAPS ON
THIS SHEET CLOSE TO SB AS POSSIBLE.



PROJECT : QT8
Quanta Computer Inc.

Size Custom	Document Number SB700-PWR/DECOUPLING 4/4	Rev 1A
Date: Thursday, October 16, 2008	Sheet 15 of 48	

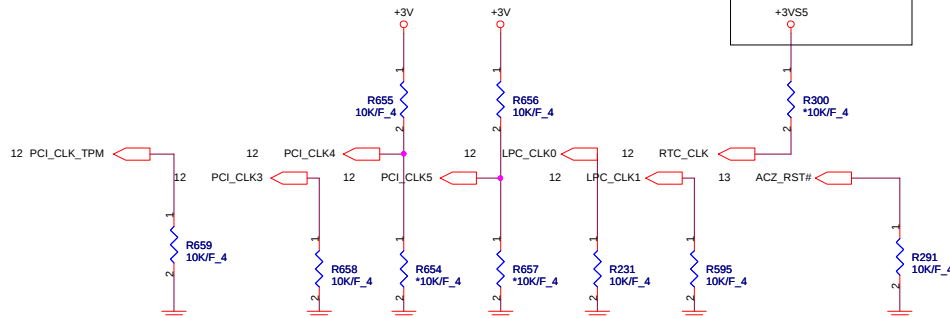


OVERLAP COMMON PADS WHERE
POSSIBLE FOR DUAL-OP RESISTORS.

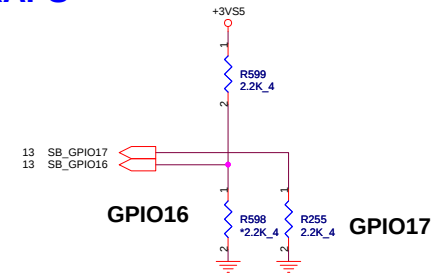
16

REQUIRED STRAPS

It must ready
before RSMRST#



	PCI_CLK_TPM	PCI_CLK3	PCI_CLK4	PCI_CLK5	LPC_CLK0	LPC_CLK1	RTC_CLK	AZ_RST#
PULL HIGH	BOOTFAIL TIMER ENABLED	USE DEBUG STRAPS	RESERVED	RESERVED	IMC ENABLED	CLKGEN ENABLED	INTERNAL RTC DEFAULT	ENABLE PCI ROM BOOT
PULL LOW	BOOTFAIL TIMER DISABLED DEFAULT	IGNORE DEBUG STRAPS DEFAULT			IMC DISABLED DEFAULT	CLKGEN DISABLED DEFAULT	EXT. RTC (PD on X1, apply 32KHz to RTC_CLK)	DISABLE PCI ROM BOOT DEFAULT

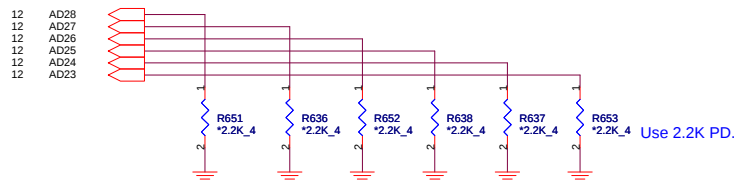


GPIO16 GPIO17

TYPE	GPIO16	GPIO17
FWH	L : 2.2K pull down	L : 2.2K pull down
LPC	NC	L : 2.2K pull down
SPI	L : 2.2K pull down	NC
RSVD	NC	NC

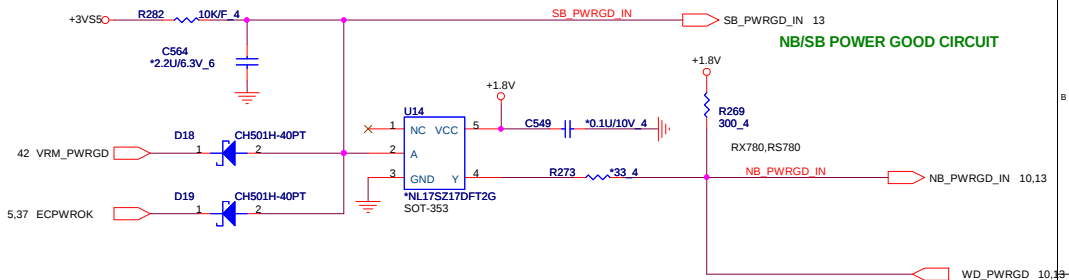
DEBUG STRAPS

SB700 HAS 15K INTERNAL PU FOR PCI_AD[28:23]



	PCI_AD28	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
PULL HIGH	USE LONG RESET DEFAULT	USE PCI PLL DEFAULT	USE ACPI BCLK DEFAULT	USE IDE PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	RESERVED
PULL LOW	USE SHORT RESET	BYPASS PCI PLL	BYPASS ACPI BCLK	BYPASS IDE PLL	USE EEPROM PCIE STRAPS	

NB_PWRGD_IN:
RS780/RX780 = 1.8V; RS740 = 3.3V
Do NOT share it with SB_PWRGD when use Internal Clk Gen
(Need SB PLL initialize firstly)

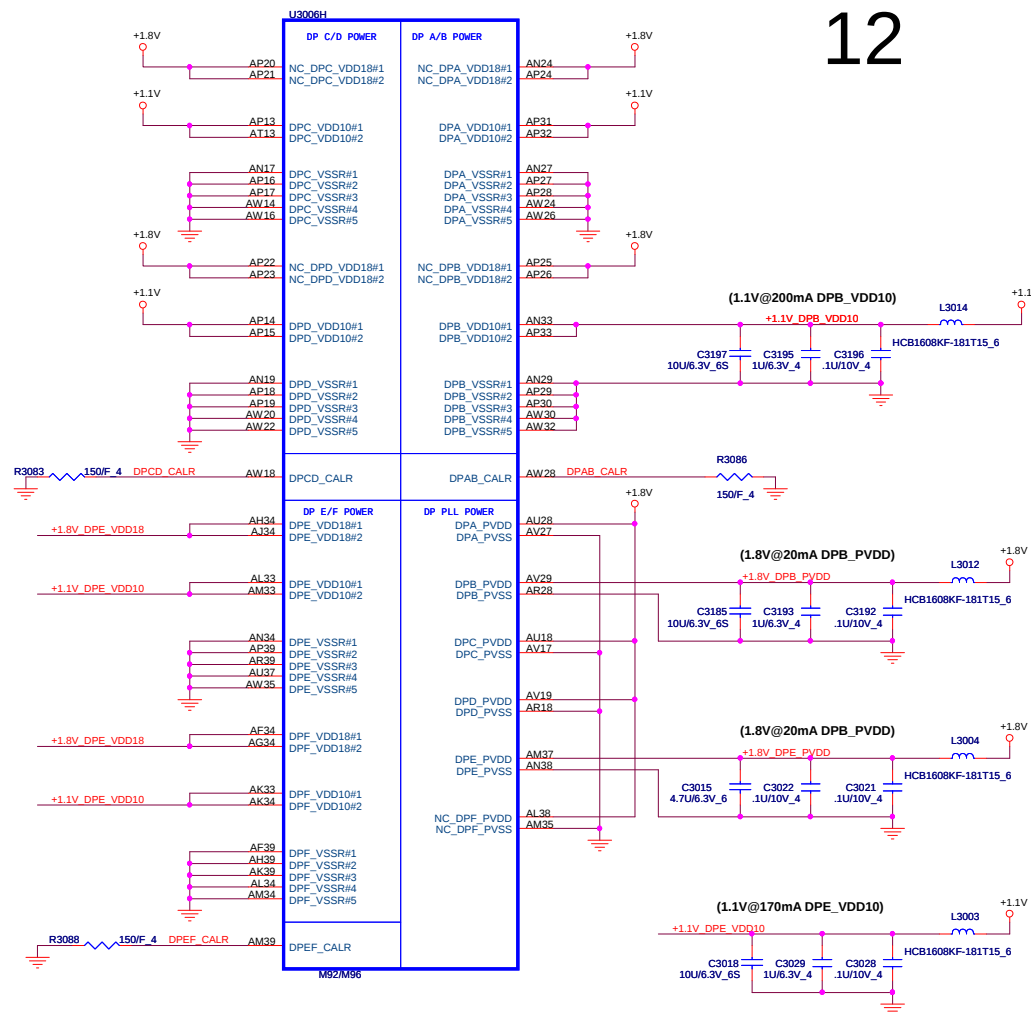
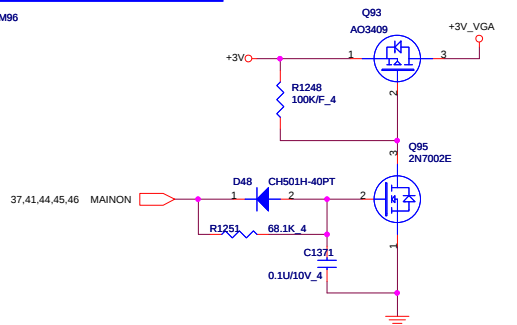


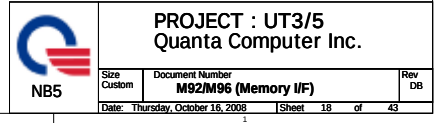
AL17S217000 IC(5P) NL17S217DFT2G(SOT-353) SOT-353
ALUC1G17000 IC OTHER(5P) SN74AUC1G17DBVR(SOT23-5) SOT23-5

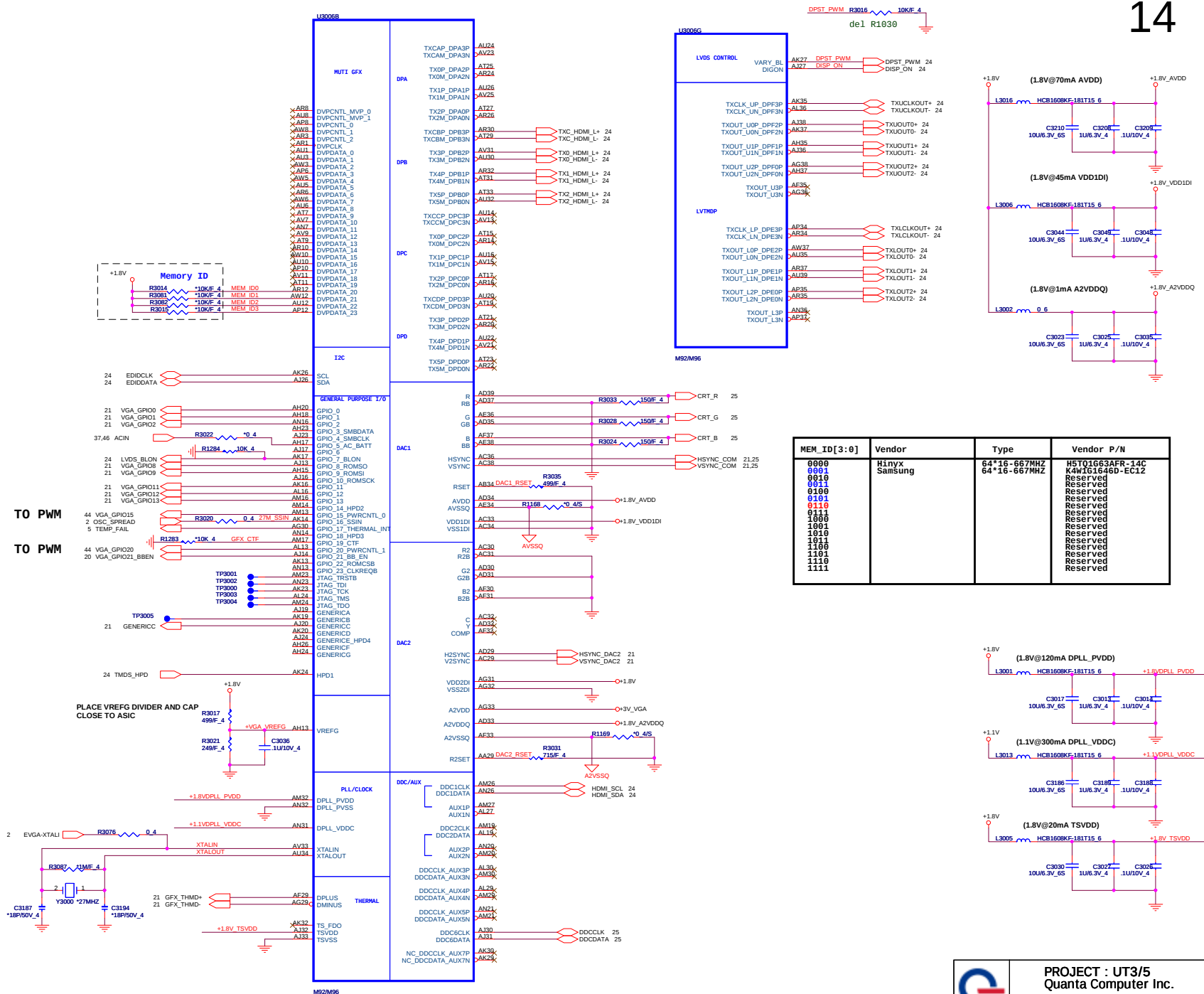


PROJECT : QT8
Quanta Computer Inc.

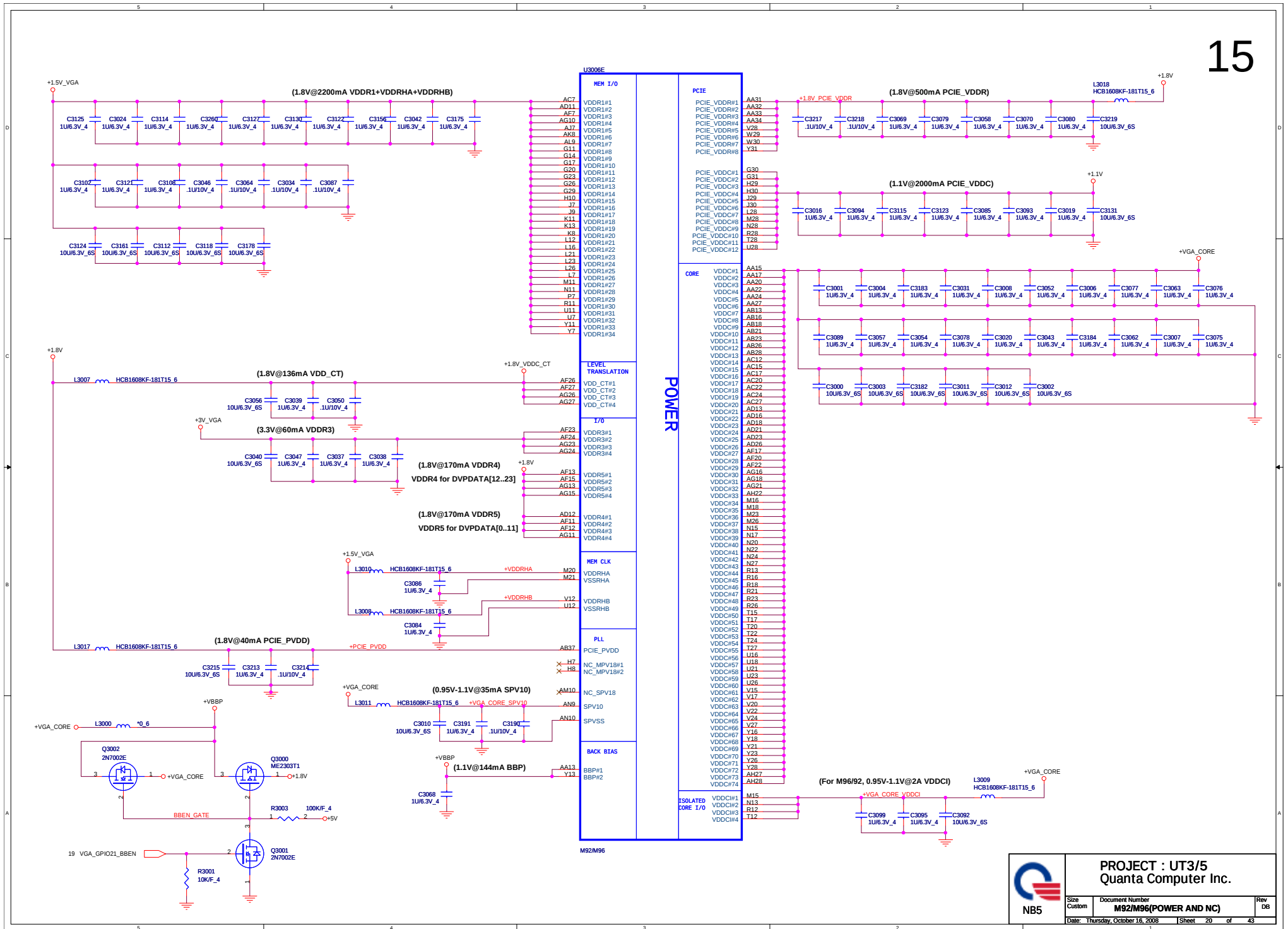
Size Custom	Document Number SB700-STRAPS	Rev 1A
Date: Thursday, October 16, 2008 Sheet 16 of 48		

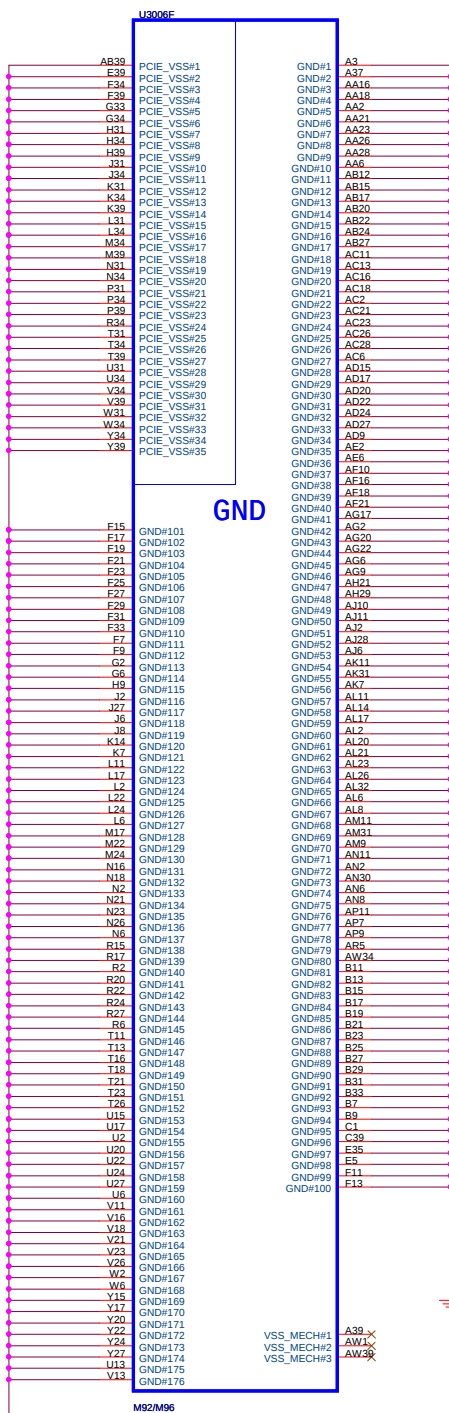




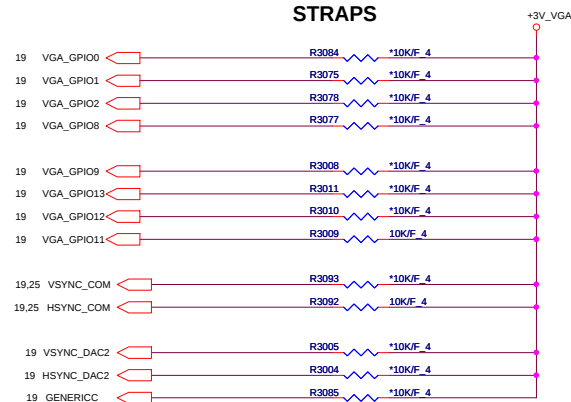


PROJECT : UT3/5
Quanta Computer Inc.

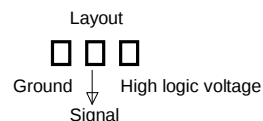




STRAPS

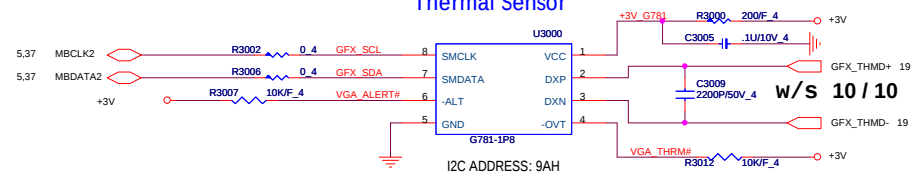


Overlap pads to save space
and to prevent assembly of
both resistors.



Strap Name	Pin Straps description	Default Value
TX_PWRS_ENB	GPIO0 Transmitter Power Savings Enable 0: 50% Tx output swing for mobile mode 1: full Tx output swing (Default setting for Desktop)	0
TX_DEEMPH_EN	GPIO1 PCI Express Transmitter De-emphasis Enable 0: Tx de-emphasis disabled for mobile mode 1: Tx de-emphasis enabled (Default setting for Desktop)	0
BIF_GEN2_EN	GPIO2 0 = Advertises the PCI-E device as 2.5 GT/s capable at power-on. 1 = Advertises the PCI-E device as 5.0 GT/s capable at power-on. 5.0 GT/s capability will be controlled by software.	0
STRAP_BIF_CLK_PM_EN	GPIO8 Enable CLKREQ# Power Management 0 - CLKREQ# power management capability is disabled 1 - CLKREQ# power management capability is enabled	0
CONFIG[3] CONFIG[2] CONFIG[1] CONFIG[0]	GPIO9 GPIO13 GPIO12 GPIO11 GPIO9,13,12,11 (config 3.2.1.0): a> If BIOS_ROM_EN = 1, then Config[3:0] defines the ROM Type: b> If BIOS_ROM_EN = 0, then Config[3:0] defines the Aperture size:Size of the primary memory apertures claimed in PCI configuration space 000 = 128MB 001 = 256MB 010 = 512MB 011 = 1GB 100 = 2GB 101 = 4GB 110 = 8GB 111 = 16GB	0001
BIOS_ROM_EN	GPIO22 Enable external BIOS ROM device 0 - Disable external BIOS ROM device 1 - Enable external BIOS ROM device	0
AUDIO[0]	VSYNC	
AUD(1)	HSYNC HSYNC - HDMI_EN HDMI connector presence. 0 ?No HDMI connector is present on PCB 1 - HDMI connector is present on the PCB HDMI	1
VIP_DEVICE_STRAP_DIS	V2SYNC If VIP_DEVICE_STRAP_EN is set to ?? then this pin is used to sense whether a VIP slave device is connected to the VIP host interface. If VIP_DEVICE_STRAP_EN is set to ?? then this pin is not used as a strap at all (i.e. its value during reset is unimportant), and it can be used as a regular GPIO	0
SMS_EN_HARD	H2SYNC	0
CCBYPASS	GENERICC	0

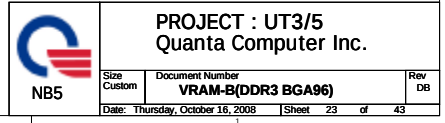
Thermal Sensor

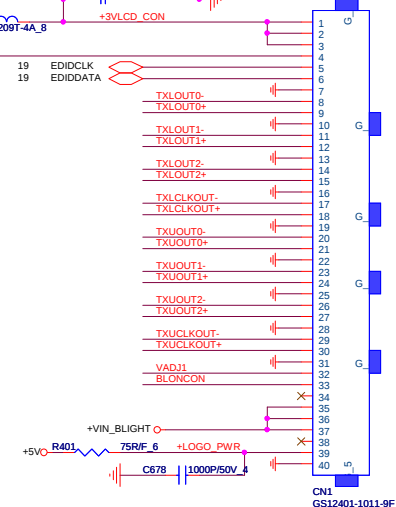
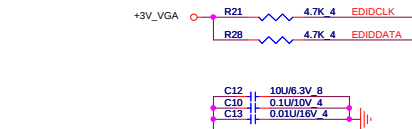
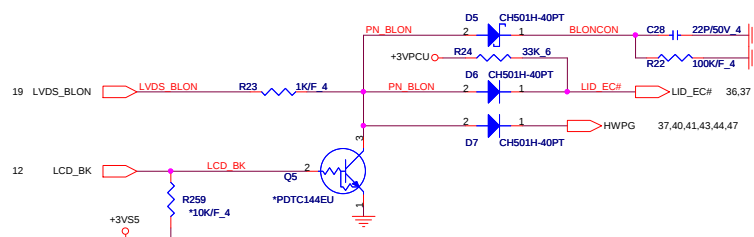
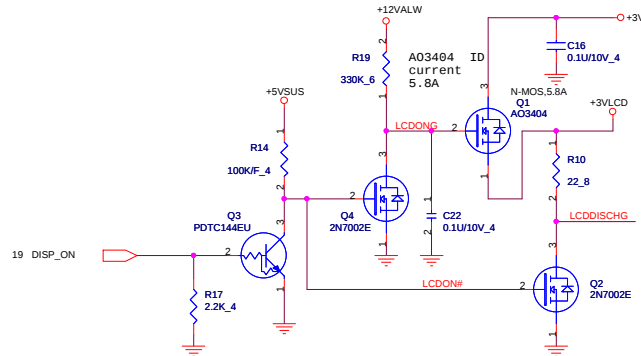
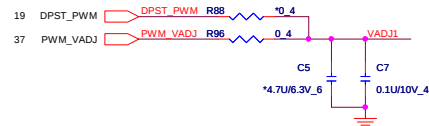
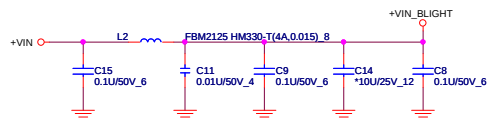
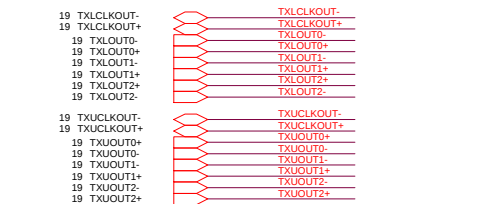


PROJECT : UT3/5
Quanta Computer Inc.

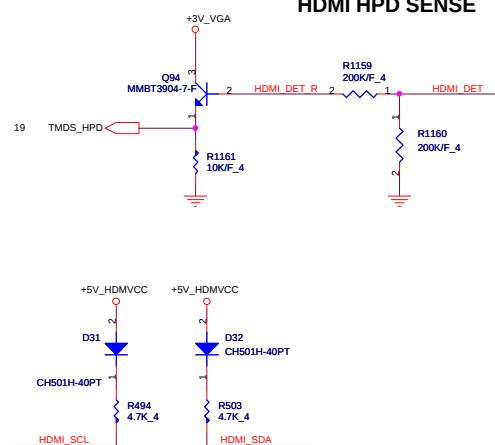
Size	Document Number	Rev
Custom	M92/M96 (GND/Straps/Therm)	DB
Date: Thursday, October 16, 2008	Sheet 21 of 43	





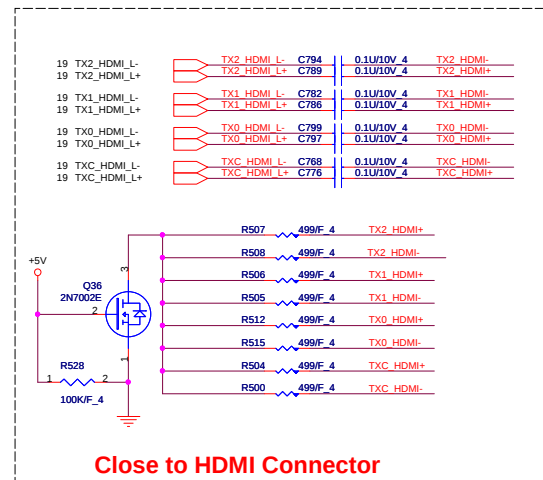
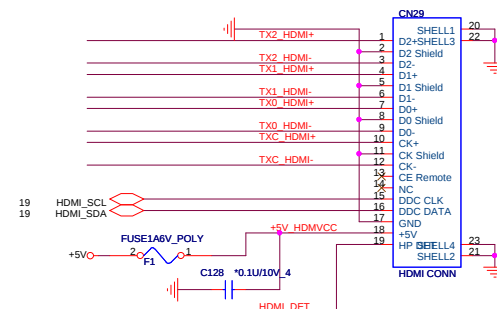


HDMI HPD SENSE



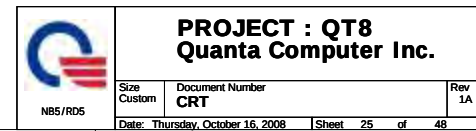
Change R494,
R503 to 4.7K for
AMD on PV
PV2
DIS use 4.7K
UMA use 2K

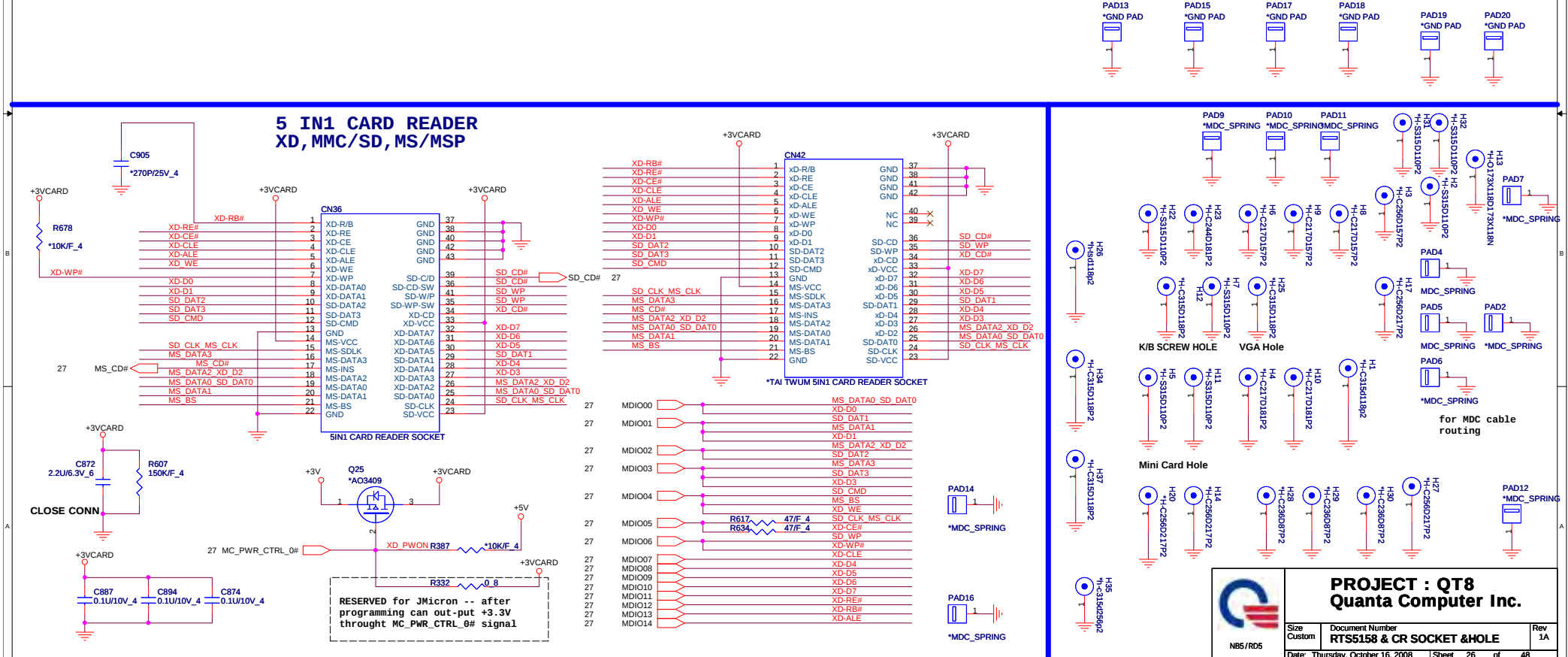
HDMI PORT



PROJECT : QT8
Quanta Computer Inc.

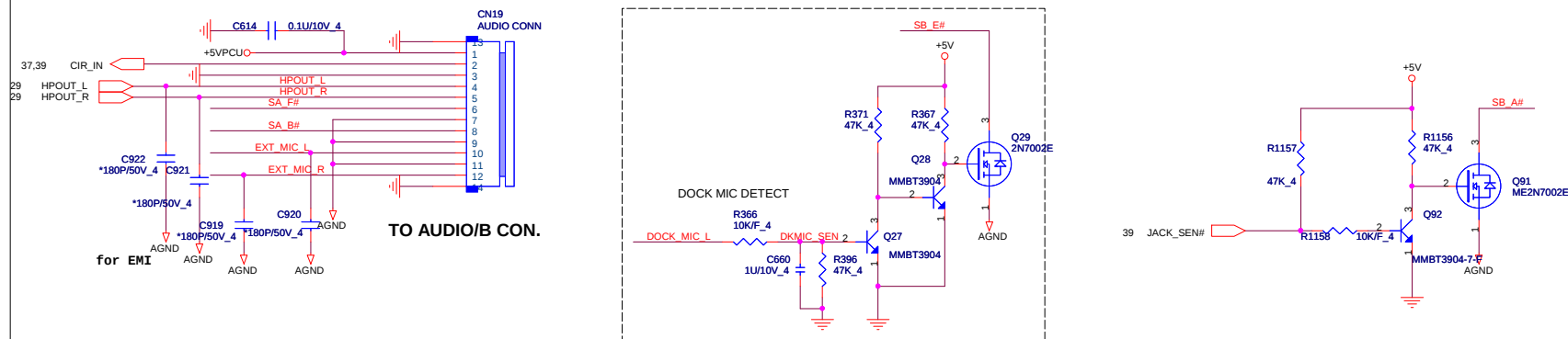
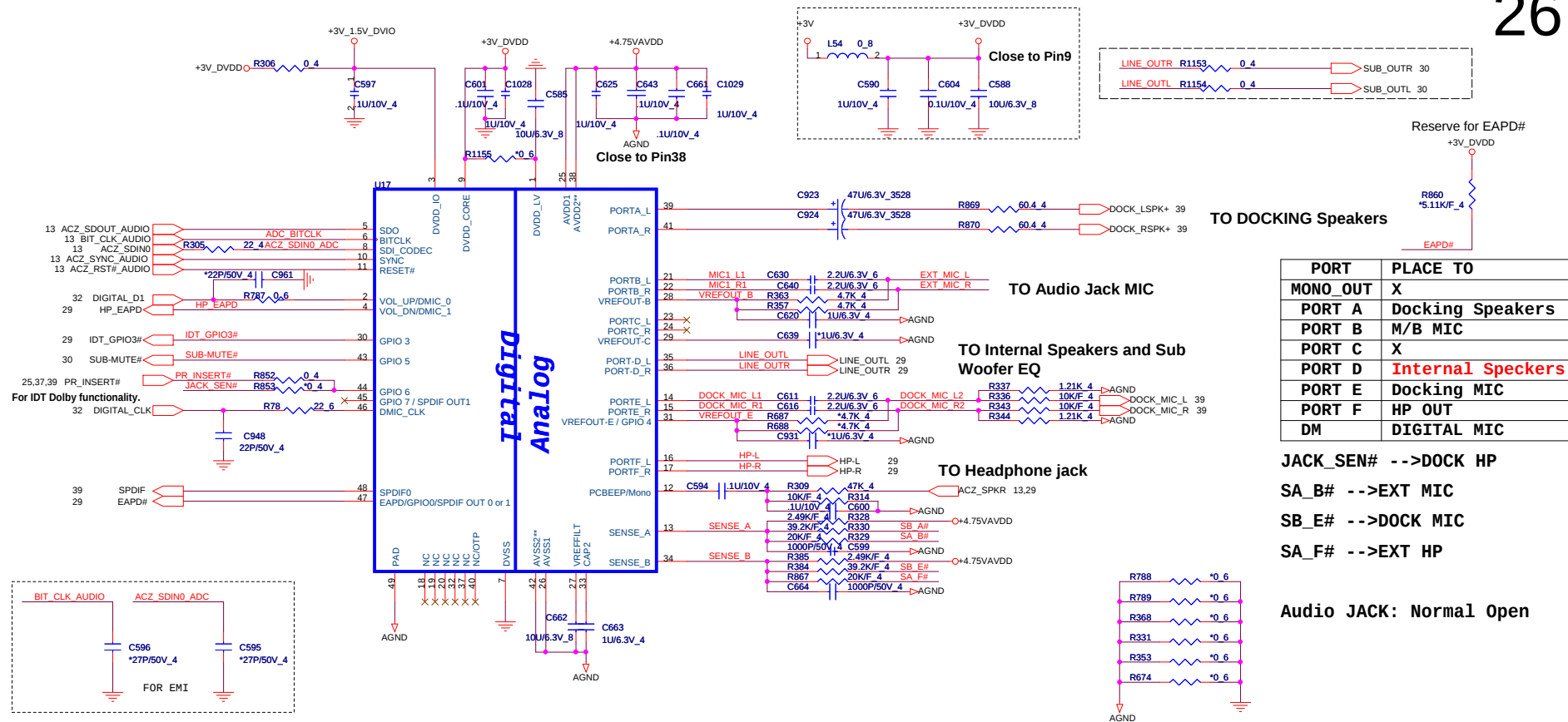
Size Custom Document Number
LCD CONN,HDMI CONN
Date: Thursday, October 16, 2008 Sheet 24 of 48 Rev 1A





Size B	Document Number JMB380 Controller/1394	Rev 1A
Date: Thursday, October 16, 2008		Sheet 27 of 48

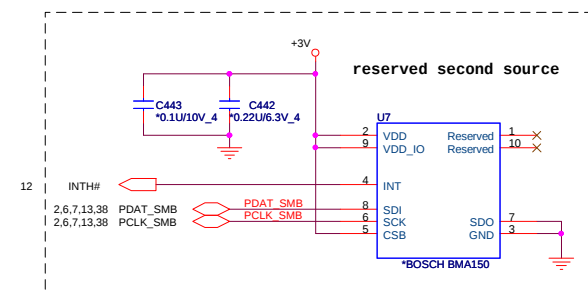




27



Acceleration sensor



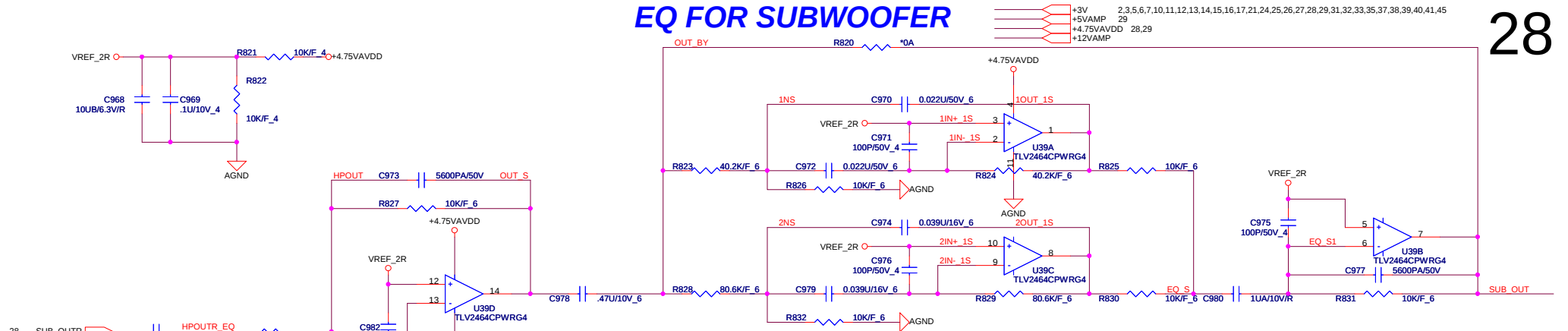
PROJECT : QT8
Quanta Computer Inc.

Size Custom	Document Number AMP_TPA6017/INT SPK
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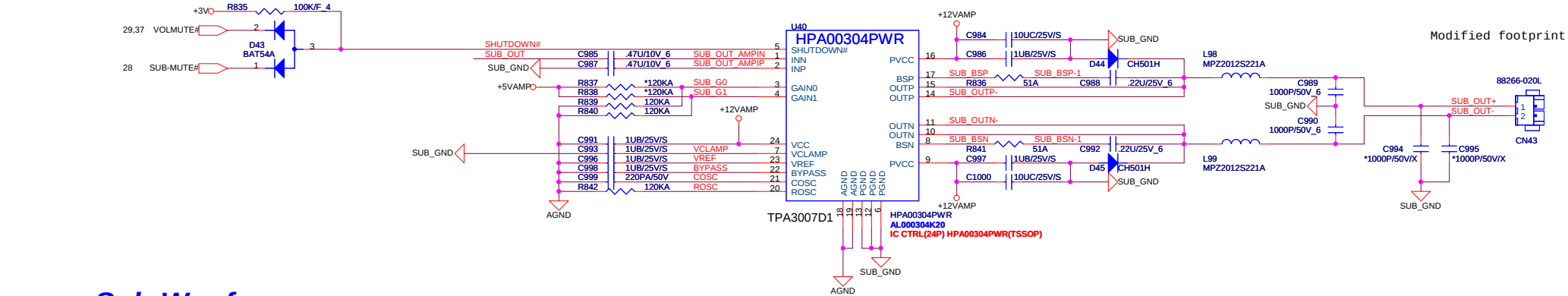
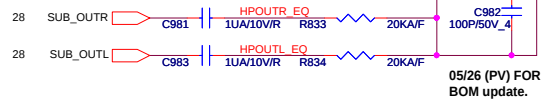
Date: Thursday, October 16, 2008	Sheet 29 of 48
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EQ FOR SUBWOOFER

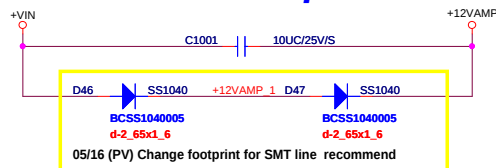
28



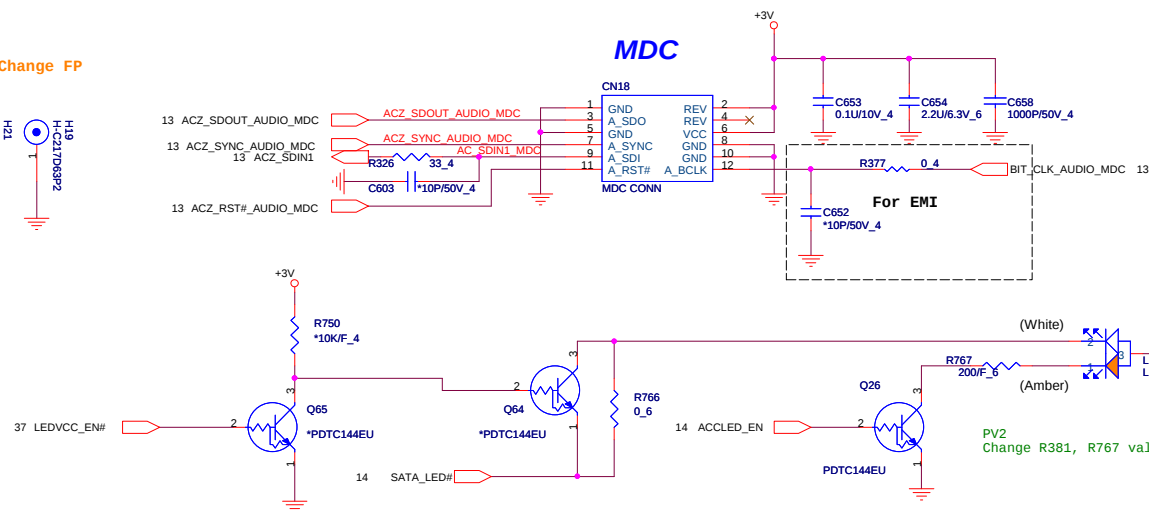
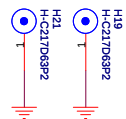
MODEL	UT1	UT2
R823	60.4K/F_6	40.2K/F_6
R824	60.4K/F_6	40.2K/F_6
R828	60.4K/F_6	80.6K/F_6
R829	60.4K/F_6	80.6K/F_6
C970	0.027U/25V_6	0.022U/50V_6
C972	0.027U/25V_6	0.022U/50V_6
C974	0.027U/25V_6	0.039U/16V_6
C979	0.027U/25V_6	0.039U/16V_6



Sub-Woofer power



DB-1 Change FP

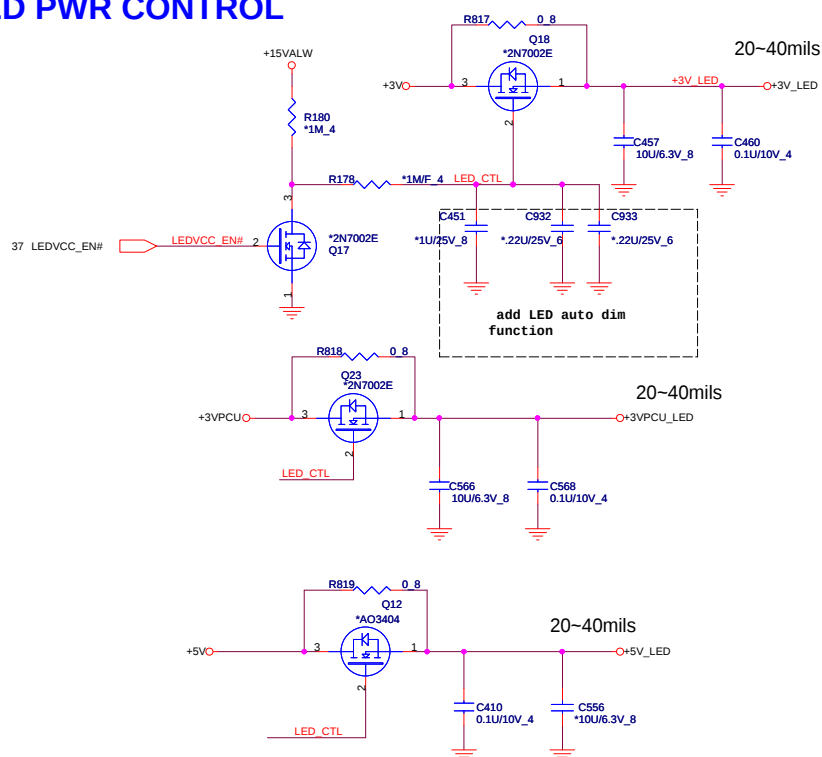


LED



SI-1 modified --
for fix SATA LED
no support LED
light control

LED PWR CONTROL

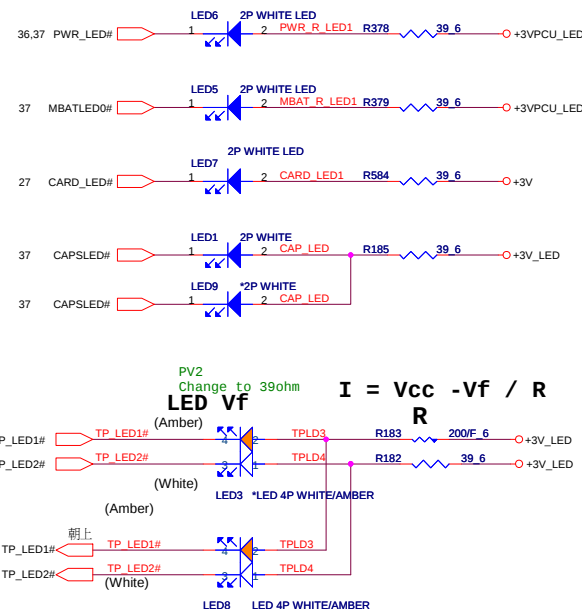


LED1 for 17.3"

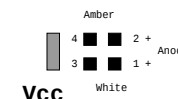
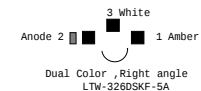
LED9 for 16.3"

LED3 for 16.3"

LED8 for 17.3"



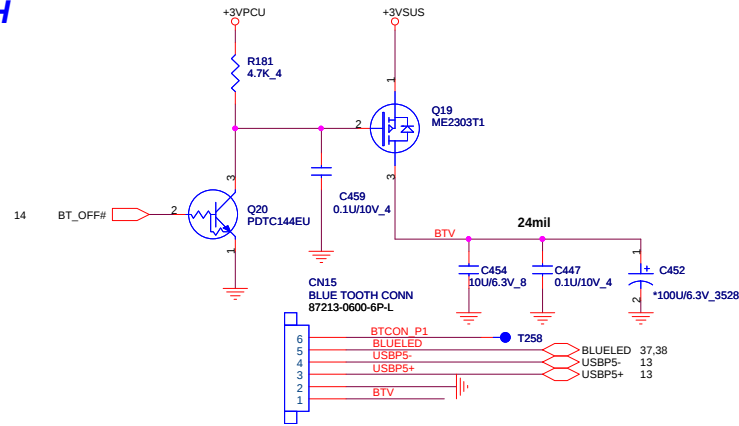
SI-1 modified --
change LED part
number



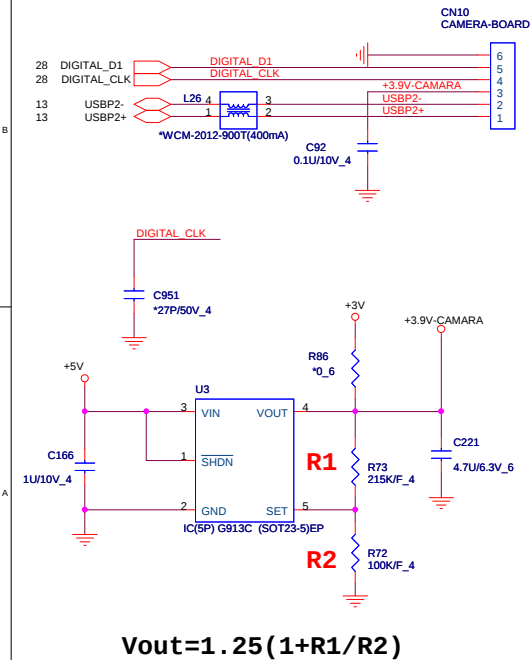
PROJECT : QT8
Quanta Computer Inc.

Size Custom	Document Number MDC1.5 Con Accelerometer/LED	Rev 1A
Date: Thursday, October 16, 2008	Sheet 31	of 48

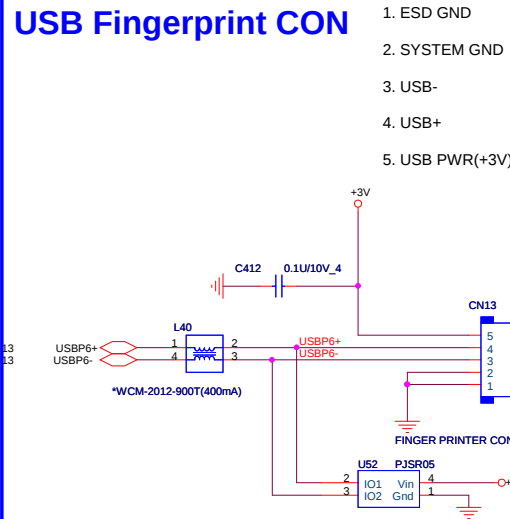
BLUETOOTH



USB CAMERA CONNECT



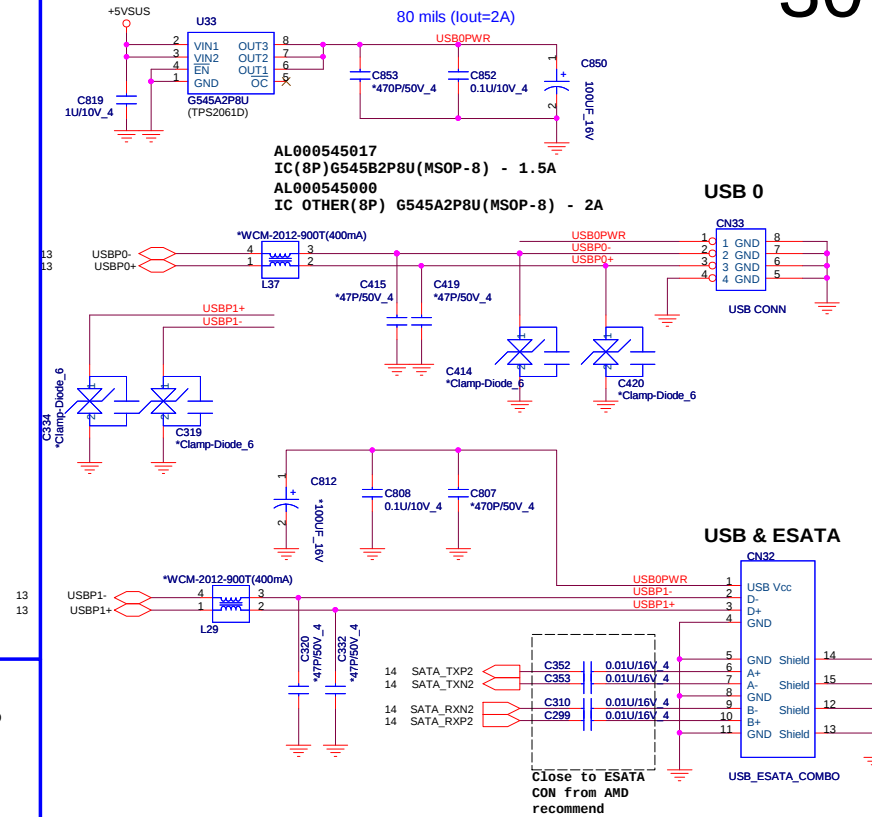
USB Fingerprint CON



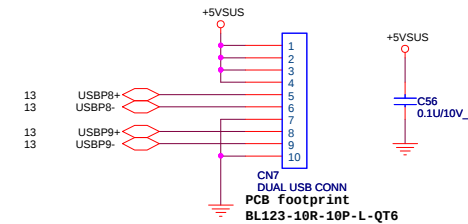
1. ESD GND
2. SYSTEM GND
3. USB-
4. USB+
5. USB PWR(+3V)

LEFT SIDE USBX1 and E-SATA/USB COMBO

30

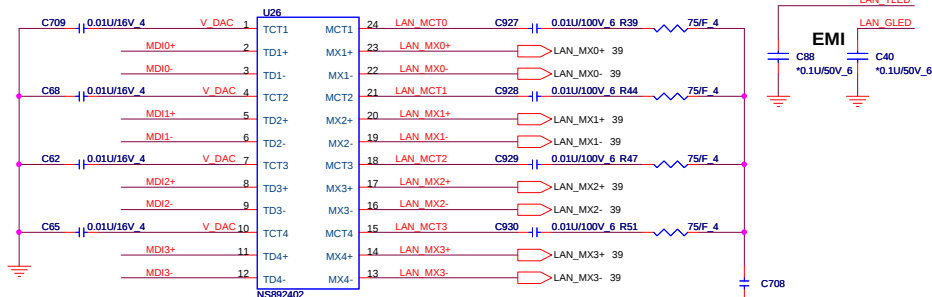
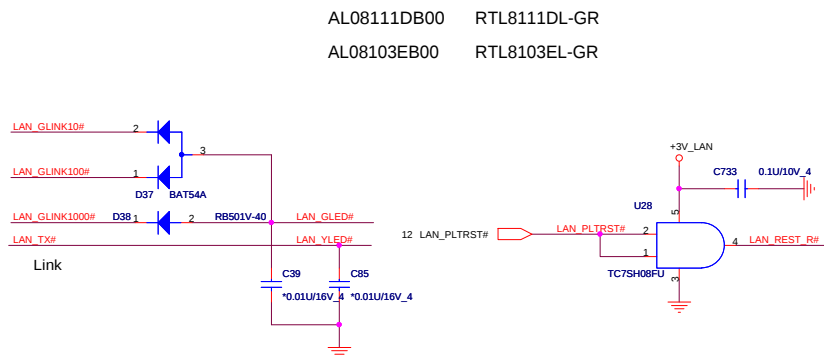
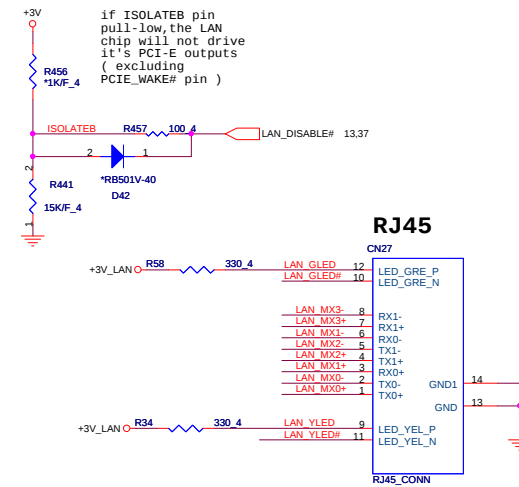
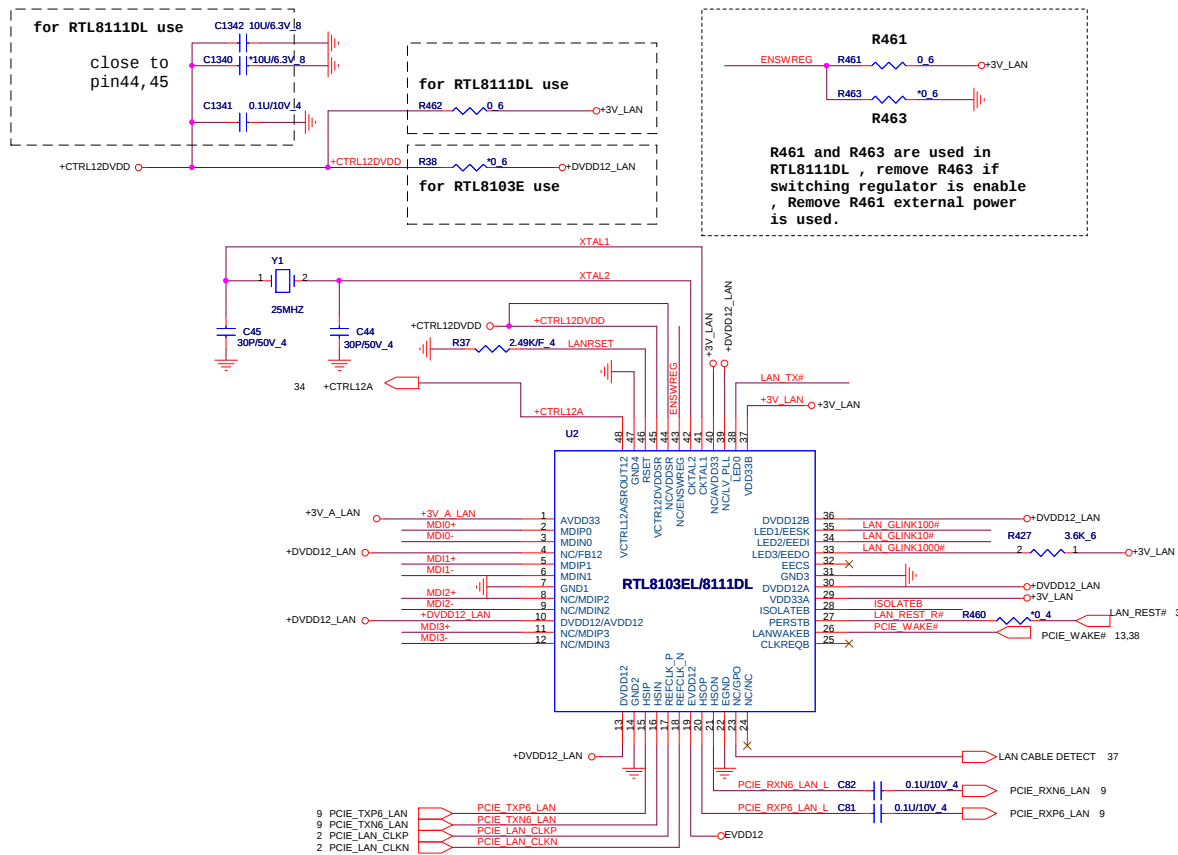


RIGHT SIDE USBX2



PROJECT : QT8
Quanta Computer Inc.

Size Custom	Document Number BT/WEBCAM/FT/USBX4/ESATA	Rev 1A
Date: Thursday, October 16, 2008	Sheet 32 of 48	



NS892402:GIGABIT

NS892405:10/100

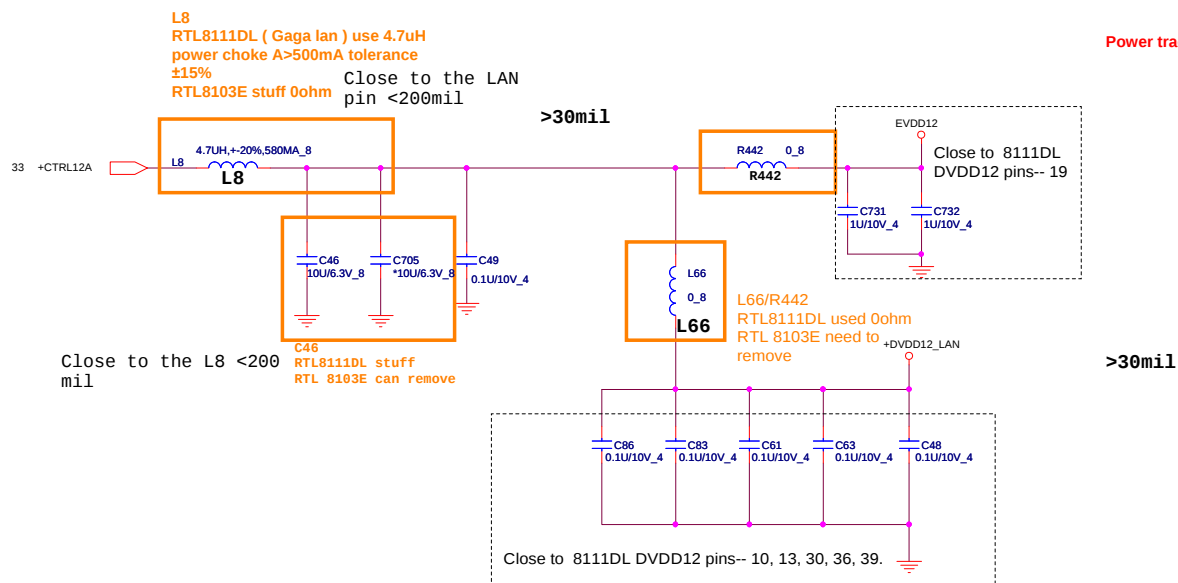
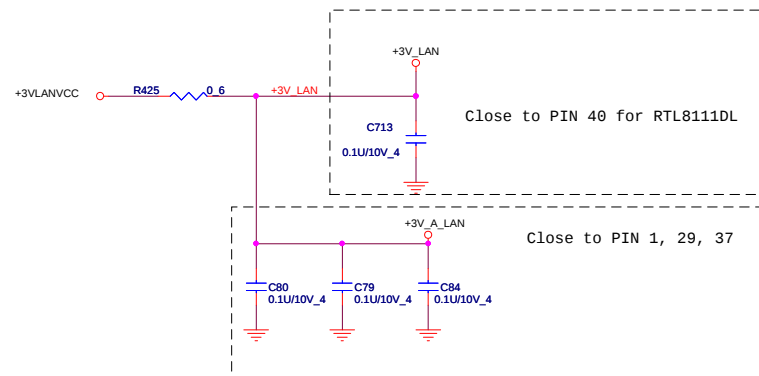
DB0AT9LAN05

DB0ZB1LAN04



PROJECT : UT12
Quanta Computer Inc.

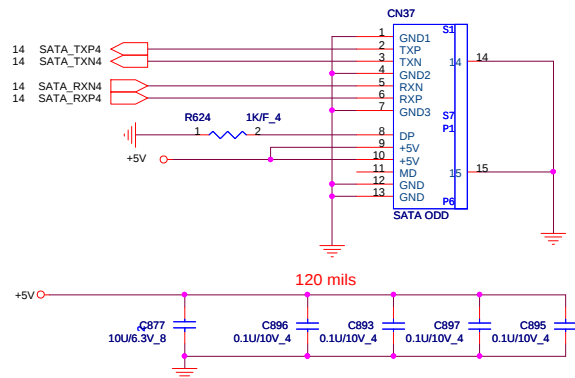
Size Custom	Document Number RTL8111C/8101E/RJ11-RJ45 CN	Rev 1A
Date: Thursday, October 16, 2008		Sheet 33 of 47



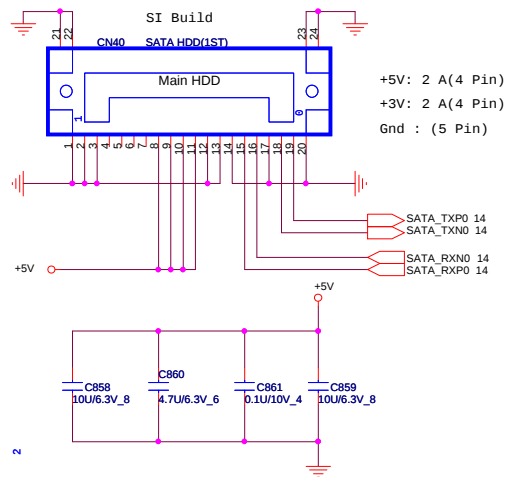
PROJECT : UT12
Quanta Computer Inc.

Size Custom	Document Number LAN Power	Rev 1A
Date: Thursday, October 16, 2008 Sheet 34 of 47		

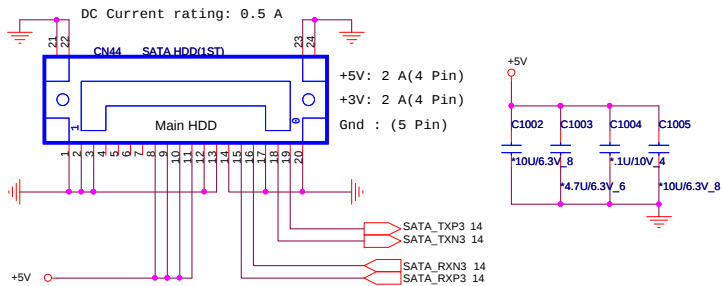
SATA CD-ROM



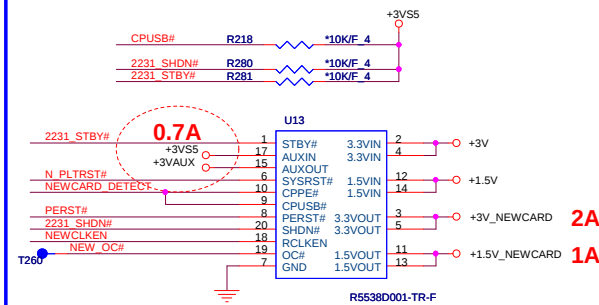
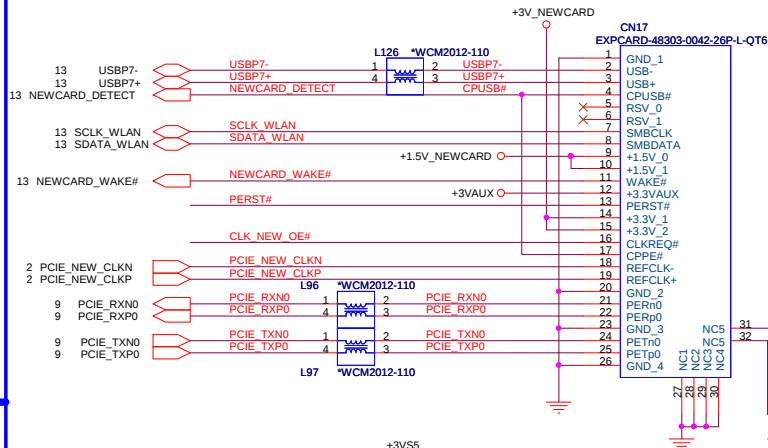
SATA_1 HDD CONNECTOR



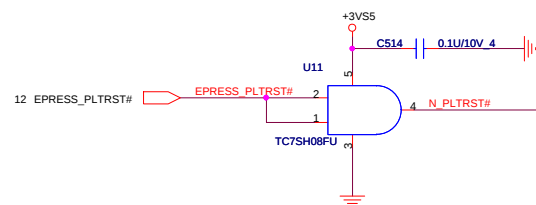
SATA_2 CONNECTOR



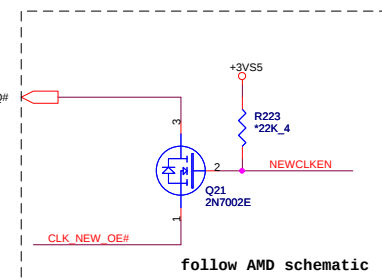
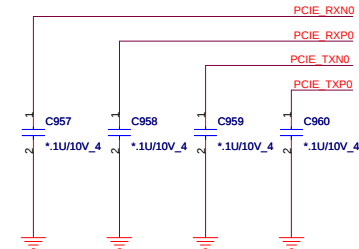
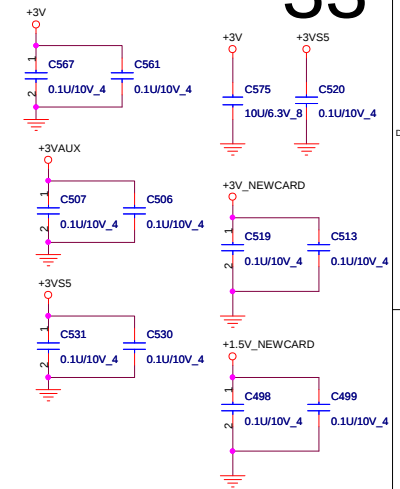
NEWCARD



pin name	pull hi/low
CPPE#	internal pull up to AUXIN
SYSRST#	internal pull up to AUXIN
CPUSB##	internal pull up to AUXIN
PERST#	a logic level power good
SHDN#	internal pull up to AUXIN
RCLKEN	internal pull up to AUXIN
OC#	over current status
STBY#	internal pull up to AUXIN



NEWCARD (PCIEXPRESS*1 + USB*1)

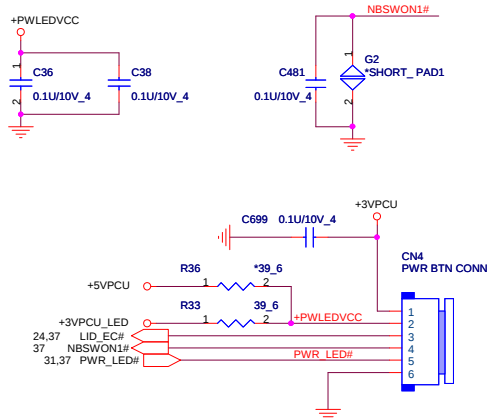


PROJECT : QT8
Quanta Computer Inc.



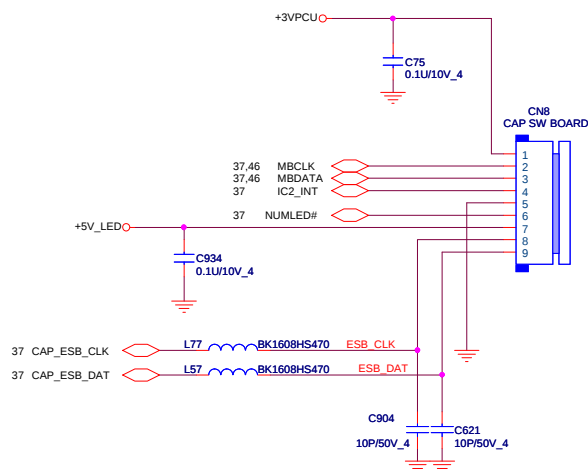
Size Custom	Document Number NEW CARD/SATA ODD/SATA HDD	Rev 1A
Date: Thursday, October 16, 2008	Sheet 35	of 48

POWER BUTTON CONNECT



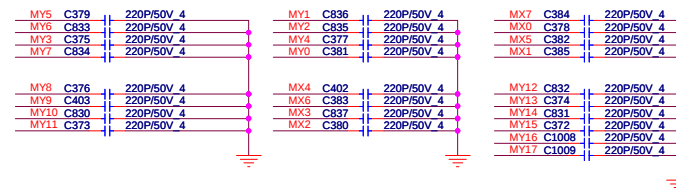
1. +3VPCU(LIDSWITCH PWR)
2. LEDVCC(+3VPCU)
3. LIDSWITCH
4. POWERON#
5. PWRLD#
6. GND

CAP SW CONNECT

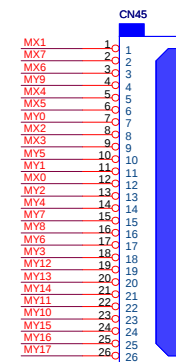
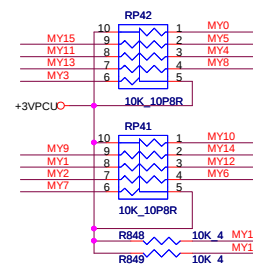


1. +3VPCU
2. MBCLK
3. MBDATA
4. CAP_INT
5. GND
6. NUM LOCK LED
7. +5V
8. ESB_CLK
9. ESB_DAT

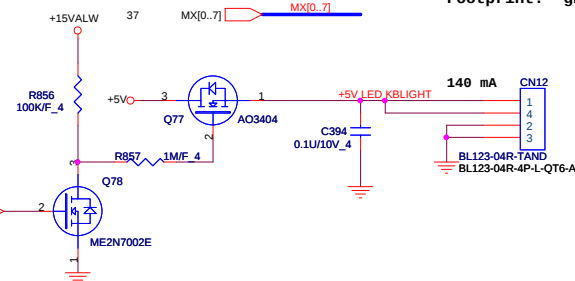
34



KEYBOARD PULL-UP

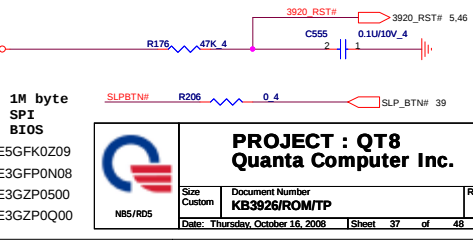
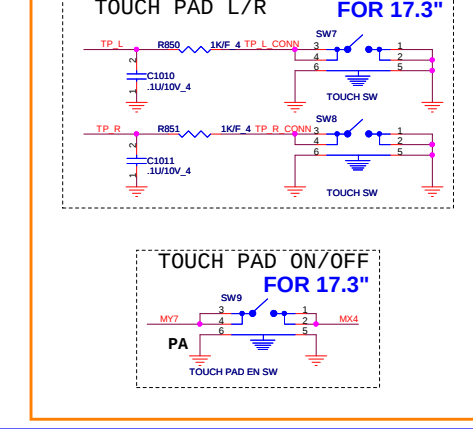
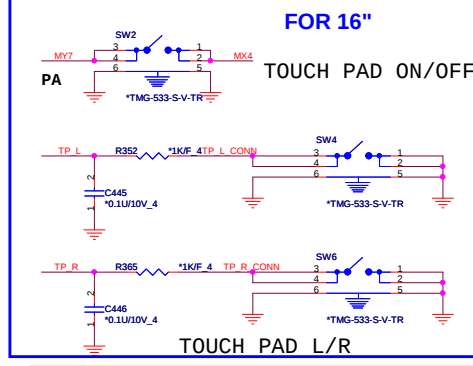
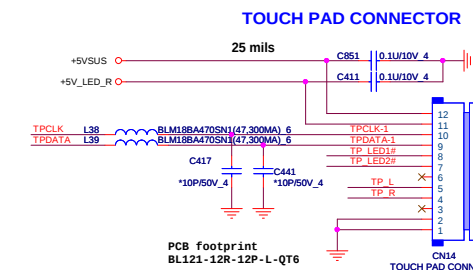
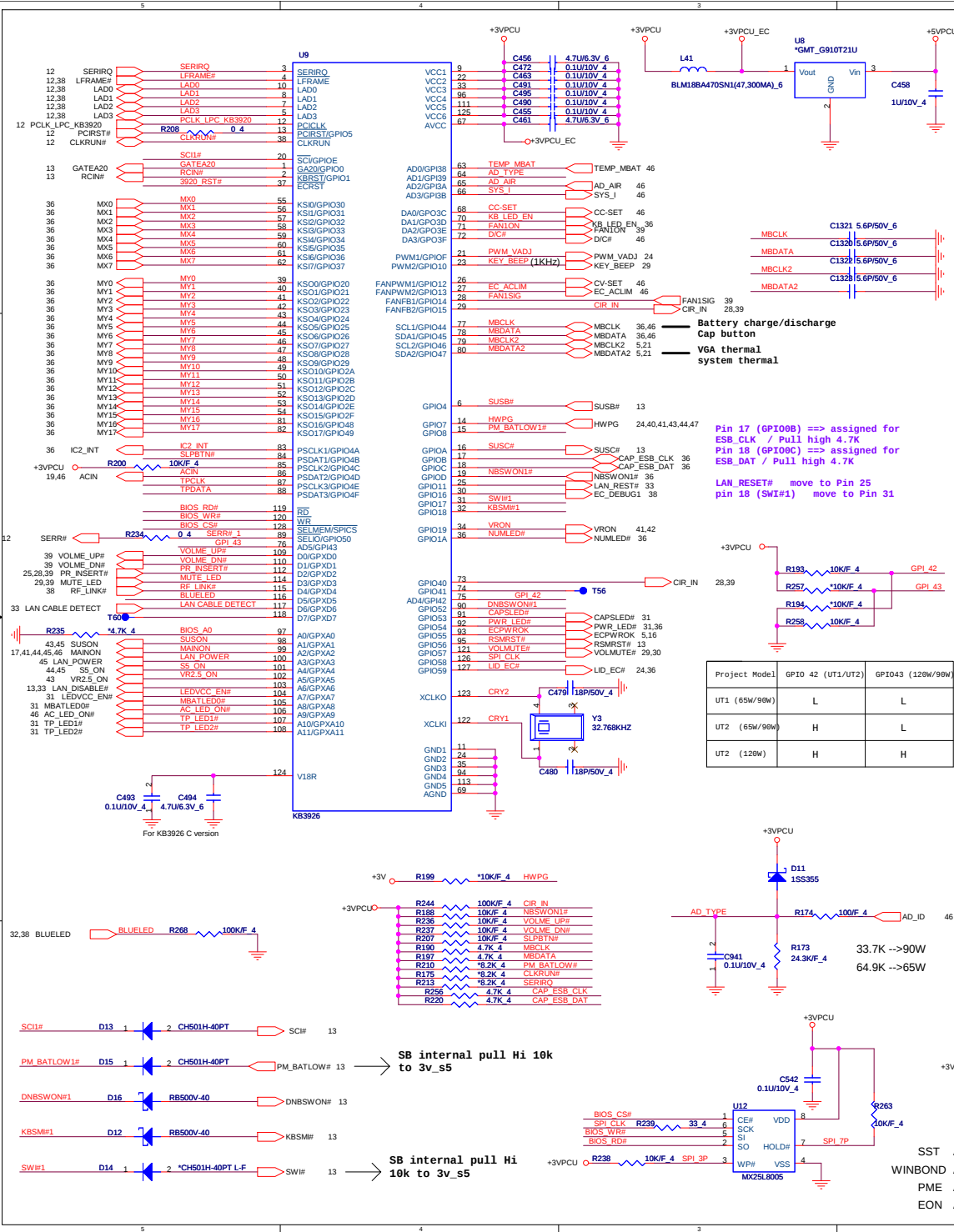


Footprint: "gb1rf260-1253-7f-26p-1"



PROJECT : QT8
Quanta Computer Inc.

Size Custom	Document Number LED/KEYBOARD/SW	Rev 1A
Date: Thursday, October 16, 2008	Sheet 36	of 48



Project Model	GPIO 42 (UT1/UT2)	GPIO43 (120W/90W)
UT1 (65W/90W)	L	L
UT2 (65W/90W)	H	L
UT2 (120W)	H	H

1M byte
SPI
BIOS

SST AKE5GFKOZ09
WINBOND AKE3GFPON08
PME AKE3GZPO500
EON AKE3GZPOQ00

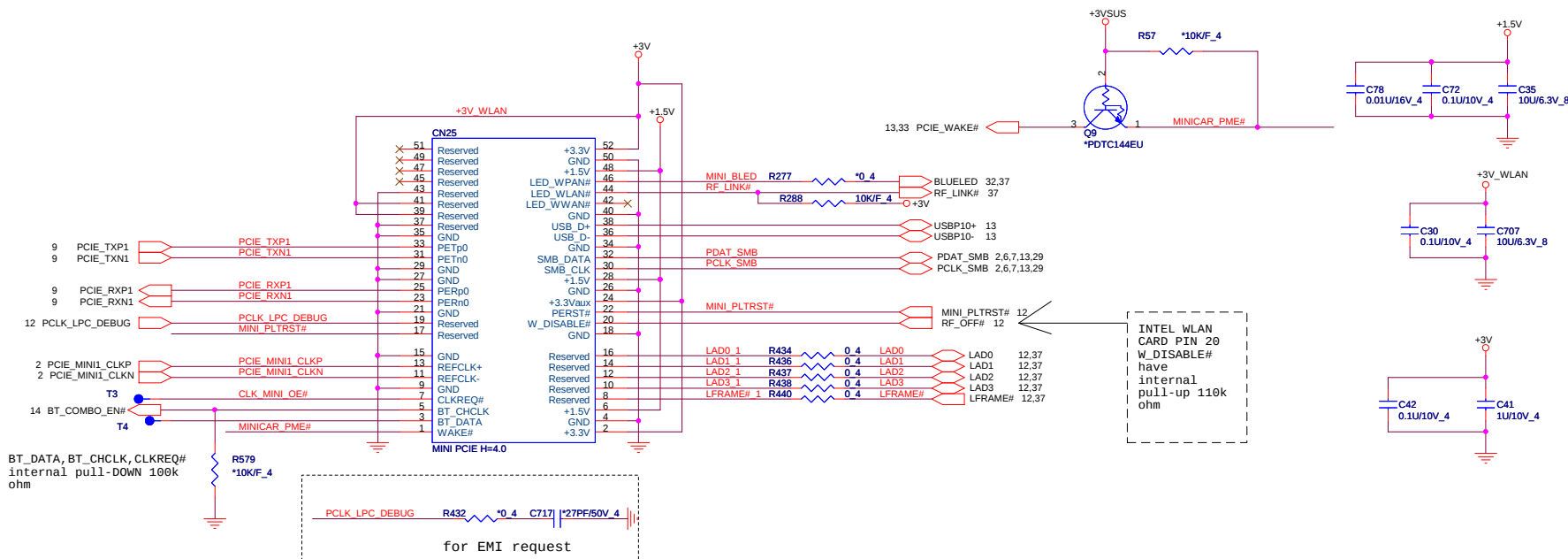
PROJECT : QT8
Quanta Computer Inc.

Size Custom
Document Number
KB3926/ROM/TP
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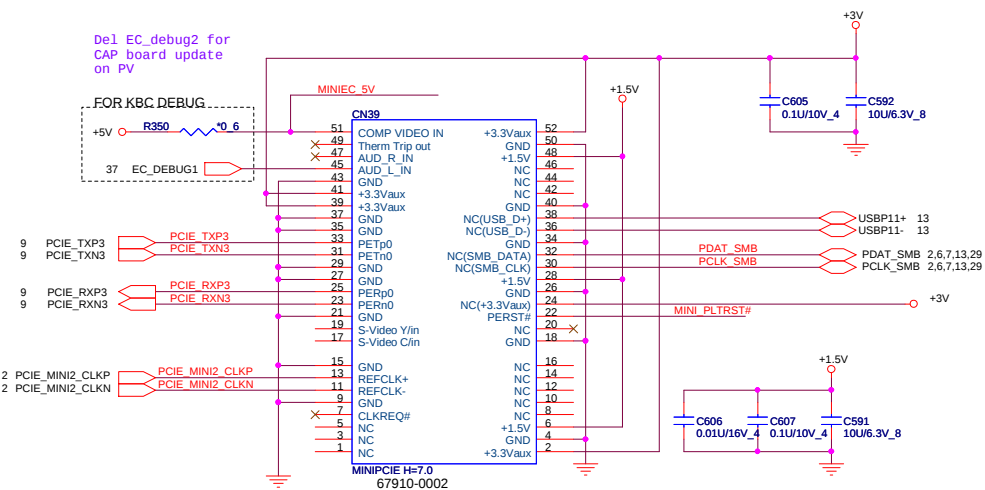
Rev 1A

Mini PCI-E Card 1 WLAN

36



Mini PCI-E Card 2 TV tuner card



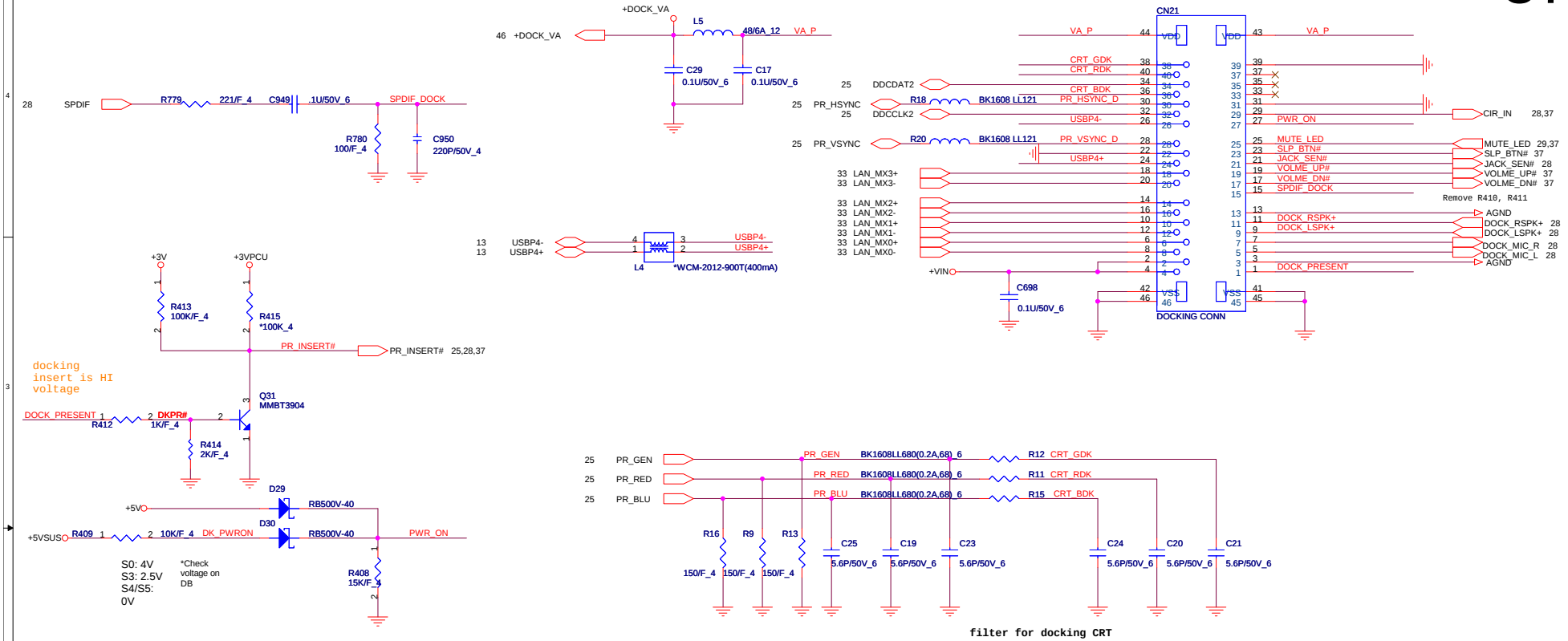
PROJECT : QT8
Quanta Computer Inc.

Size Custom	Document Number Mini CARD X 3	Rev 1A
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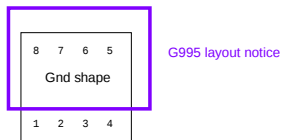
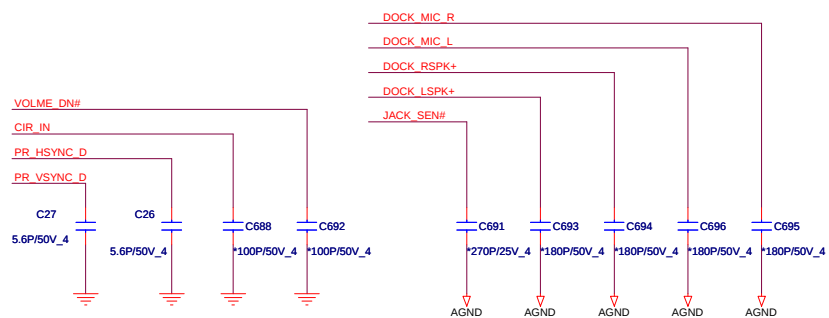
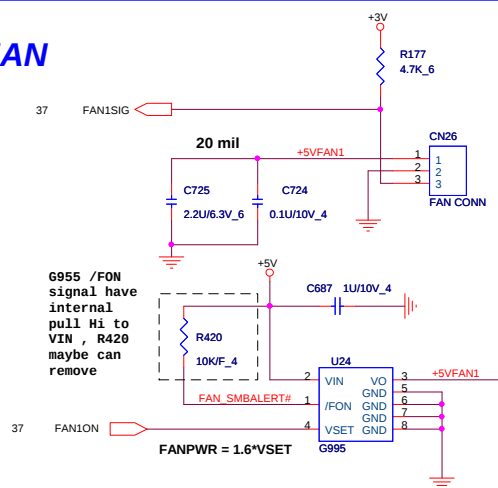
CABLE DOCK

support 6A 200mils
CX000480005

37



CPU FAN

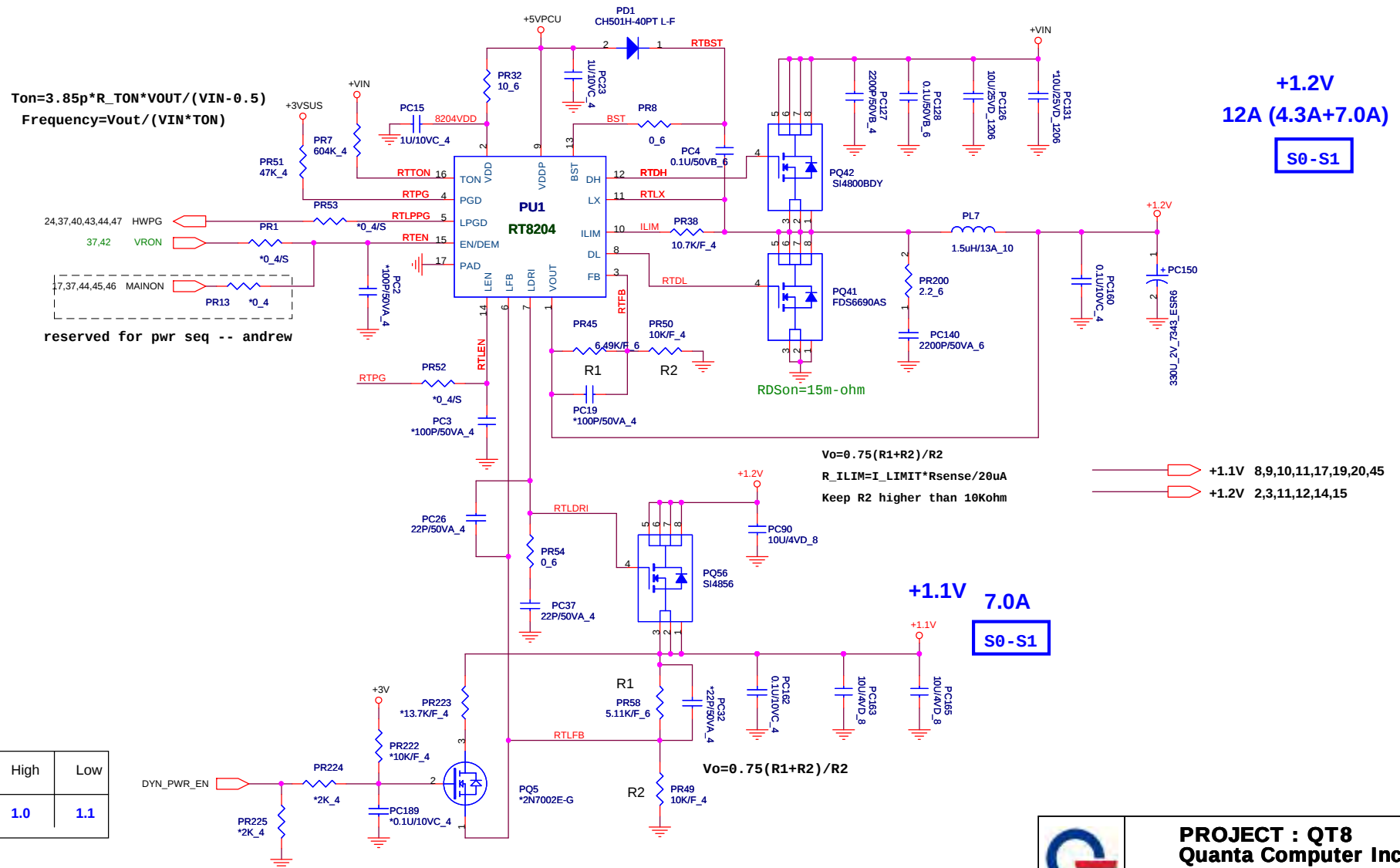


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Size Custom	Document Number CABLE DOCKING/FAN	R
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+1.1V & +1.2V

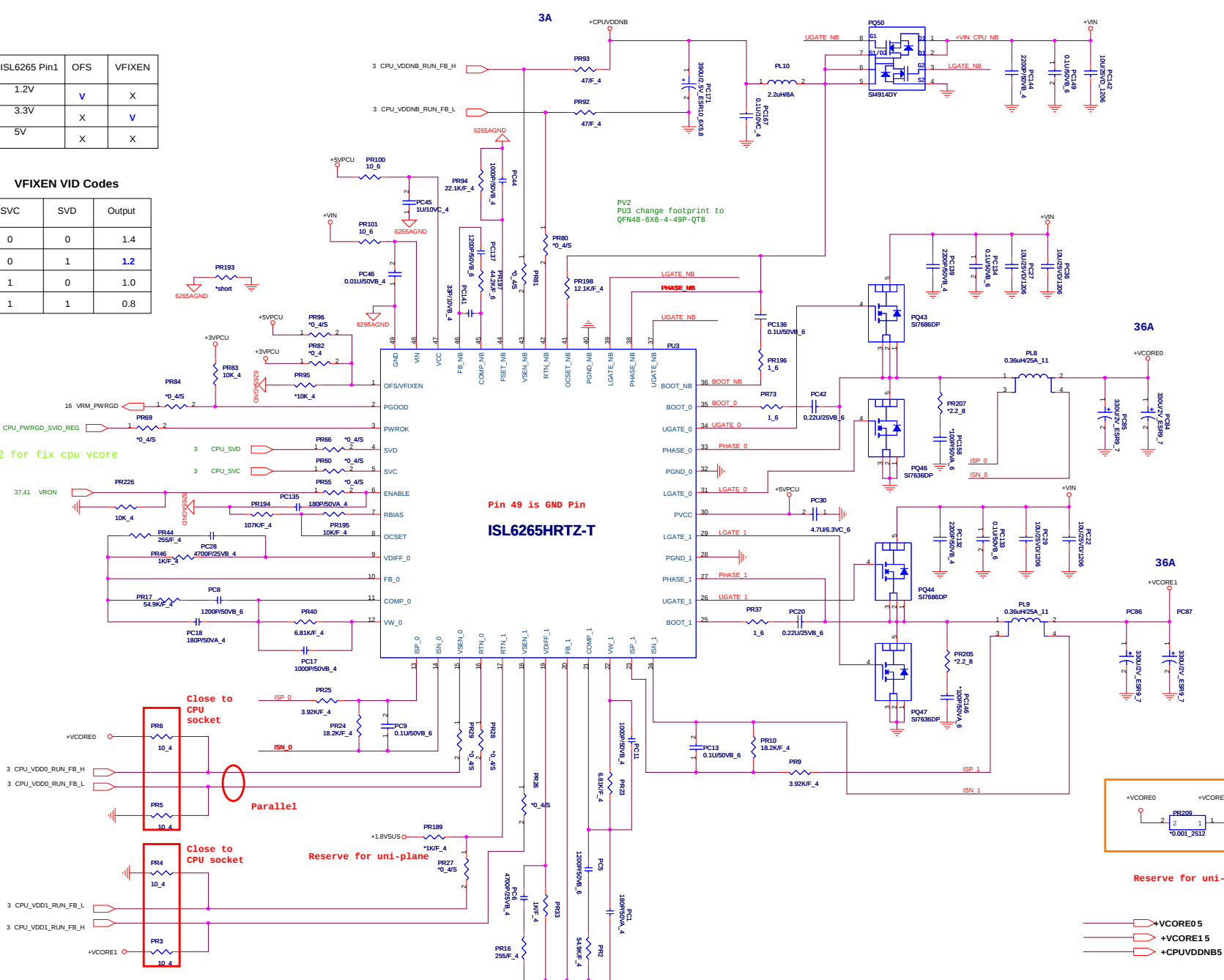
41



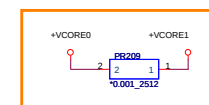
DYN_PWR_EN	High	Low
+1.1V_DYN	1.0	1.1

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+1.2V & +1.1V(RT8204)		
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SVC	SVD	Output
0	0	1.4
0	1	1.2
1	0	1.0
1	1	0.8



Pin 49 is GND Pin

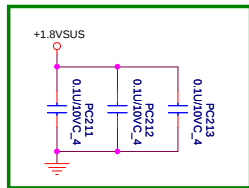


Reserve for uni-plane

Diagram showing three power pins:

- +VCORE05
- +VCORE15
- +CPUVDDNB5

Add 0.1u CAP PC211, PC212, PC213 for EMI



+2.5V 3
+1.8VSUS 3,4,5,6,7,42,44,47

$I_{lim}(\text{Valley}) = 10\mu A * R_{ILIM} / R_{DS_ON}$
For OCP set.

23.65A

S0-S3

Fix 1.8V Output
51116_VSFILT
PR36
*0.4

$$R_a = (V_{out} - 0.75) / 0.75 * R_b$$

Rb value from 100K to 300K ohm

+2.5V
0.25A

S0-S1

$$V_{out} = 1.25 * (1 + R1/R2)$$

For EMI

Discrete: SI4856
UMA: SI4800

For EMI

+1.8V
10.4A

S0-S1

+0.9VSMVT

2.25A

S0-S3

Mode	Discharge Mode
V5IN	No discharge
VDDQ	Tracking discharge
Gnd	Non-tracking discharge

$$V_TRIP(mV) = R_TRIP(Kohm) * 10(\mu A)$$

$$I_OCP = V_trip / R_{ds_on} + I_Ripple / 2$$

VDDQSET	VDDQ(V)	VTTREF and Vtt	Note
GND	2.5	V_ vddqsns/2	DDR
V5IN	1.8	V_ vddqsns/2	DDR2
FB	adjustable	V_VDDQSN/2	1.5V < VDDQ < 3V



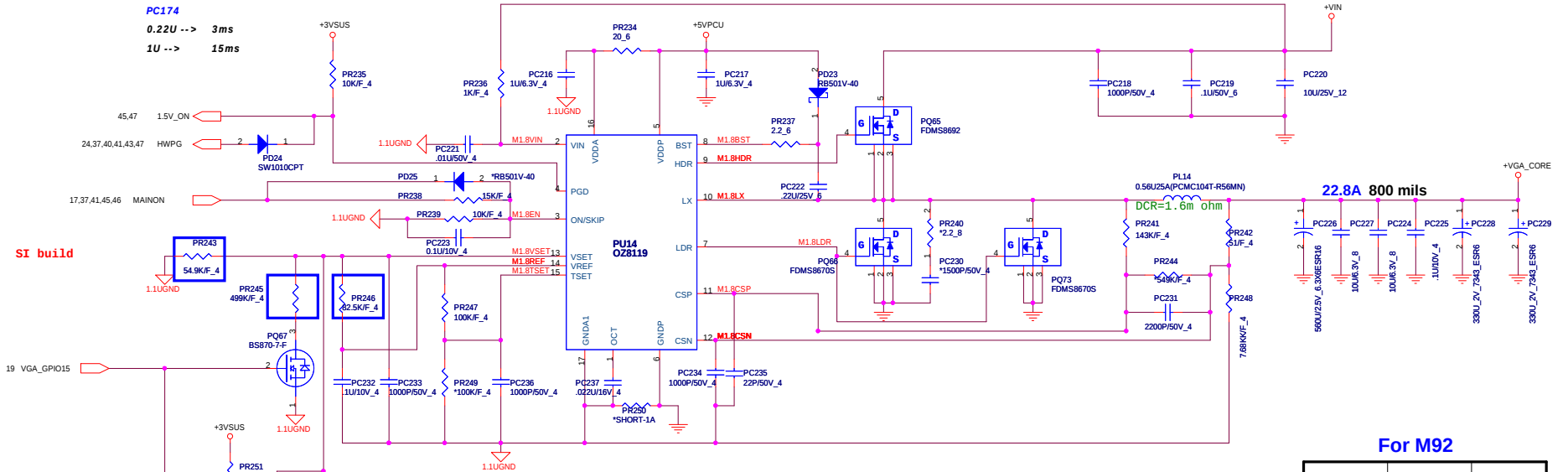
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VGA Core & VCC1.1

+1.1Volt +/- 5%
 Countinue current:17.54A
 Peak current:22.8A
 OCP minimum 23A

44



For M96

GPI015	GPI020	VDDC
Low	Low	1.1V
High	Low	1.05V
Low	High	0.95V
High	High	0.9V

For M96

- 1, PR246 use 82.5K. (CS38252FB17)
- 2, PR243 use 54.9K. (CS35492FB14)
- 3, PR245 use 499K. (CS44992FB11)
- 4, PR254 use 210K. (CS42102FB00)

For M92

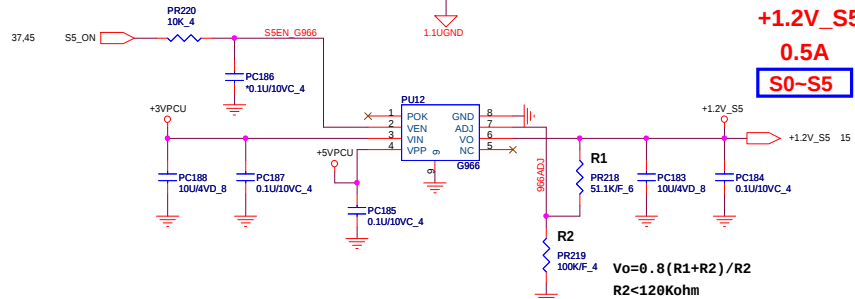
- 1, PR246 use 86.6K. (CS38662FB16)
- 2, PR243 use 49.9K. (CS34992FB10)
- 3, PR245 use 604K. (CS46042FB11)
- 4, PR254 use 287K. (CS42872FB13)

For M92

GPI015	GPI020	VDDC
Low	Low	1.0V
High	Low	0.95V
Low	High	0.9V
High	High	0.85V

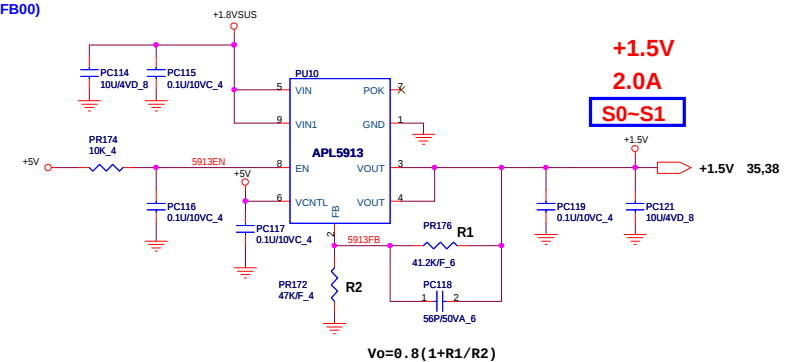
+1.2V_S5

0.5A
 S0-S5



$$V_o = 0.8(R1+R2)/R2$$

$$R2 < 120Kohm$$

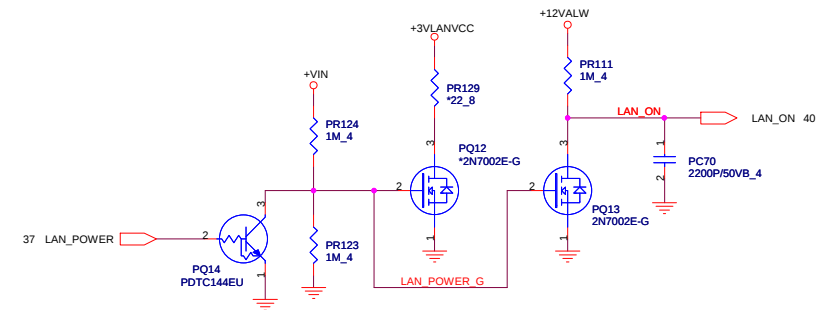
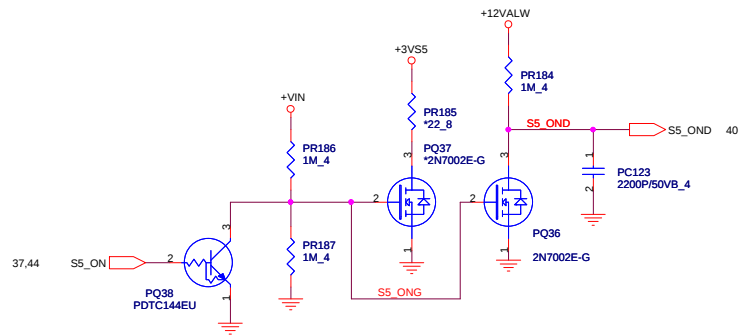
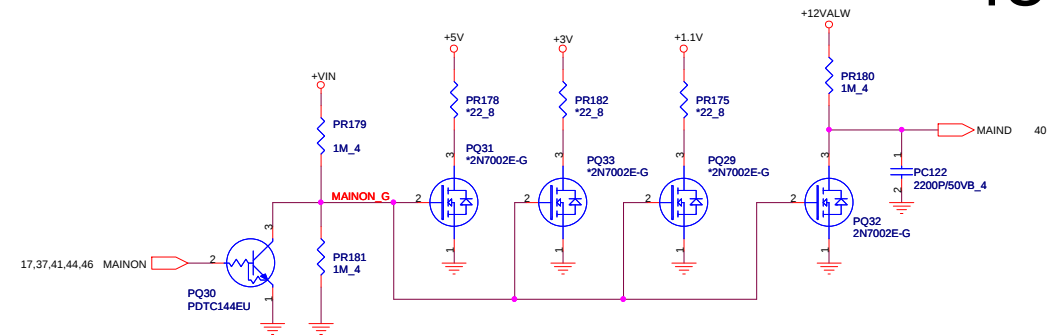
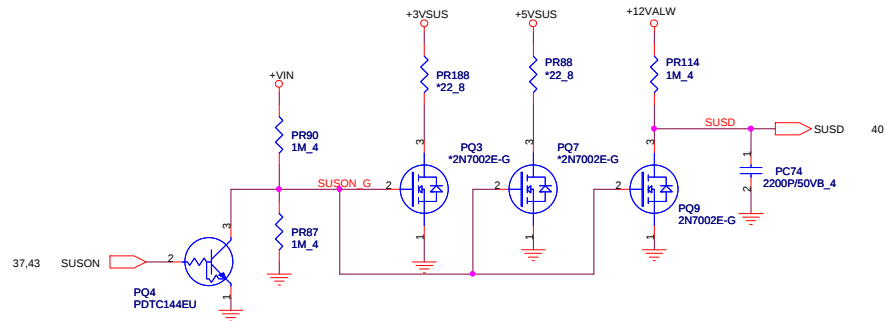


$$V_o = 0.8(1+R1/R2)$$

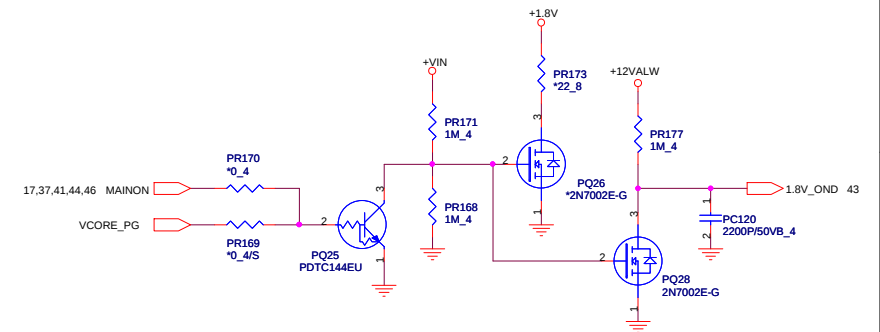
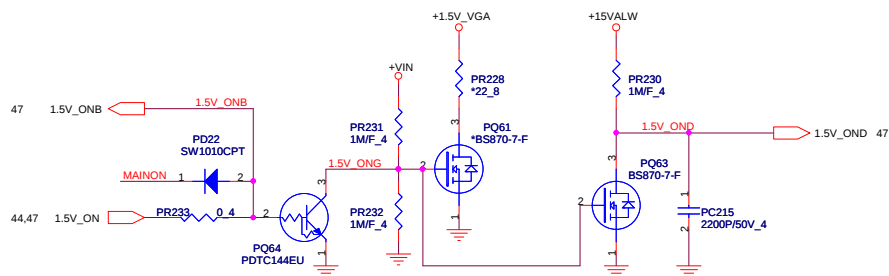
NBS/RDS

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VGA PWR OZ8118/1.2V_S5/+1.5		
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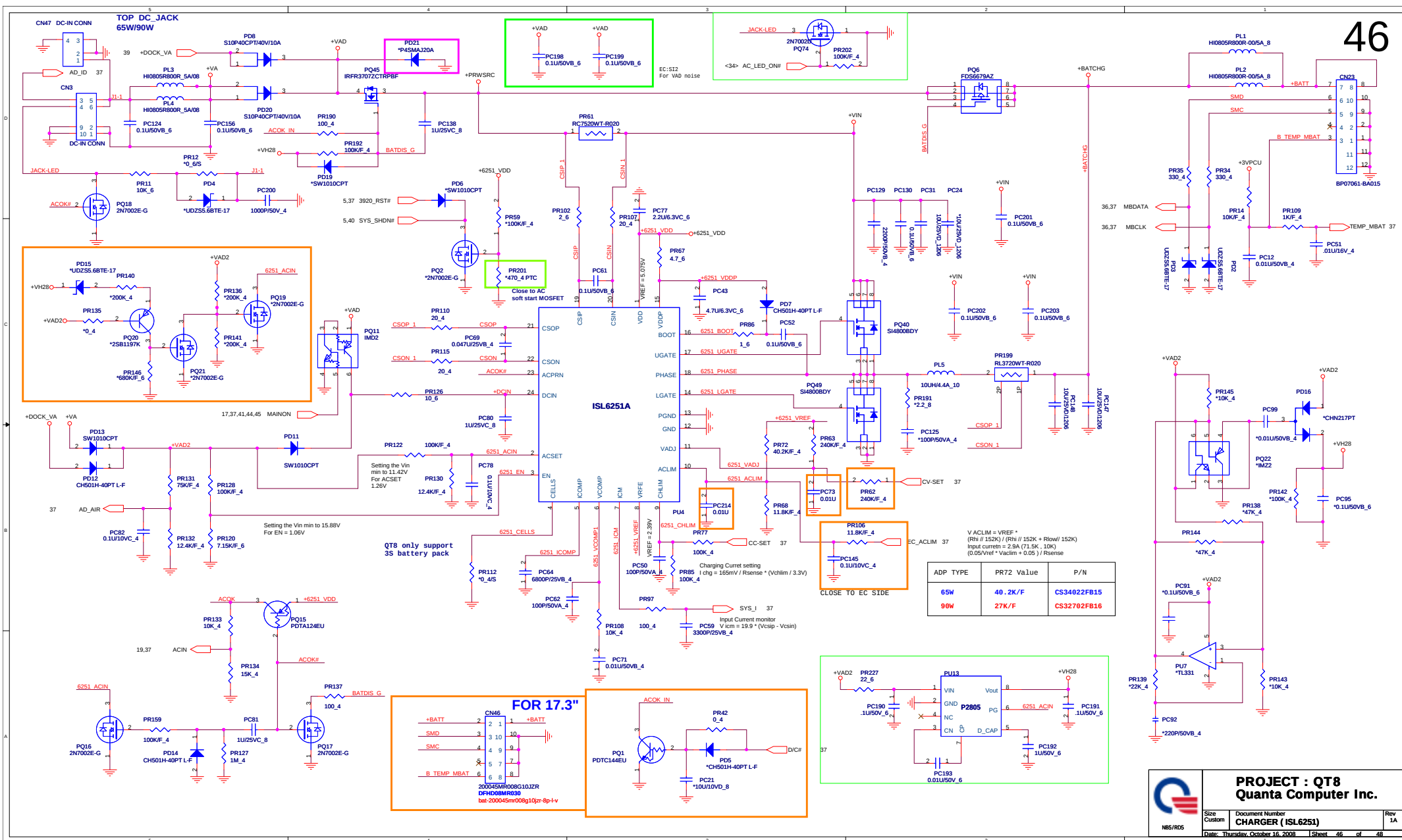
For Discrete Only

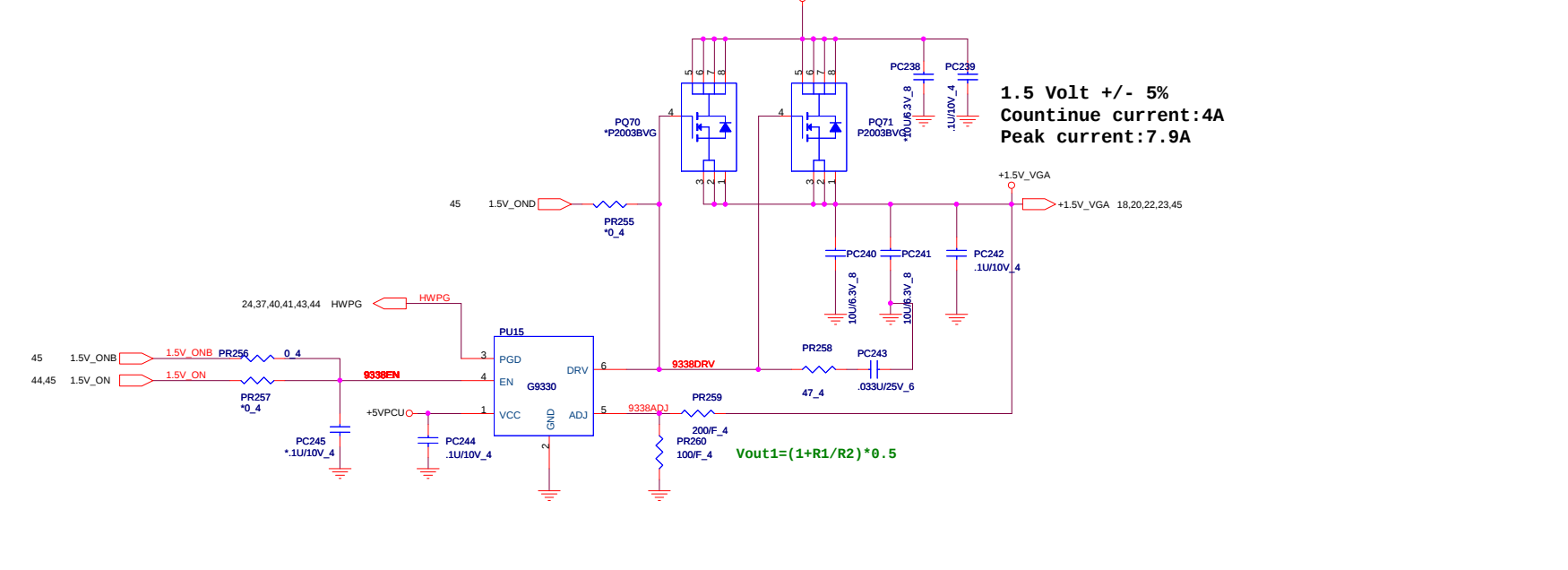


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NB5/RD5





VRAM	Mounted	N/A	PQ33 P/N
Others	PR218, PR219, PR220, PR226, PR231, PC196, PC197, PC199, PC201, PD20, PU10	PR232	BAM44960000
Samsung	PR232	PR218, PR219, PR220, PR226, PR231, PC196, PC197, PC199, PC201, PD20, PU10	BAM48560029

