

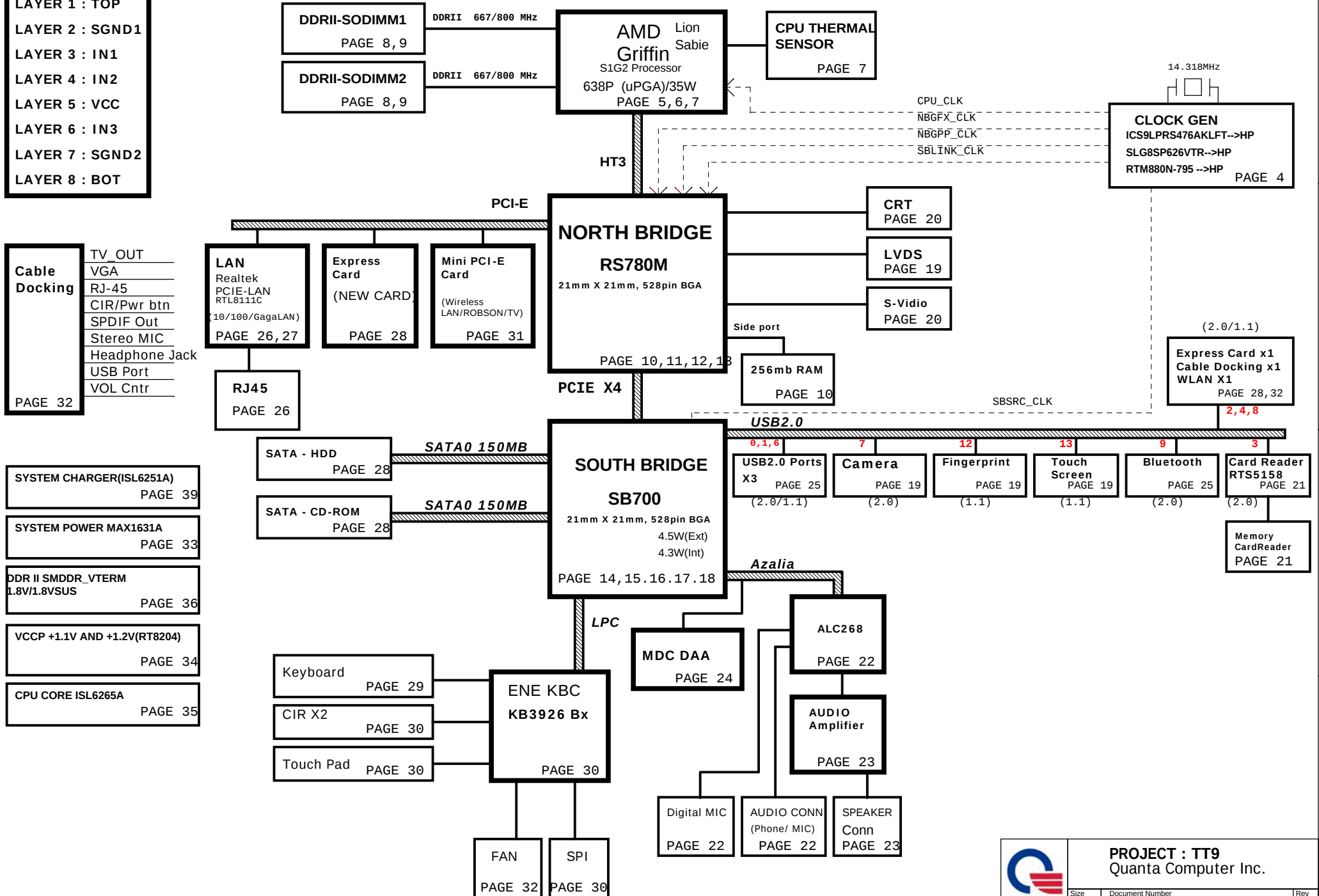
## PCB STACK UP

LAYER 1 : TOP  
LAYER 2 : SGND1  
LAYER 3 : IN1  
LAYER 4 : IN2  
LAYER 5 : VCC  
LAYER 6 : IN3  
LAYER 7 : SGND2  
LAYER 8 : BOT

# Soyuz 2.0 SYSTEM DIAGRAM



01



PROJECT : TT9  
Quanta Computer Inc.

|                                   |                                  |           |
|-----------------------------------|----------------------------------|-----------|
| Size<br>Custom                    | Document Number<br>Block Diagram | Rev<br>1A |
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## INDEX

| Pg#   | Description                   | NOTE |
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| 33    | 3V/5V(MAX1631A)               |      |
| 34    | +1.2V/+1.1V (RT8204)          |      |
| 35    | +CPU_CORE ISL6265             |      |
| 36    | +1.8VSUS/+1.8V/+2.5V          |      |
| 37    | +1.1V/+1.2V_S5/+1.5V          |      |
| 38    | DISCHARGE                     |      |
| 39    | Charger (ISL6251)             |      |

\* --> Un-stuff (ex. \*1K/04)  
**04-- 0402 footprint**  
**06-- 0603 footprint**  
**08-- 0805 footprint**  
**12-- 1206 footprint**  
**F-- 1% tolerance**

## Power & Ground

| Label                                                                                    | ACTIVE         | Description                         | Control Signal |
|------------------------------------------------------------------------------------------|----------------|-------------------------------------|----------------|
| +VIN                                                                                     | S0, S3, S4, S5 | AC ADAPTER (18.5V)                  |                |
| +BATT                                                                                    | S0, S3, S4, S5 | MAIN BATTERY + (6.2V-8.4V)          |                |
| +AVBAT                                                                                   | S0, S3, S4, S5 | RTC & KBC POWER (3.3V)              |                |
| +12VALW                                                                                  | S0, S3, S4, S5 | +12V                                |                |
| +VCORE                                                                                   | S0             | CPU CORE POWER (0.375-1.5V)         | VRON           |
| +CPUVDDNB                                                                                | S0             | CPU CORE POWER (1.375-1.5V)         | VRON           |
| +1.1V_NB                                                                                 | S0             | +1.1 to +1.0 DYN                    | VRON           |
| +1.1V                                                                                    | S0             | +1.1V                               | VRON           |
| +1.2VS5                                                                                  | S0, S3, S4, S5 |                                     | S5_ON          |
| +1.2V                                                                                    | S0             | +1.2V                               | VRON           |
| +3V                                                                                      | S0             |                                     | MAINON         |
| +3VSUS                                                                                   | S0, S3         |                                     | SUSON          |
| +3VS5                                                                                    | S0, S3, S4, S5 |                                     | S5_ON          |
| +3VPCU                                                                                   | S0, S3, S4, S5 | ALWAYS POWER (3V)                   |                |
| +5V                                                                                      | S0             |                                     | MAIND          |
| +5VSUS                                                                                   | S0, S3         |                                     | SUSON          |
| +5VPCU                                                                                   | S0, S3, S4, S5 | ALWAYS POWER (5V)                   |                |
| +1.5V                                                                                    | S0             |                                     | MAIND          |
| +1.8VSUS                                                                                 | S0, S3         | DDR CORE POWER                      | SUSON          |
| +1.8V                                                                                    | S0             |                                     | MAINON         |
| +2.5V                                                                                    | S0             | CPU VDDA                            | VR2.5_ON       |
| +0.9VSMVTT                                                                               | S0             | DDR COMMAND & CONTROL PULL UP POWER | MAINON         |
| +0.9VSMVREF_DIMM                                                                         | S0, S3         | DDR REF POWER                       | SUSON          |
| +AVDD                                                                                    | S0             | AUDIO ANALOG POWER (5V)             | MAINON         |
| +3VLAVCC                                                                                 | S0, S3, S4, S5 | LAN Power                           | LAN_ON         |
|                                                                                          |                |                                     |                |
|  GND  | ALL PAGES      | DIGITAL GROUND                      |                |
|  AGND |                | AUDIO GND                           |                |

|                    |                                  |
|--------------------|----------------------------------|
| SMBUS              | SMBUS function define            |
| SMBCLK0<br>SMBDAT0 | DDR / DDR THER / CLOCK GEN (+3V) |
| SMBCLK1<br>SMBDAT1 | Mini Card (+3VS5)                |
| SMBCLK2<br>SMBDAT2 | New CARD (+3VS5)                 |



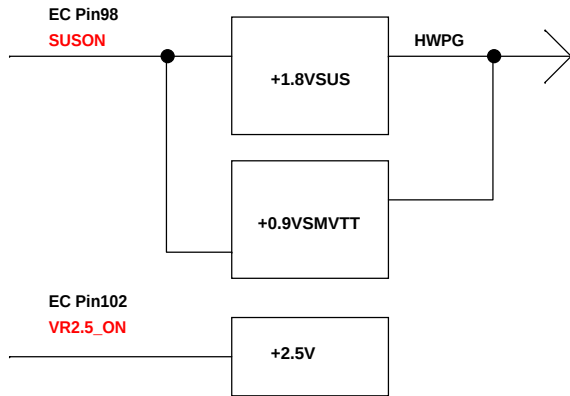
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Size Custom Document Number System Information Rev 1A

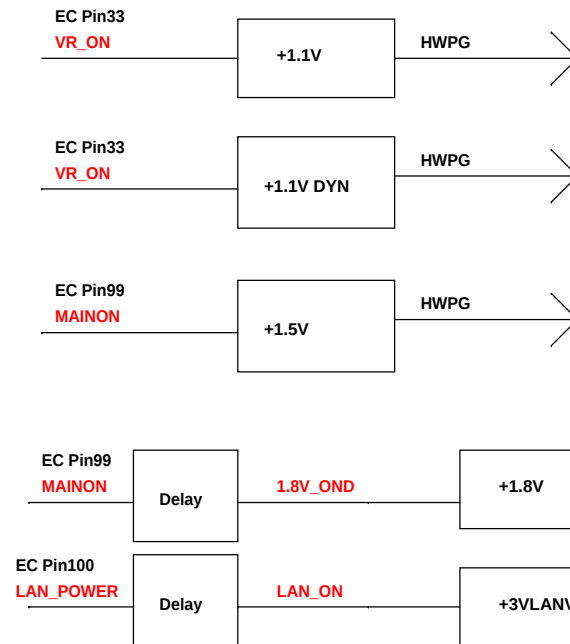
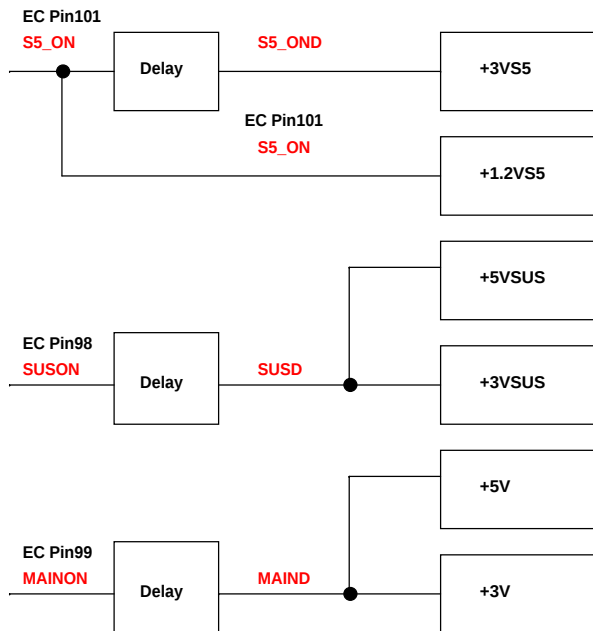
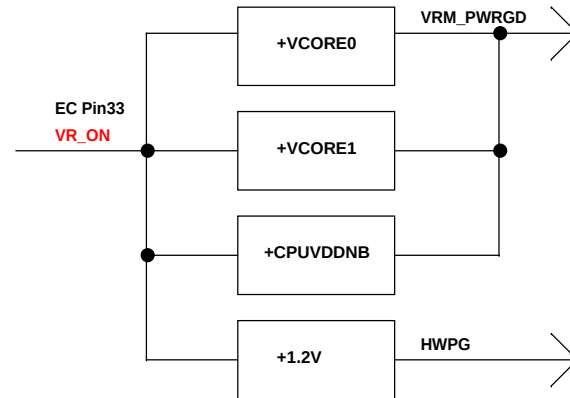
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02

## CPU Power Group A



## CPU Power Group B



3VPCU/5VPCU

NBSWON1#

DNBSWON#

S5\_ON

RSMRST#

SUSC

SUSB

SUSON

MAINON

VR\_ON

VR2.5\_ON

HWPG

VRM\_PWRGD

ECPWROK

NB\_PWRGD\_IN

SB\_PWRGD\_IN

CPU CLK IN

CPU RESET

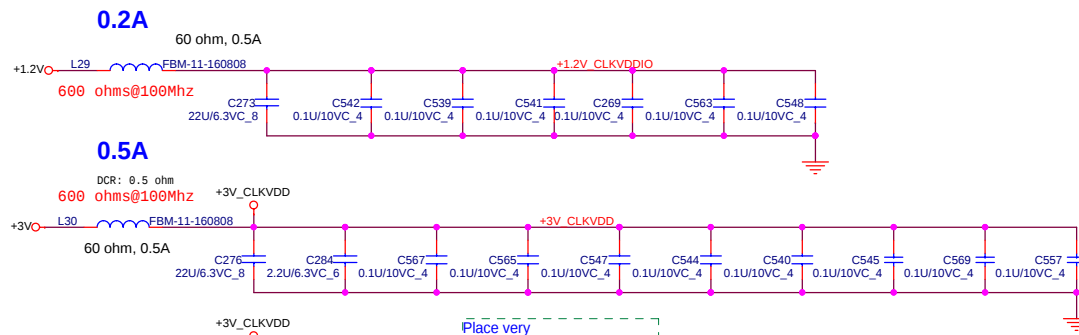
CPU POWER OK

CPU\_LDTSTOP#



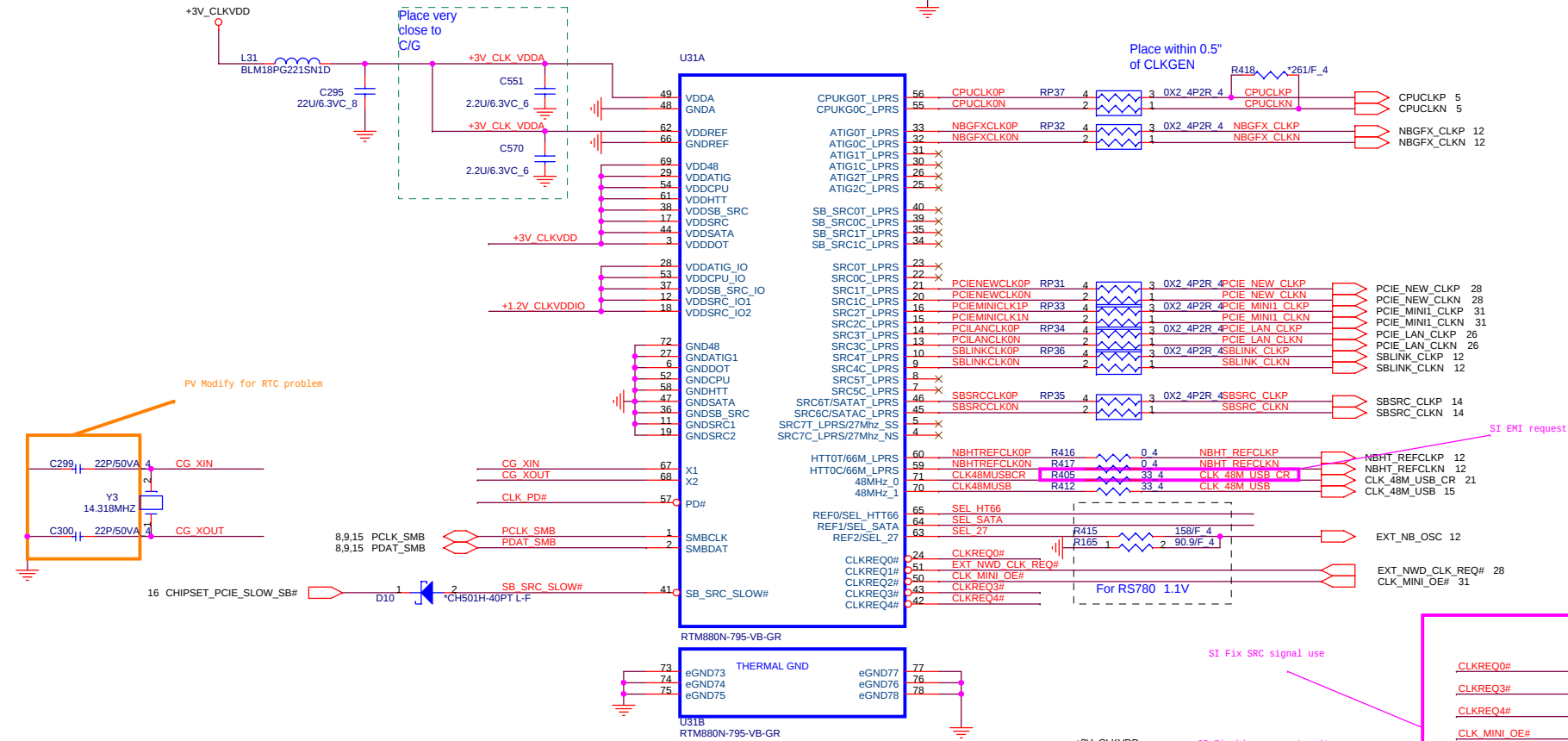
**PROJECT : TT9**  
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|                                                 |                                  |           |
|-------------------------------------------------|----------------------------------|-----------|
| Size<br>Custom                                  | Document Number<br>Power control | Rev<br>1A |
| Date: Wednesday, January 23, 2008 Sheet 3 of 41 |                                  |           |



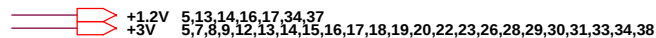
| NB CLOCKS    | RX780         | RS780                  |
|--------------|---------------|------------------------|
| HT_REFCLKP   | 100M DIFF     | 100M DIFF              |
| HT_REFCLKN   | 100M DIFF     | 100M DIFF              |
| REFCLK_P     | 14M SE (1.8V) | 14M SE (1.1V)          |
| REFCLK_N     | NC            | vref                   |
| GFX_REFCLK   | 100M DIFF     | 100M DIFF(IN/OUT)*     |
| GPP_REFCLK   | 100M DIFF     | NC or 100M DIFF OUTPUT |
| GPPSB_REFCLK | 100M DIFF     | 100M DIFF              |

Clock chip has internal serial terminations  
for differential pairs, external resistors  
are reserved for debug purpose.

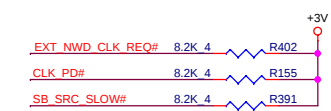
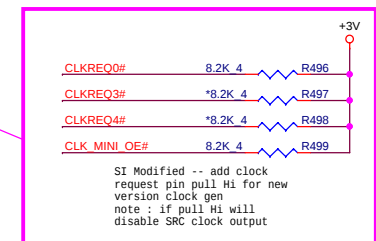
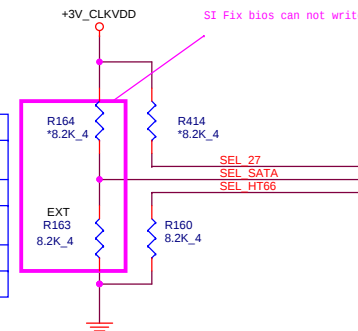


when driven low SB\_SRC clocks slow only supported with  
to reduced setpoint custom CG IC

\* RS780 can be used as clock buffer to output two PCIE reference clocks  
By default, chip will configured as input mode, BIOS can program it to output mode.

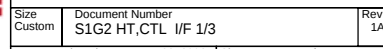


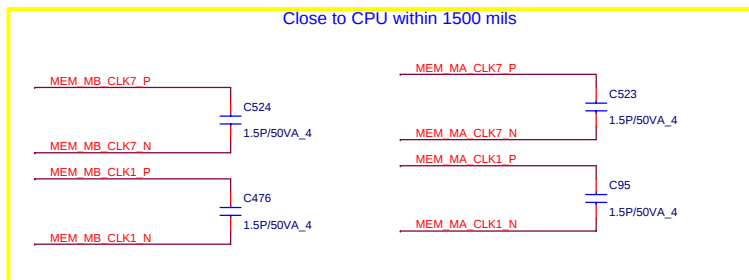
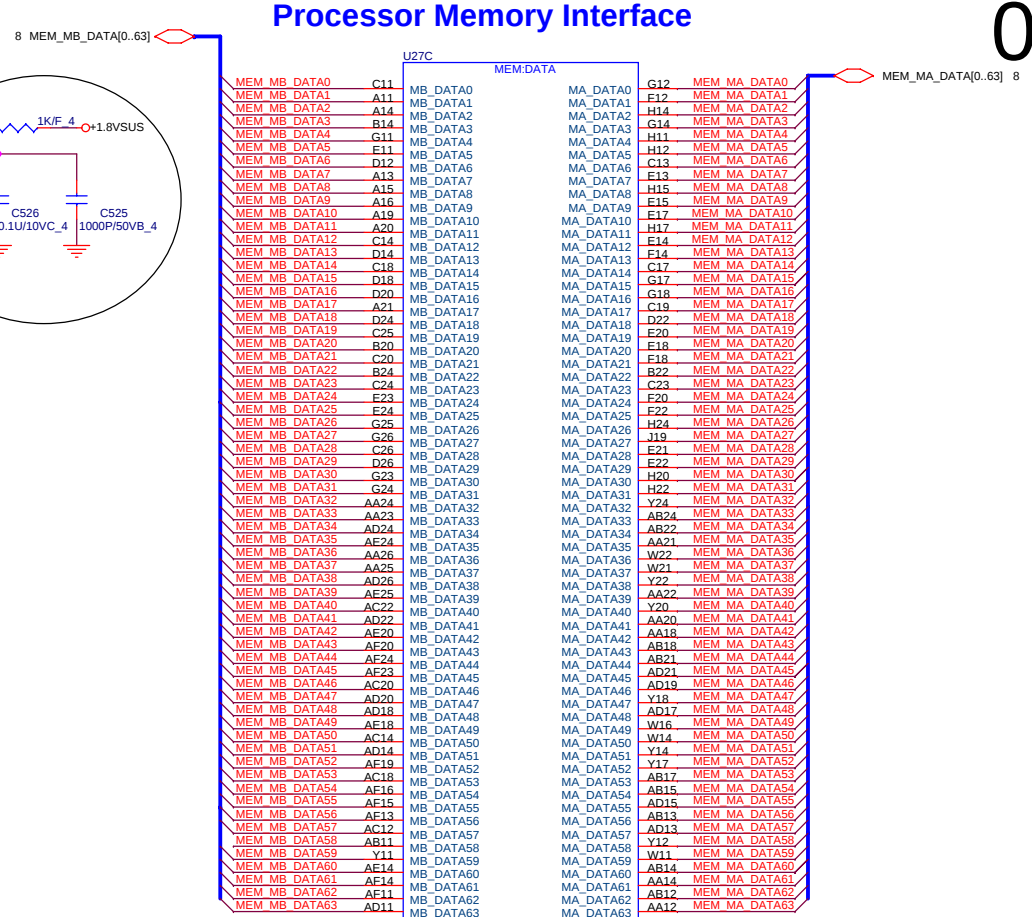
| * default |    |                                              |
|-----------|----|----------------------------------------------|
| SEL_HTTG6 | 1  | 66 MHz 3.3V single ended HTT clock           |
|           | 0* | 100 MHz differential HTT clock               |
| SEL_SATA  | 1* | 100 MHz non-spreading differential SRC clock |
|           | 0  | 100 MHz spreading differential SRC clock     |
| SEL_27    | 1* | 27MHz non-spreading singled clock            |
|           | 0  | 100 MHz spreading differential SRC clock     |



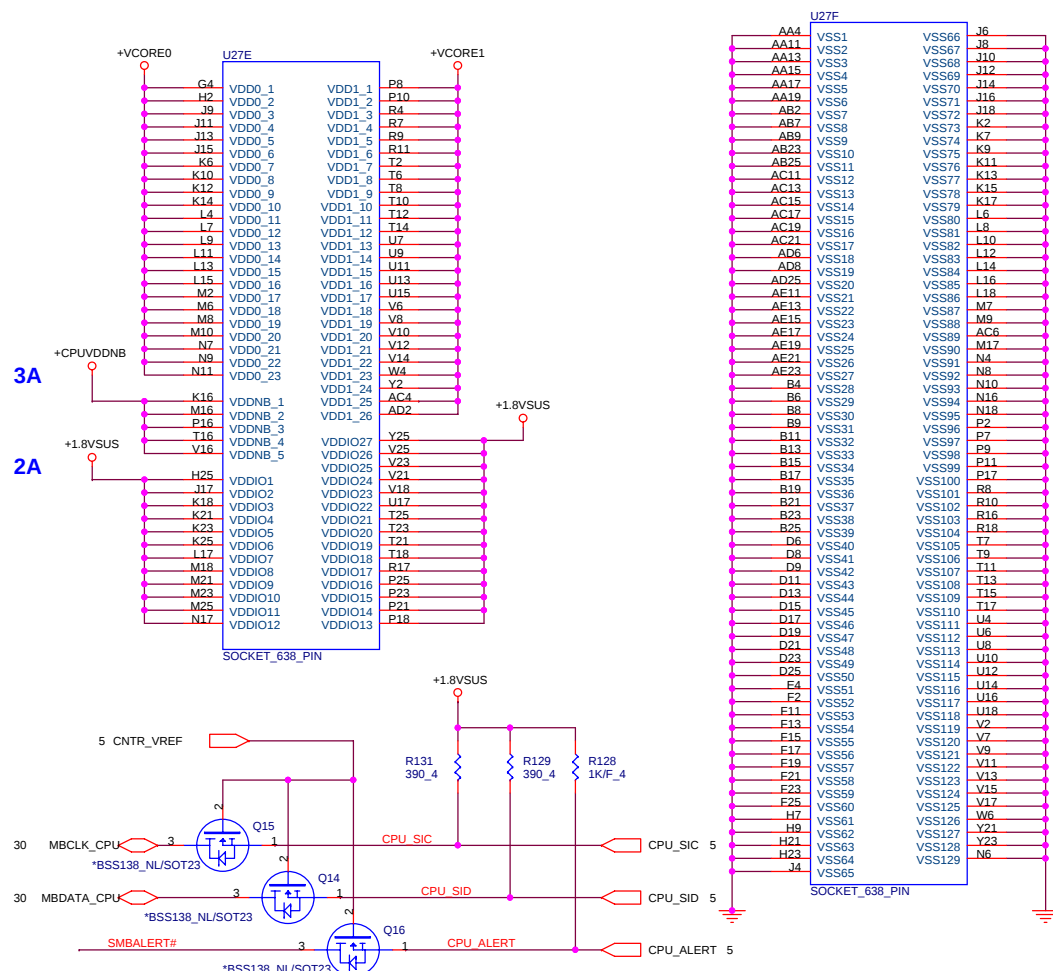


Change from +1.8VSUS to +1.8

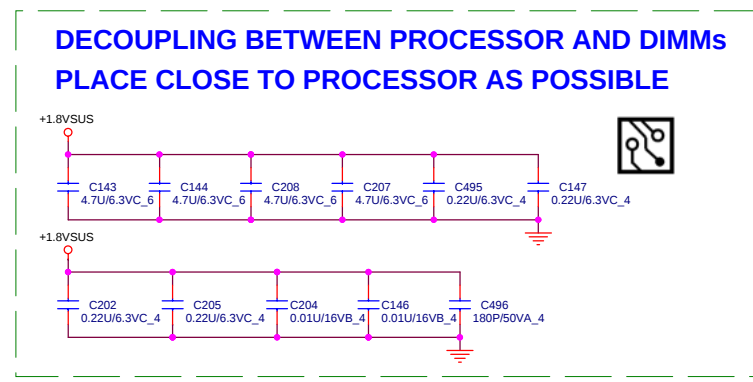
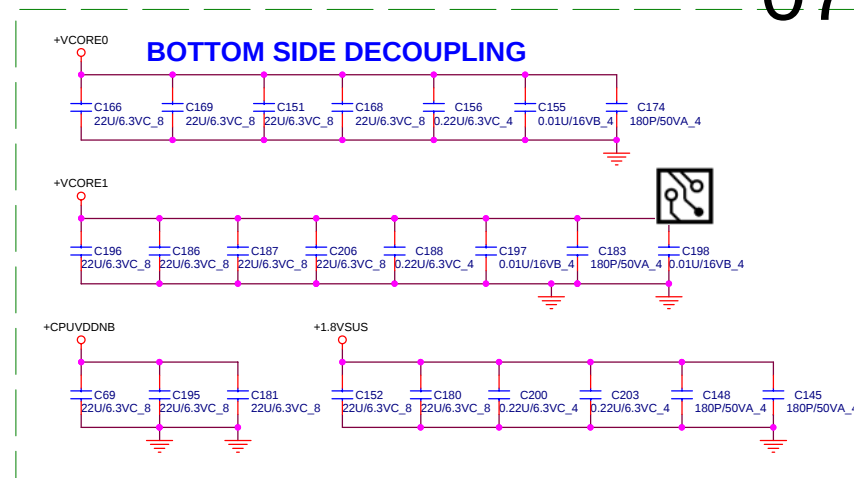
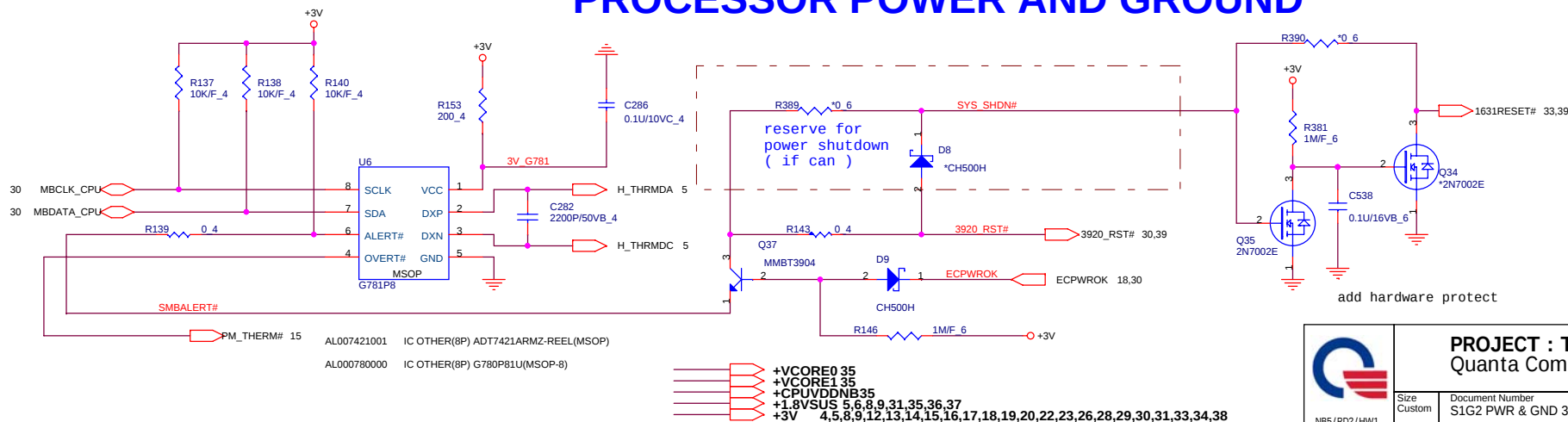




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## PROCESSOR POWER AND GROUND



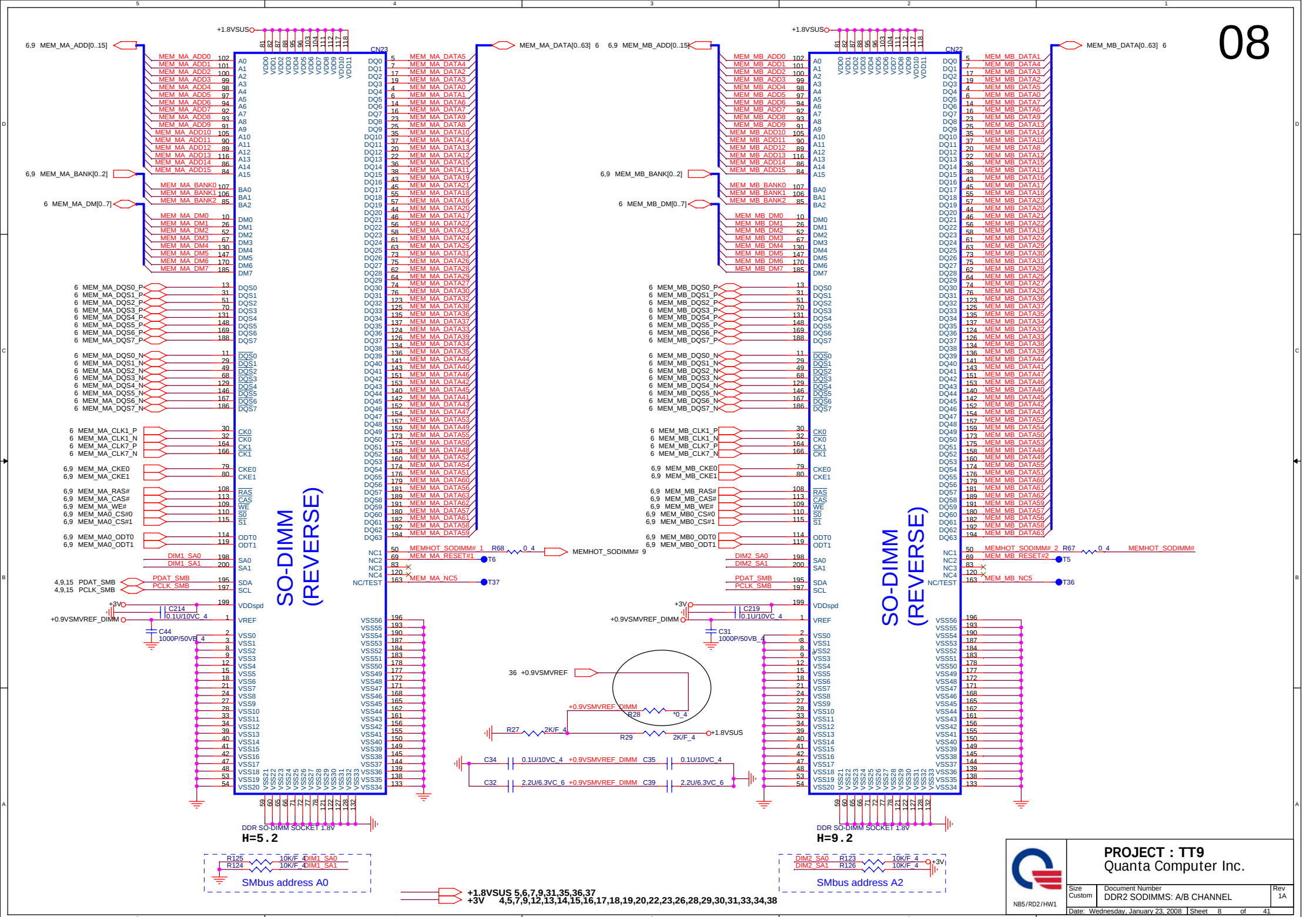
**PROJECT : TT9**  
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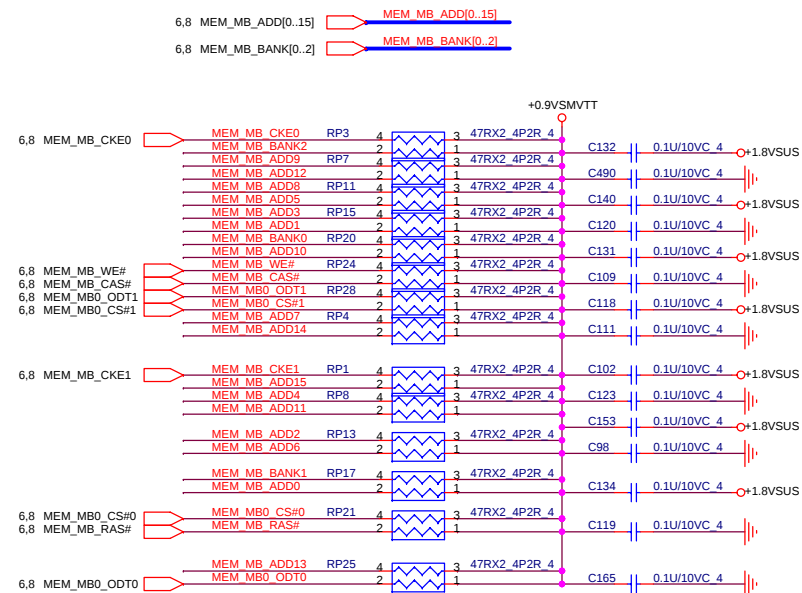
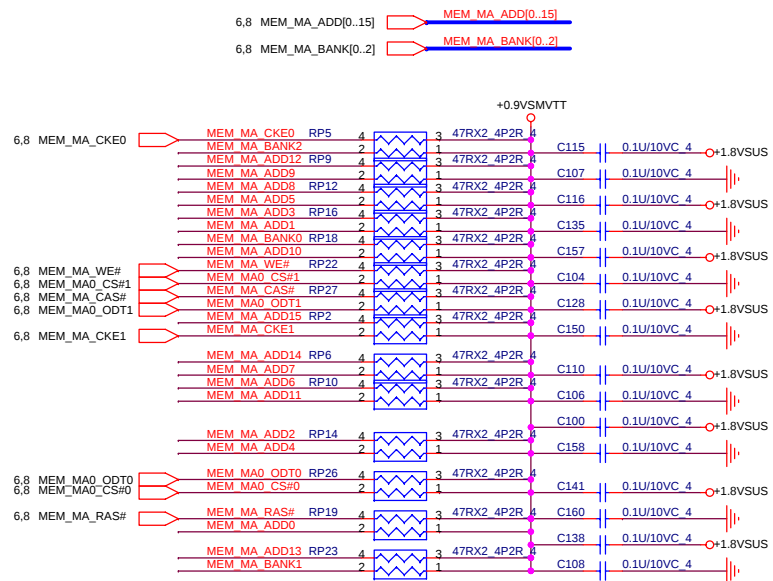
Size Custom Document Number  
S1G2 PWR & GND 3/3

Rev 1A

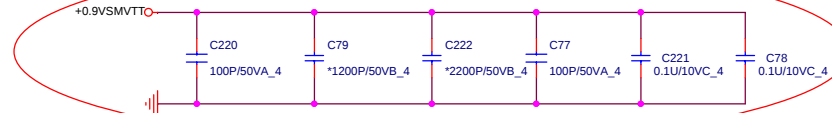
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**+VCORE0 35**  
**+VCORE1 35**  
**+CPUVDDNB35**  
**+1.8VSUS 5,6,8,9,31,35,36,37**  
**+3V 4,5,8,9,12,13,14,15,16,17,18,19,20,22,23,26,28,29,30,31,33,34,38**





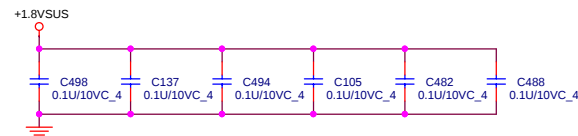
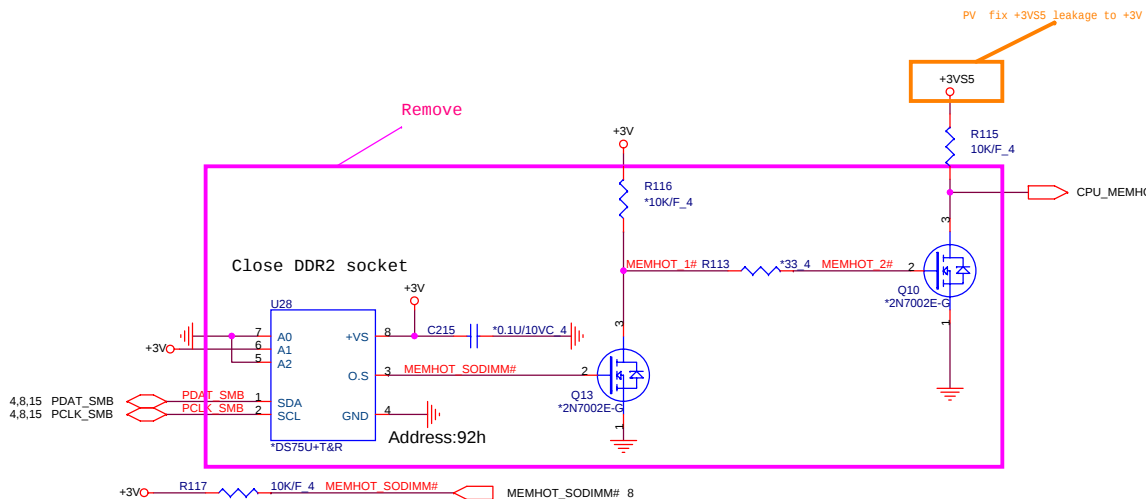
Emi request



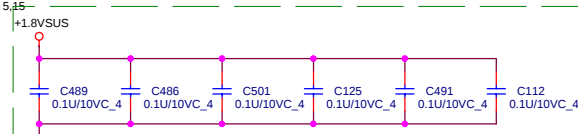
PLACE CLOSE TO PROCESSOR  
WITHIN 1.5 INCH



**PLACE CLOSE TO PROCESSOR  
WITHIN 1.5 INCH**

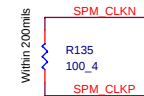
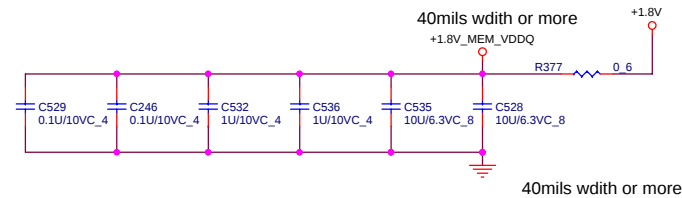


**PLACE CLOSE TO SOCKET( PER EMI/EMC)**

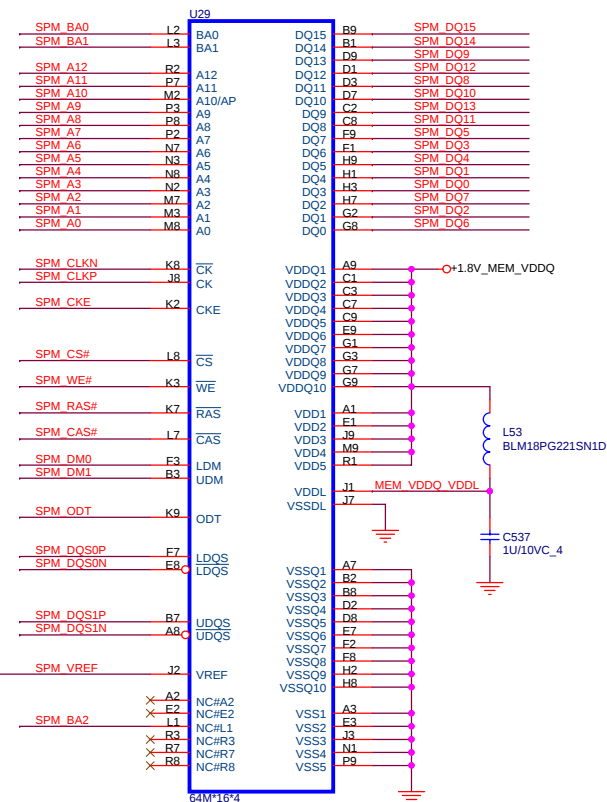


PLACE CLOSE TO SOCKET( PER EMI/EMC)





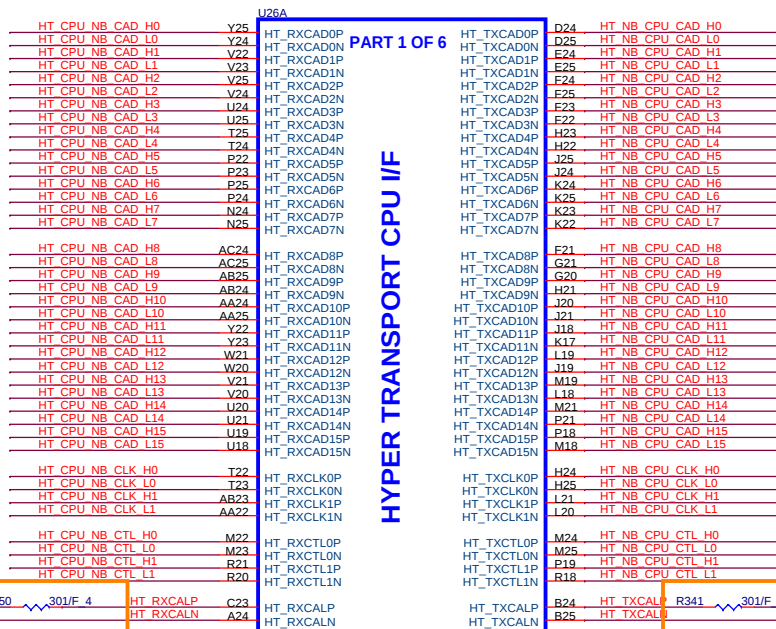
Close to U23



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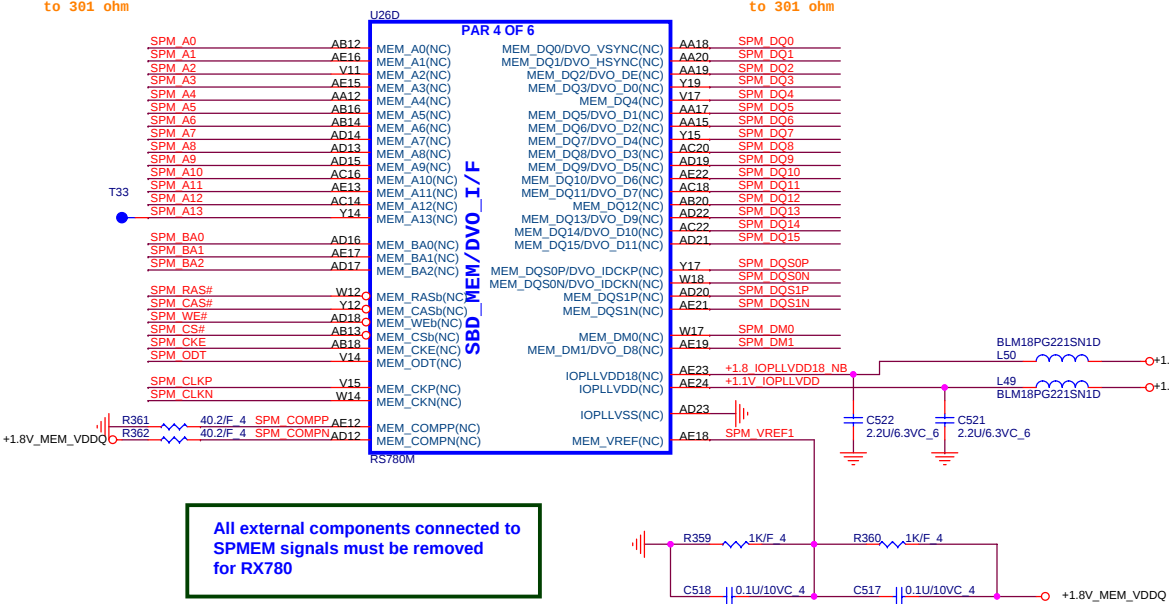
PART 1 OF 6

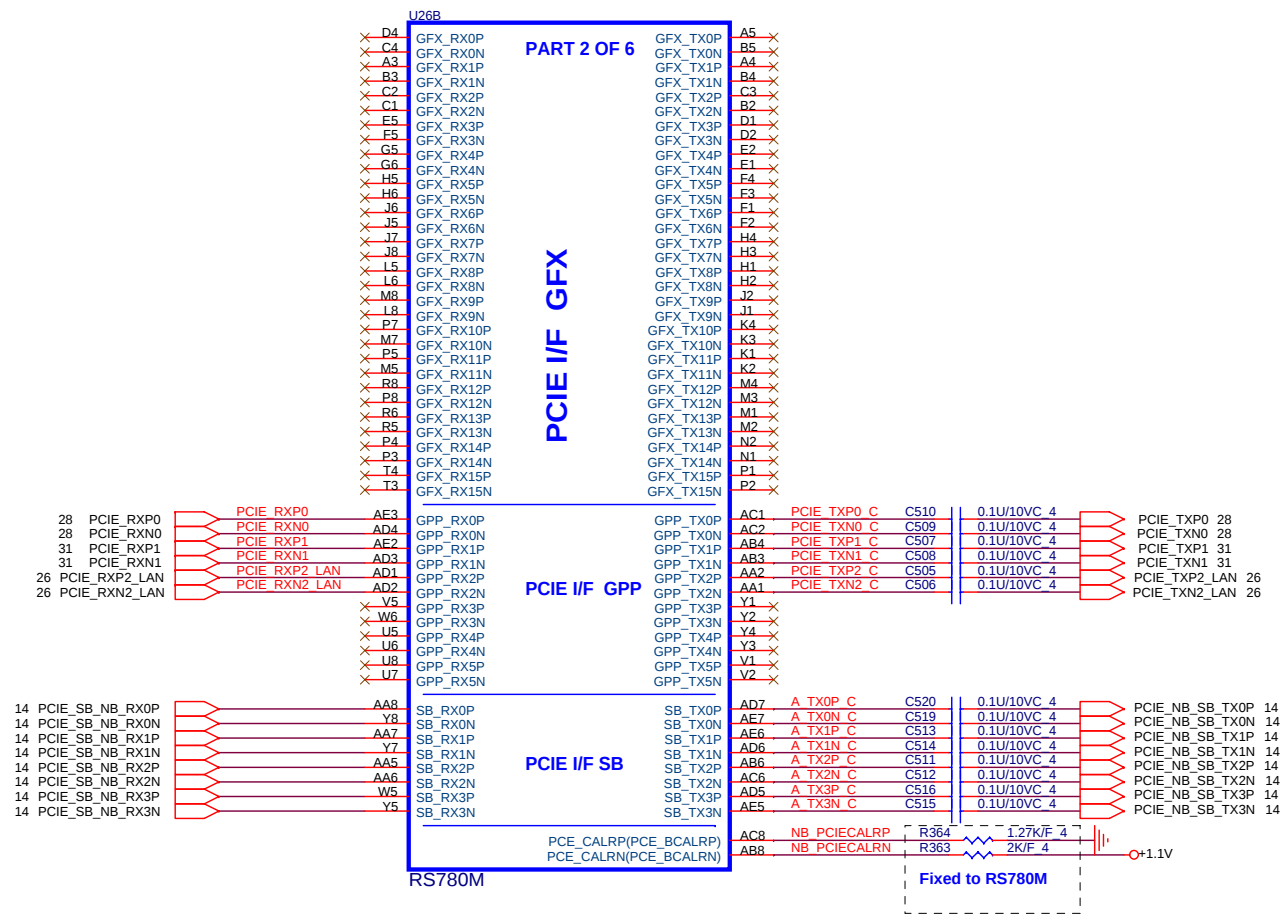
HYPER TRANSPORT CPU I/F



PAR 4 OF 6

SBD\_MEM/DVO\_I/F





- GPP0** EXPRESS CARD (NEW CARD)
- GPP1** Wireless Lan
- GPP2** PCIE LAN(Realtek)

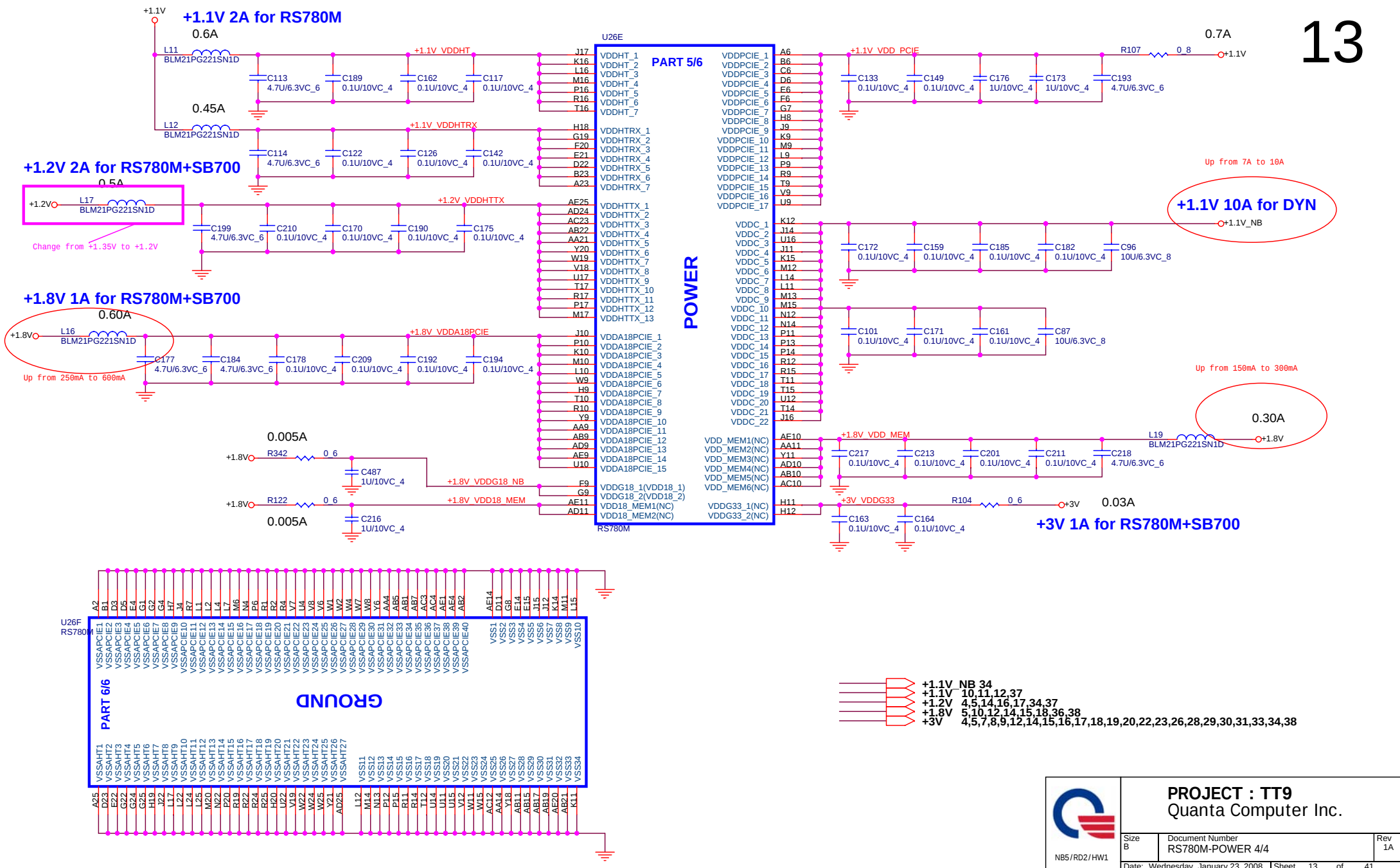
+1.1V 10,12,13,37



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|                                                  |                                        |           |
|--------------------------------------------------|----------------------------------------|-----------|
| Size<br>B                                        | Document Number<br>RS780M-PCIE I/F 2/4 | Rev<br>1A |
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**PROJECT : TT9**  
Quanta Computer Inc.

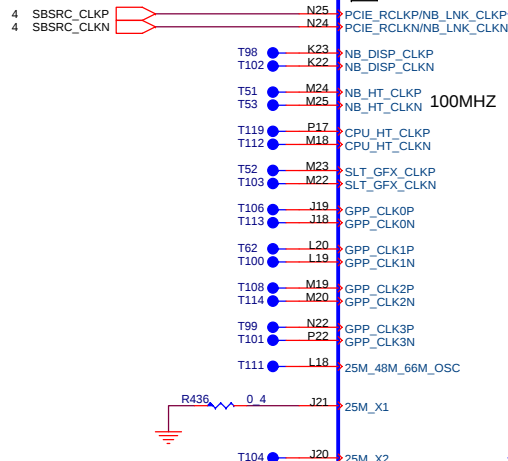
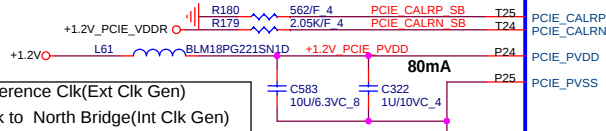
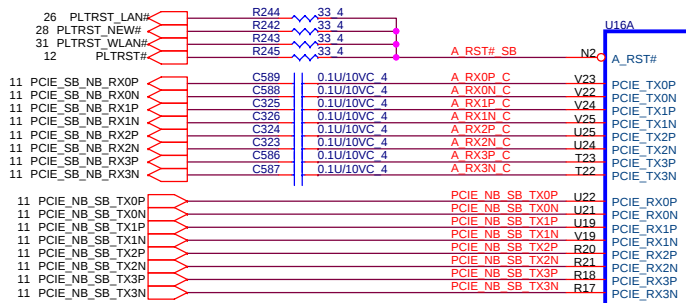
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| Size<br>B                         | Document Number<br>RS780M-POWER 4/4 | Rev<br>1A |
| Date: Wednesday, January 23, 2008 | Sheet 13                            | of 41     |

PLACE THESE  
PCIe AC  
COUPLING CAPS  
CLOSE TO U600

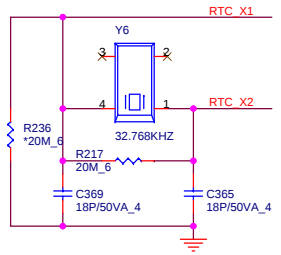


To RS780

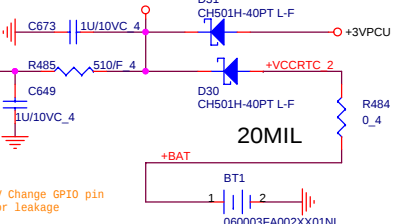
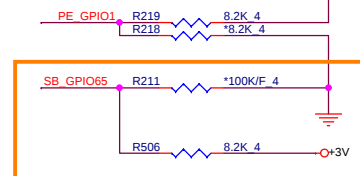
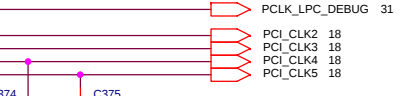
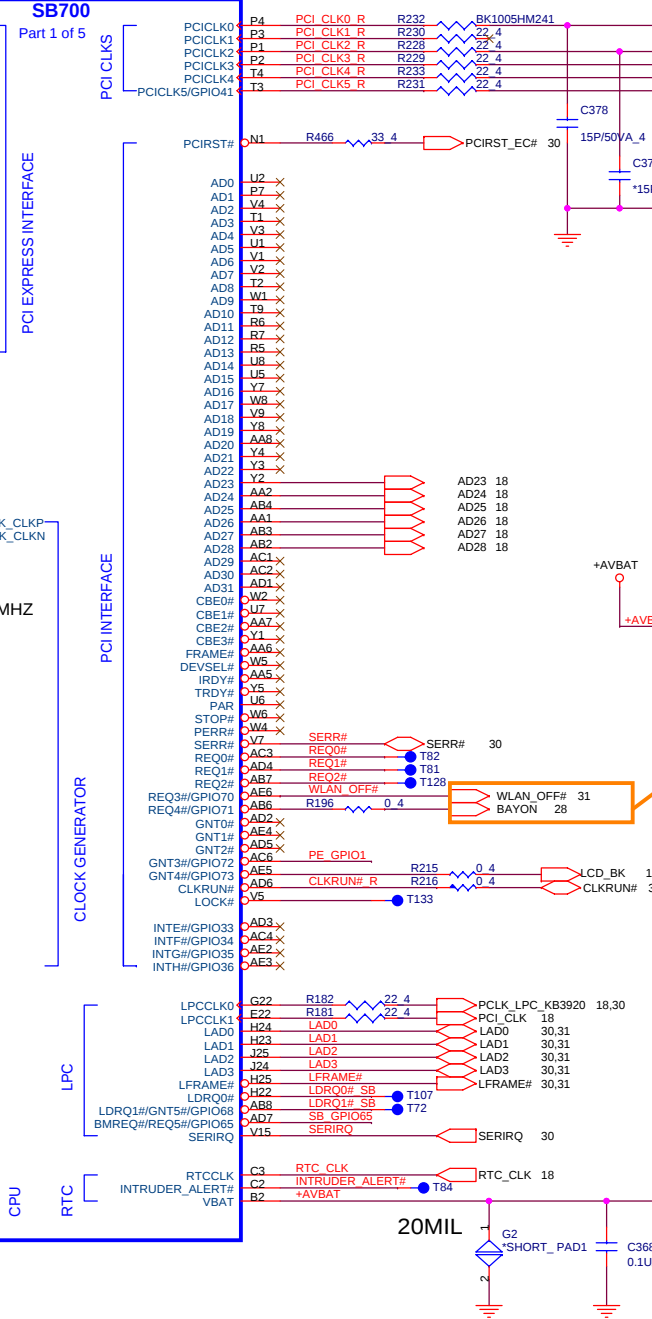
1. PCIe Reference Clk(Ext Clk Gen)
2. A-link Clk to North Bridge(Int Clk Gen)



PV fix +3VS5 leakage to +3V

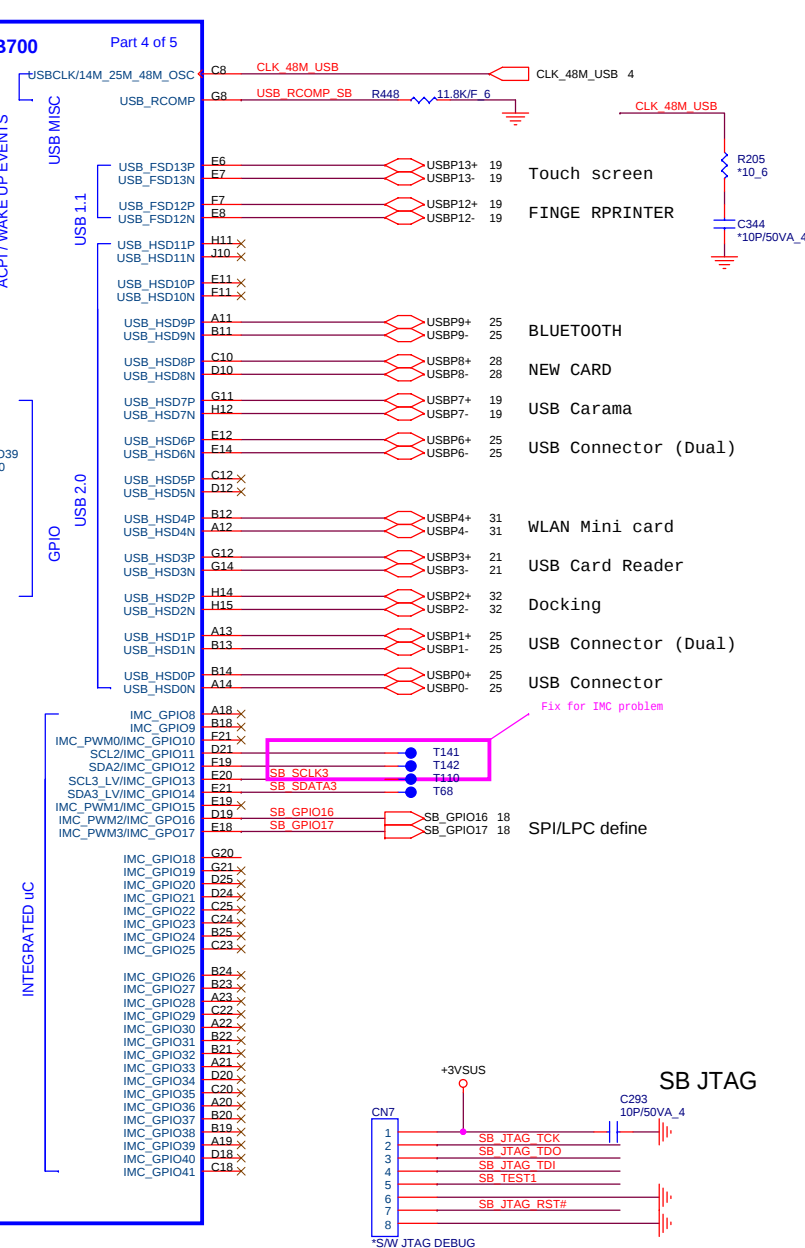
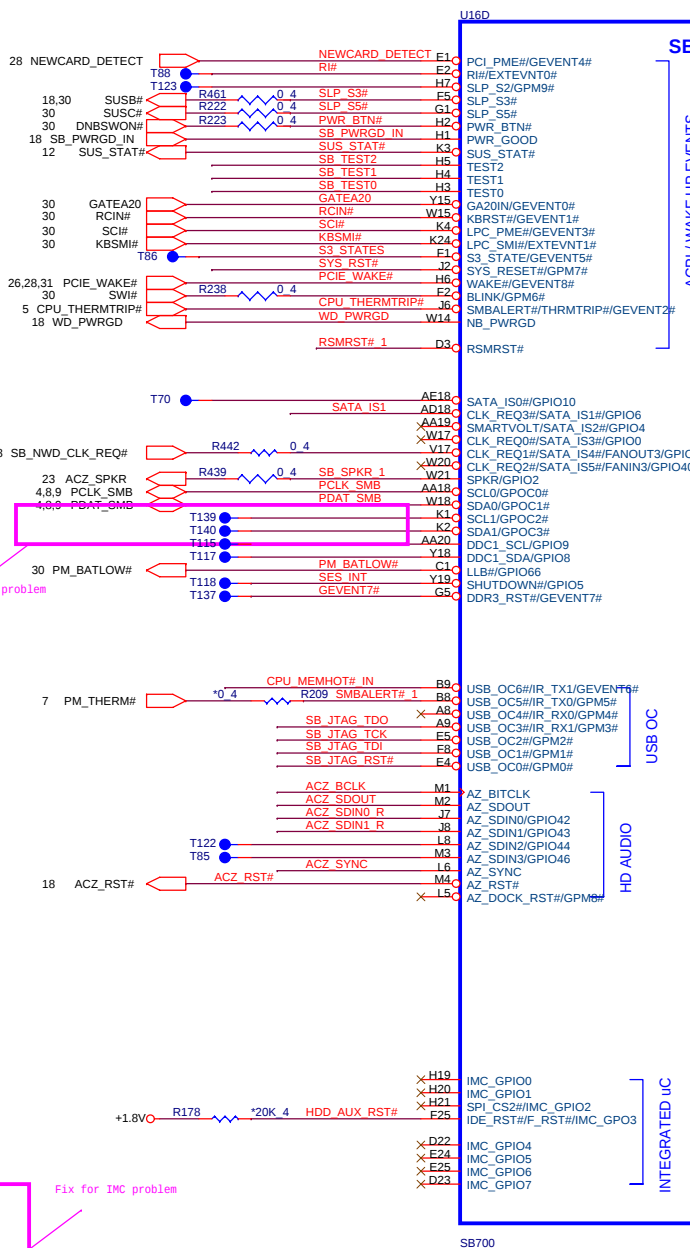
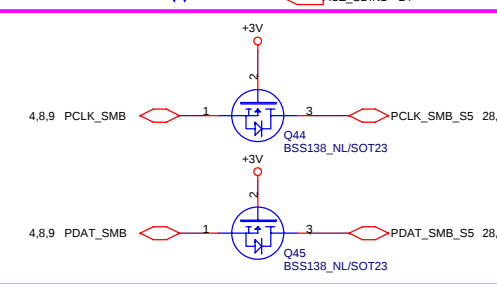
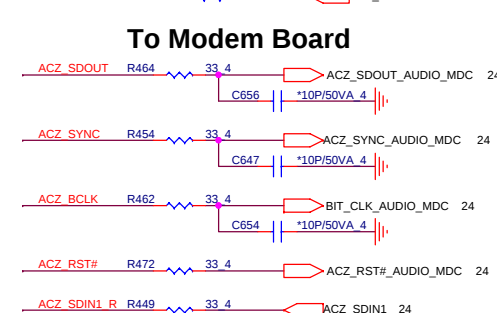
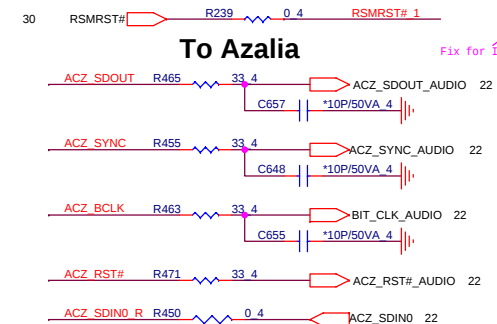
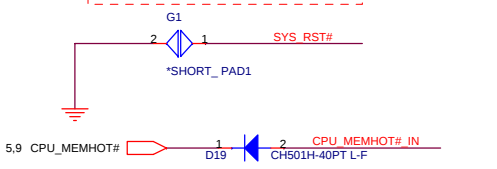
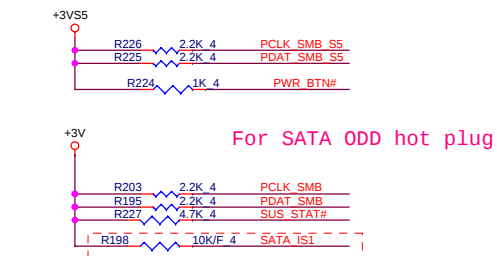
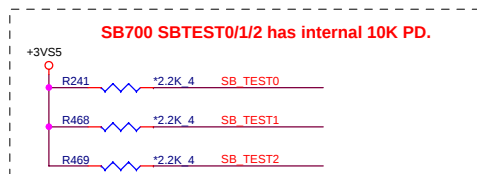


- +1.2V 4,5,13,16,17,34,37
- +1.8V 5,10,12,13,15,18,36,38
- +3V 4,5,7,8,9,12,13,15,16,17,18,19,20,22,23,26,28,29,30,31,33,34,38
- +3VPCU 5,19,25,29,30,32,33,35,37,39



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|                                                  |                            |     |
|--------------------------------------------------|----------------------------|-----|
| Size                                             | Document Number            | Rev |
| Custom                                           | SB700-PCIe/PCI/CPU/LPC 1/4 | 1A  |
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+1.8VSUS 5,6,7,8,9,31,35,36,37  
 +1.8V 5,10,12,13,14,18,36,38  
 +3VSUS 21,24,25,28,31,33,34,35,36,38  
 +3VS5 9,14,16,17,18,28,33,38  
 +3V 4,5,7,8,9,12,13,14,16,17,18,19,20,22,23,26,28,29,30,31,33,34,38



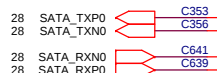
**PROJECT : TT9**  
Quantia Computer Inc.

|                                   |                                         |        |
|-----------------------------------|-----------------------------------------|--------|
| Size Custom                       | Document Number SB700-ACPI/GPIO/USB 2/4 | Rev 1A |
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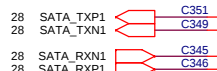


PLACE SATA AC COUPLING  
CAPS CLOSE TO SB600

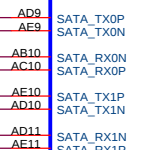
## SATA1



## SATA ODD



U16B

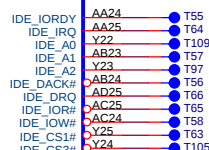
SB700  
Part 2 of 5

SERIAL ATA

SATA PWR

HW MONITOR

Part 2 of 5



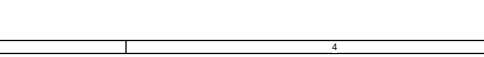
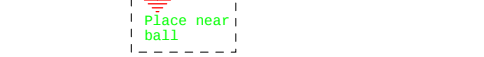
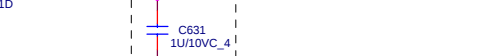
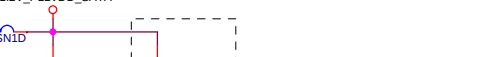
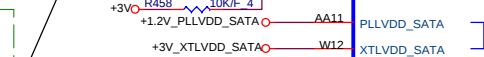
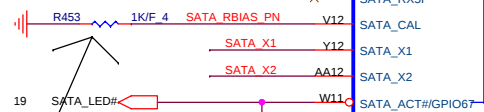
IF THERE IS NO IDE, TEST  
POINTS FOR DEBUG BUS  
IS MANDATORY

SPI ROM

HW MONITOR



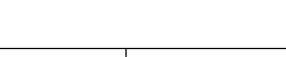
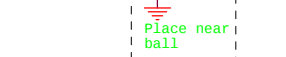
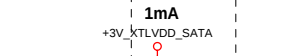
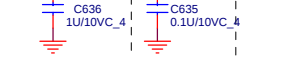
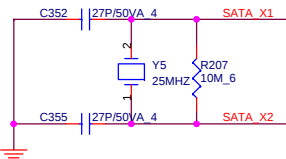
SB700



PLACE SATA\_CAL  
RES VERY CLOSE  
TO BALL OF U600

## NOTE:

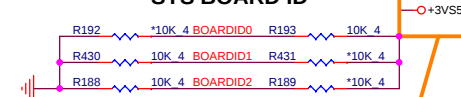
R635 IS 1K 1% FOR 25MHz  
XTAL, 4.99K 1% FOR 100MHz  
INTERNAL CLOCK



| Board ID | VRAM / Clock Gen | Samsung<br>Realtek<br>(0.0.0) | Qimonda<br>ICS<br>(1.0.0) | Hynix<br>Silego<br>(0.1.0) |
|----------|------------------|-------------------------------|---------------------------|----------------------------|
| BOARDID0 |                  | R192<br>Stuff                 | R193<br>Stuff             | R192<br>Stuff              |
| BOARDID1 |                  | R430<br>Stuff                 | R430<br>Stuff             | R431<br>Stuff              |
| BOARDID2 |                  | R188<br>Stuff                 | R188<br>Stuff             | R188<br>Stuff              |

## DB2 MODIFY

## SYS BOARD ID



PV SB internal pull  
Hi to 3V55, modified  
to samepower rail  
with SB

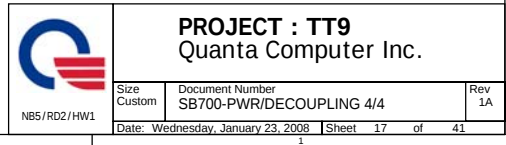
PV fix +3V55 leakage to +3V

+1.2V 4,5,13,14,17,34,37  
+3V 4,5,7,8,9,12,13,14,15,17,18,19,20,22,23,26,28,29,30,31,33,34,38

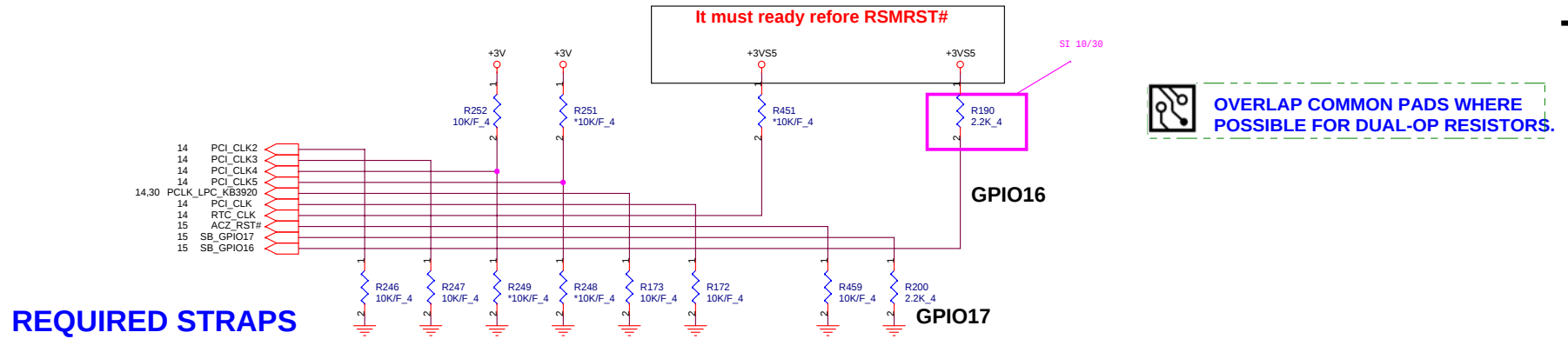
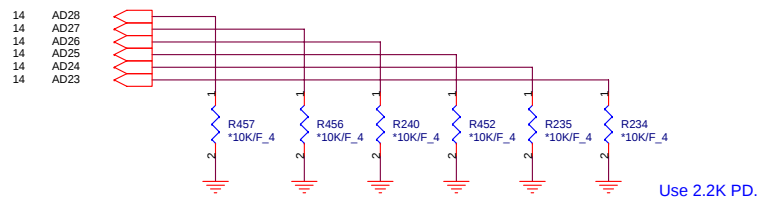


PROJECT : TT9  
Quanta Computer Inc.

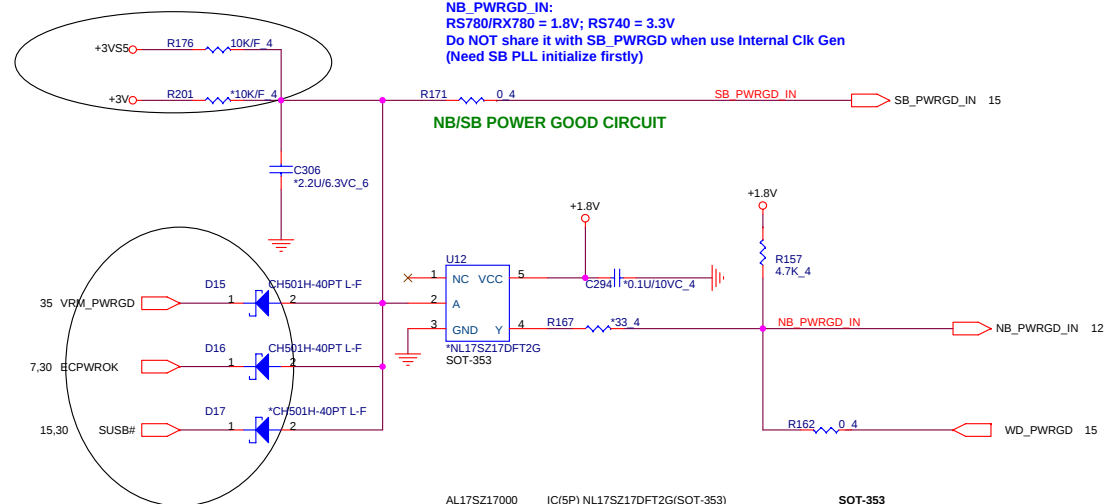
| Size<br>Custom | Document Number<br>SB700-SATA/IDE/HWM/SPI 3/4 | Rev<br>1A      |
|----------------|-----------------------------------------------|----------------|
| NB5/RD2/HW1    | Date: Wednesday, January 23, 2008             | Sheet 16 of 41 |



NOTE: SB700 HAS INTERNAL 15K PULL UP RESISTOR FOR RTC\_CLK

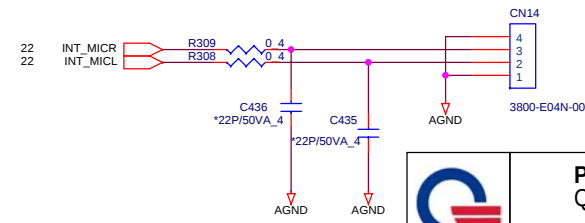
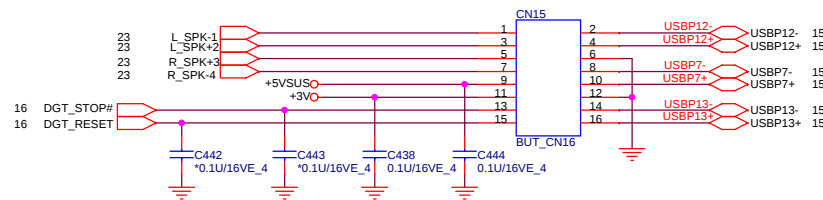
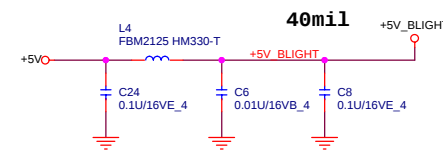
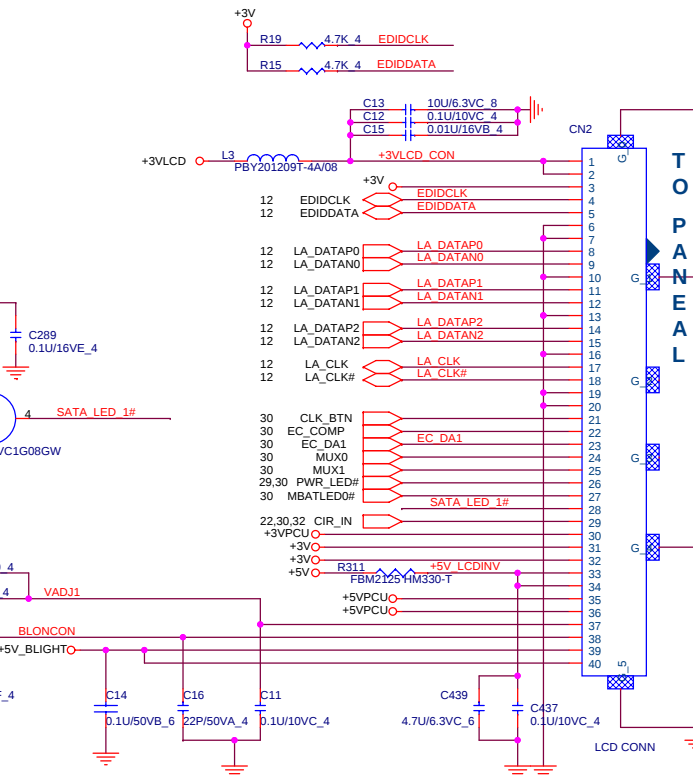
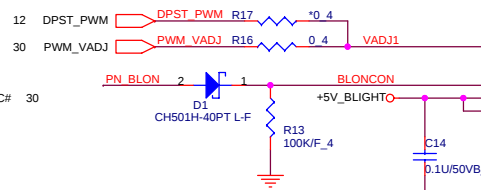
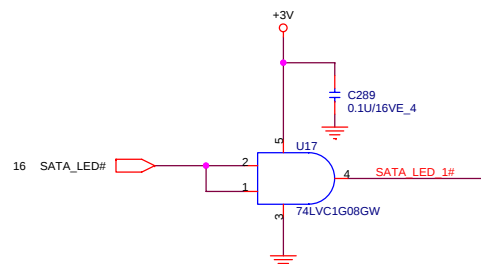
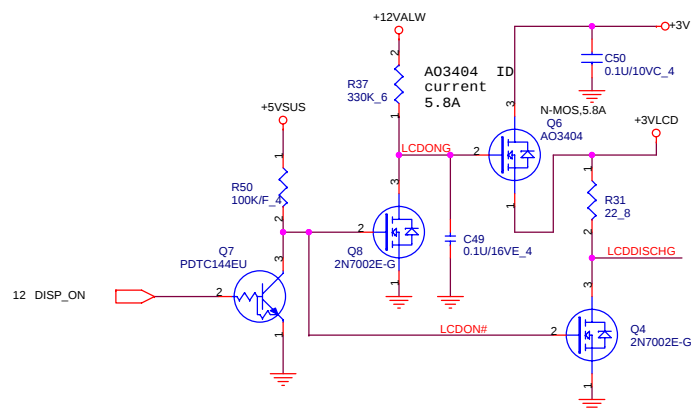
**DEBUG STRAPS** SB700 HAS 15K INTERNAL PU FOR PCI\_AD[28:23]

|                  | PCI_AD28                        | PCI_AD27                  | PCI_AD26                    | PCI_AD25                  | PCI_AD24                              | PCI_AD23 |
|------------------|---------------------------------|---------------------------|-----------------------------|---------------------------|---------------------------------------|----------|
| <b>PULL HIGH</b> | USE<br>LONG<br>RESET<br>DEFAULT | USE PCI<br>PLL<br>DEFAULT | USE ACPI<br>BCLK<br>DEFAULT | USE IDE<br>PLL<br>DEFAULT | USE DEFAULT<br>PCIE STRAPS<br>DEFAULT | RESERVED |
| <b>PULL LOW</b>  | USE<br>SHORT<br>RESET           | BYPASS<br>PCI PLL         | BYPASS<br>ACPI<br>BCLK      | BYPASS IDE<br>PLL         | USE EEPROM<br>PCIE STRAPS             |          |



AL17SZ17000 IC(5P) NL17SZ17DFT2G(SOT-353) SOT-353  
ALUC1G17000 IC OTHER(5P) SN74AUC1G17DBVR(SOT23-5) SOT23-5

+1.8V 5,10,12,13,14,15,36,38  
 +3V 4,5,7,8,9,12,13,14,15,16,17,19,20,22,23,26,28,29,30,31,33,34,36  
 +3V5 9,14,15,16,17,28,33,38

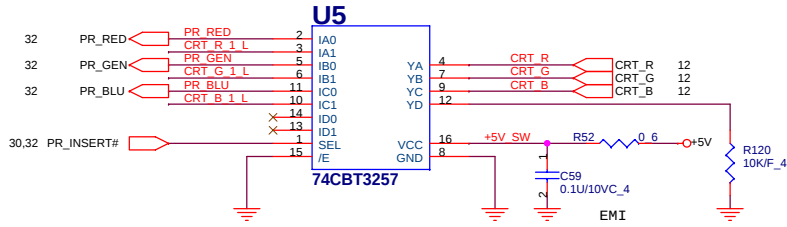


+3VPCU 5,14,25,29,30,32,33,35,37,39  
 +3V 4,5,7,8,9,12,13,14,15,16,17,18,20,22,23,26,28,29,30,31,33,34,38  
 +3VSUS 15,21,24,25,29,31,33,34,35,36,38  
 +5V 17,20,22,23,28,31,32,33,37,38,39  
 +12VALW 28,31,33,38

Speaker  
 +5VSUS --> Camera  
 +3V --> FP/Digitizer  
 Digitizer control signal

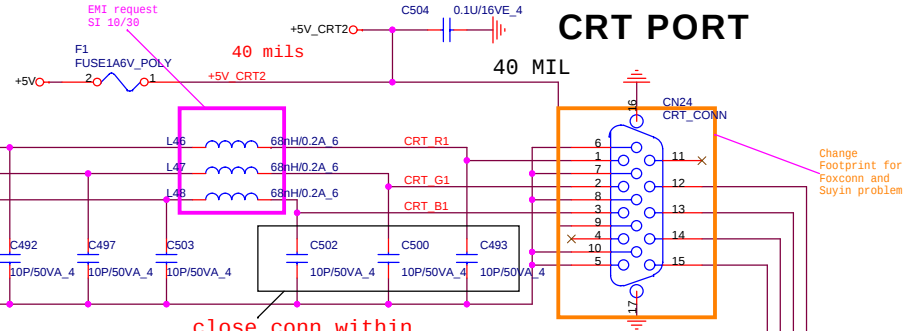
Finger Printer  
 Camera  
 Digitizer

# CRT SWITCH

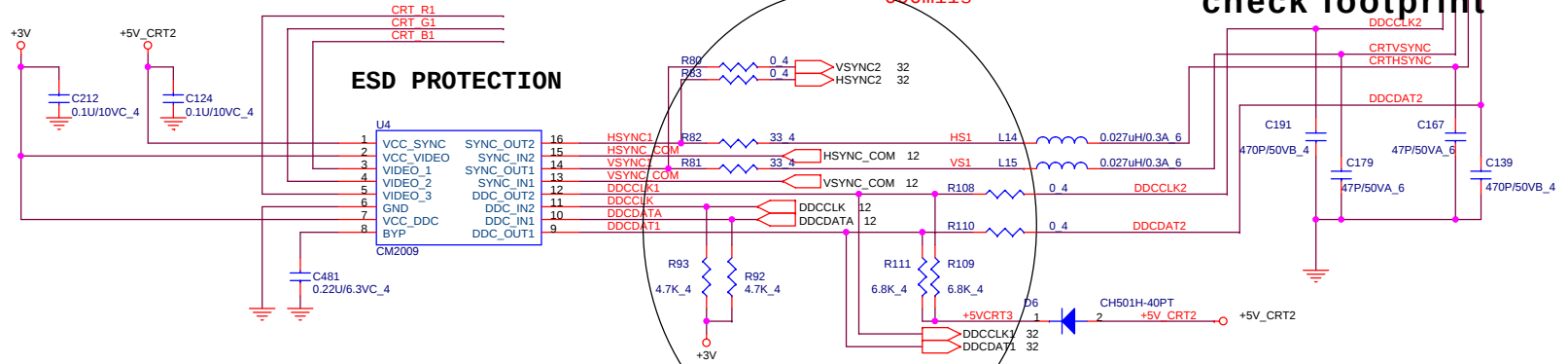


| inputs |     | function   |
|--------|-----|------------|
| /E     | SET |            |
| L      | L   | Y - port 0 |
| L      | H   | Y - port 1 |
| H      | X   | Disconnect |

# CRT PORT

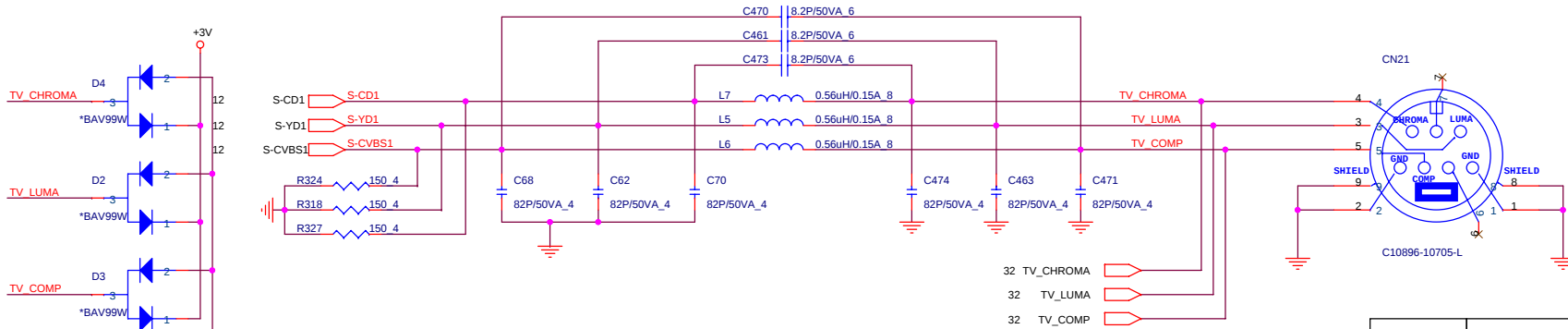


# ESD PROTECTION



close conn within 600mils

check footprint

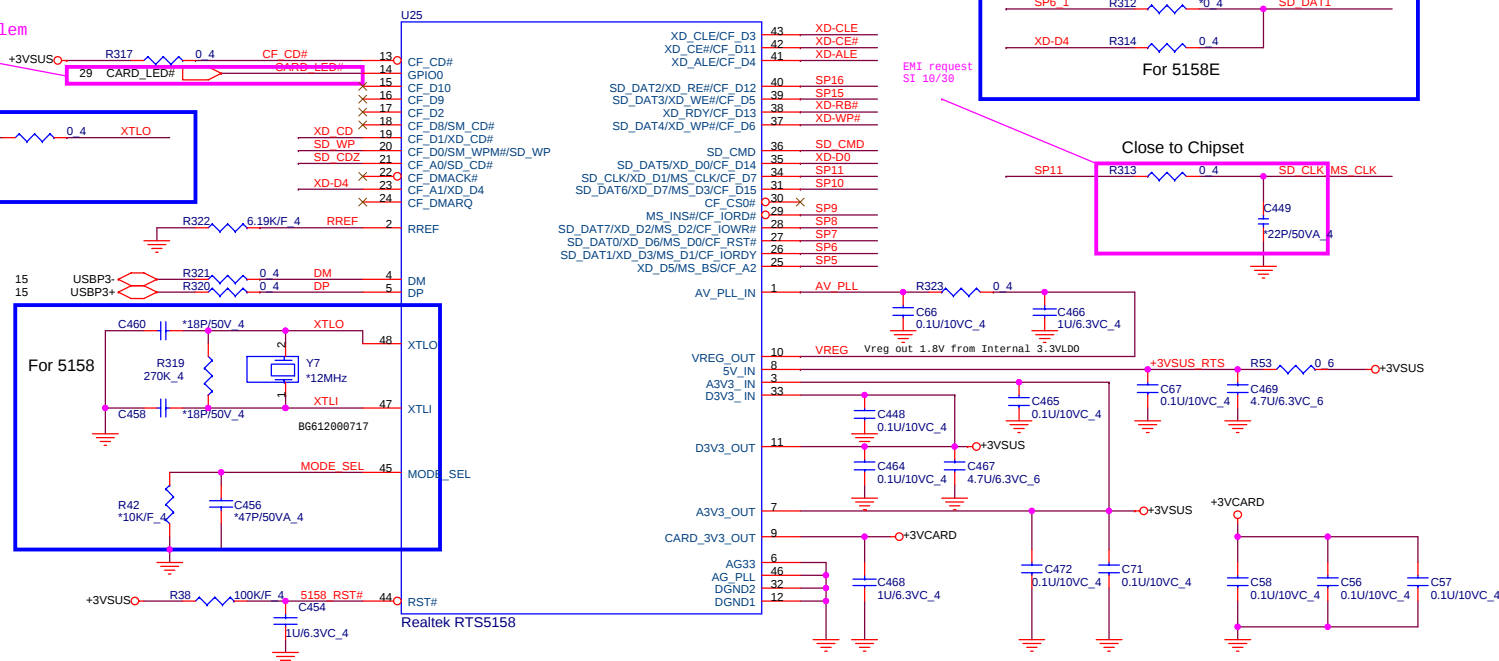
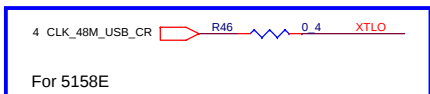


**PROJECT : TT9**  
Quanta Computer Inc.

|                                                  |                            |        |
|--------------------------------------------------|----------------------------|--------|
| Size Custom                                      | Document Number CRT_TV_OUT | Rev 1A |
| Date: Wednesday, January 23, 2008 Sheet 20 of 41 |                            |        |

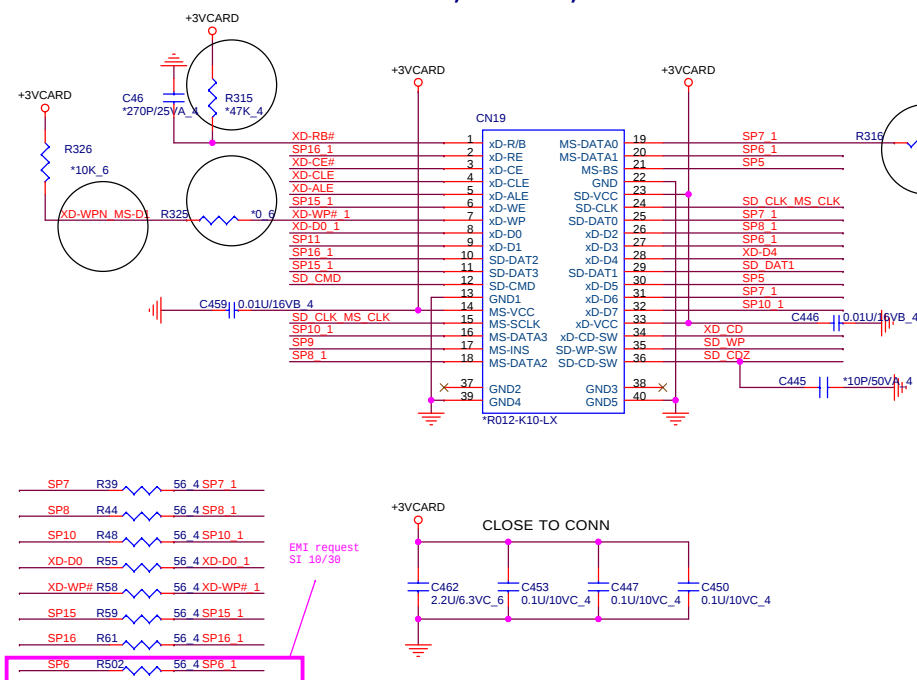
4,5,7,8,9,12,13,14,15,16,17,18,19,22,23,26,28,29,30,31,33,34,38  
17,19,22,23,28,31,32,33,37,38,39

## Fix card reader led problem

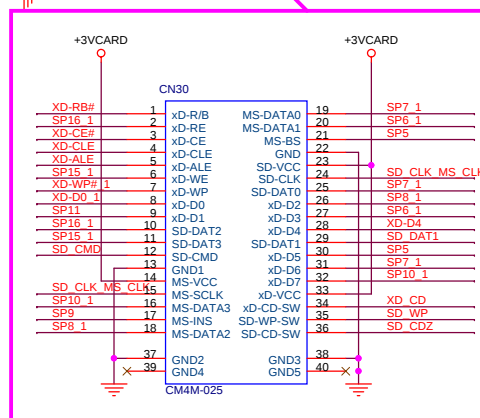


|      | SD/MMC  | MS      | XD      |
|------|---------|---------|---------|
| SP0  |         |         |         |
| SP1  |         |         | XD CD#  |
| SP2  | SD WP   |         |         |
| SP3  | SD CD#  |         |         |
| SP4  |         |         | XD D4   |
| SP5  |         | MS BS   | XD D5   |
| SP6  |         | MS D1   | XD D3   |
| SP7  | SD DAT0 | MS D0   | XD D6   |
| SP8  | SD DAT7 | MS D2   | XD D2   |
| SP9  |         | MS INS# |         |
| SP10 | SD DAT6 | MS D3   | XD D7   |
| SP11 | SD CLK  | MS SCLK |         |
| SP12 | SD DAT5 |         | XD D0   |
| SP13 | SD DAT4 |         | XD WP#  |
| SP14 |         |         | XD R/B# |
| SP15 | SD DAT3 |         | XD WE#  |
| SP16 | SD DAT2 |         | XD RE#  |
| SP17 |         |         | XD ALE  |
| SP18 |         |         | XD CE#  |
| SP19 |         |         | XD CLE  |

**4 IN1 CARD READER**  
**XD, MMC/SD, MS/MSP**



### Add Connector for joint



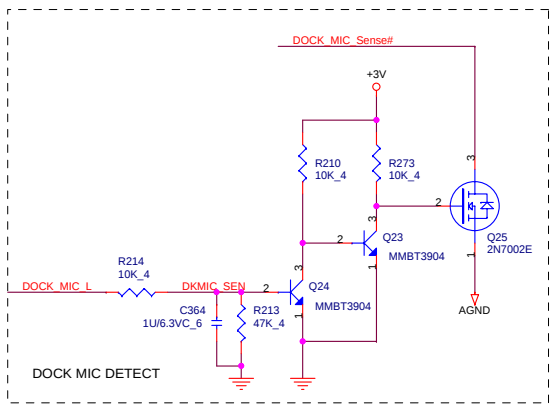
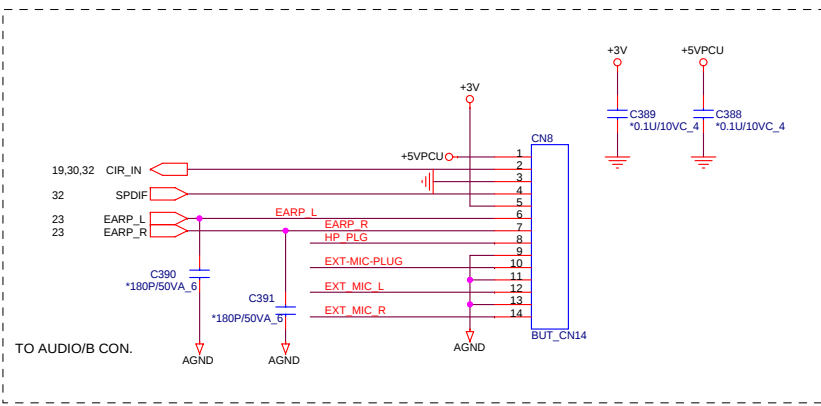
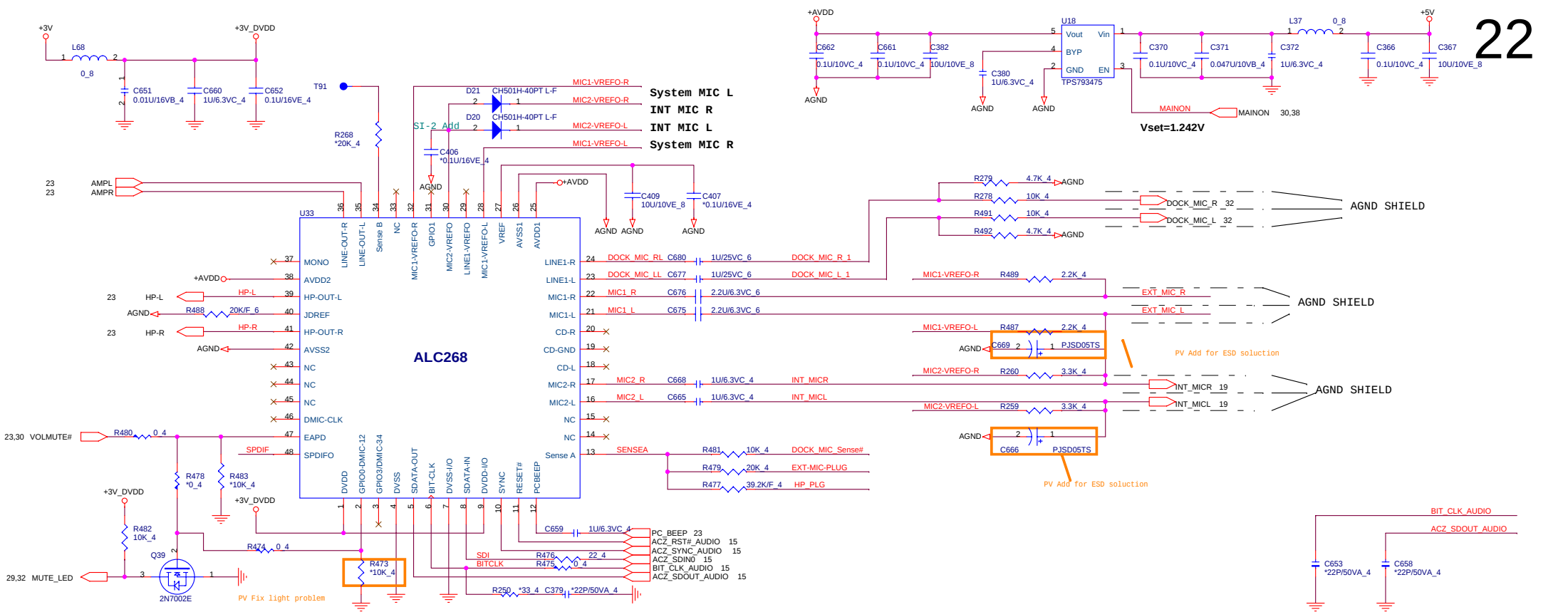
Remove Acce from HP information

➤ +3VSUS 15,24,25,29,31,33,34,35,36,38



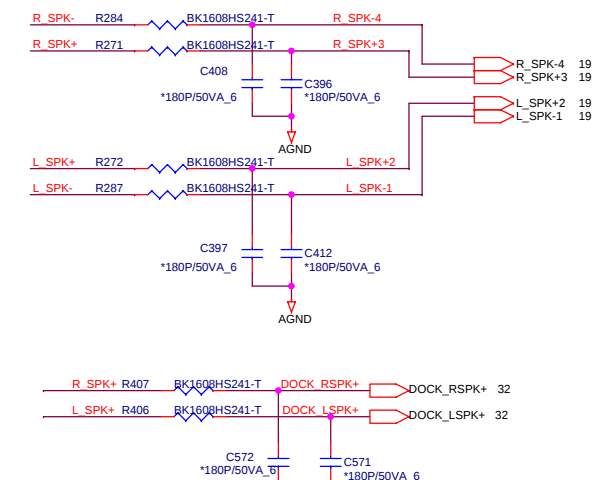
**PROJECT : TT9**  
Quanta Computer Inc.

|                                   |                                                  |                |
|-----------------------------------|--------------------------------------------------|----------------|
| Size<br>Custom                    | Document Number<br>RT5158 CARD READER CONTROLLER | Rev<br>1A      |
| Date: Wednesday, January 23, 2008 |                                                  | Sheet 21 of 41 |

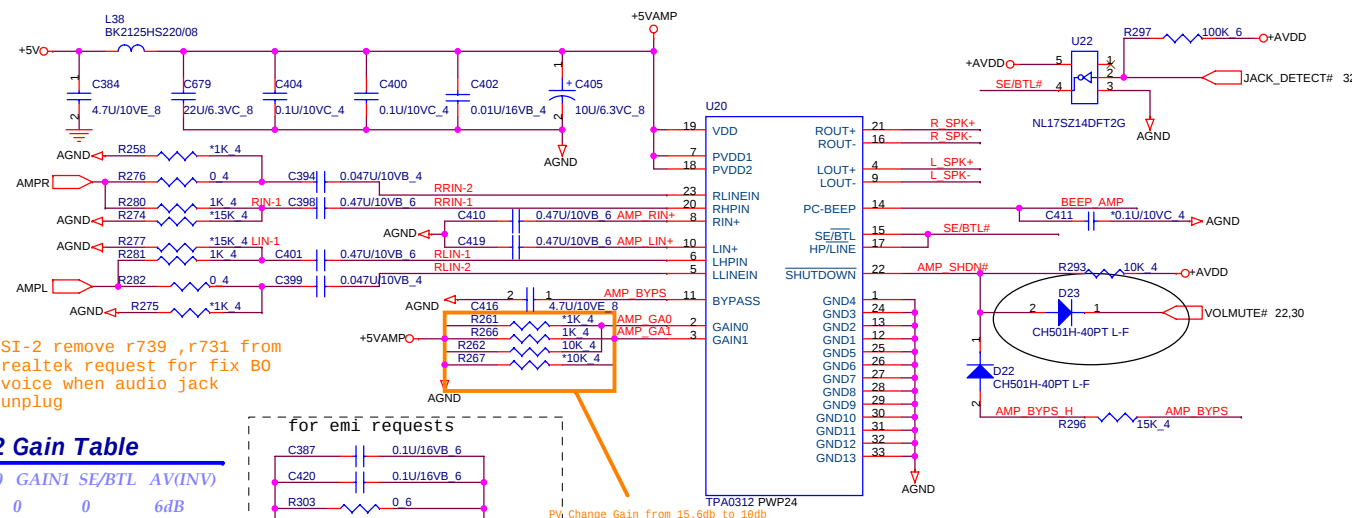


- +3V 4,5,7,8,9,12,13,14,15,16,17,18,19,20,23,26,28,29,30,31,33,34,38
- +5V 17,19,20,23,28,31,32,33,37,38,39
- +AVDD 23
- +5VPCU 19,28,29,30,33,34,35,36,37

## INT. SPEAKER



## AUDIO AMPLIFIER

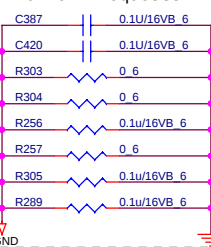


SI-2 remove r739, r731 from  
realtek request for fix B0  
voice when audio jack  
unplug

## 0312 Gain Table

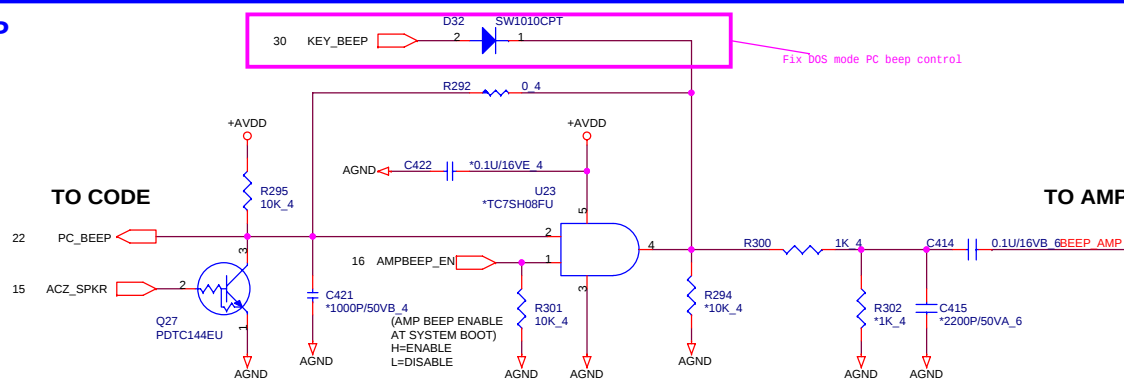
| GAIN0 | GAIN1 | SE/BTL | AV(INV) |
|-------|-------|--------|---------|
| 0     | 0     | 0      | 6dB     |
| 0     | 1     | 0      | 10dB    |
| 1     | 0     | 0      | 15.6dB  |
| 1     | 1     | 0      | 21.6dB  |
| x     | x     | 1      | 4.1dB   |

for emi requests



Pv Change Gain from 15.6db to 10db

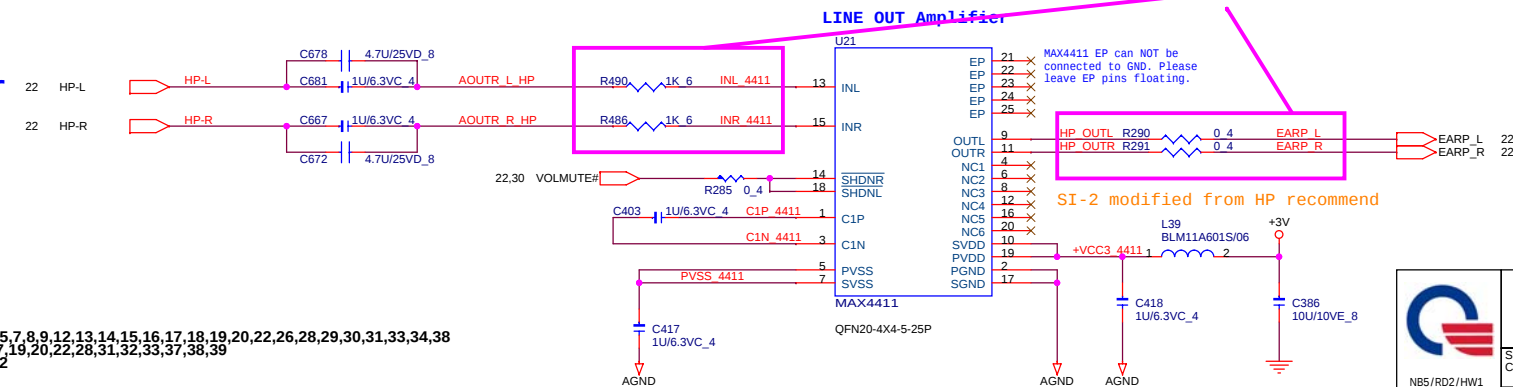
## PCSPK BEEP



TO CODE

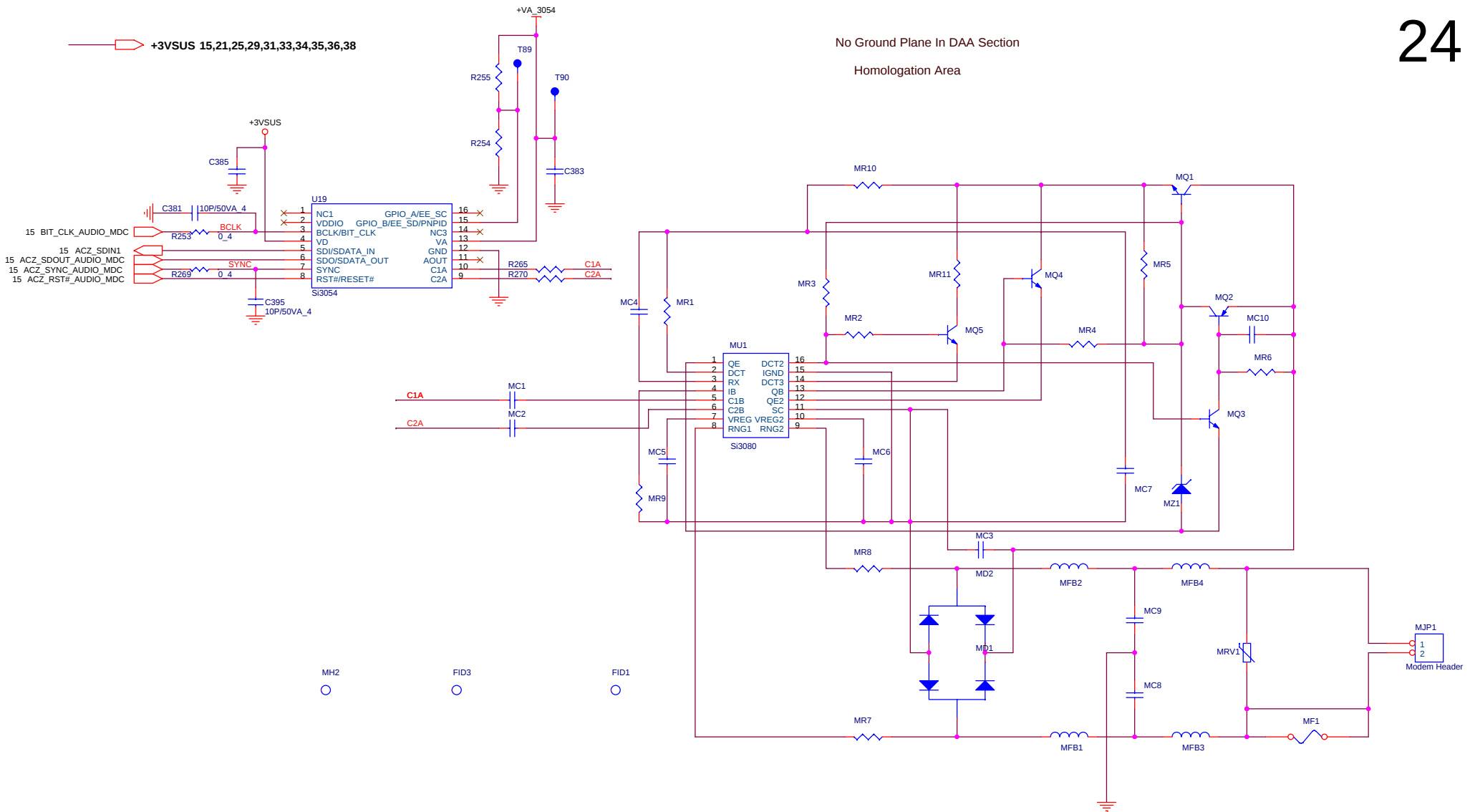
TO AMP

## LINE OUT Amplifier



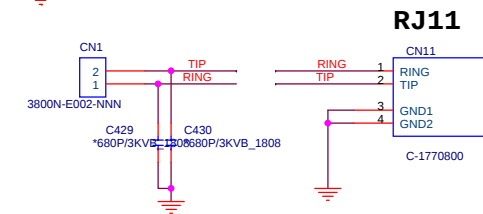
PROJECT : TT9  
Quanta Computer Inc.

|                                   |                                  |        |
|-----------------------------------|----------------------------------|--------|
| Size Custom                       | Document Number JACK/AMP_TAP0312 | Rev 1A |
| Date: Wednesday, January 23, 2008 | Sheet 23                         | of 41  |

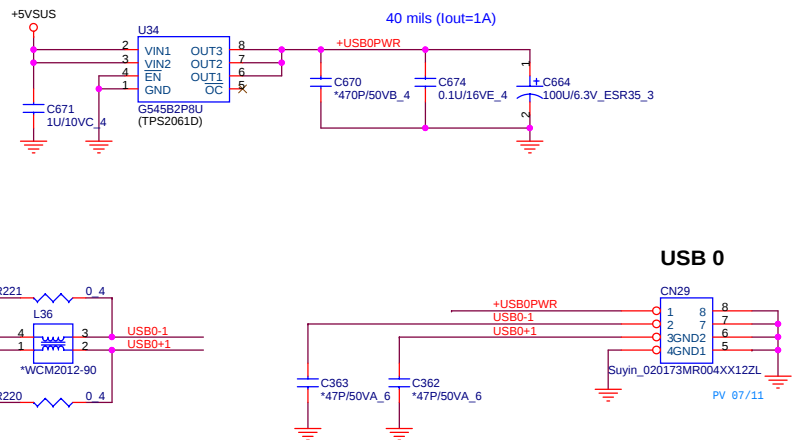


DESIGN SUBJECT TO CHANGE

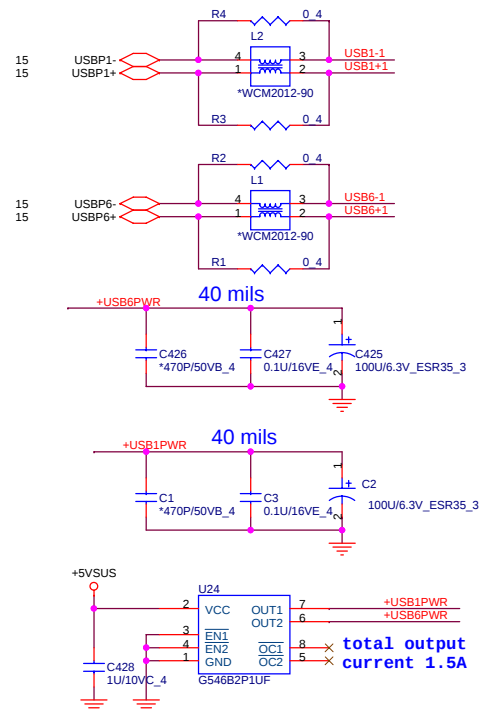
**SILICON LABORATORIES CONFIDENTIAL**



## USBX1

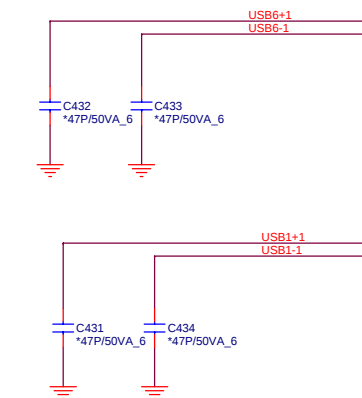
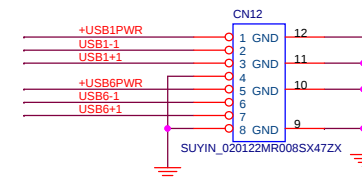


## USBX2

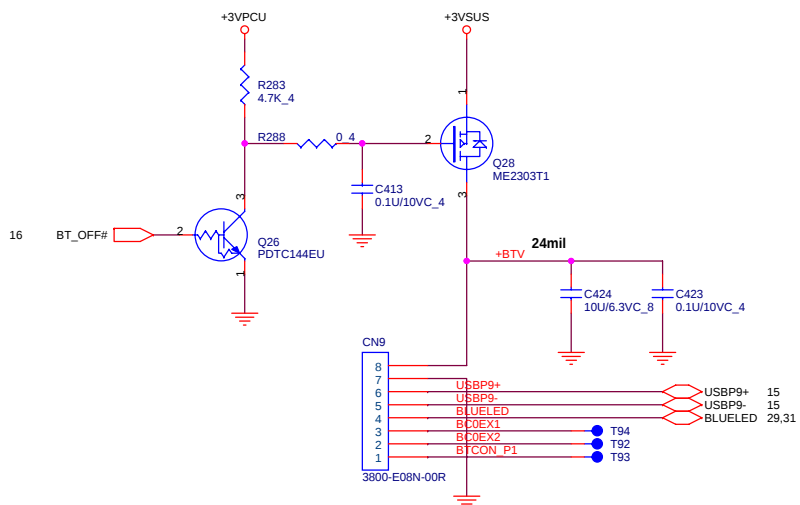


25

## USB 1& 6



## BLUETOOTH



### Select TPM function from HP information

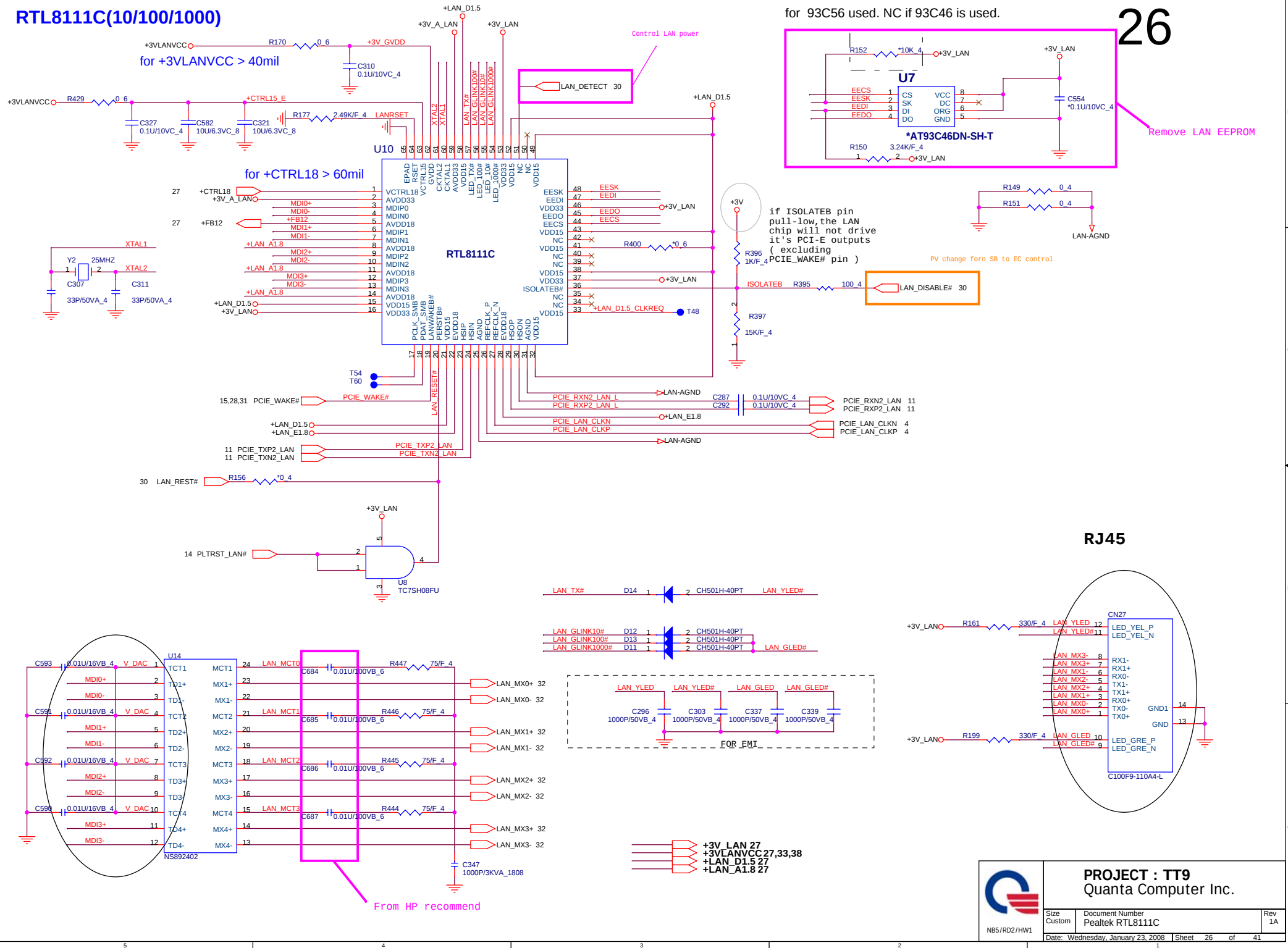

 +3VPCU 5,14,19,29,30,32,33,35,37,39  
 +3VSUS 15,21,24,29,31,33,34,35,36,38  
 +5VSUS 19,30,32,33,38



**PROJECT : TT9**  
Quanta Computer Inc.

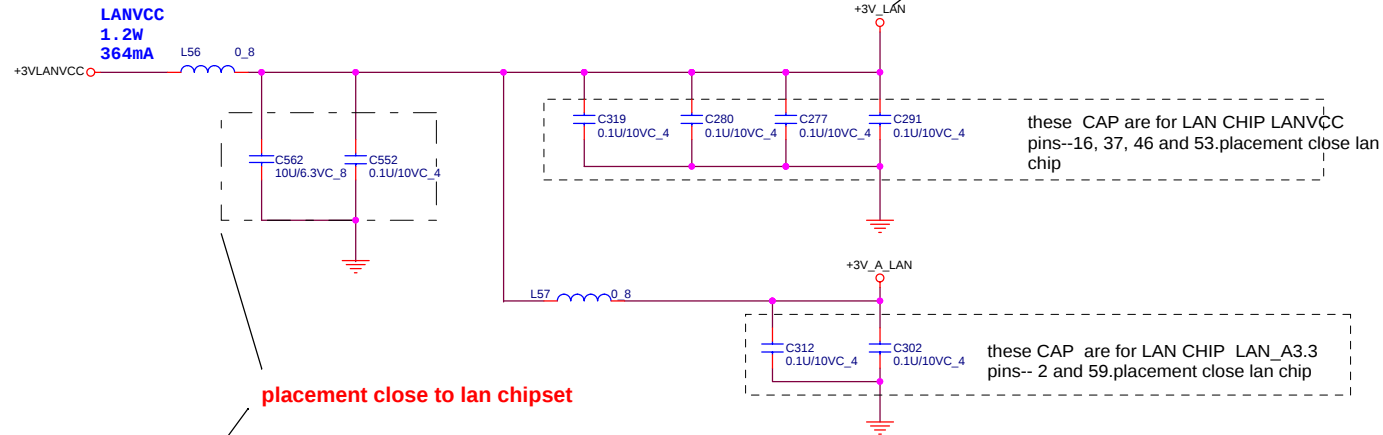
|                                   |                                     |                |
|-----------------------------------|-------------------------------------|----------------|
| Size<br>Custom                    | Document Number<br>Blue Tooth/USBX3 | Rev<br>1A      |
| Date: Wednesday, January 23, 2008 |                                     | Sheet 25 of 41 |

## RTL8111C(10/100/1000)

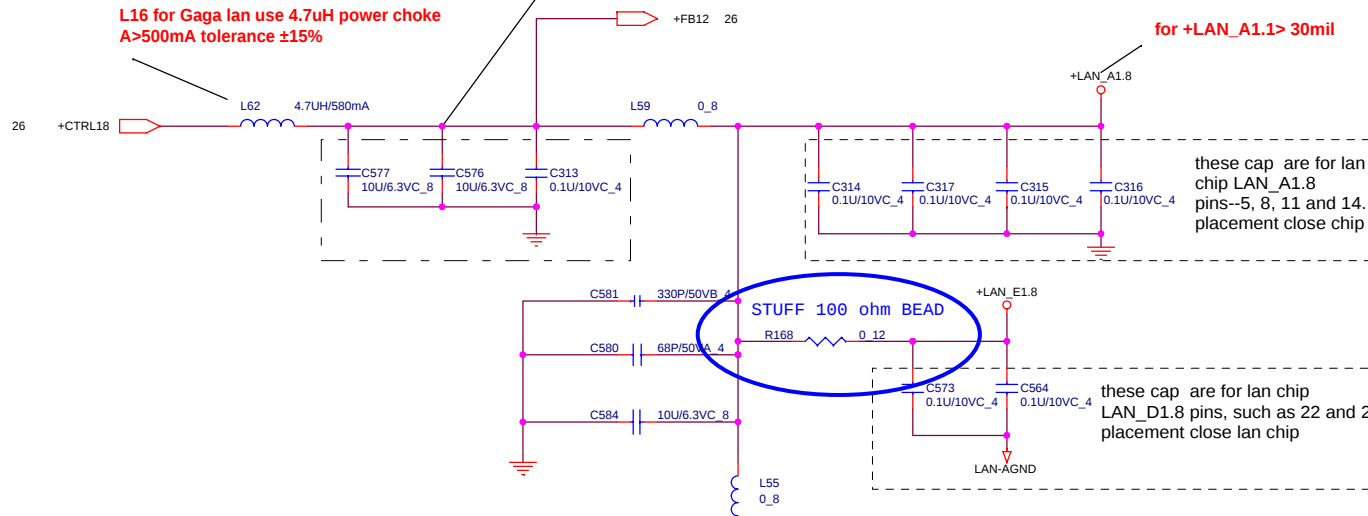


## RJ45

**PROJECT : TT9**  
Quanta Computer Inc.

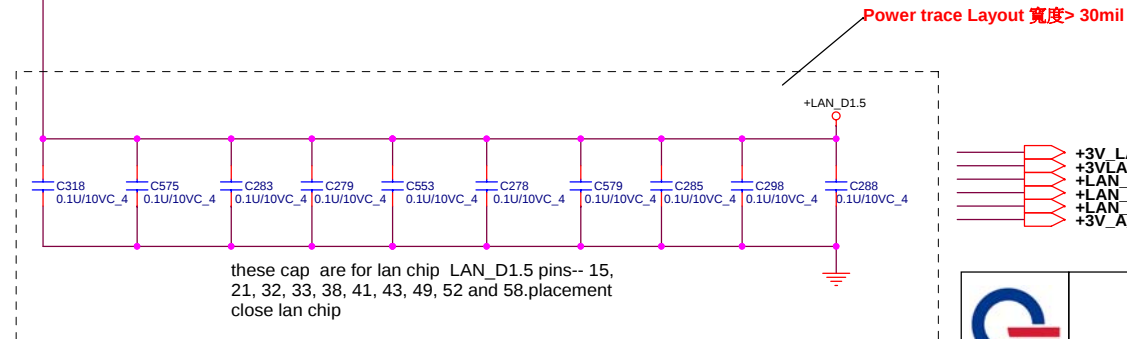


L16 for Gaga lan use 4.7uH power choke  
A>500mA tolerance ±15%



RTL8111C Power domain chart

|          |      |
|----------|------|
| LANVCC   | 3.3V |
| LAN_D1.8 | 1.2V |
| LAN_A1.8 | 1.2V |
| LAN_D1.5 | 1.2V |

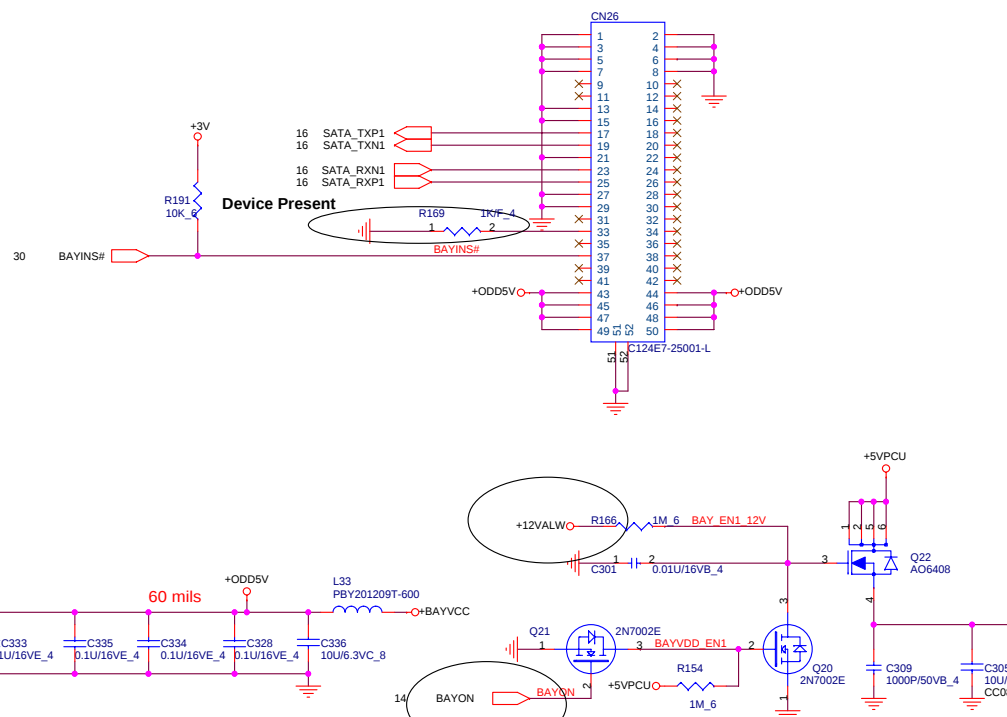


+3V\_LAN 26  
+3VLANVCC26,33,38  
+LAN\_D1.5 26  
+LAN\_A1.8 26  
+LAN\_E1.8 26  
+3V\_A\_LAN 26

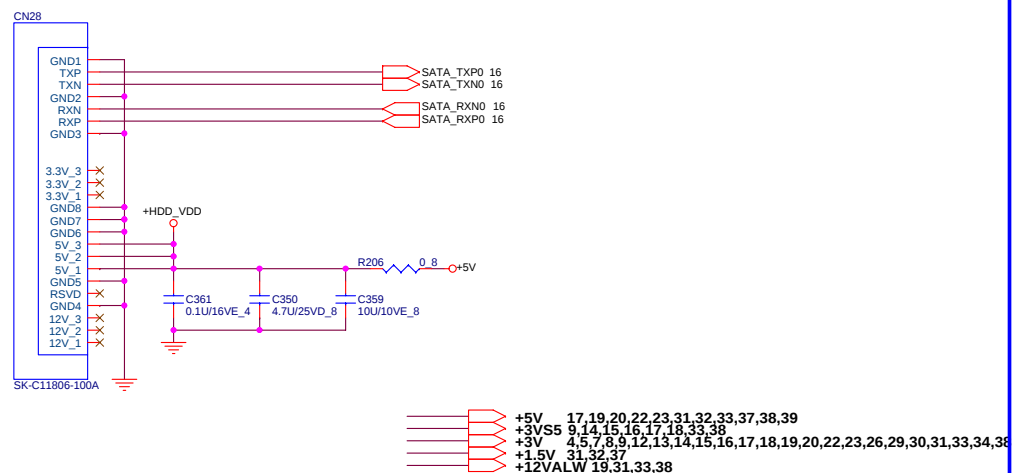


**PROJECT : TT9**  
Quanta Computer Inc.

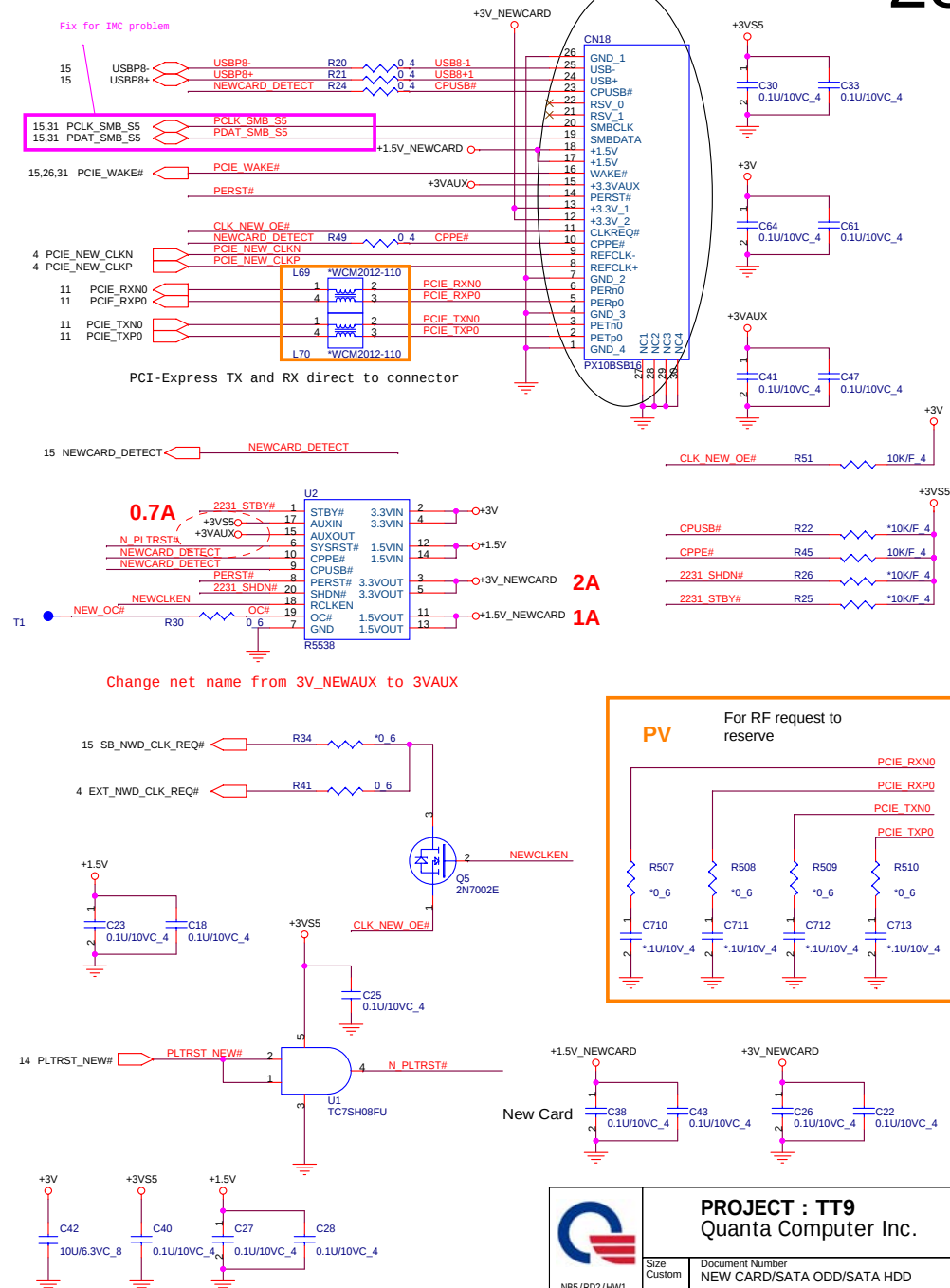
## SATA ODD



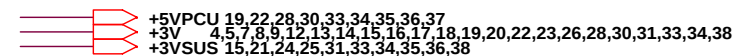
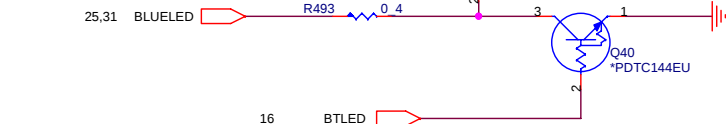
## SATA CONNECTOR

**NEWCARD**

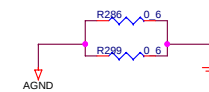
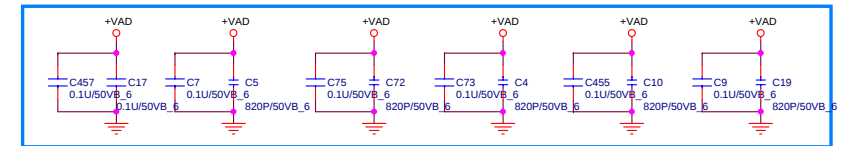
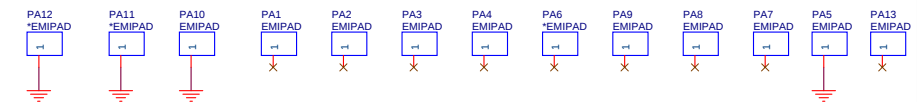
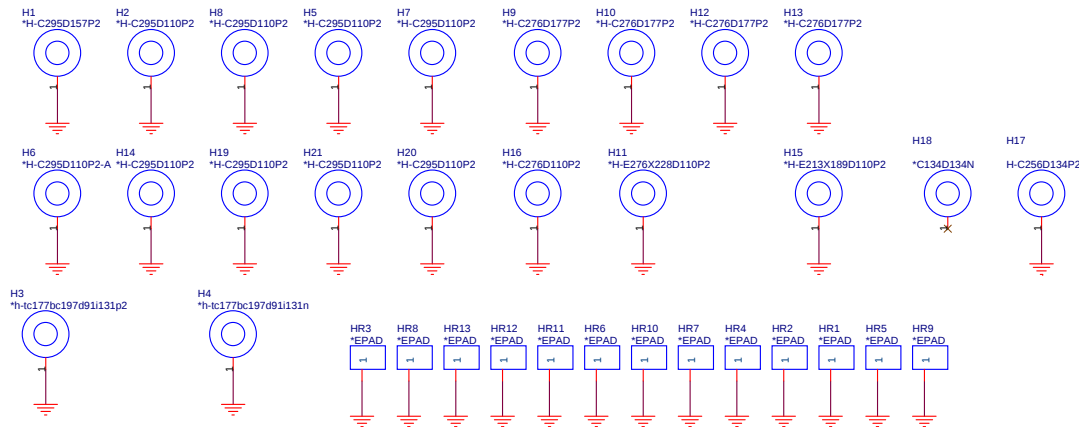
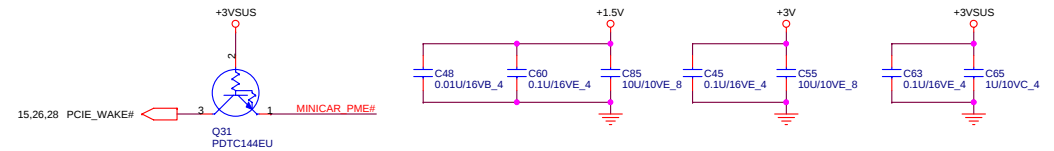
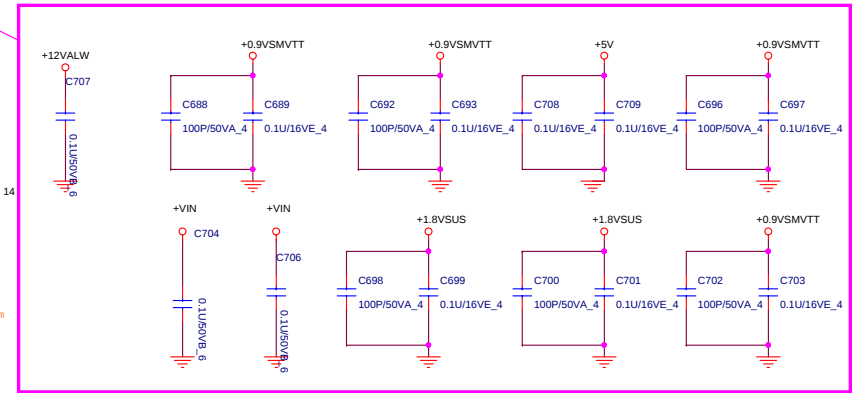
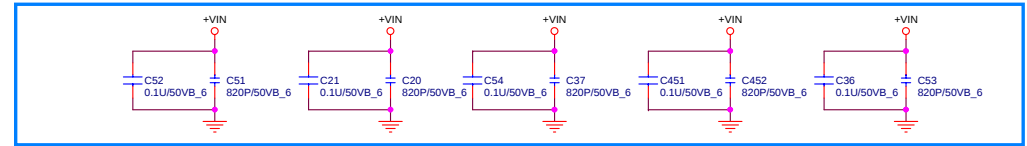
## NEWCARD (PCIEXPRESS\*1 + USB\*1)



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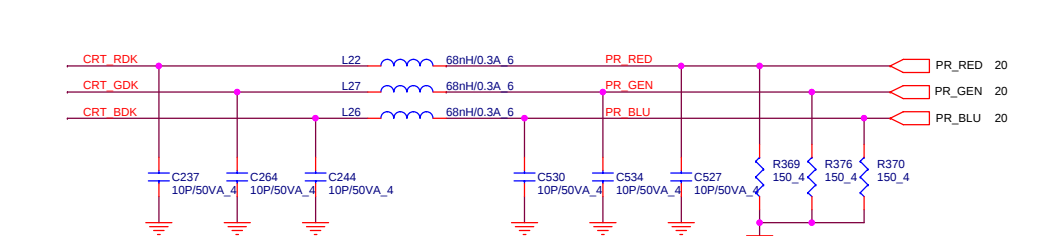
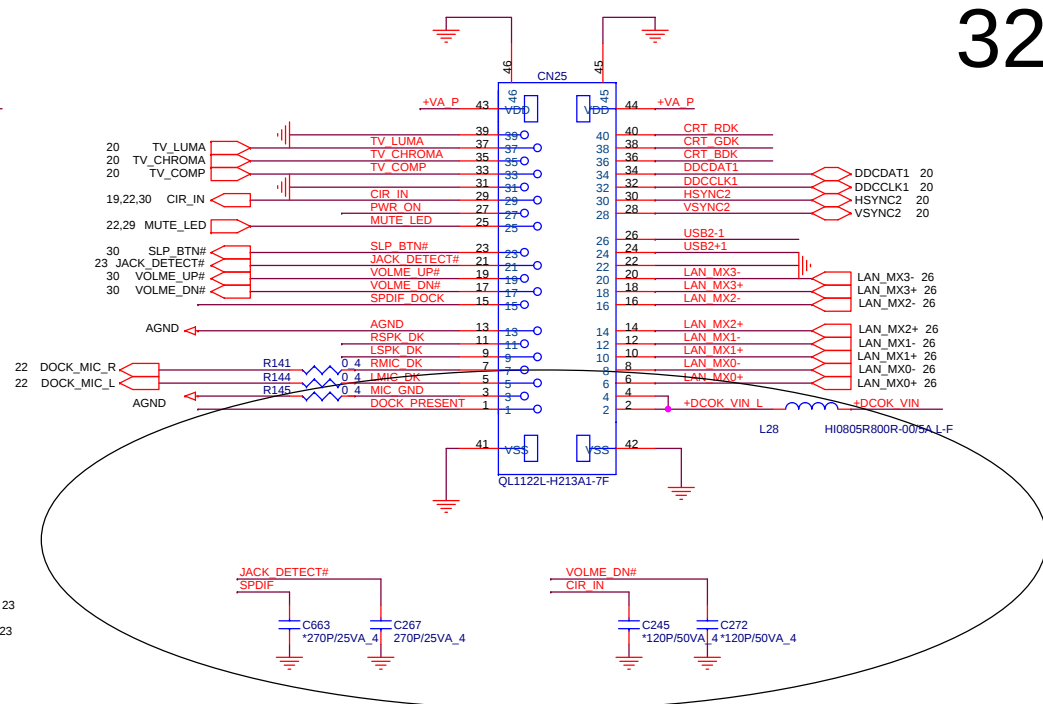
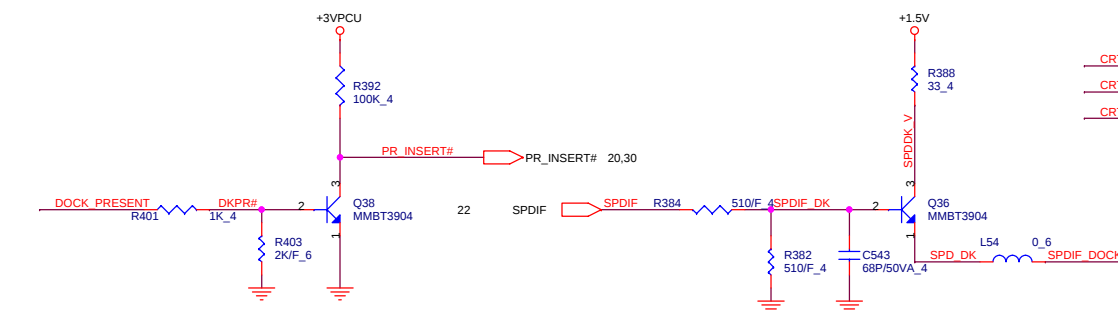
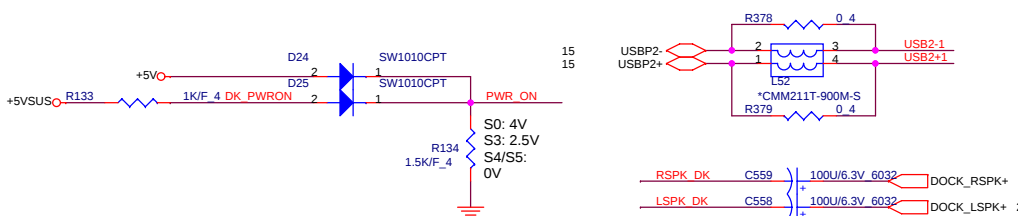
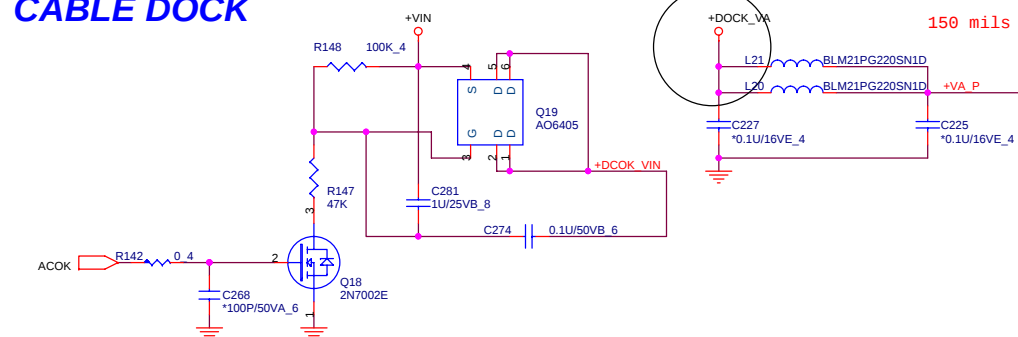




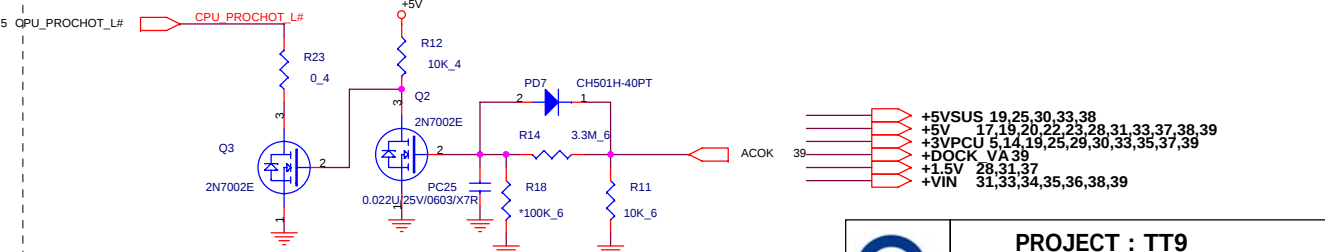
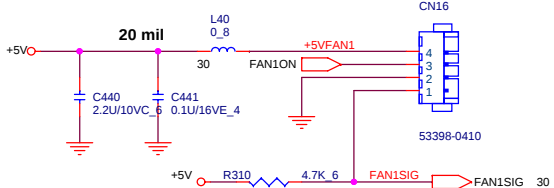


# CABLE DOCK

32

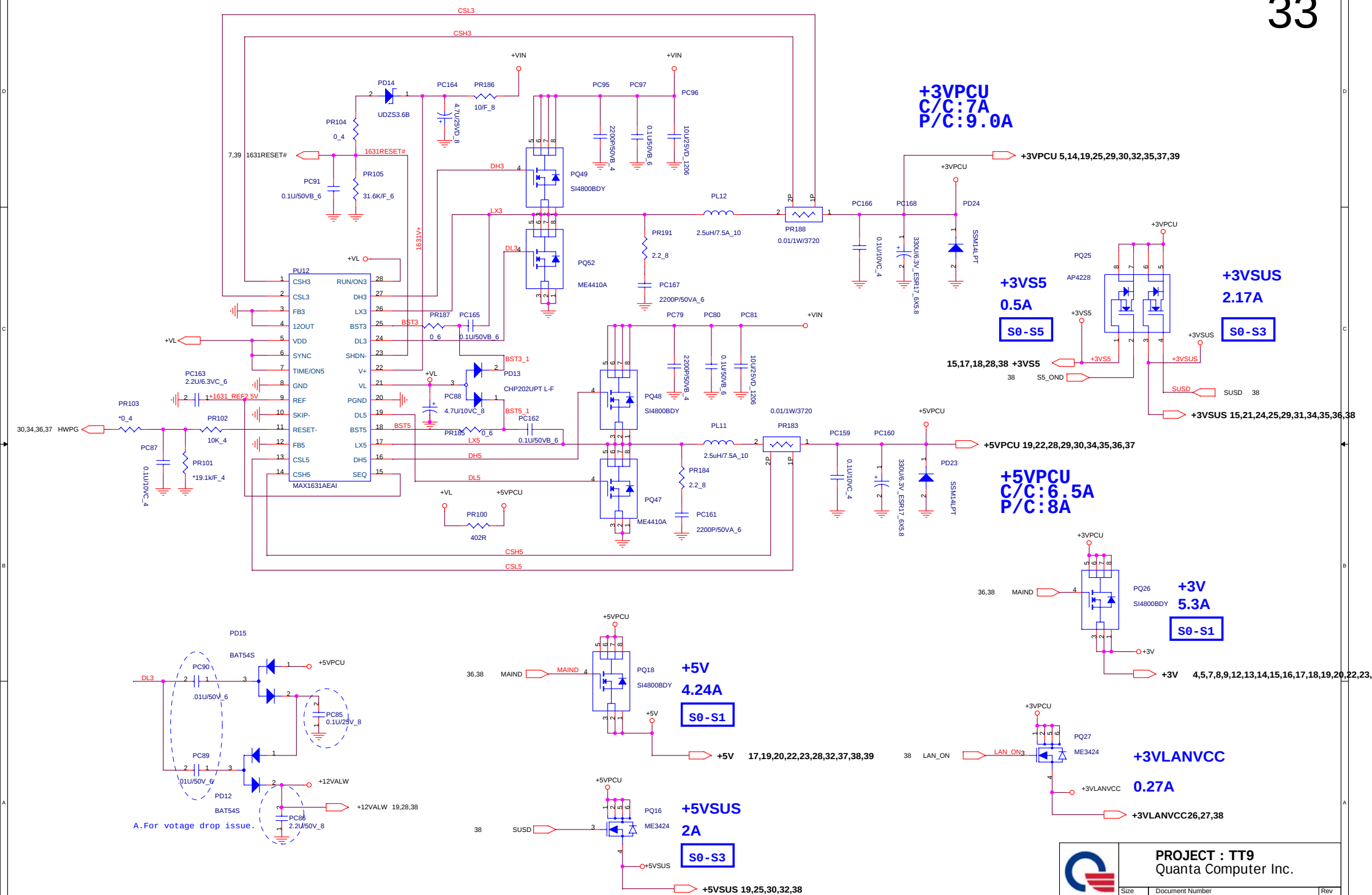


## FAN Connector



PROJECT : TT9  
Quanta Computer Inc.

| Size                              | Document Number | Rev |
|-----------------------------------|-----------------|-----|
| Custom                            | CABLE DOCKING   | 1A  |
| Date: Wednesday, January 23, 2008 | Sheet 32 of 41  |     |

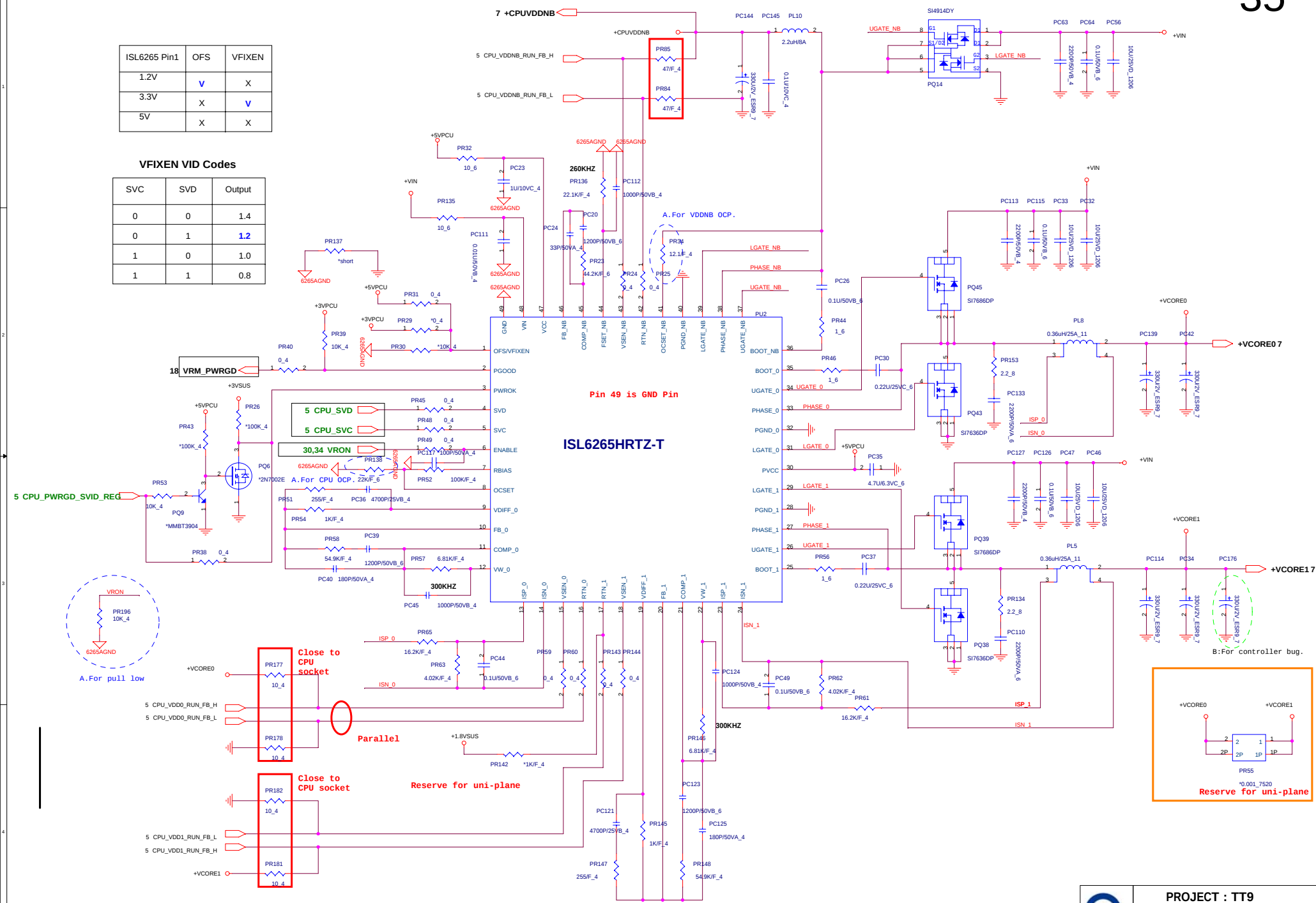




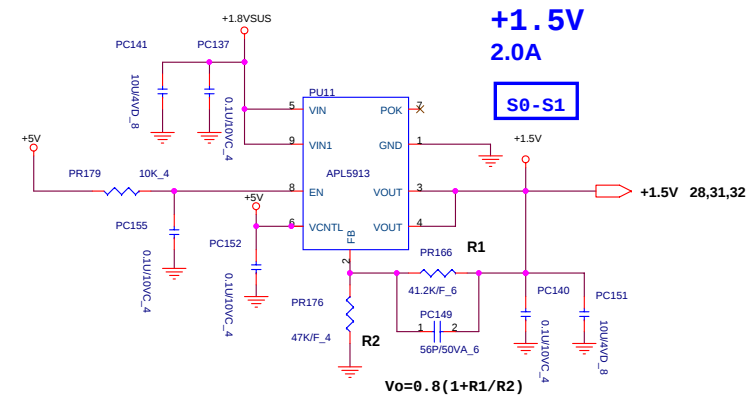
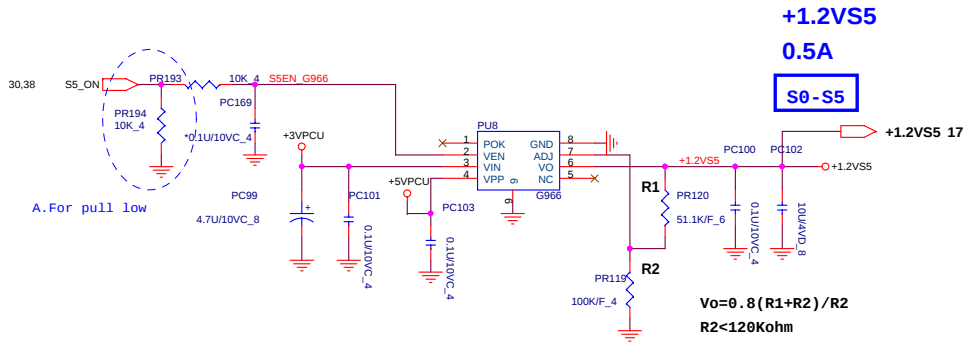
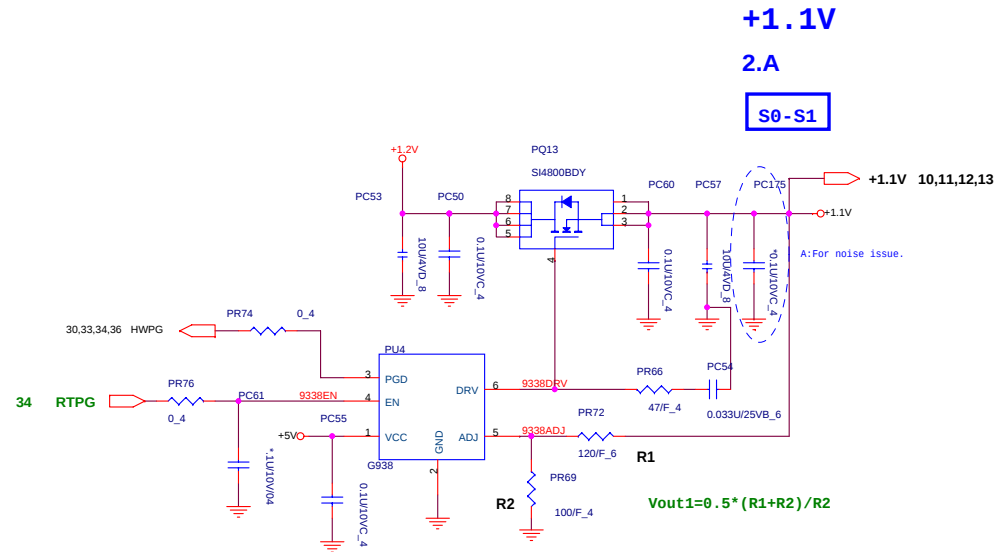
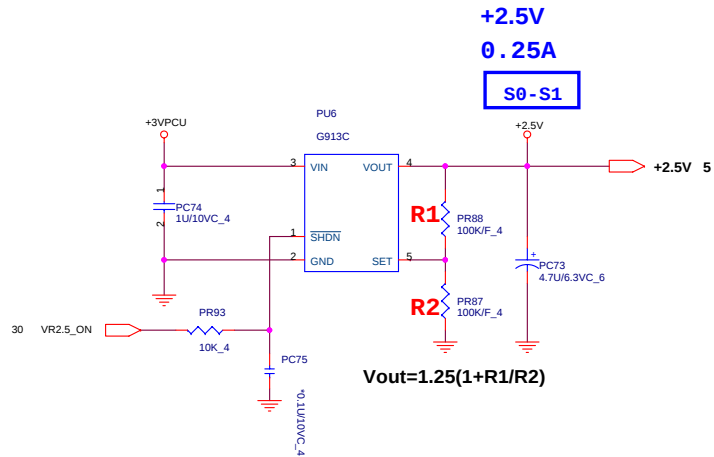
| ISL6265 Pin1 | OFS | VFIXEN |
|--------------|-----|--------|
| 1.2V         | V   | X      |
| 3.3V         | X   | V      |
| 5V           | X   | X      |

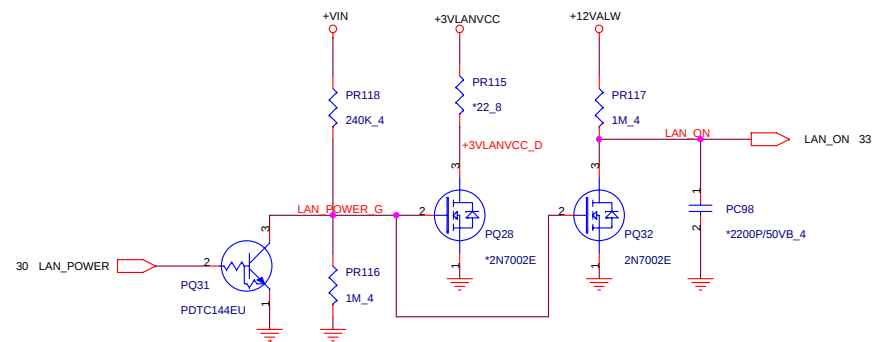
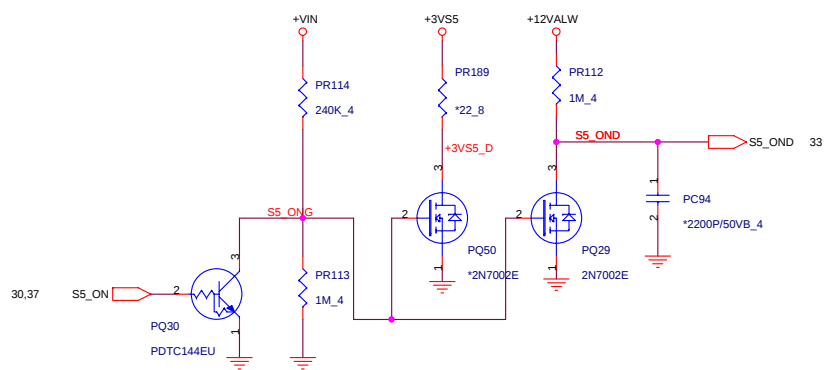
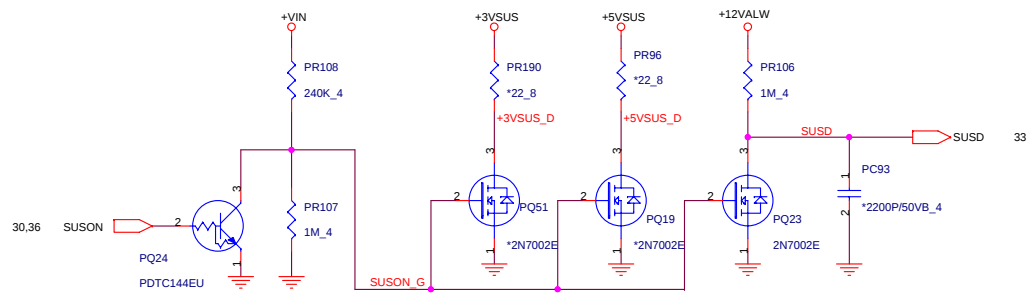
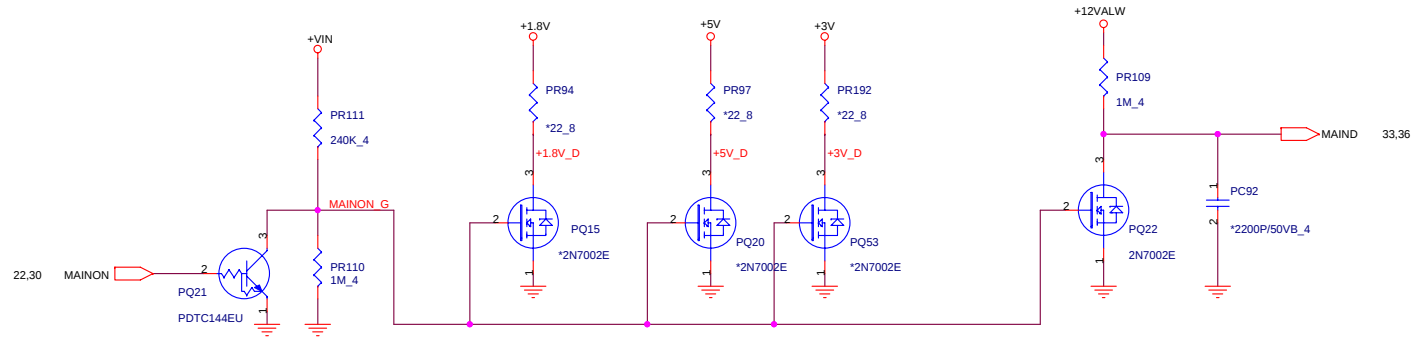
### VFIXEN VID Codes

| SVC | SVD | Output |
|-----|-----|--------|
| 0   | 0   | 1.4    |
| 0   | 1   | 1.2    |
| 1   | 0   | 1.0    |
| 1   | 1   | 0.8    |









Charge current =>  
4 & 6 cell =>3A  
8 cell =>3.5A

