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DATE	CHANGE NO.	REV																																																																																													
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28- DDR2-DAMPING		

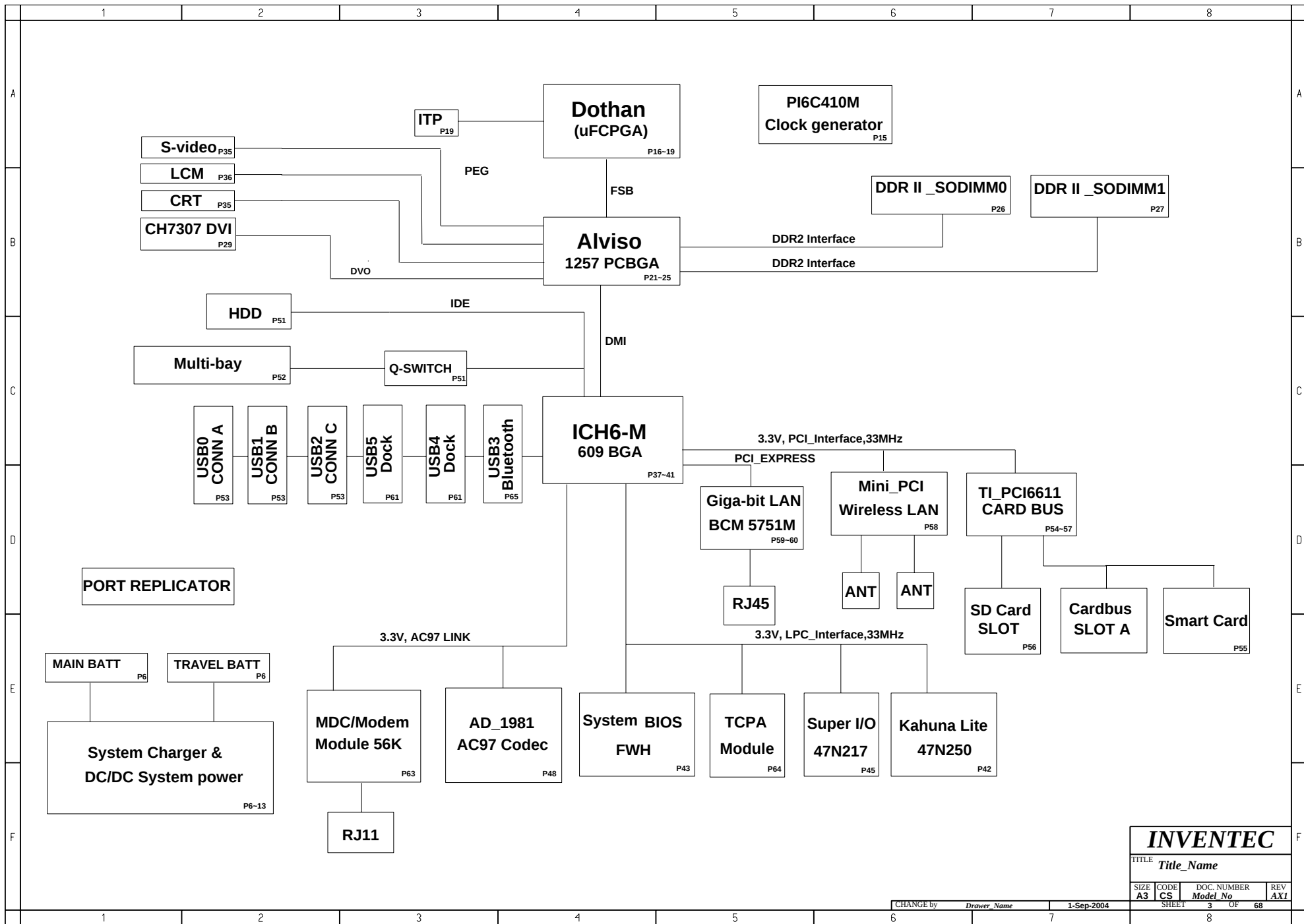
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TITLE *Title_Name*

SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AXI
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CHANGE by *Drawn Name* 1-Sep-2004

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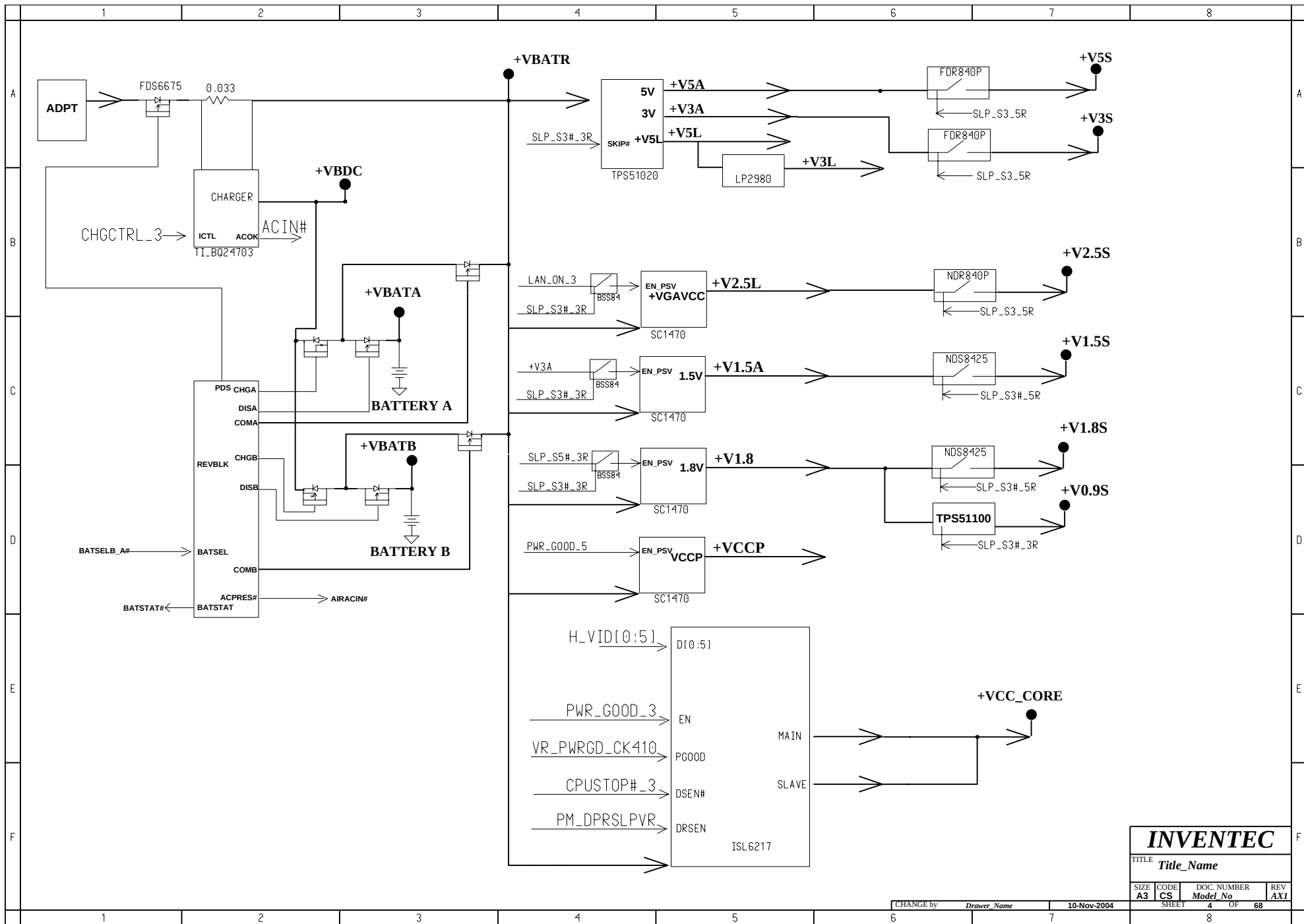
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TITLE *Title_Name*

SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AX1
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CHANGE by *Drawn Name* 1-Sep-2004

SHEET 3 OF 68



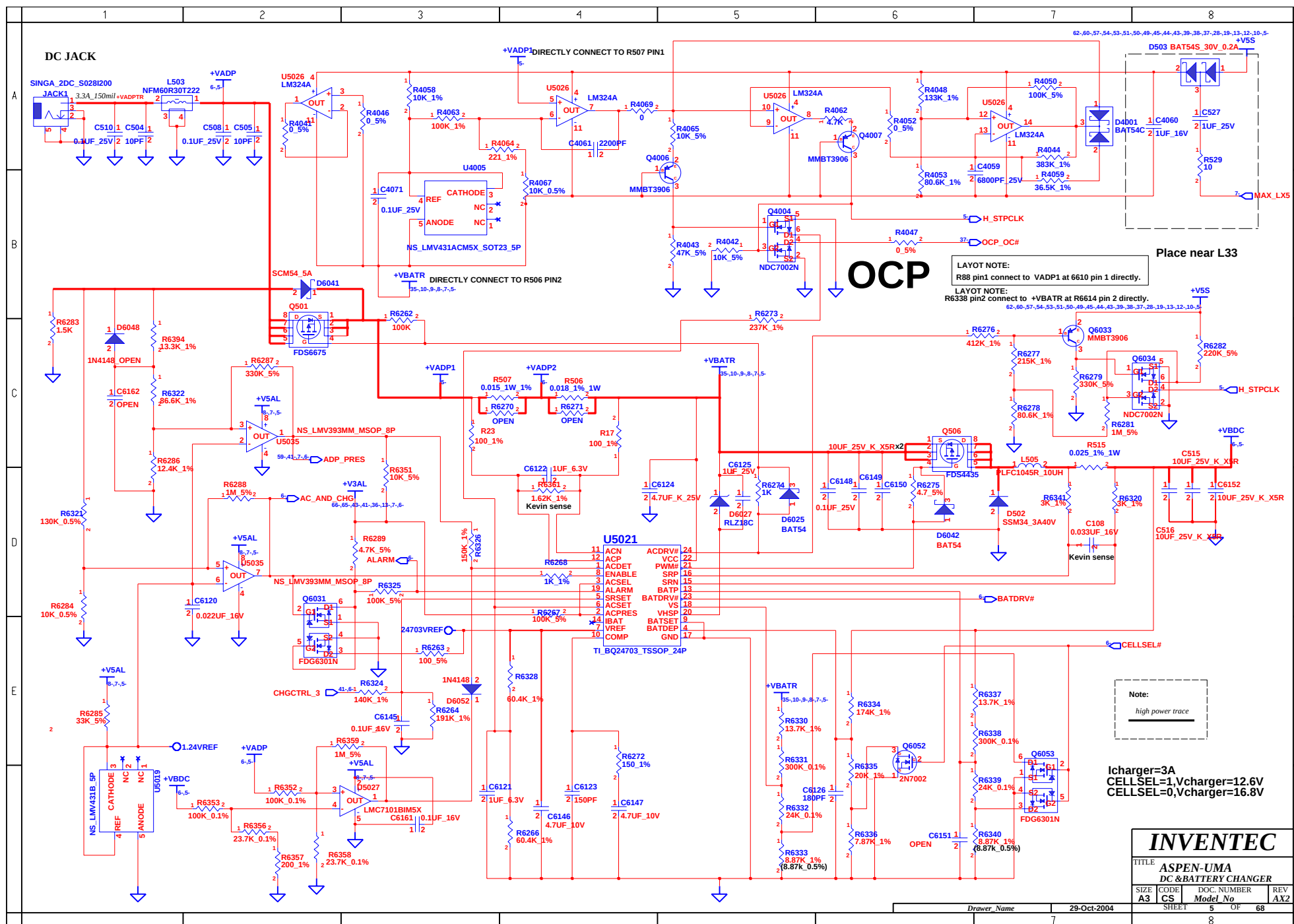
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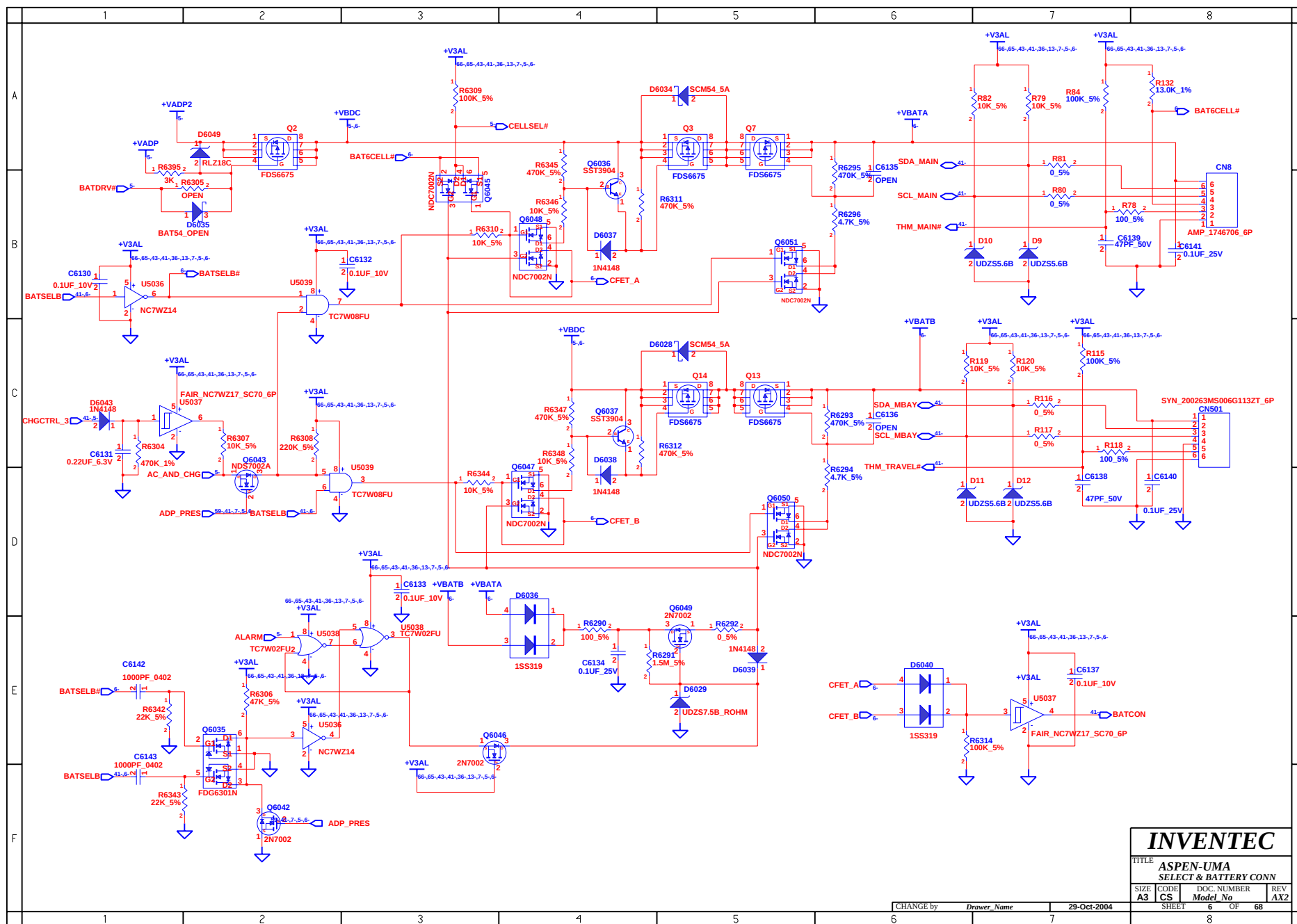
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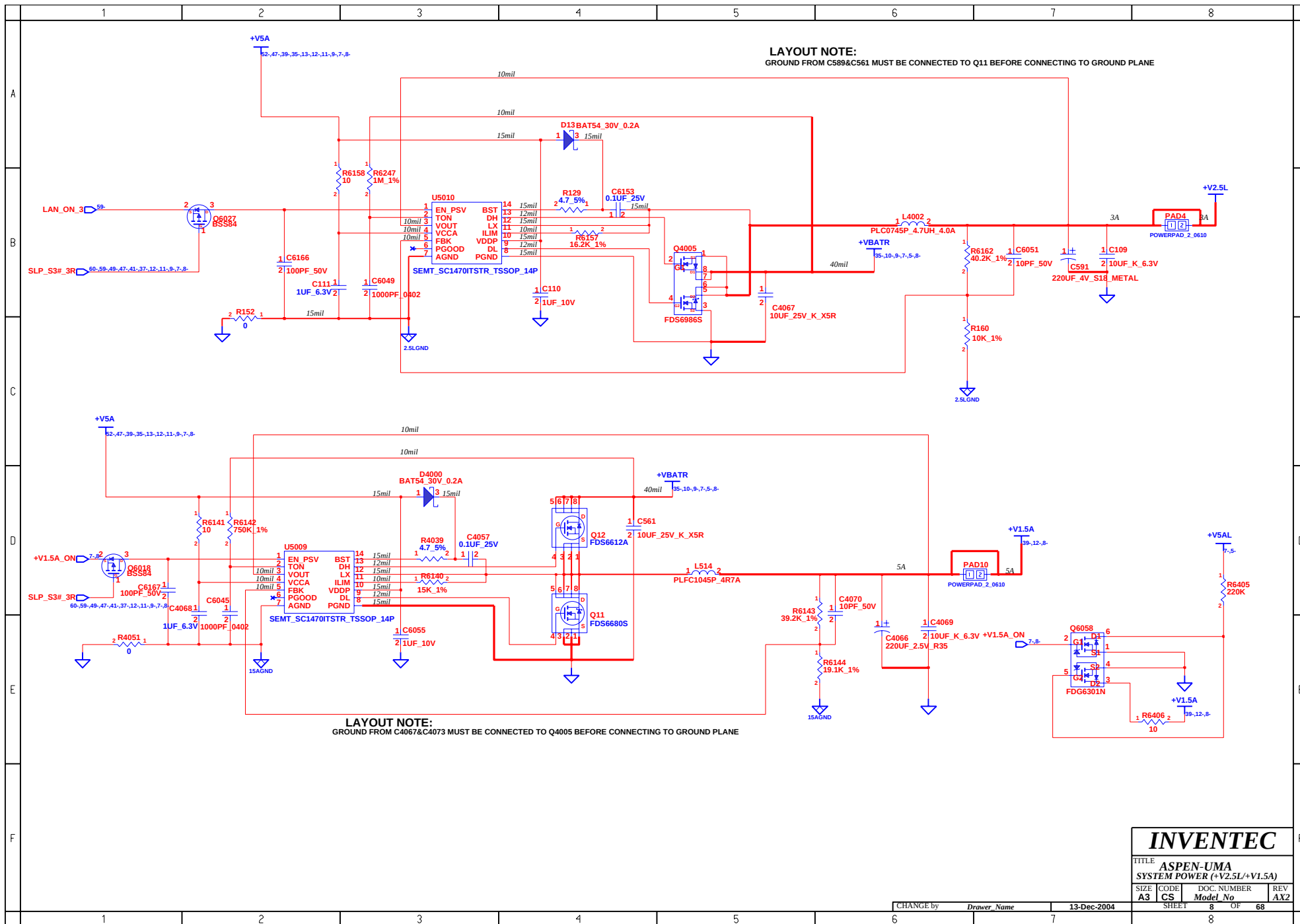
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A3	CS	Model No	AX1

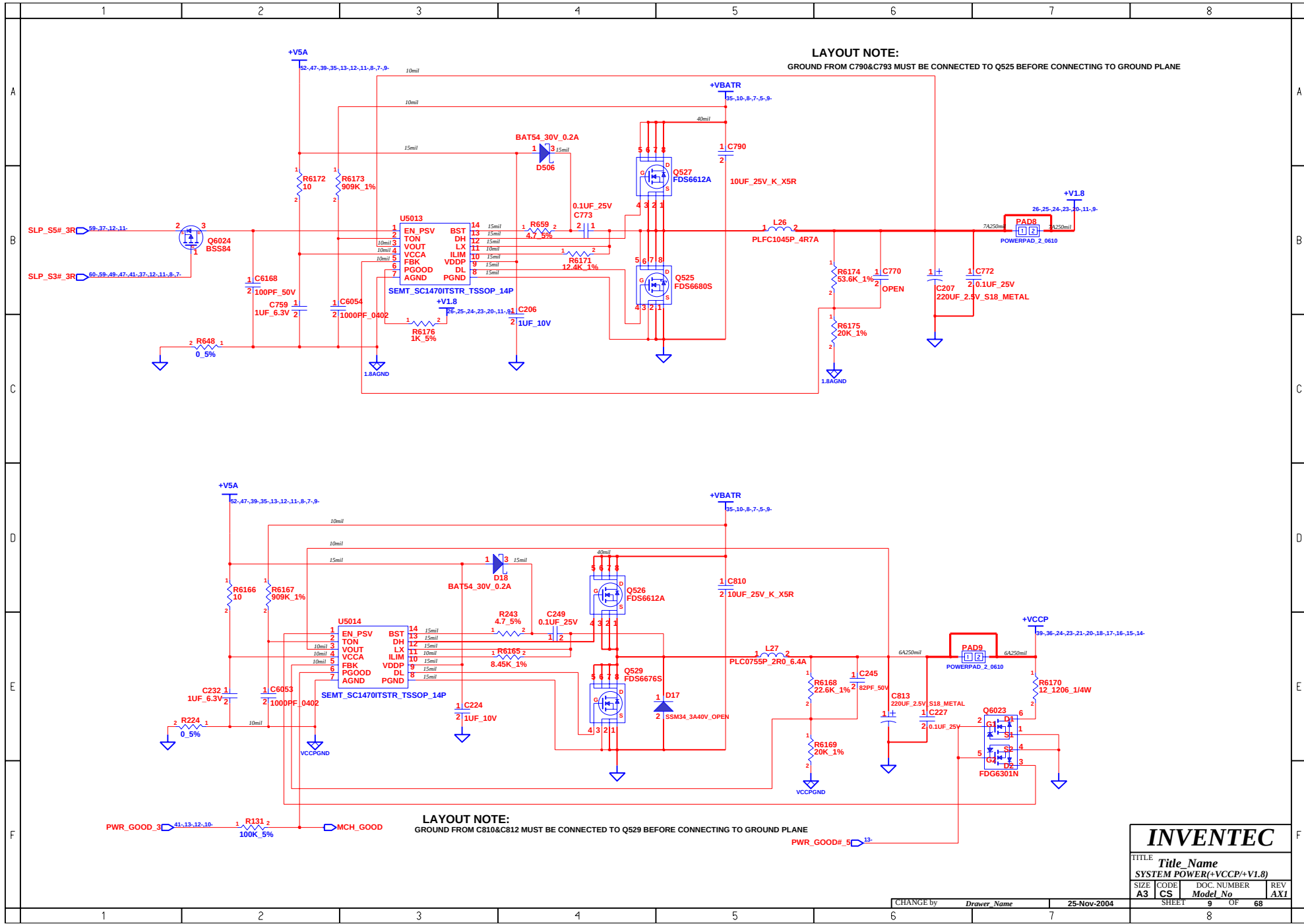
SHEET 4 OF 68

CHANGE by Drawer Name 10-Nov-2004





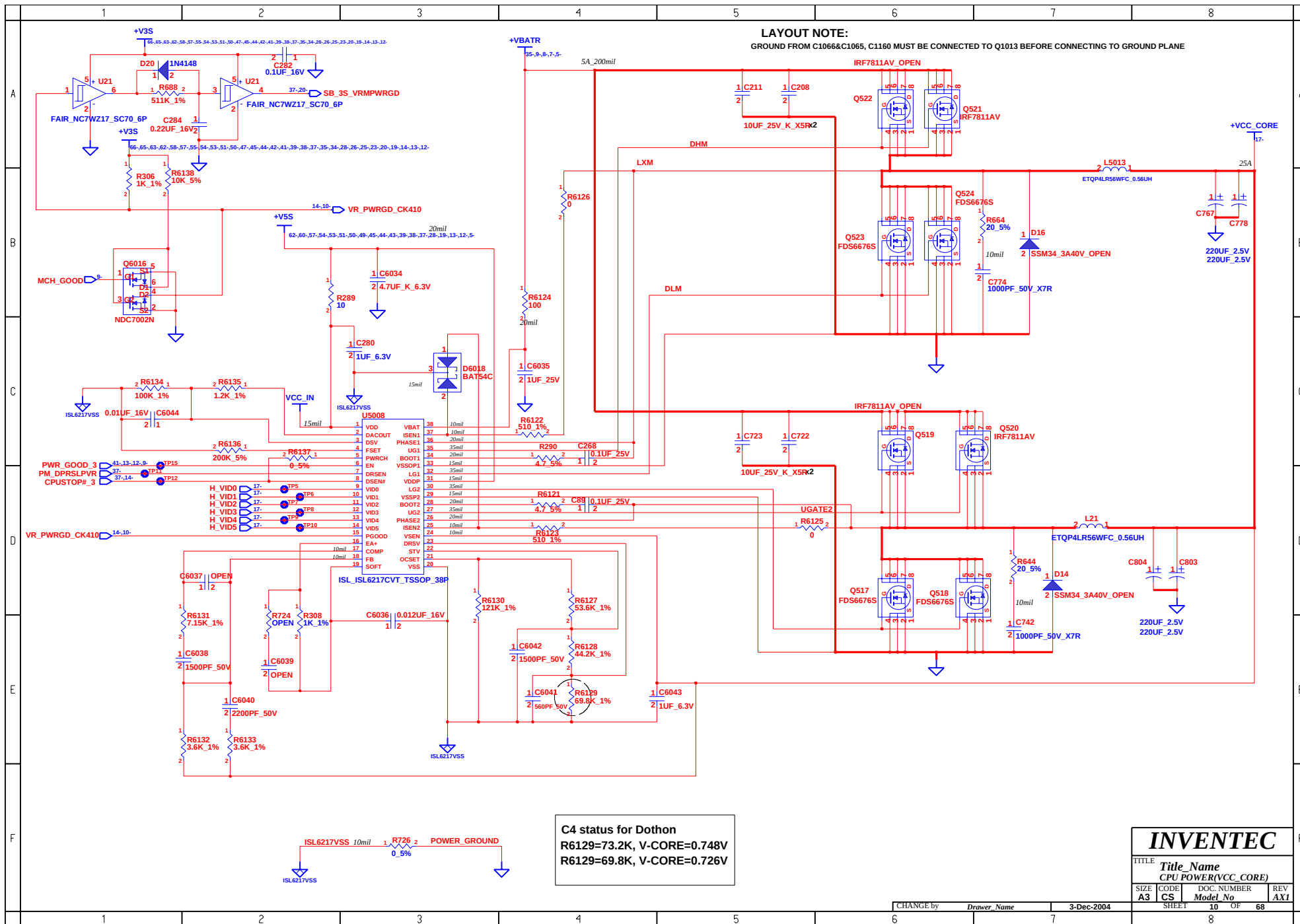


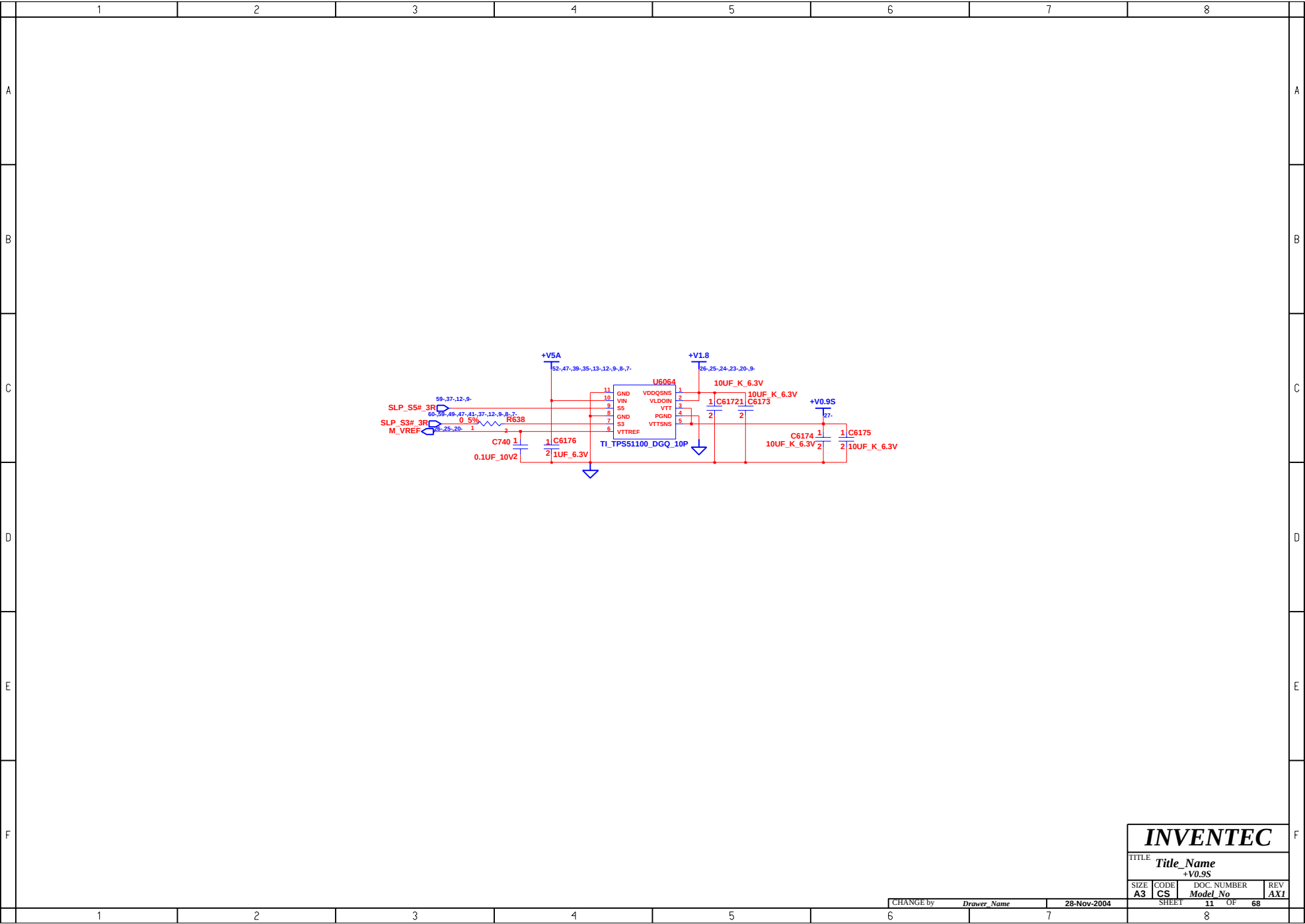


LAYOUT NOTE:
GROUND FROM C790&C793 MUST BE CONNECTED TO Q525 BEFORE CONNECTING TO GROUND PLANE

LAYOUT NOTE:
GROUND FROM C810&C812 MUST BE CONNECTED TO Q529 BEFORE CONNECTING TO GROUND PLANE

INVENTEC			
TITLE Title Name			
SYSTEM POWER(+VCCP/+V1.8)			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX1
CHANGE by		Drawer Name	25-Nov-2004
SHEET		9	OF 68



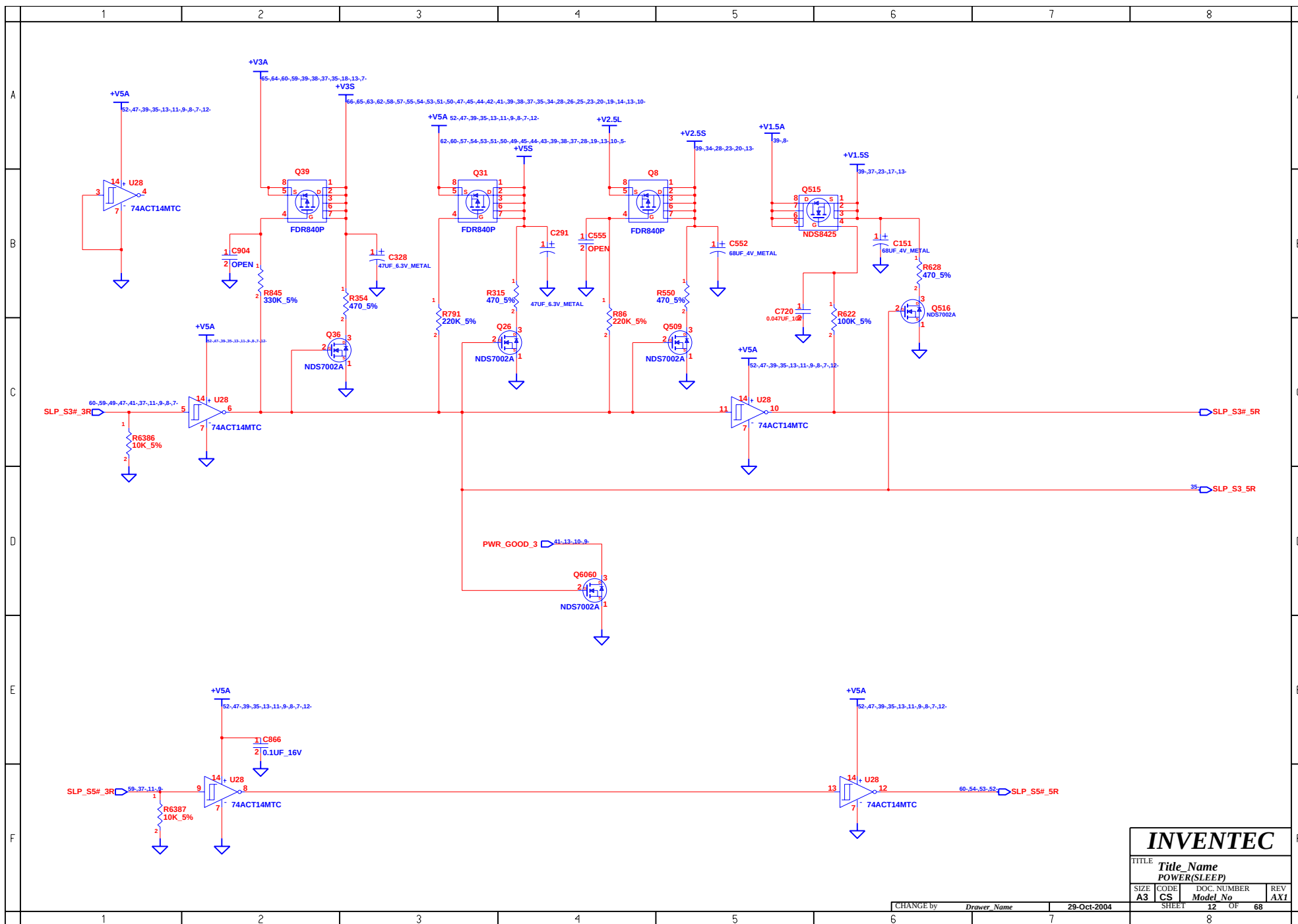


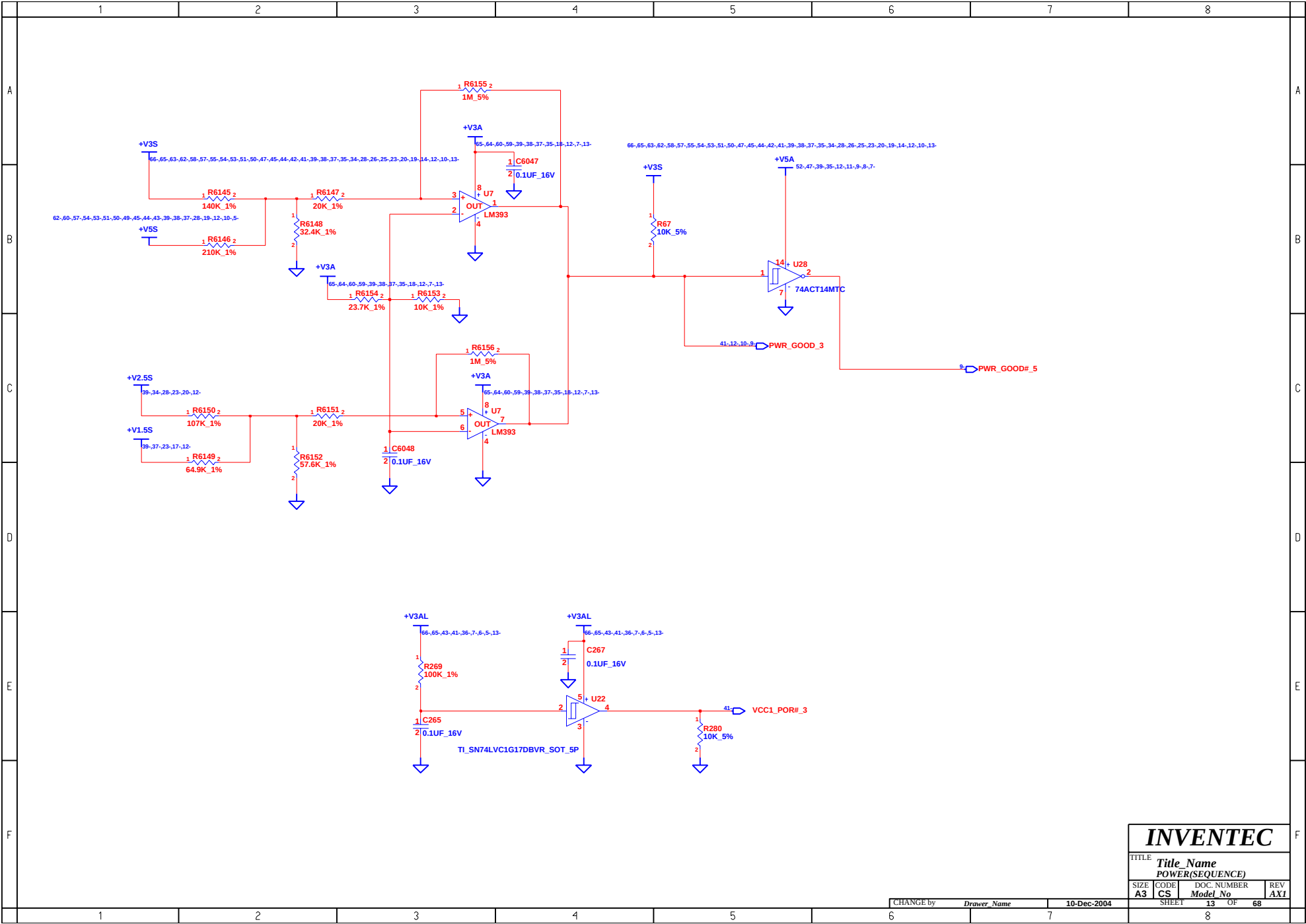
INVENTEC

TITLE			
Title_Name			
+V0.9S			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI

CHANGE by Drawer Name 28-Nov-2004

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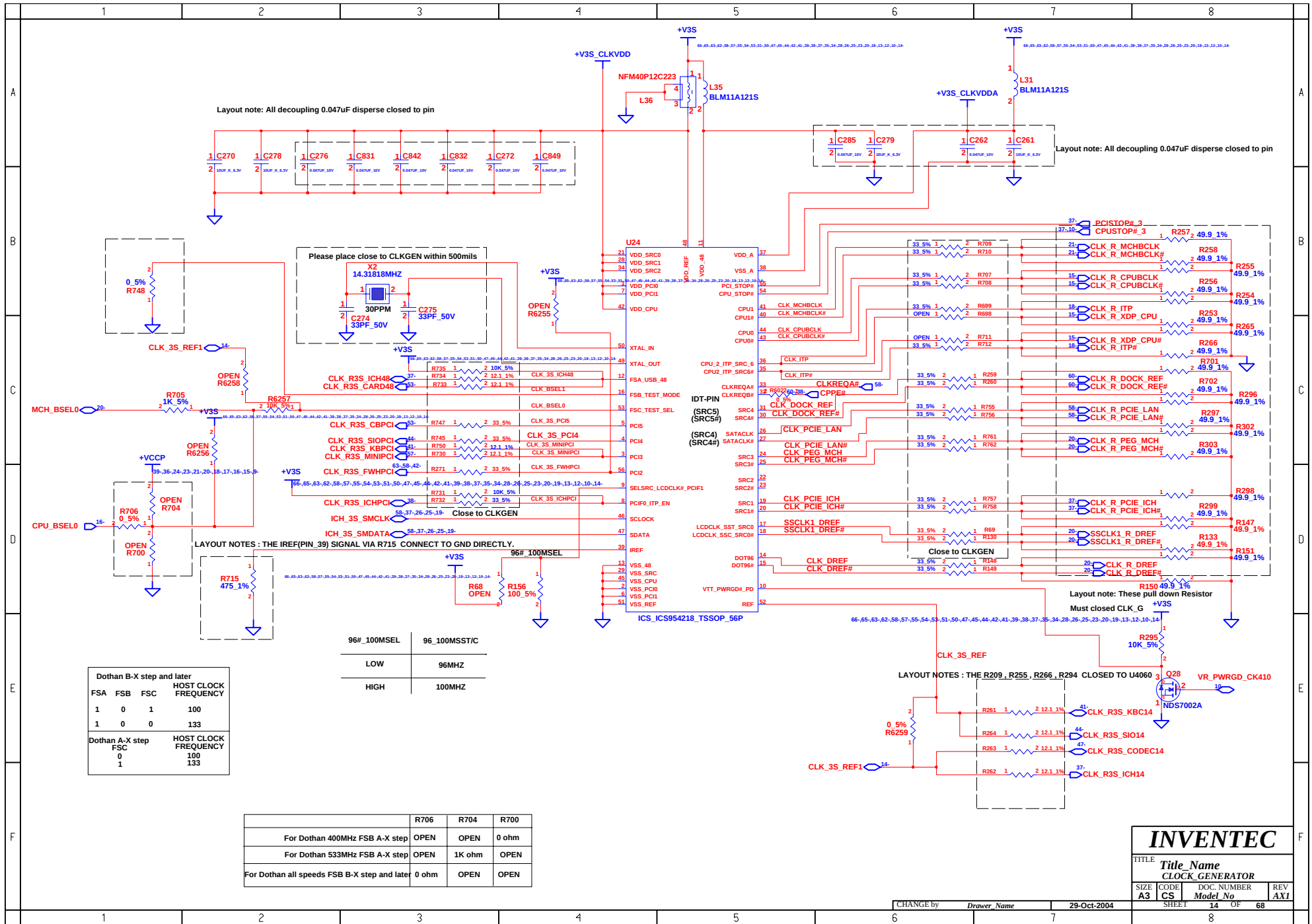


INVENTEC

TITLE			
Title_Name			
POWER(SEQUENCE)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI

CHANGE by Drawer Name 10-Dec-2004

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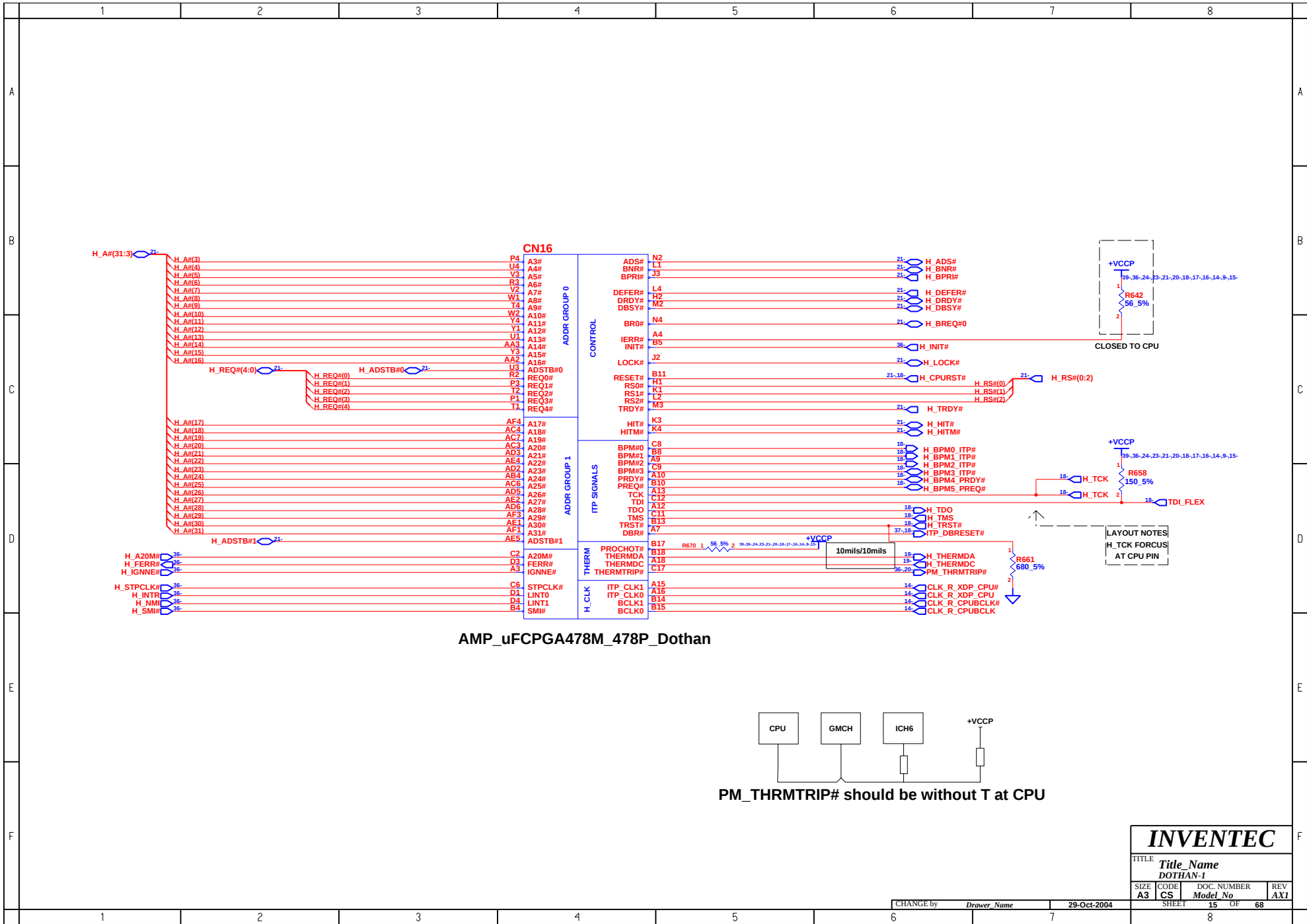


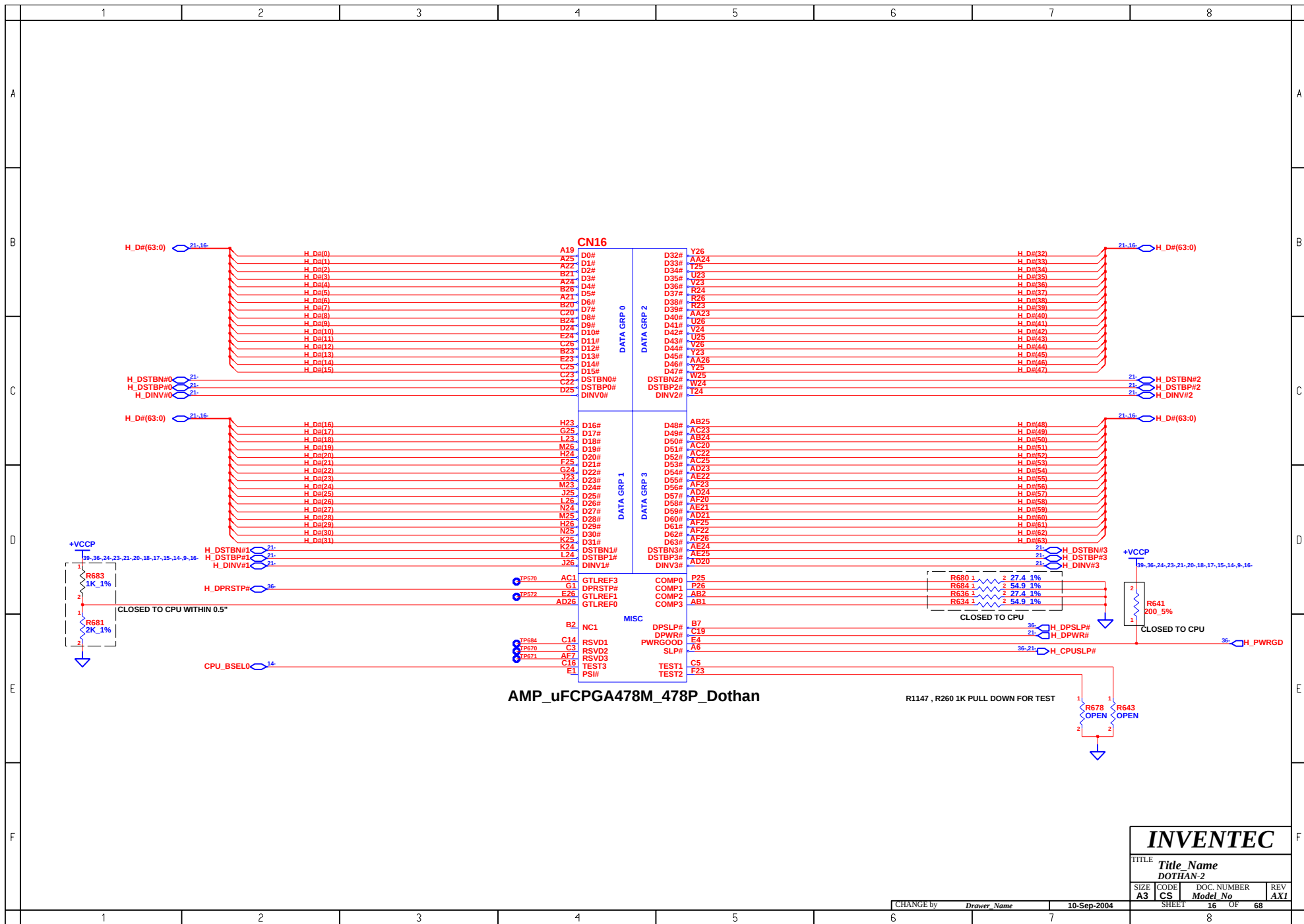
INVENTEC

TITLE			
Title Name			
CLOCK GENERATOR			
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A3	CS	Model No	AX1

CHANGE by *Drawer Name* 29-Oct-2004

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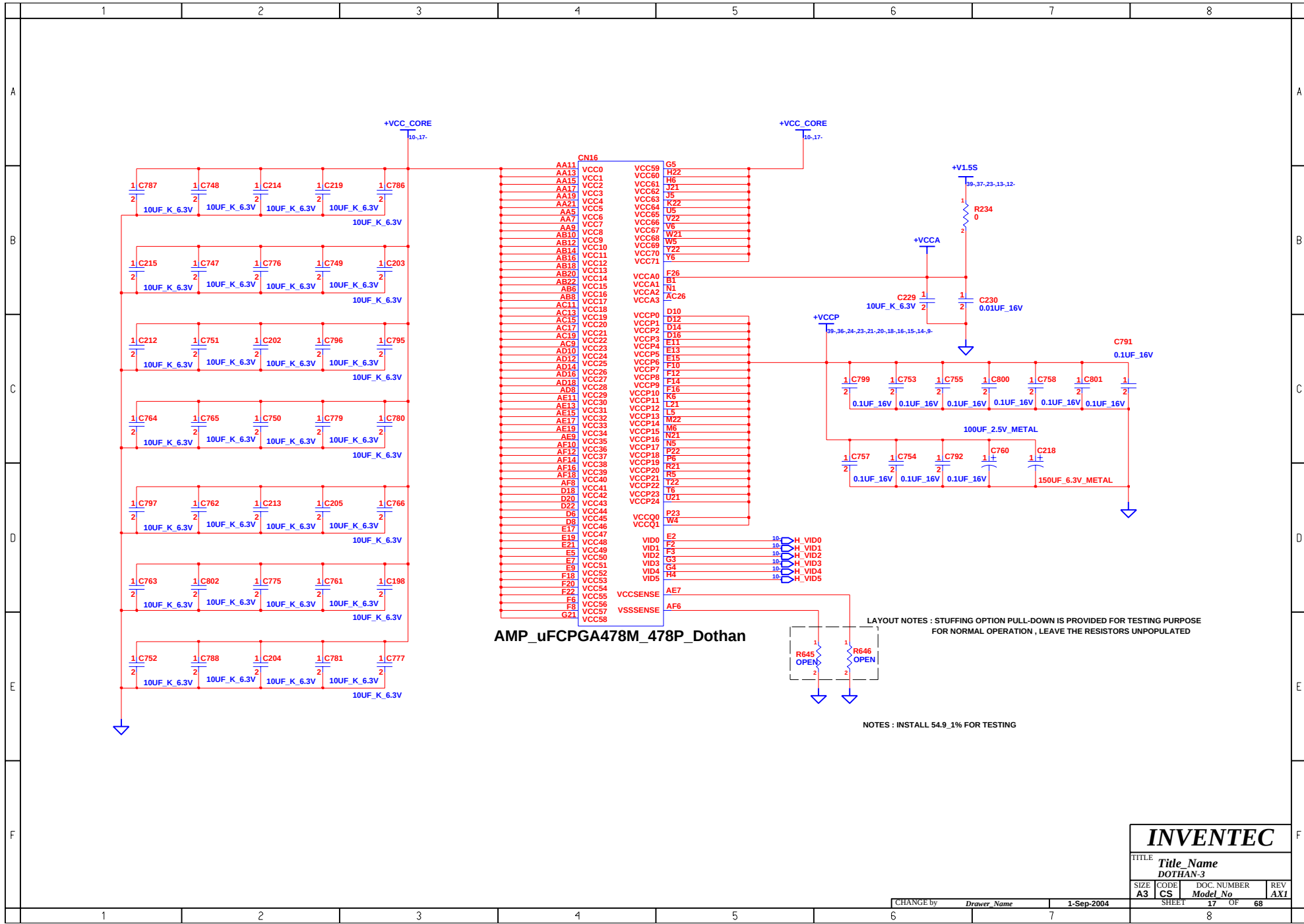


INVENTEC

TITLE			
Title_Name			
DOTHAN-2			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX1

CHANGE by *Drawer Name* 10-Sep-2004

SHEET 16 OF 68



AMP_uFCPGA478M_478P_Dothan

LAYOUT NOTES : STUFFING OPTION PULL-DOWN IS PROVIDED FOR TESTING PURPOSE
FOR NORMAL OPERATION , LEAVE THE RESISTORS UNPOPULATED

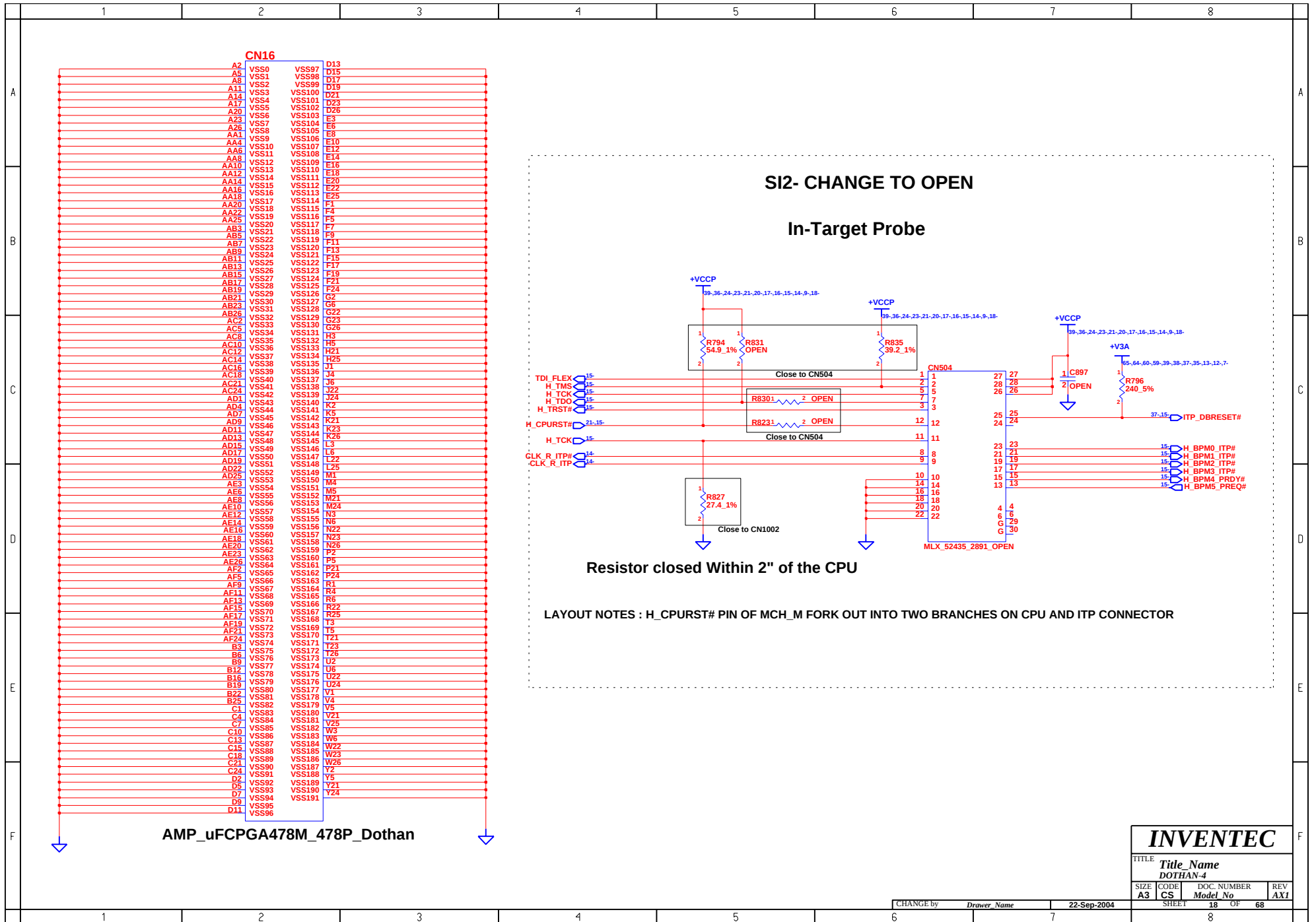
NOTES : INSTALL 54.9_1% FOR TESTING

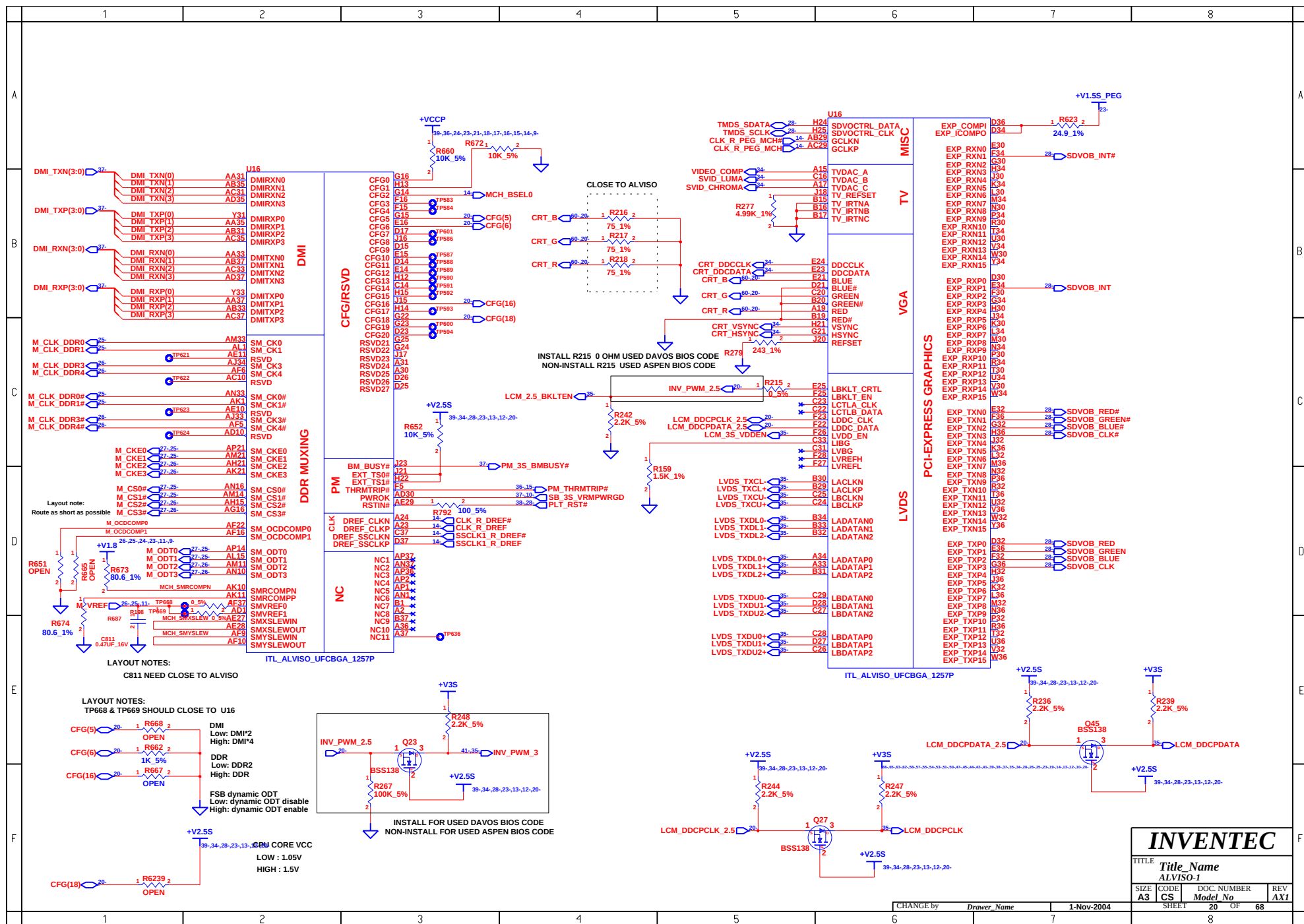
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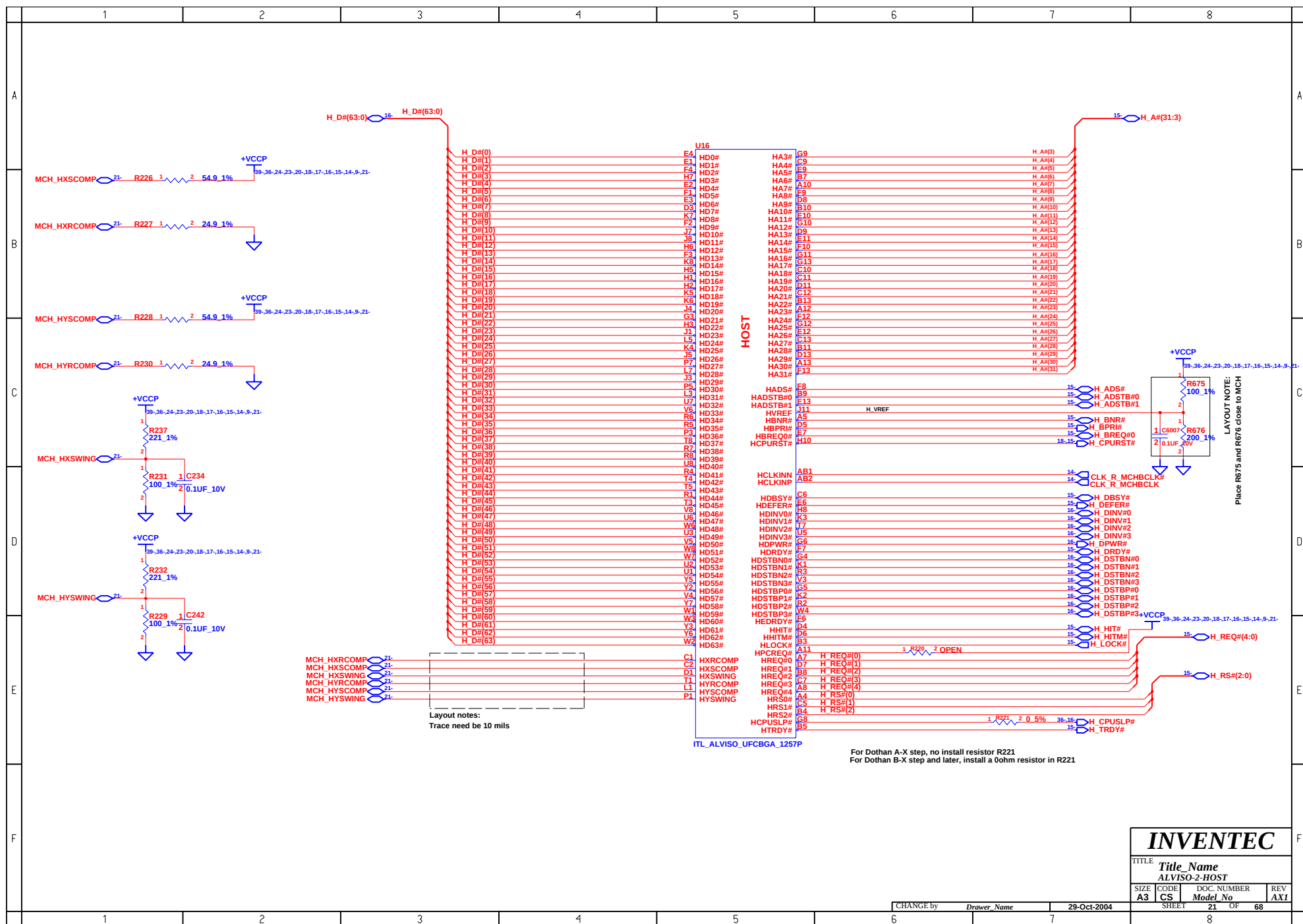
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Title Name			
DOTHAN-3			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX1

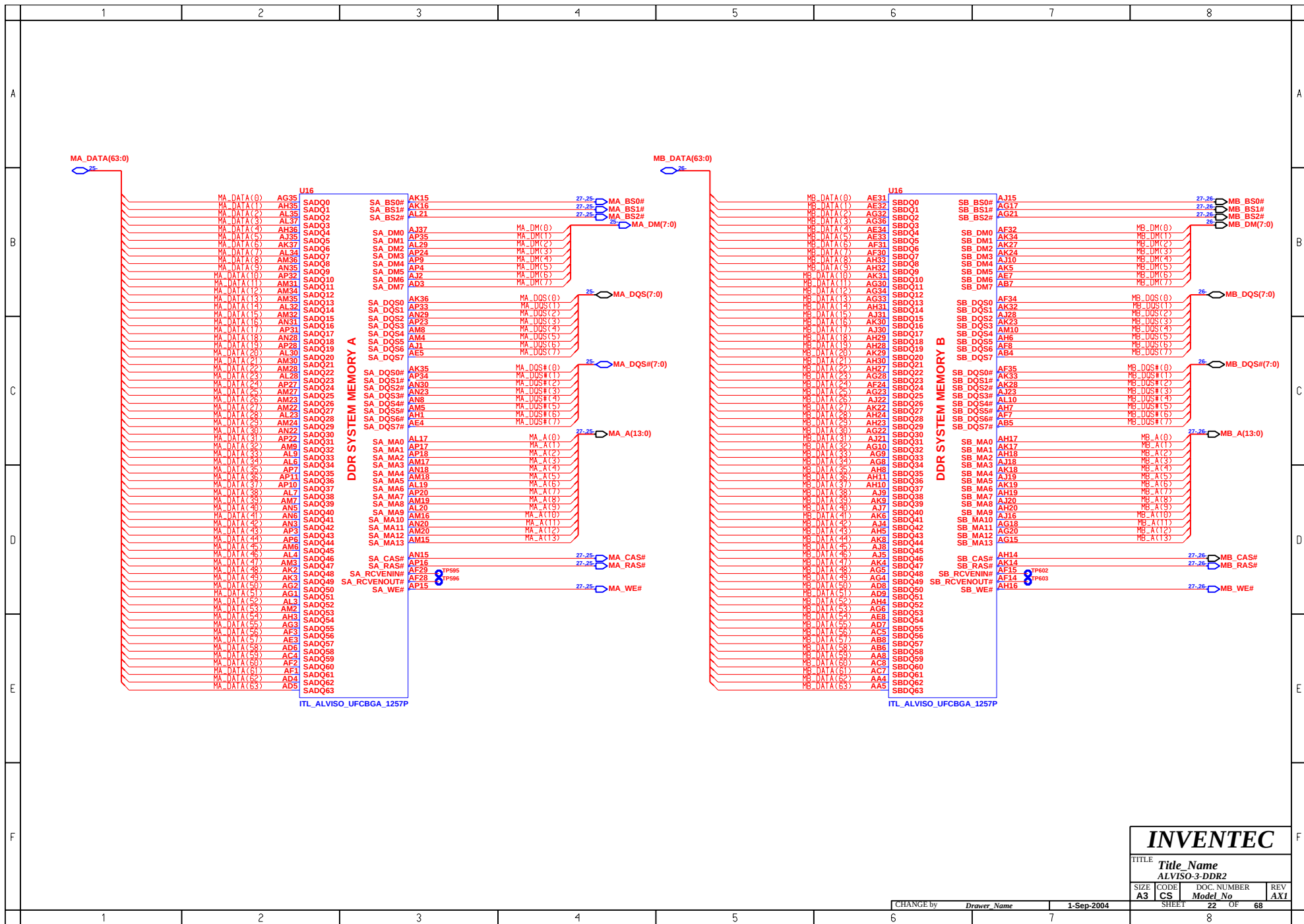
CHANGE by Drawer Name 1-Sep-2004

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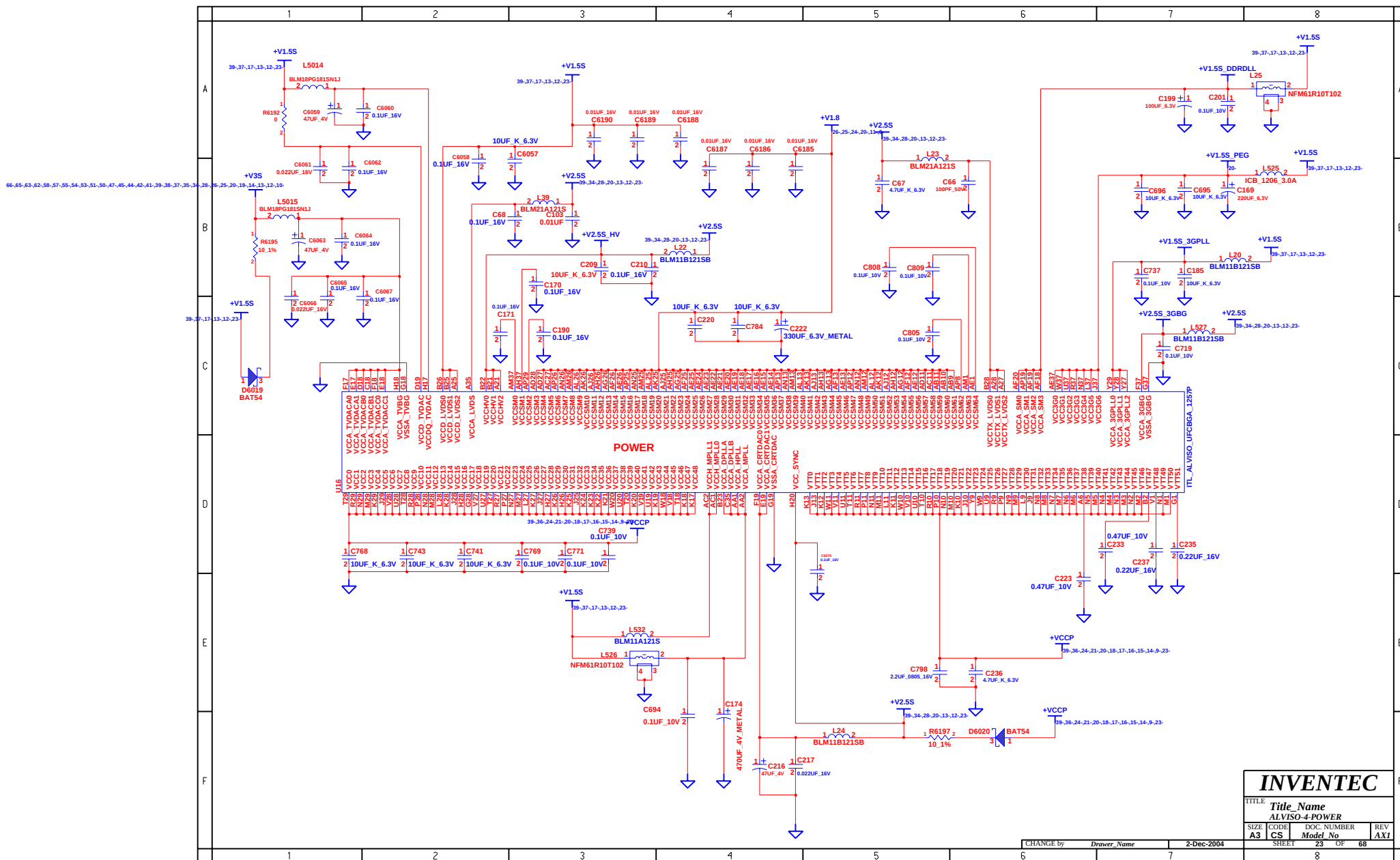


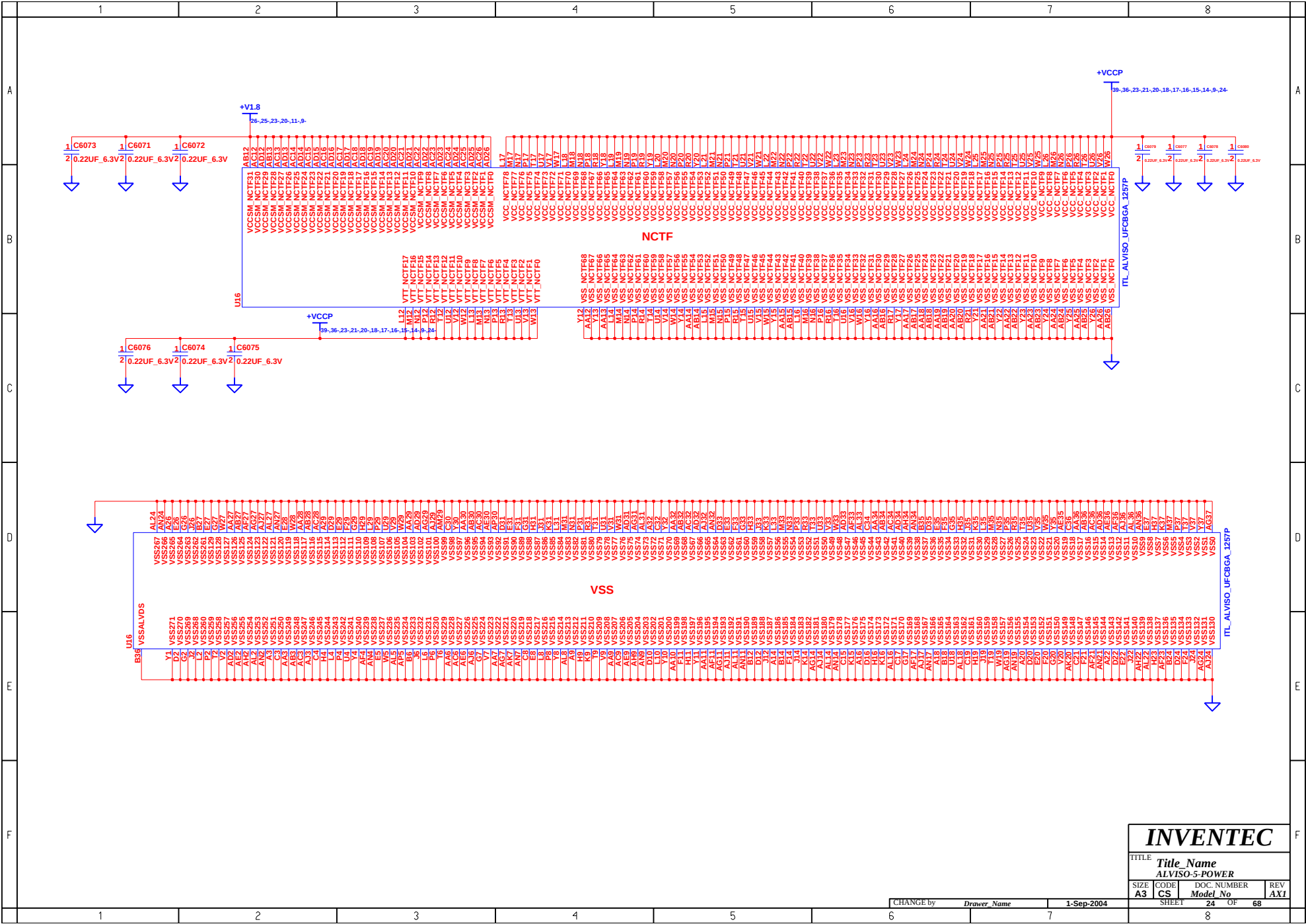
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TITLE			
Title Name			
ALVISO-3-DDR2			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX1

CHANGE by *Draver Name* 1-Sep-2004

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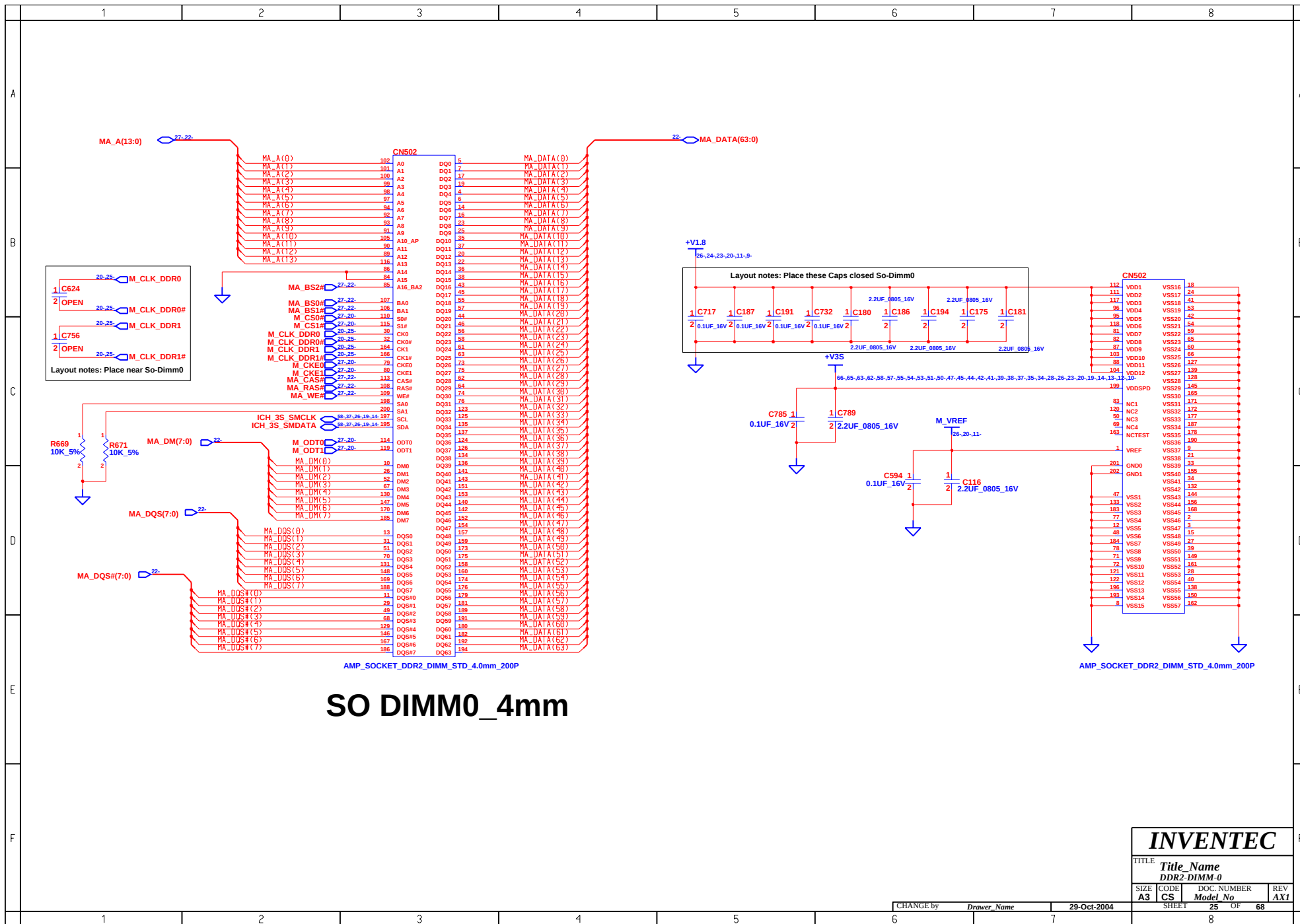


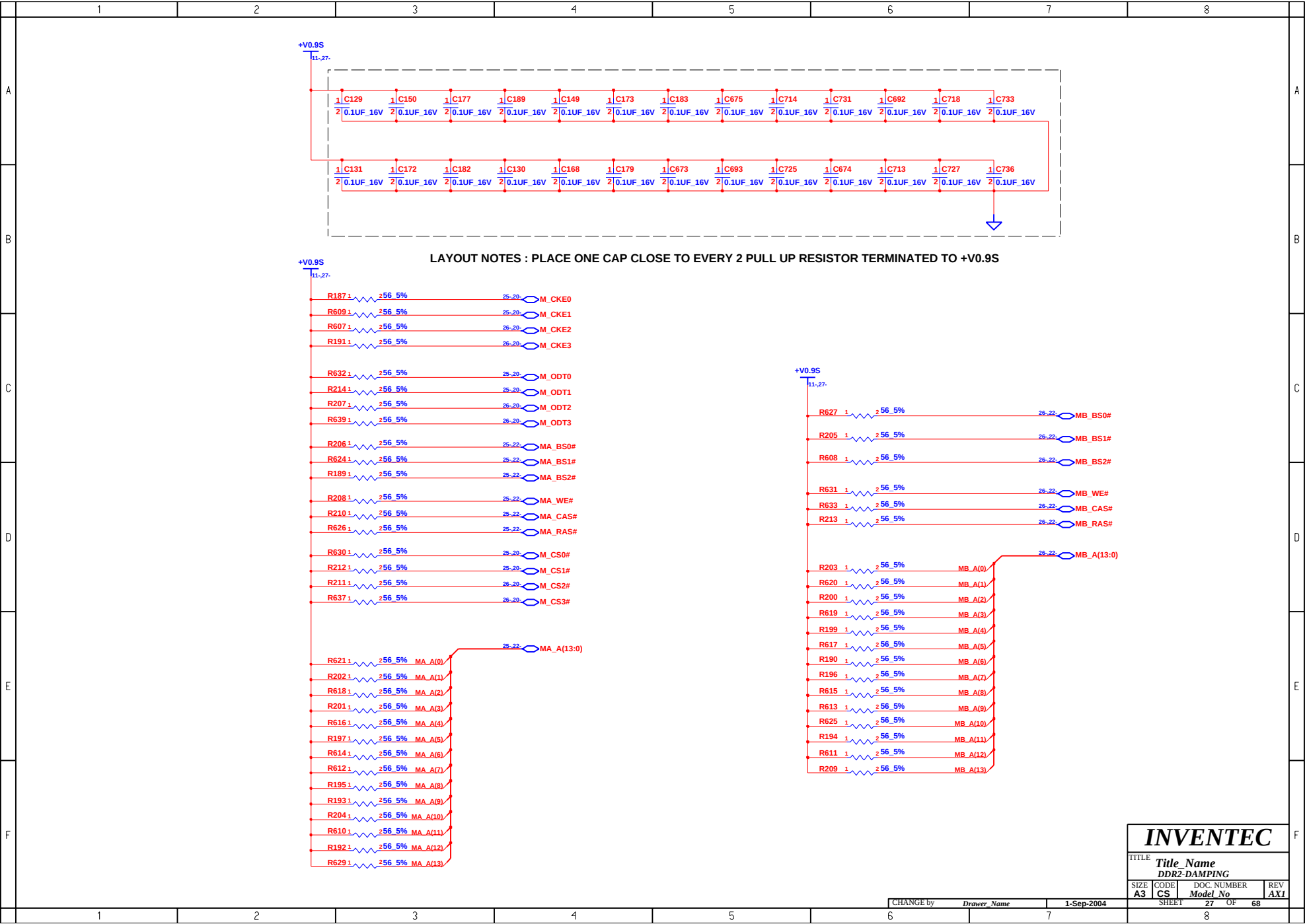
INVENTEC

TITLE			
Title Name			
ALVISO-5-POWER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX1

CHANGE by *Drawn Name* 1-Sep-2004

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	1	2	3	4	5	6	7	8	
A	BLANK								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC			
TITLE <i>Title_Name</i> <i>ATI-M24-P-3</i>			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AX1
SHEET 29		OF 68	

CHANGE by *Drawn Name* 1-Sep-2004

	1	2	3	4	5	6	7	8	
A	BLANK								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC			
TITLE <i>Title_Name</i> <i>ATI-M24-P-4</i>			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AX1
CHANGE by <i>Drawn Name</i>		1-Sep-2004	
SHEET 30		OF 68	

	1	2	3	4	5	6	7	8	
A	BLANK								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC

TITLE Title Name			
VGA DAMPING RESISTOR			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX1
SHEET 31		OF 68	

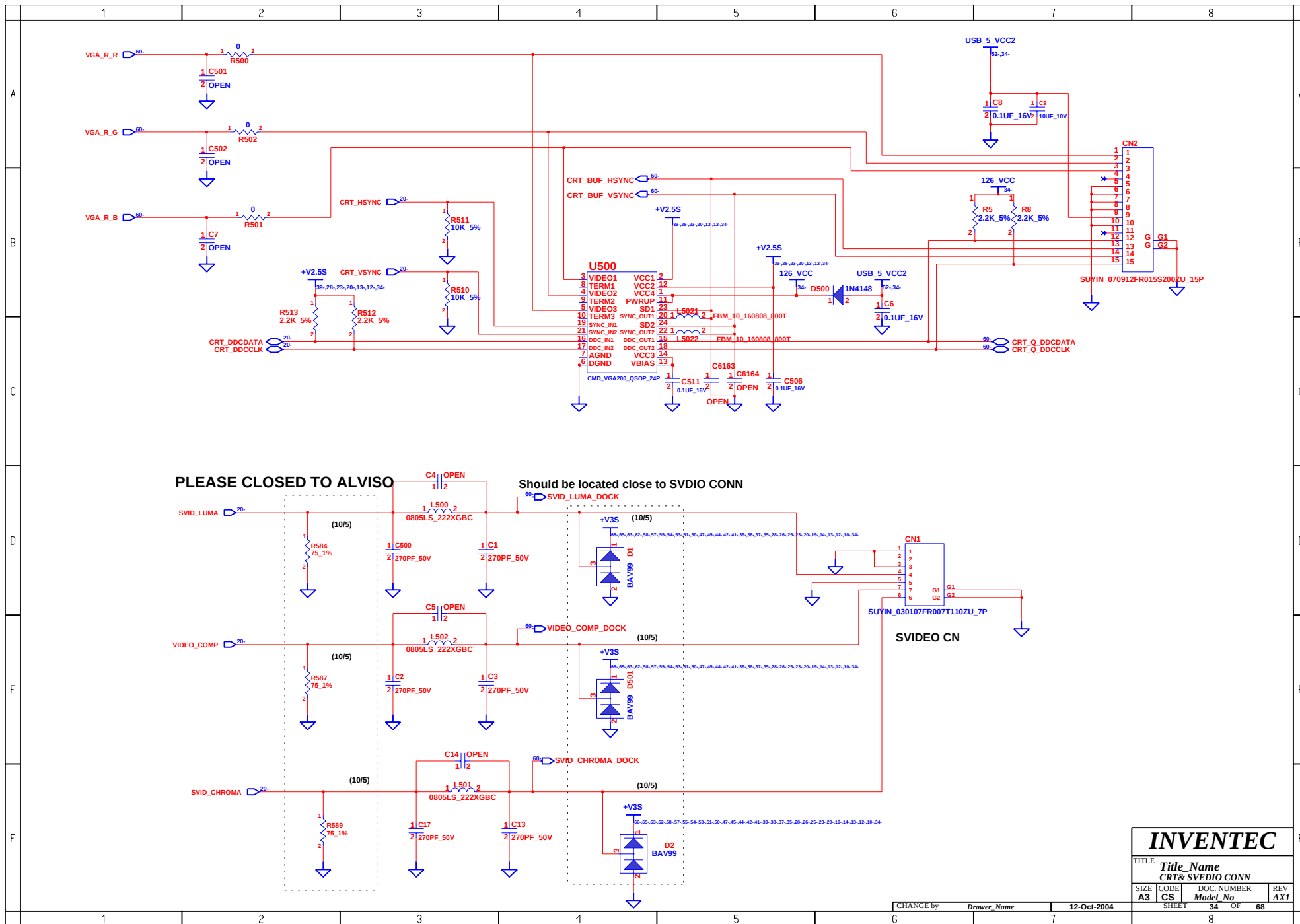
CHANGE by **Drawn Name** 27-Sep-2003

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CHANGE by				1-Sep-2004																																																													
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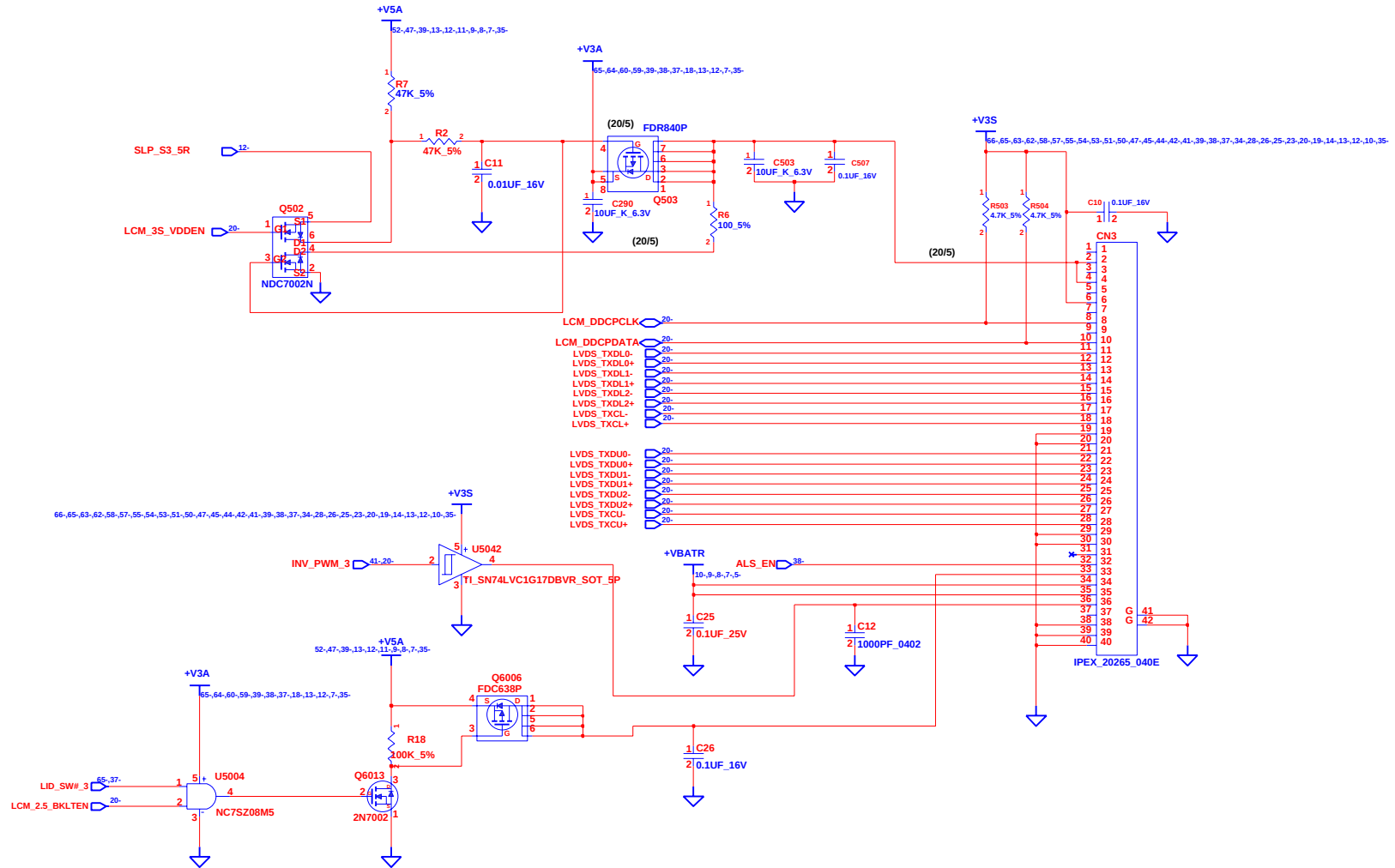
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E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC			
TITLE Title Name VIDEO RAM-2			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX1
SHEET 33		OF 68	

CHANGE by Draver Name 1-Sep-2004



Place closed to connector

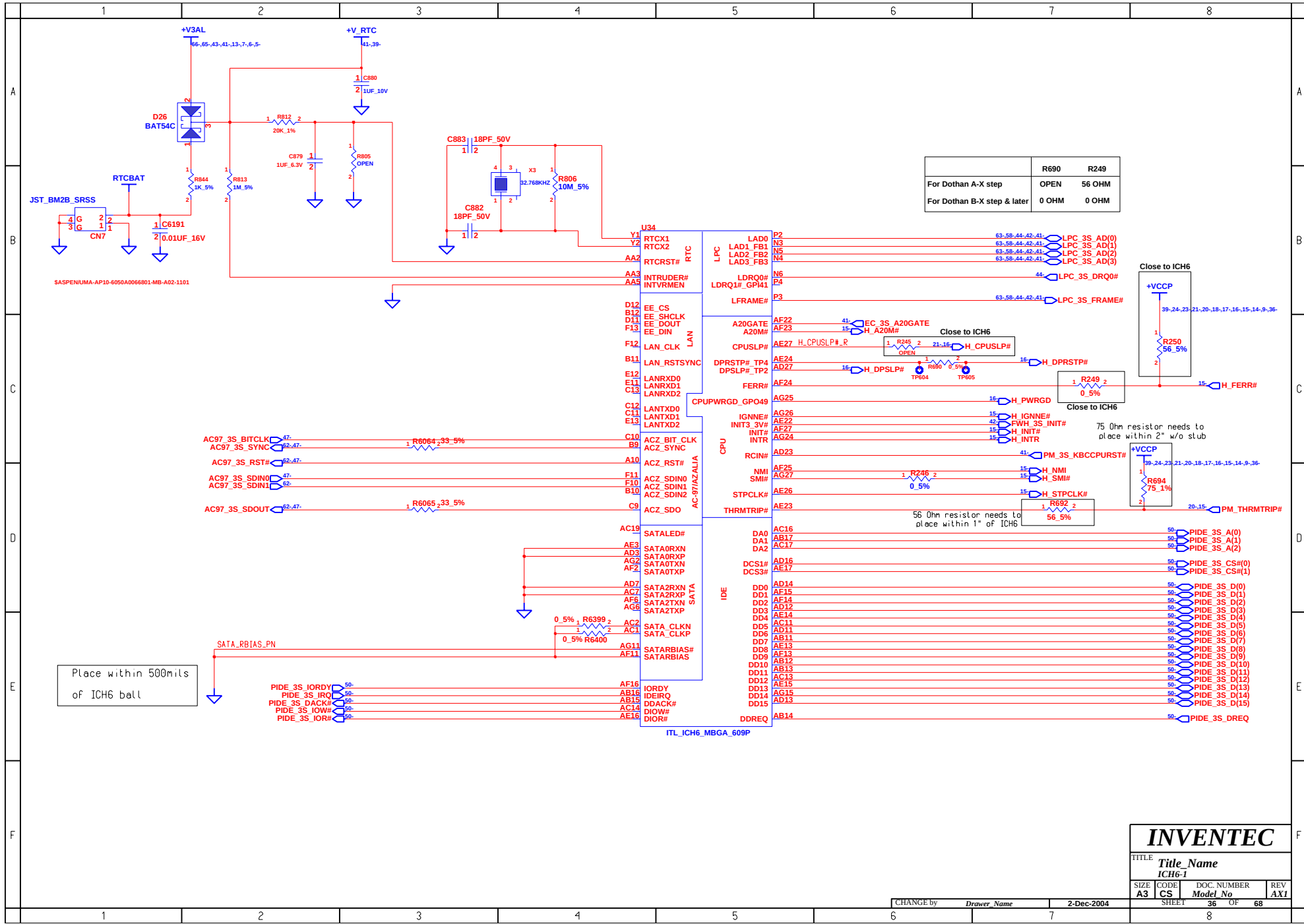


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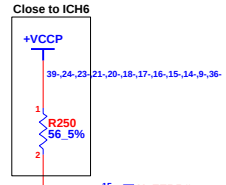
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A3	CS	Model No	AXI

CHANGE by *Drawn Name* 1-Dec-2004

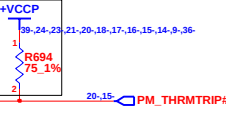
SHEET 35 OF 68



	R690	R249
For Dothan A-X step	OPEN	56 OHM
For Dothan B-X step & later	0 OHM	0 OHM



75 Ohm resistor needs to place within 2" w/o stub



56 Ohm resistor needs to place within 1" of ICH6

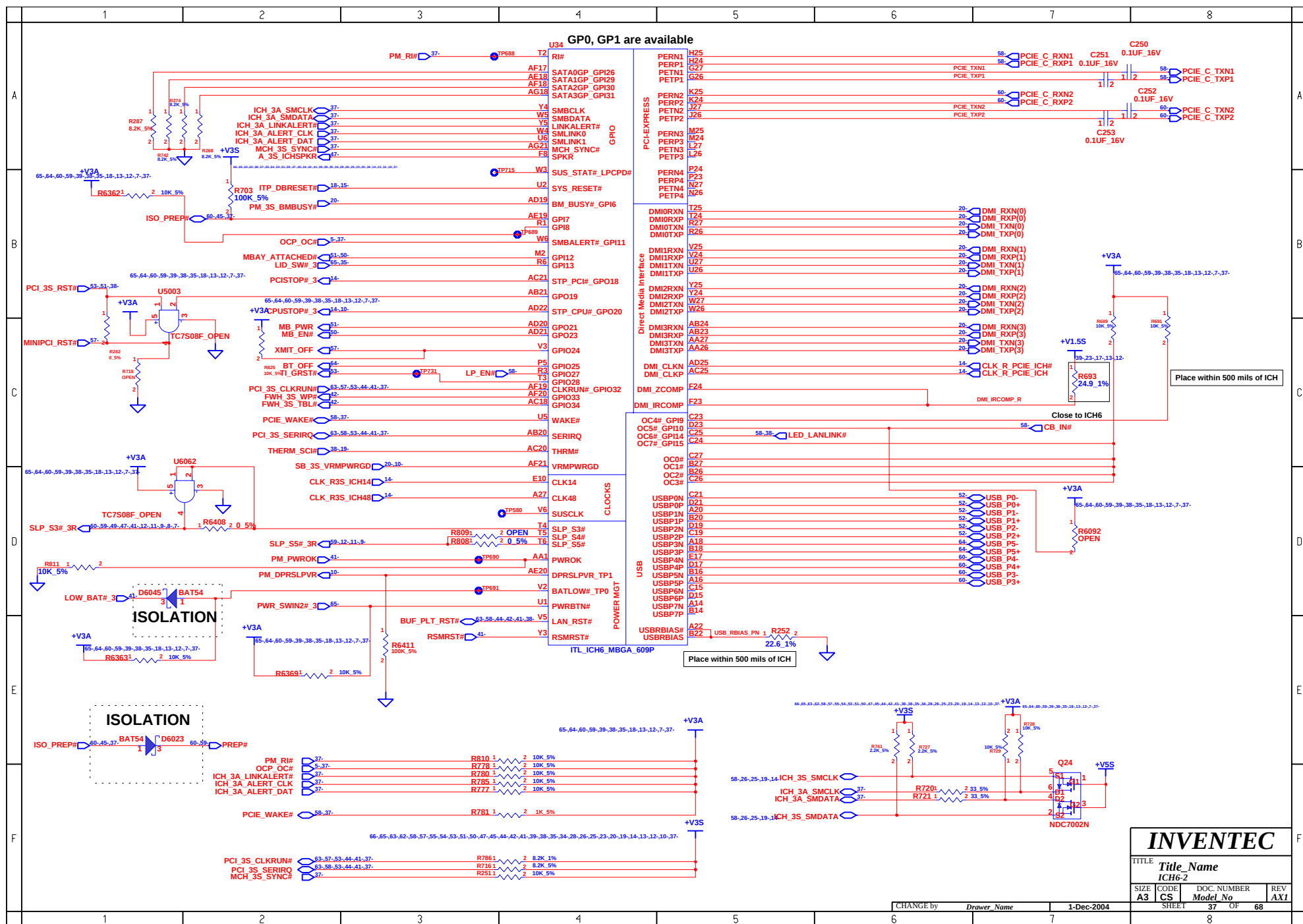


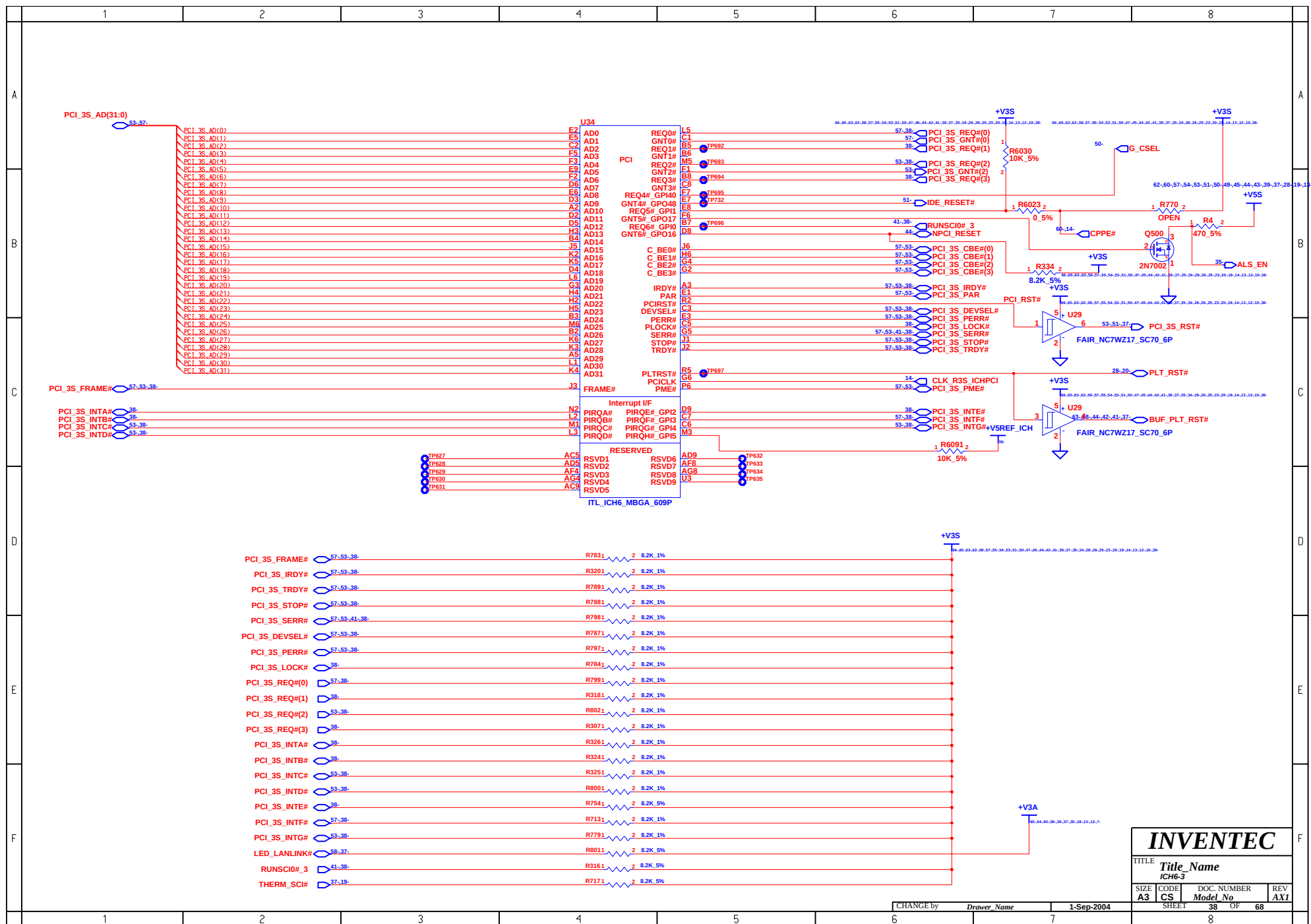
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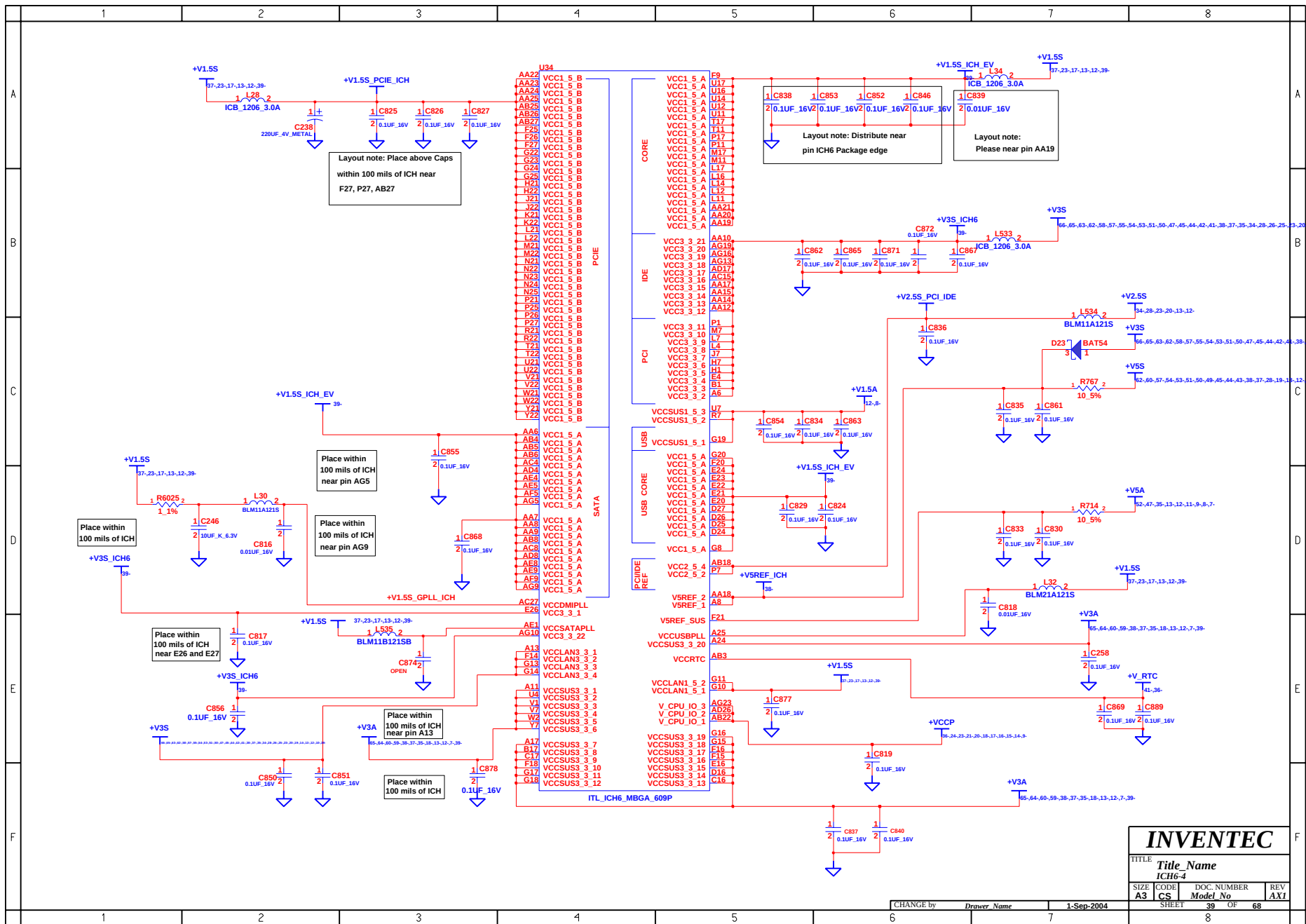
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SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX1

CHANGE by *Drawn Name* 2-Dec-2004

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INVENTEC			
TITLE Title Name ICH6-4			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AXI
CHANGE by Drawer Name		1-Sep-2004	
SHEET 39		OF 68	

	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F

U34

E27

V6

Y27

Y26

Y23

W7

W25

W24

W23

W1

V4

V27

V26

V25

U25

U24

U23

U19

U15

T7

T26

T23

T16

T15

T14

T13

T12

T1

R4

R25

R24

R23

R17

R16

R15

R14

R13

R12

R11

P22

P16

P15

P14

P13

P12

N7

N17

N16

N15

N14

N13

N12

N11

N1

M4

M27

M26

M23

M18

M15

M14

M13

M12

L25

L24

L23

L15

L13

K7

K27

K26

K23

K1

J5

J24

J23

H27

H26

H23

G9

G7

G24

G12

G1

VSS172

VSS171

VSS170

VSS169

VSS168

VSS167

VSS166

VSS165

VSS164

VSS163

VSS162

VSS161

VSS160

VSS159

VSS158

VSS157

VSS156

VSS155

VSS154

VSS153

VSS152

VSS151

VSS150

VSS149

VSS148

VSS147

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VSS122

VSS121

VSS120

VSS119

VSS118

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VSS116

VSS115

VSS114

VSS113

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VSS109

VSS108

VSS107

VSS106

VSS105

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VSS102

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VSS98

VSS97

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VSS95

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VSS91

VSS90

VSS89

VSS88

VSS87

F4

F22

F19

F17

E25

E19

E18

E15

E14

D7

D22

D20

D18

D17

D13

D10

D1

C4

C22

C20

C18

C14

B25

B24

B23

B21

B19

B15

B13

AG7

AG3

AG22

AG20

AG17

AG14

AG12

AG1

AF7

AF3

AF26

AF12

AF10

AF1

AE7

AE6

AE25

AE21

AE2

AE12

AE11

AE10

AD6

AD24

AD2

AD18

AD15

AD10

AD1

AC3

AC6

AC26

AC24

AC23

AC22

AC10

AB9

AB7

AB2

AB19

AB10

AB1

AA4

AA16

AA13

AA11

A9

A7

A4

A26

A23

A21

A19

A15

A12

A1

ITL_ICH6_MBG6_609P

INVENTEC

TITLE

Title_Name

ICH6-5

SIZE

A3

CODE

CS

DOC. NUMBER

Model No

REV

AX1

SHEET

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OF

68

CHANGE by

Drawer Name

1-Sep-2004

1

2

3

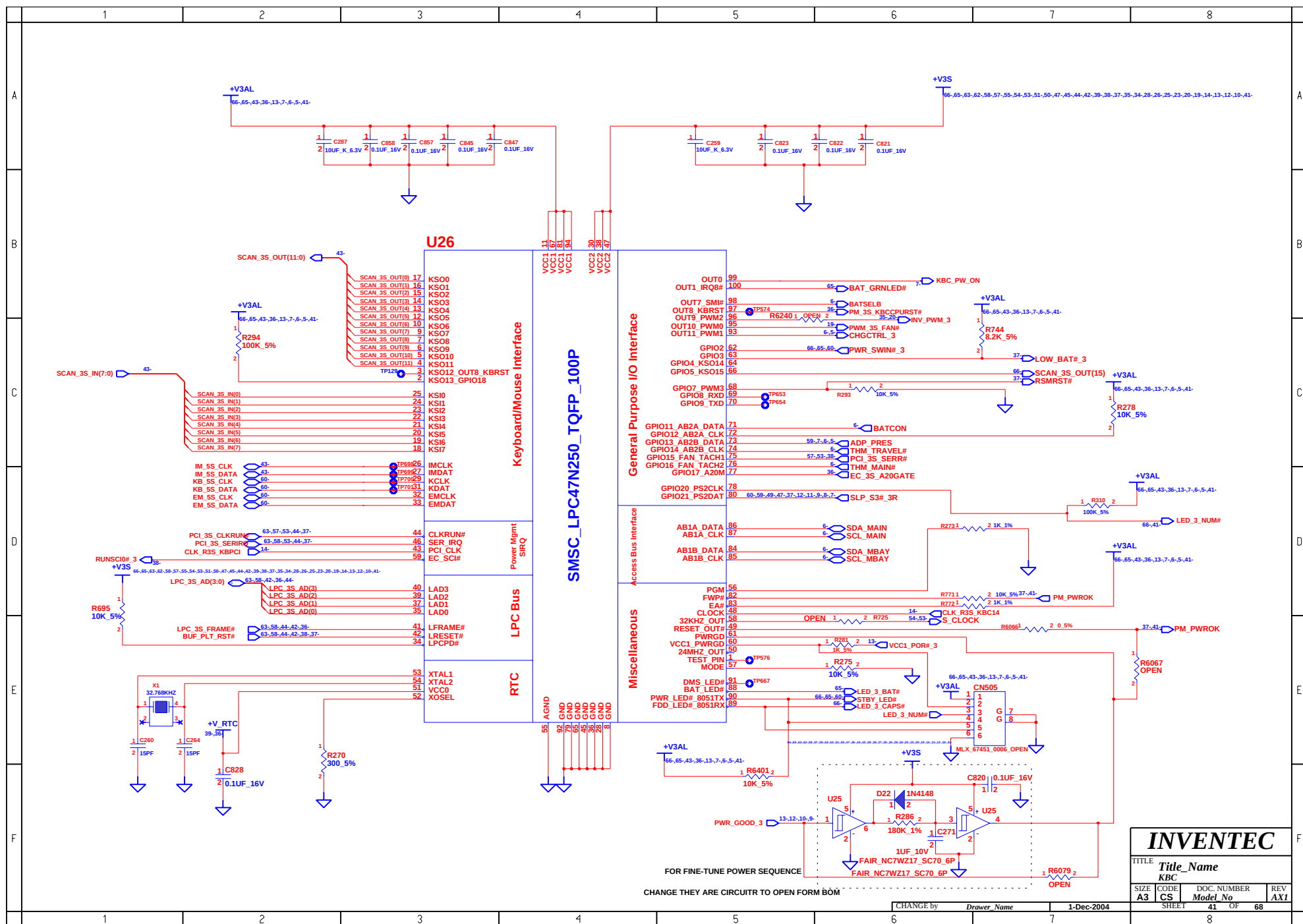
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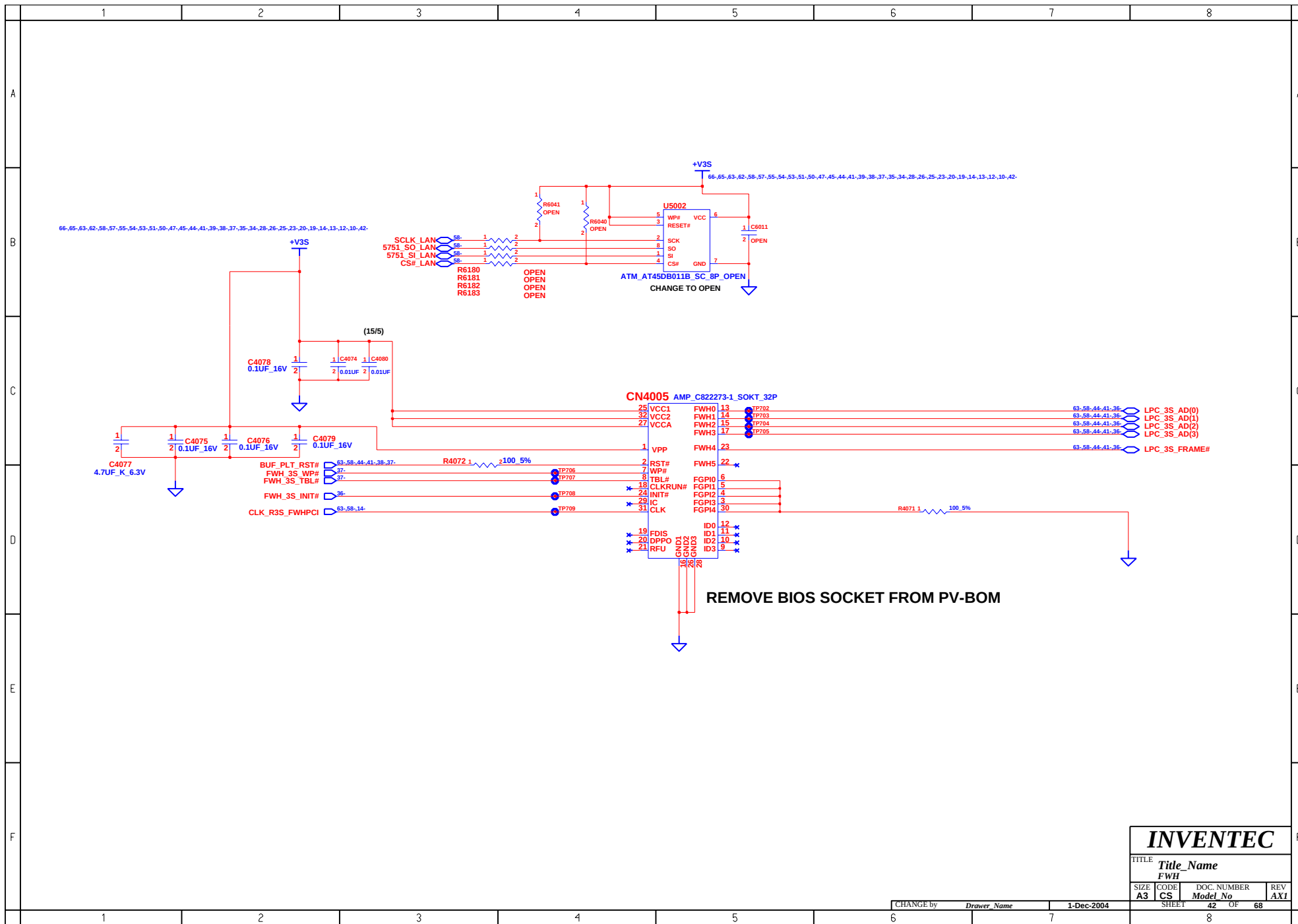
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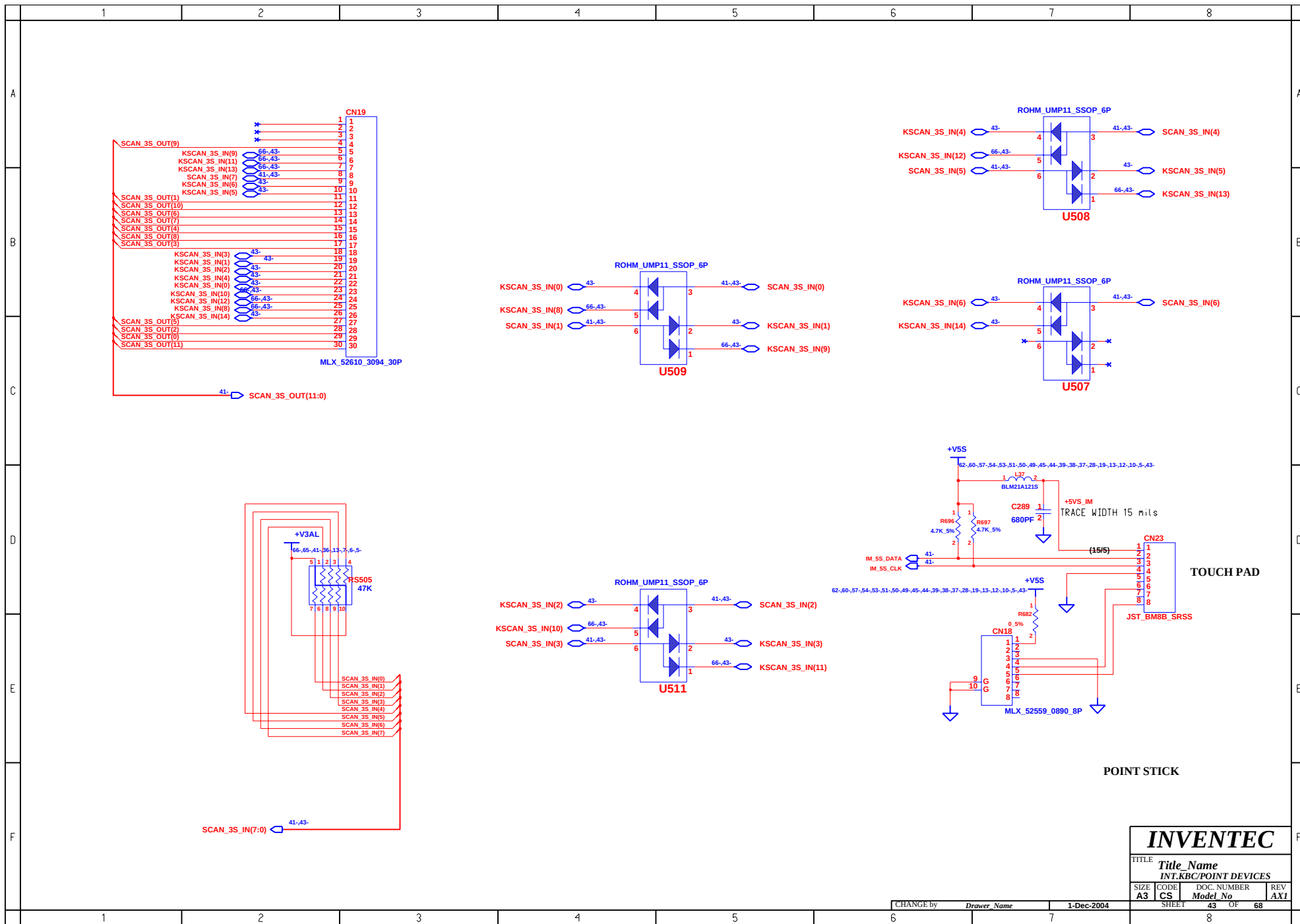


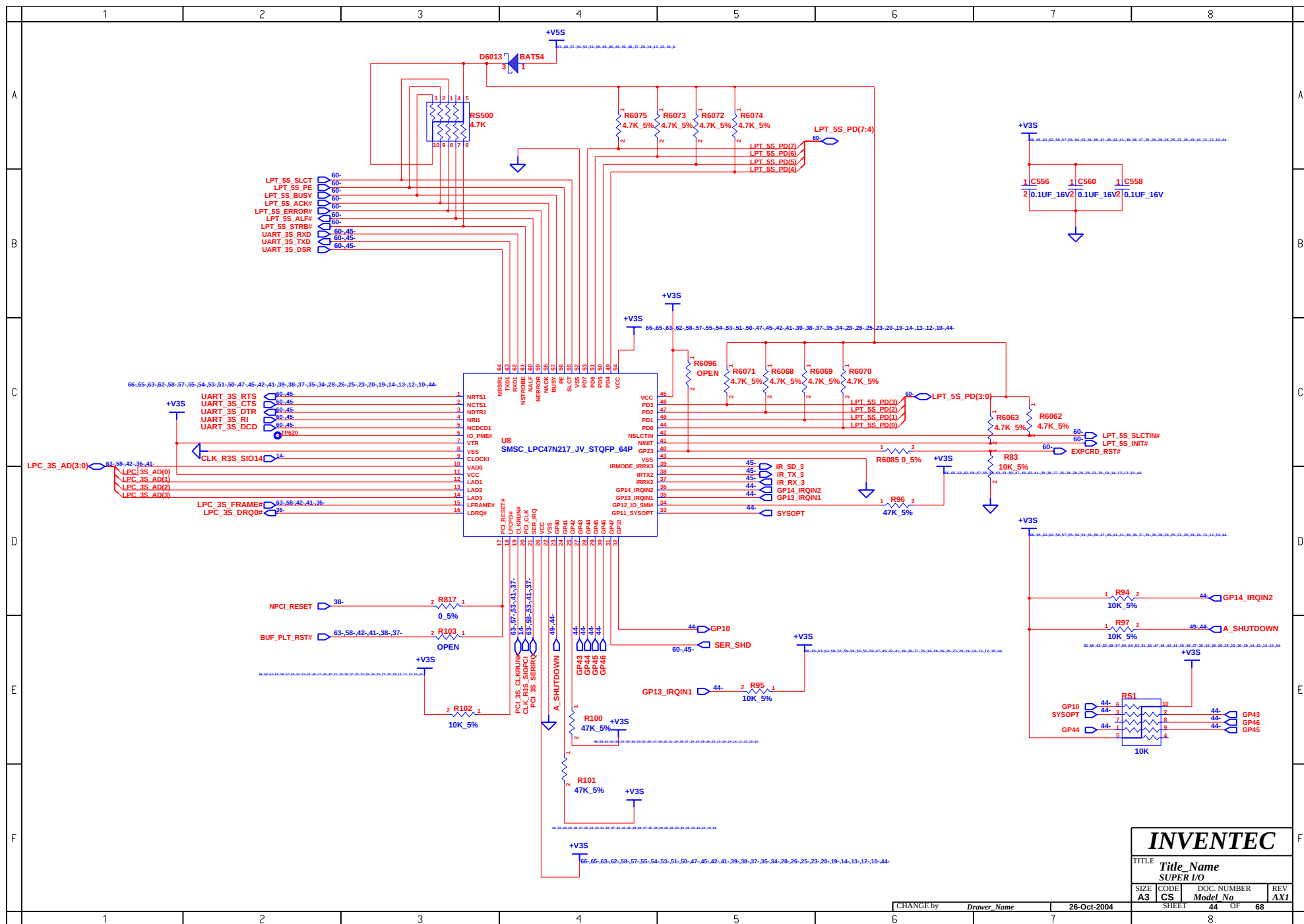
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FWH			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX1

CHANGE by Drawer Name 1-Dec-2004

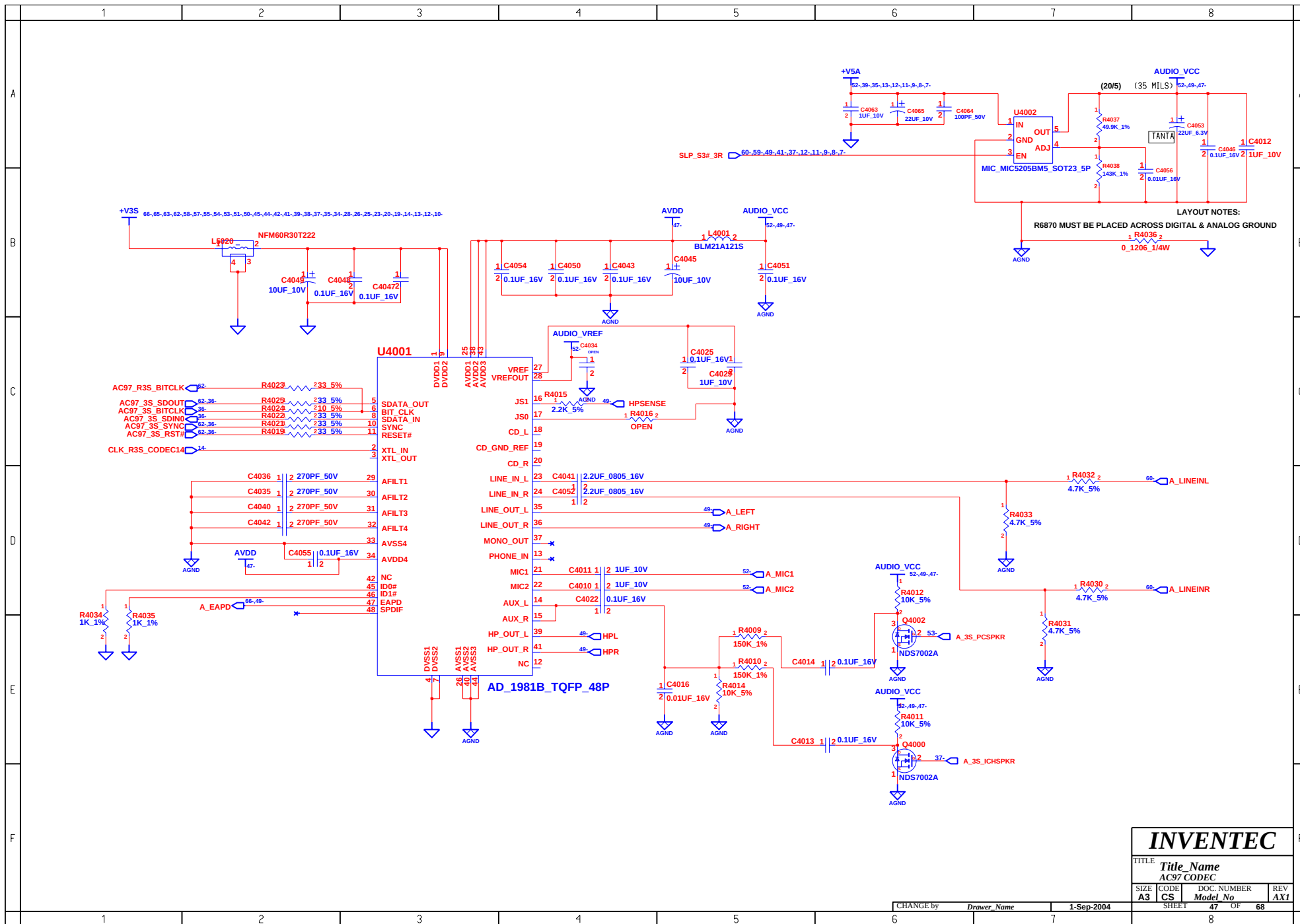
SHEET 42 OF 68

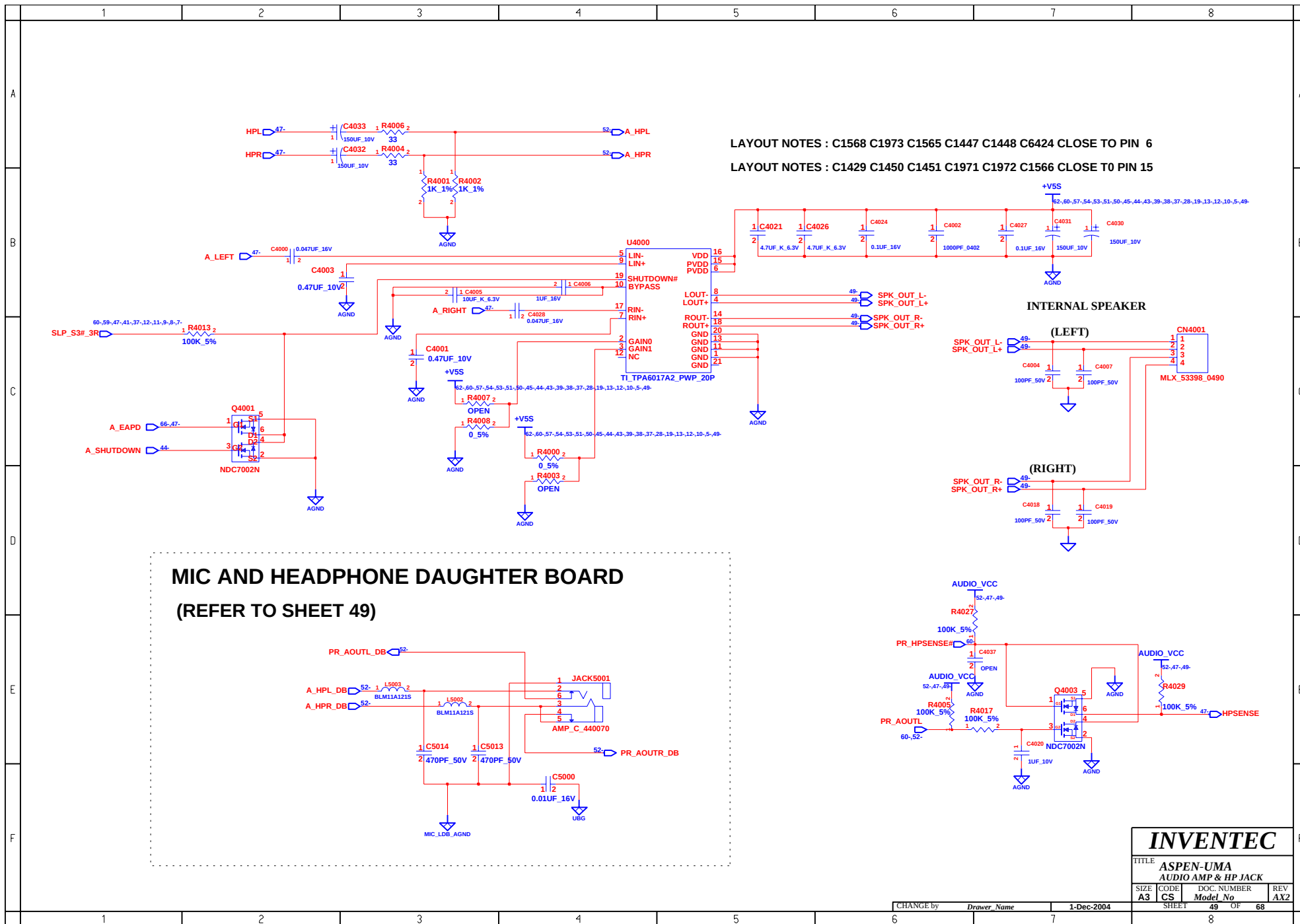


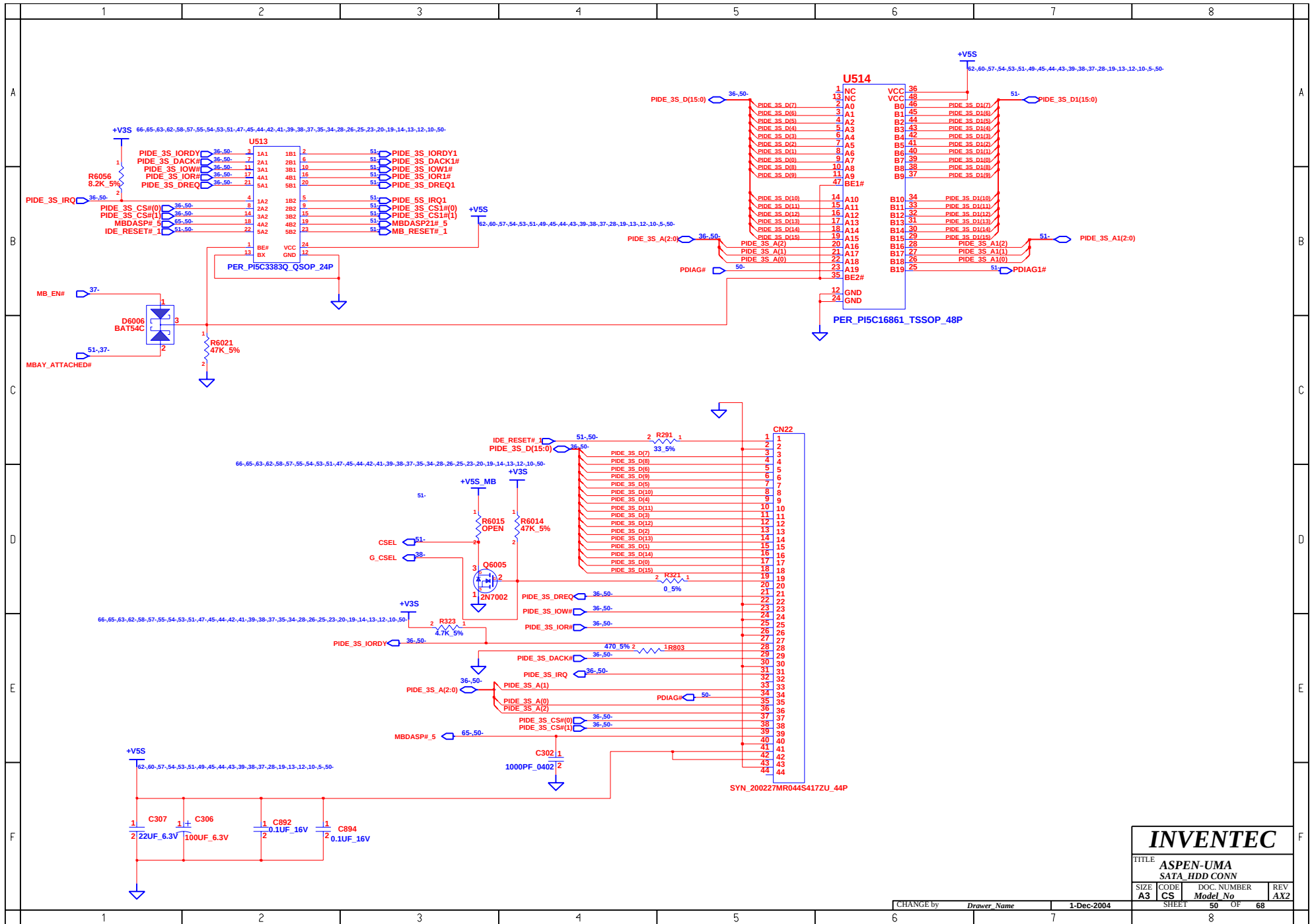


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C									C
D									D
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INVENTEC			
TITLE <i>Title Name</i> PARALLER PORT			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX1
CHANGE by <i>Drawn Name</i>		1-Sep-2004	
SHEET		46	OF 68



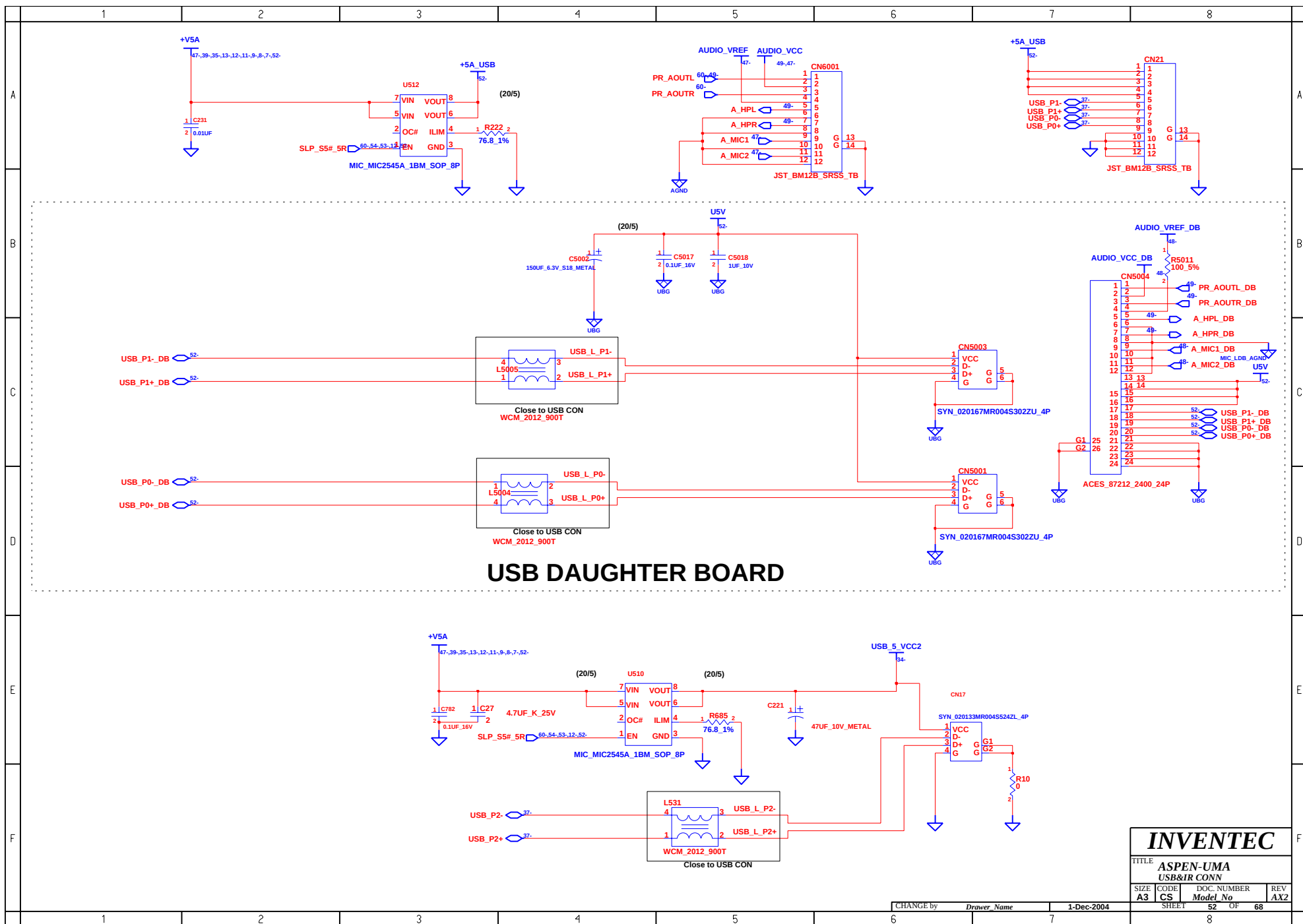


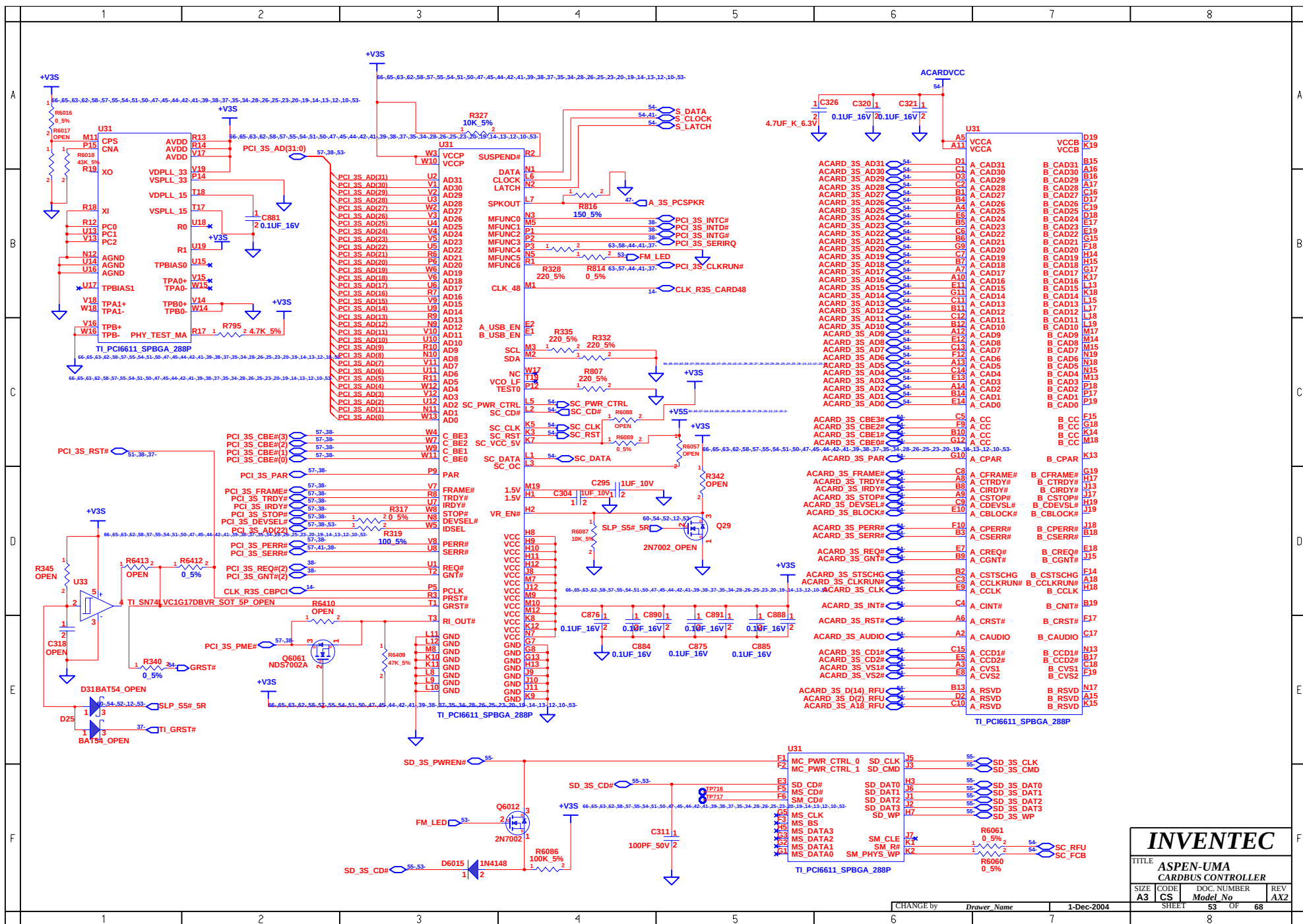


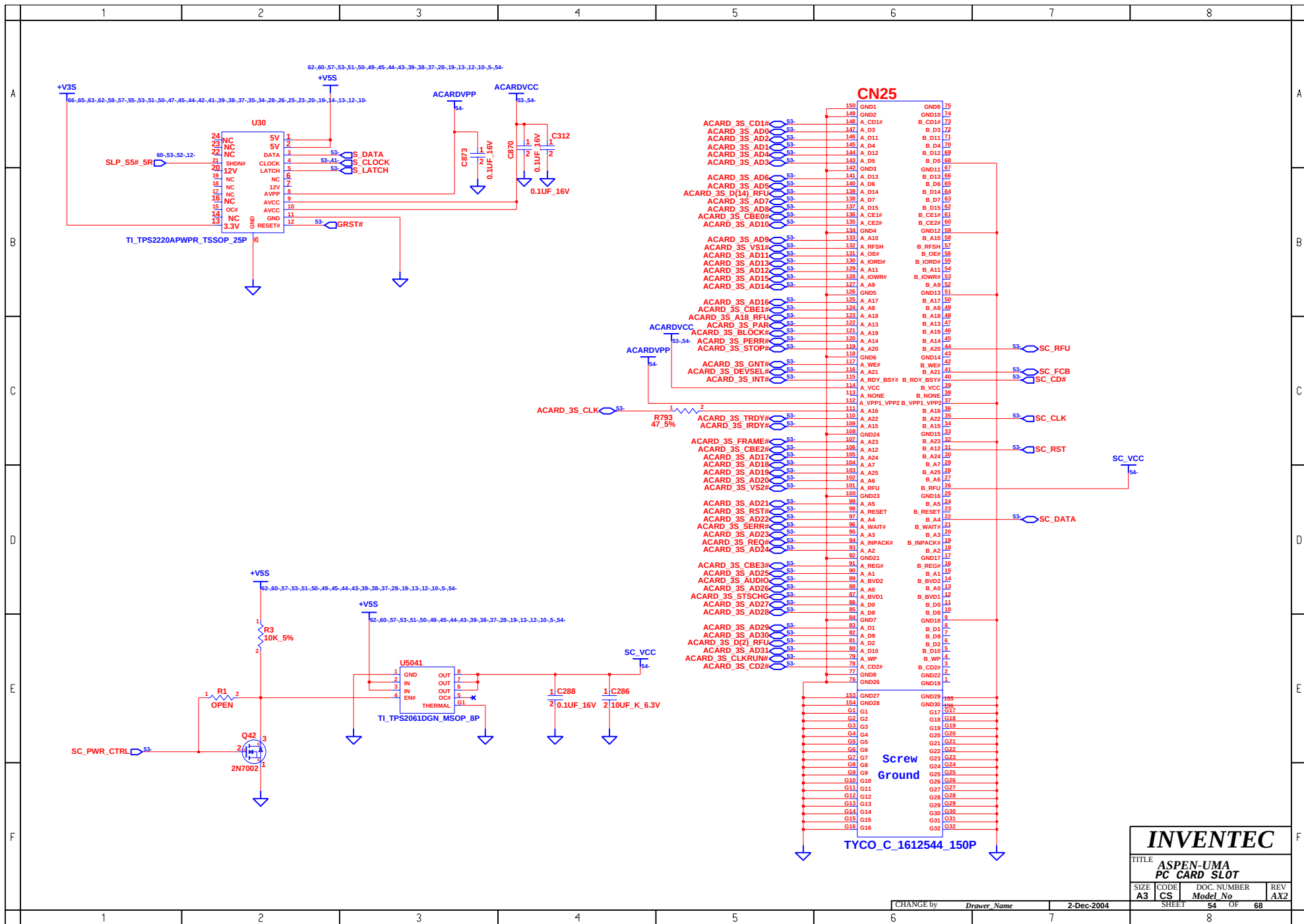
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TITLE ASPEN-UMA SATA_HDD CONN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
SHEET	50	OF	68

CHANGE by *Drawer Name* 1-Dec-2004







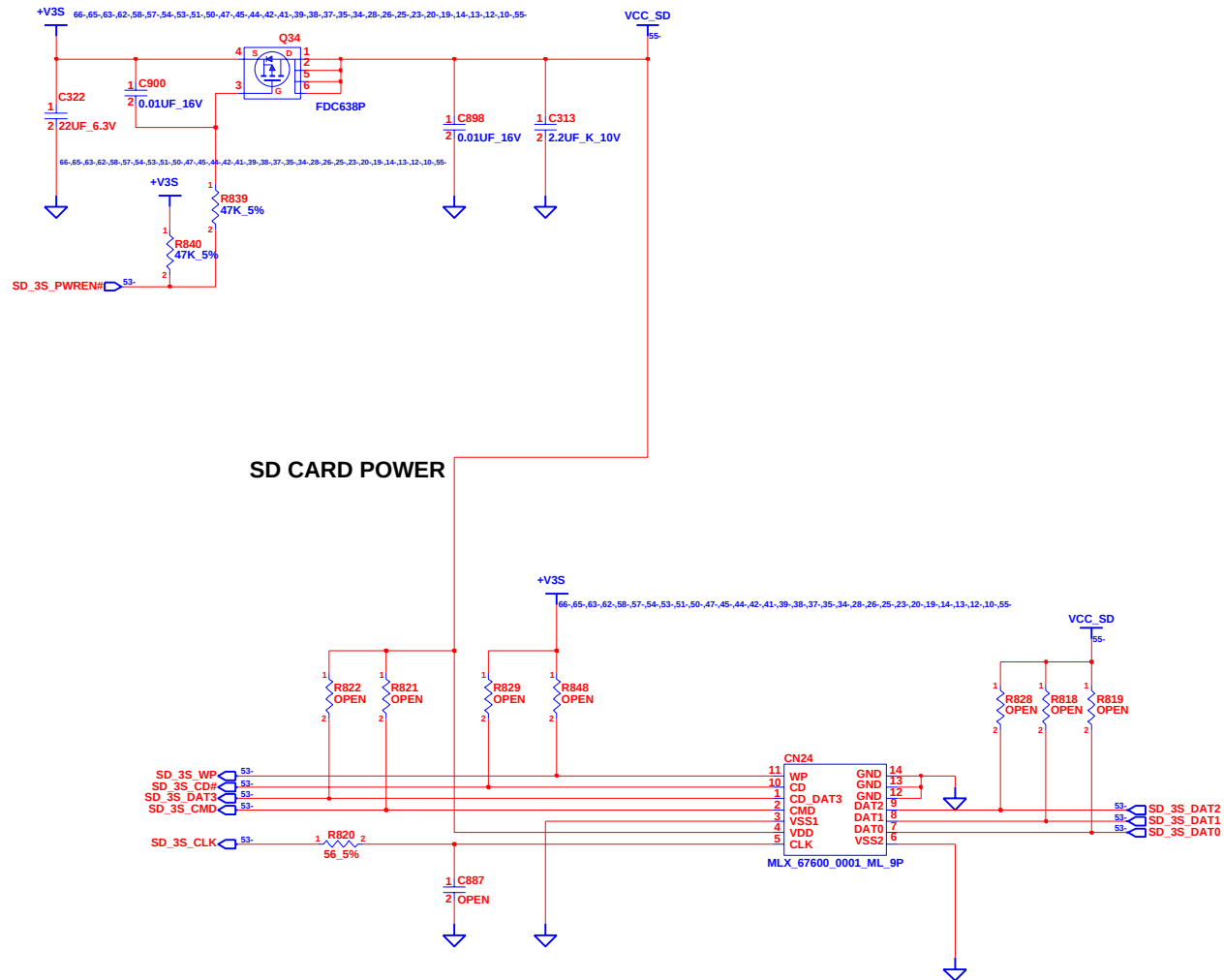
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TITLE
ASPEN-UMA
PC CARD SLOT

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

CHANGE by: *Drawn Name* 2-Dec-2004

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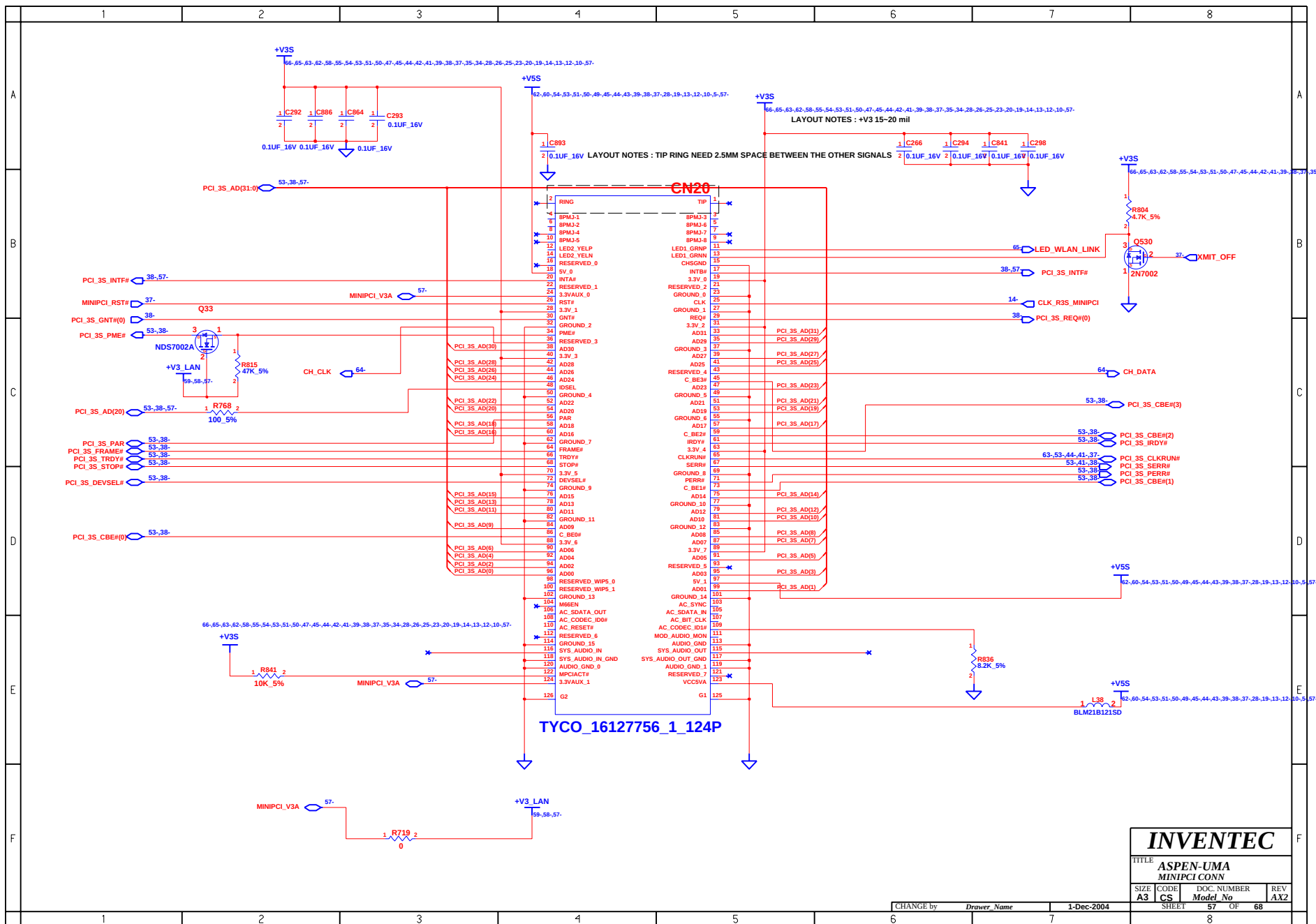


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SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
SHEET 55 OF 68			

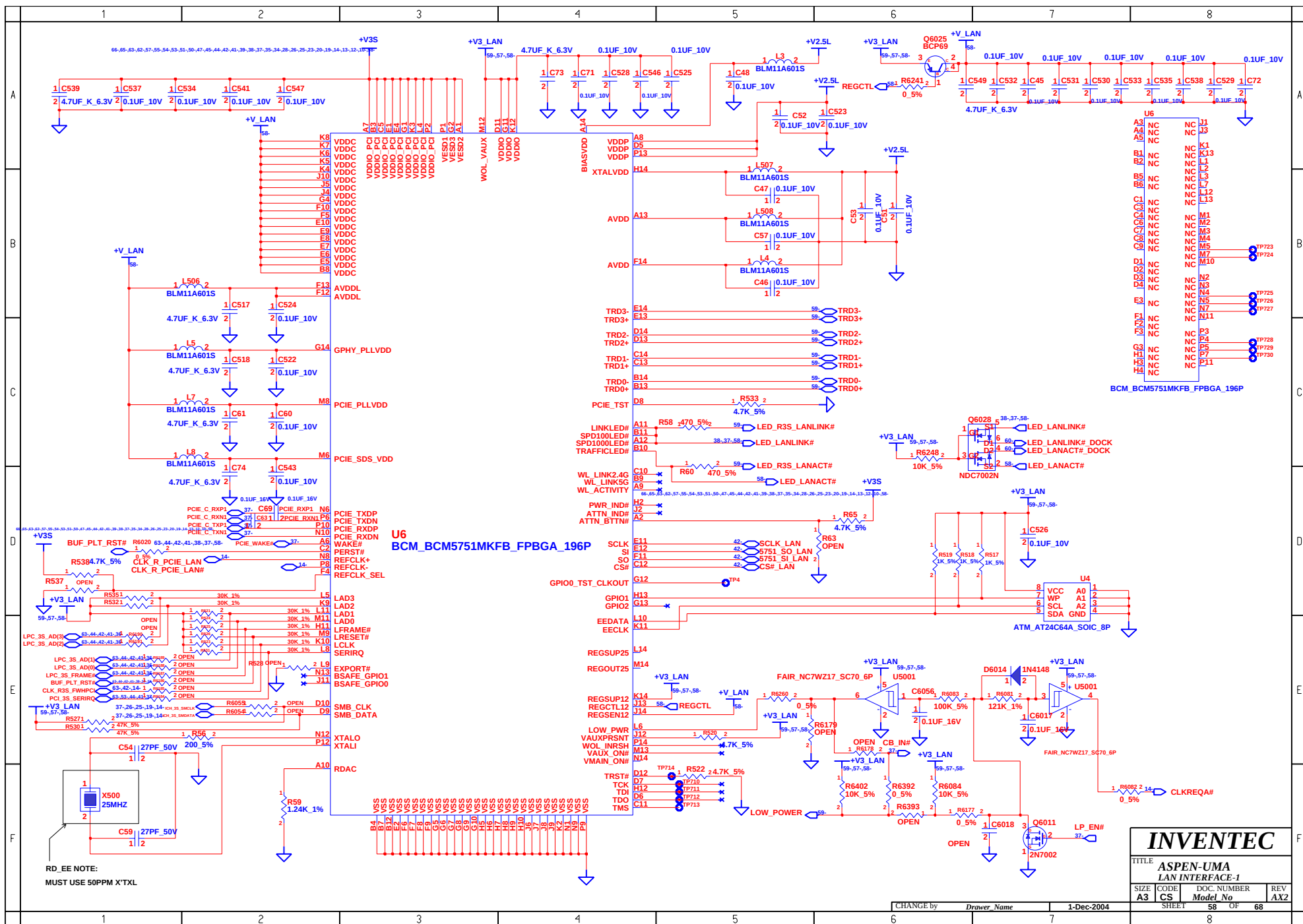
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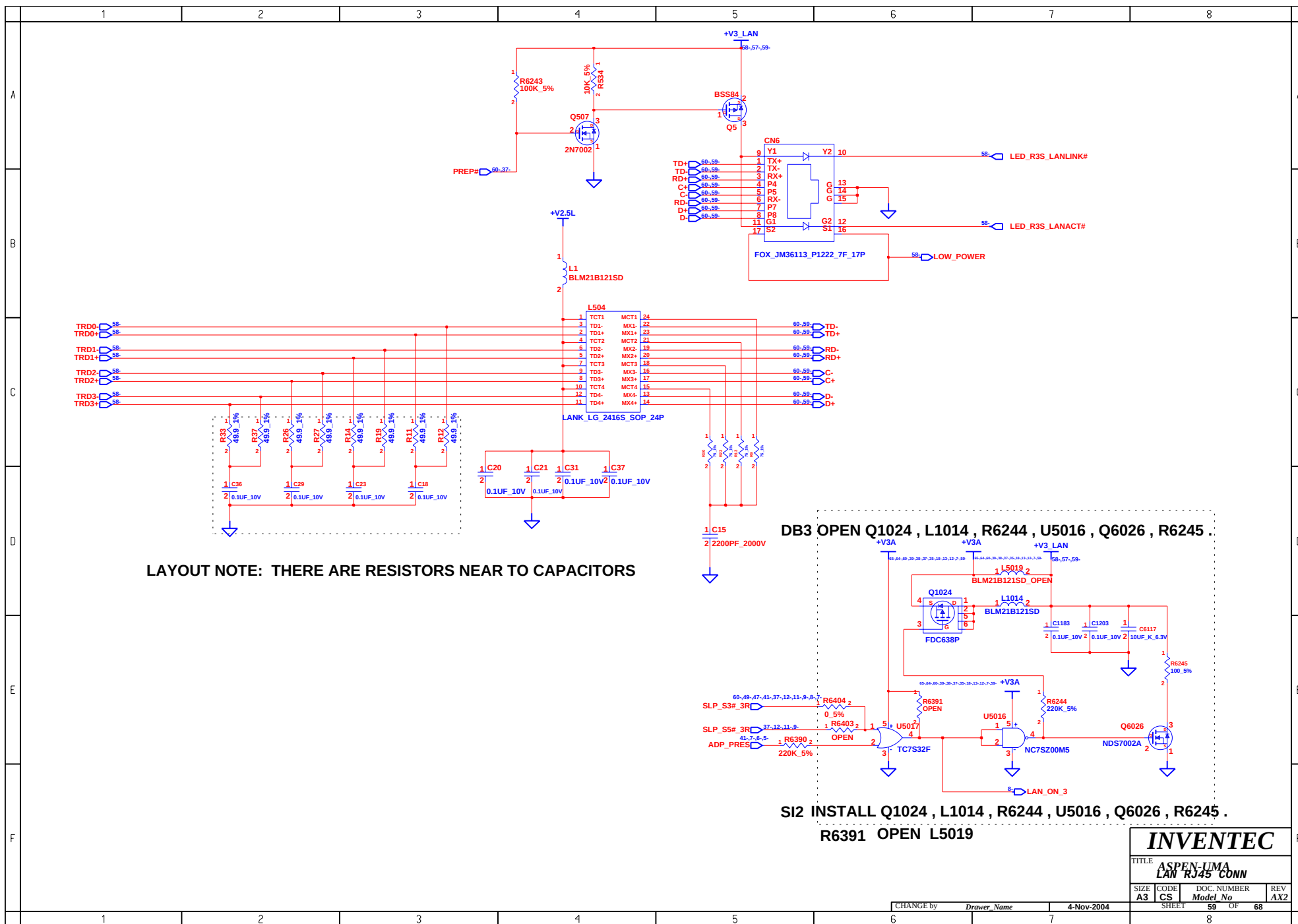
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INVENTEC			
TITLE ASPEN-UMA DAUGHTER BOARD CNN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
CHANGE by Draver Name		30-Aug-2004	SHEET 56 OF 68



INVENTEC			
TITLE			
ASPEN-UMA			
MINIPCI_CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2
CHANGE by		Drawer Name	1-Dec-2004
SHEET		57	OF 68





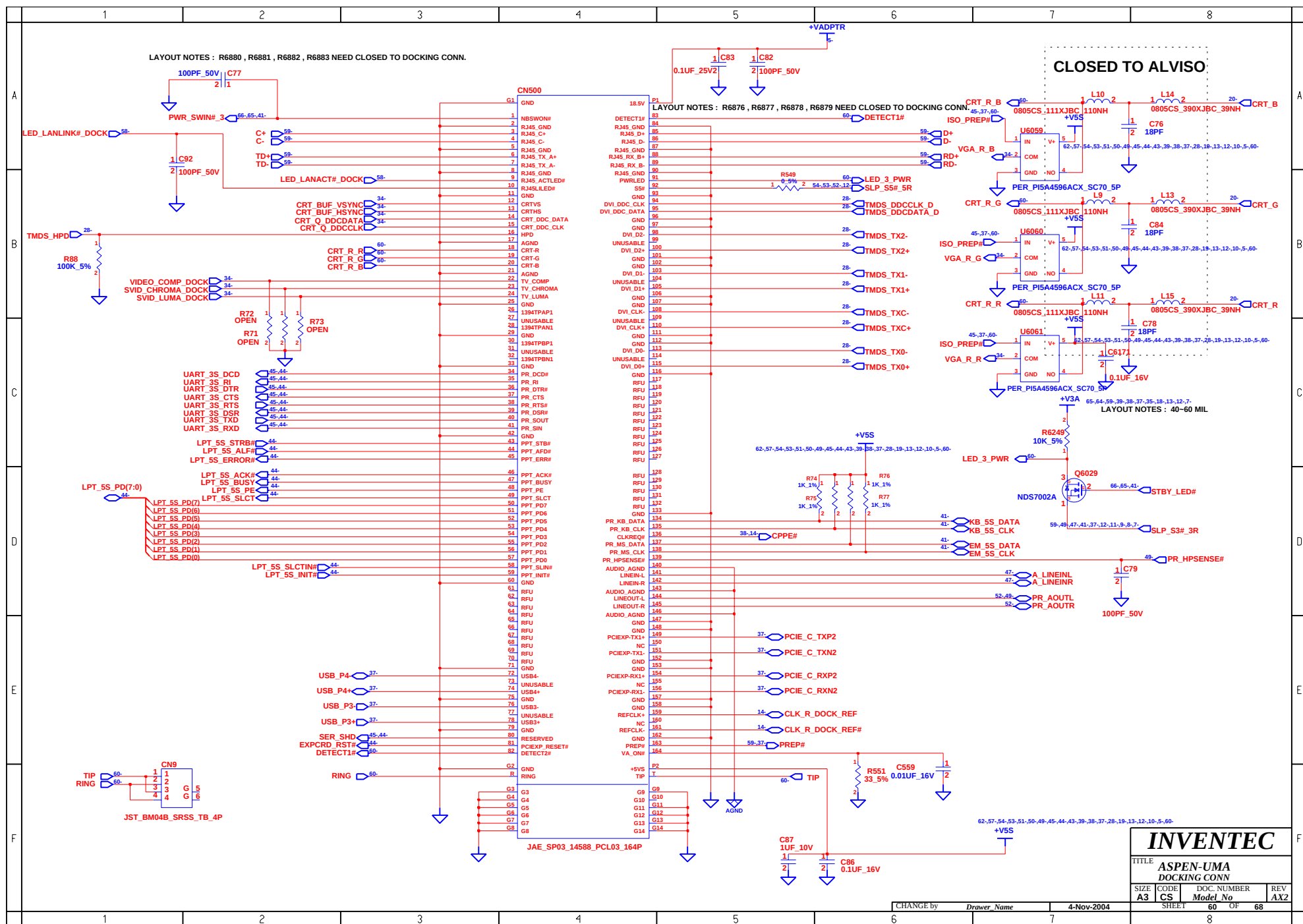
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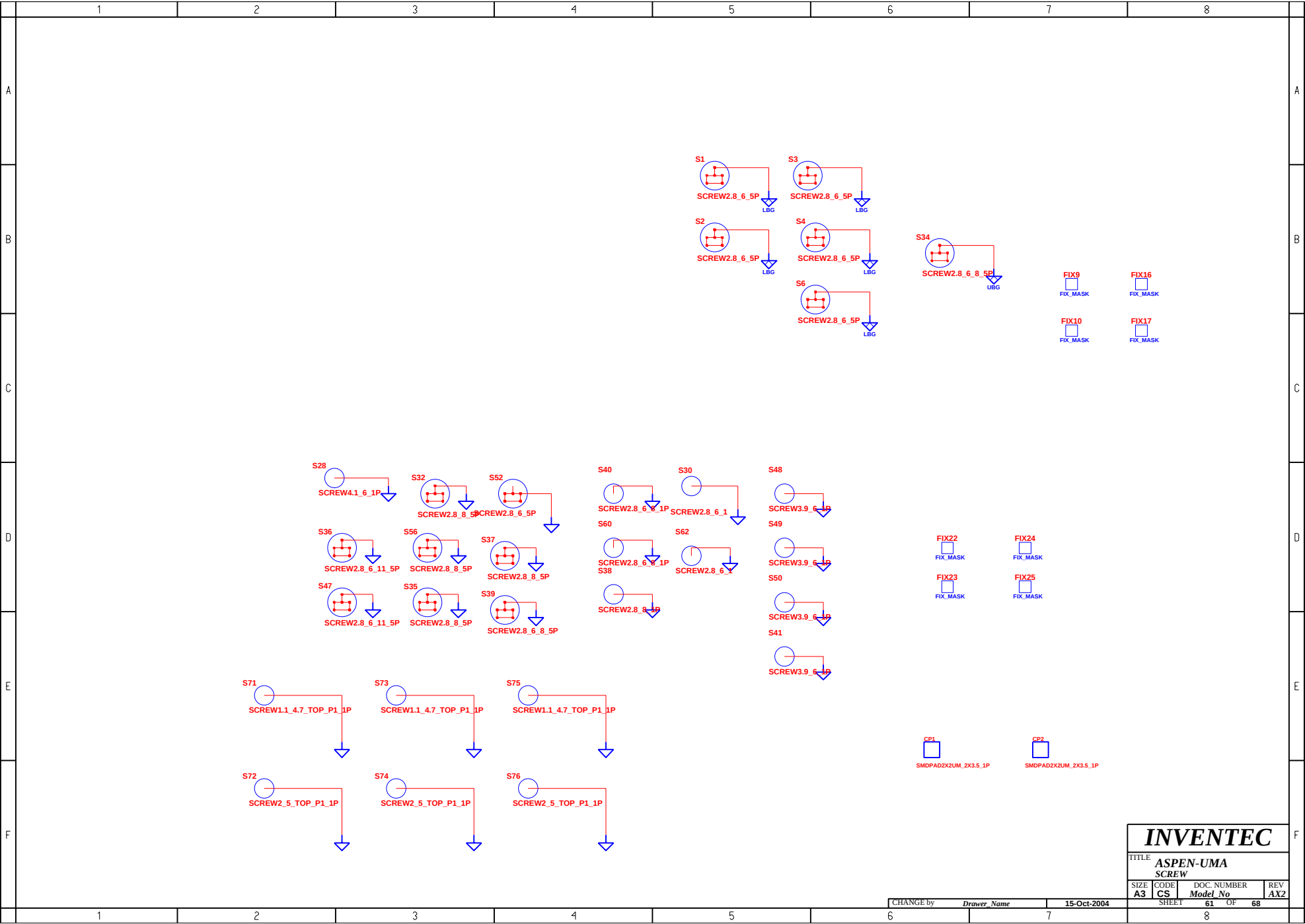
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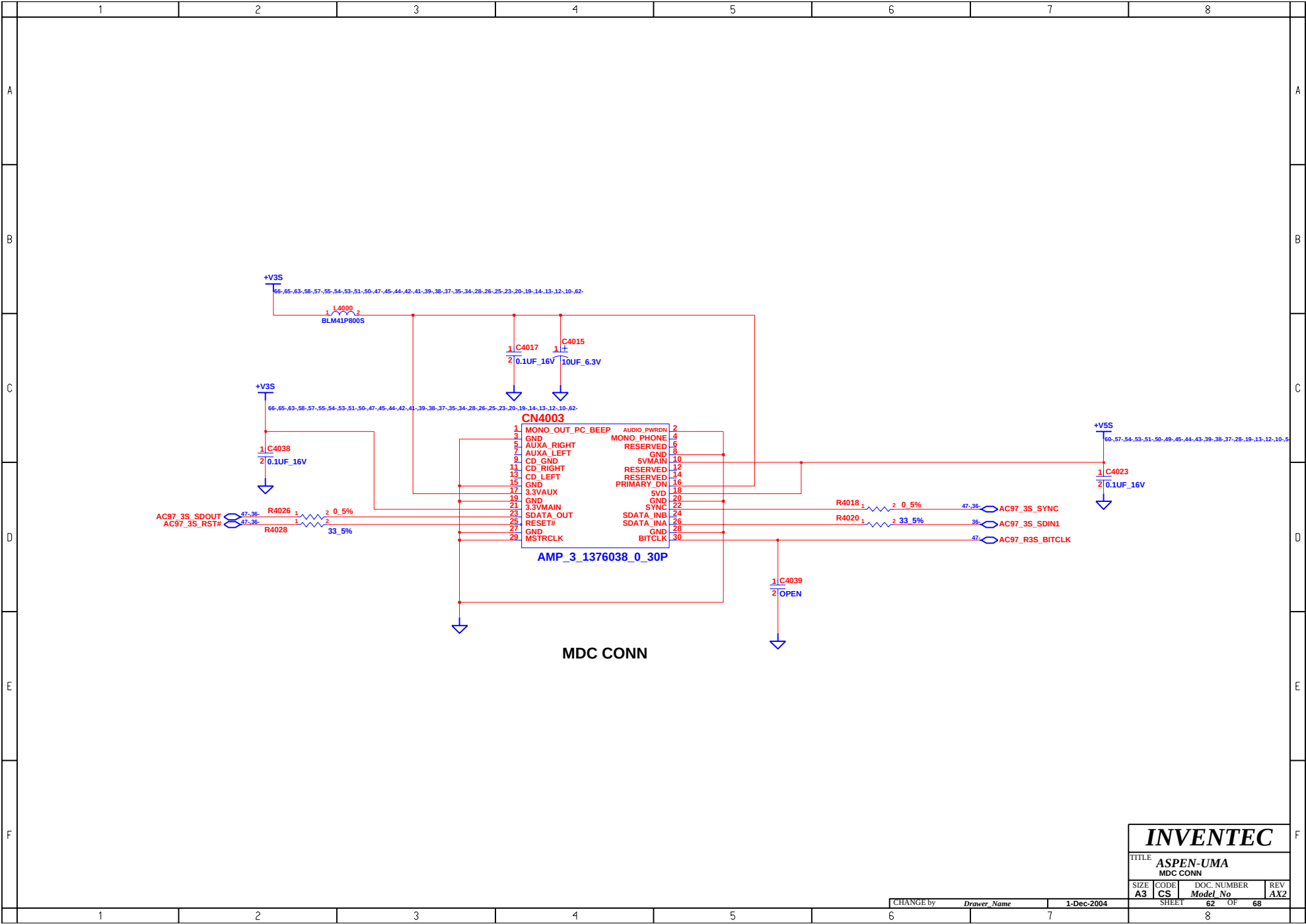
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A3	CS	Model No	AX2

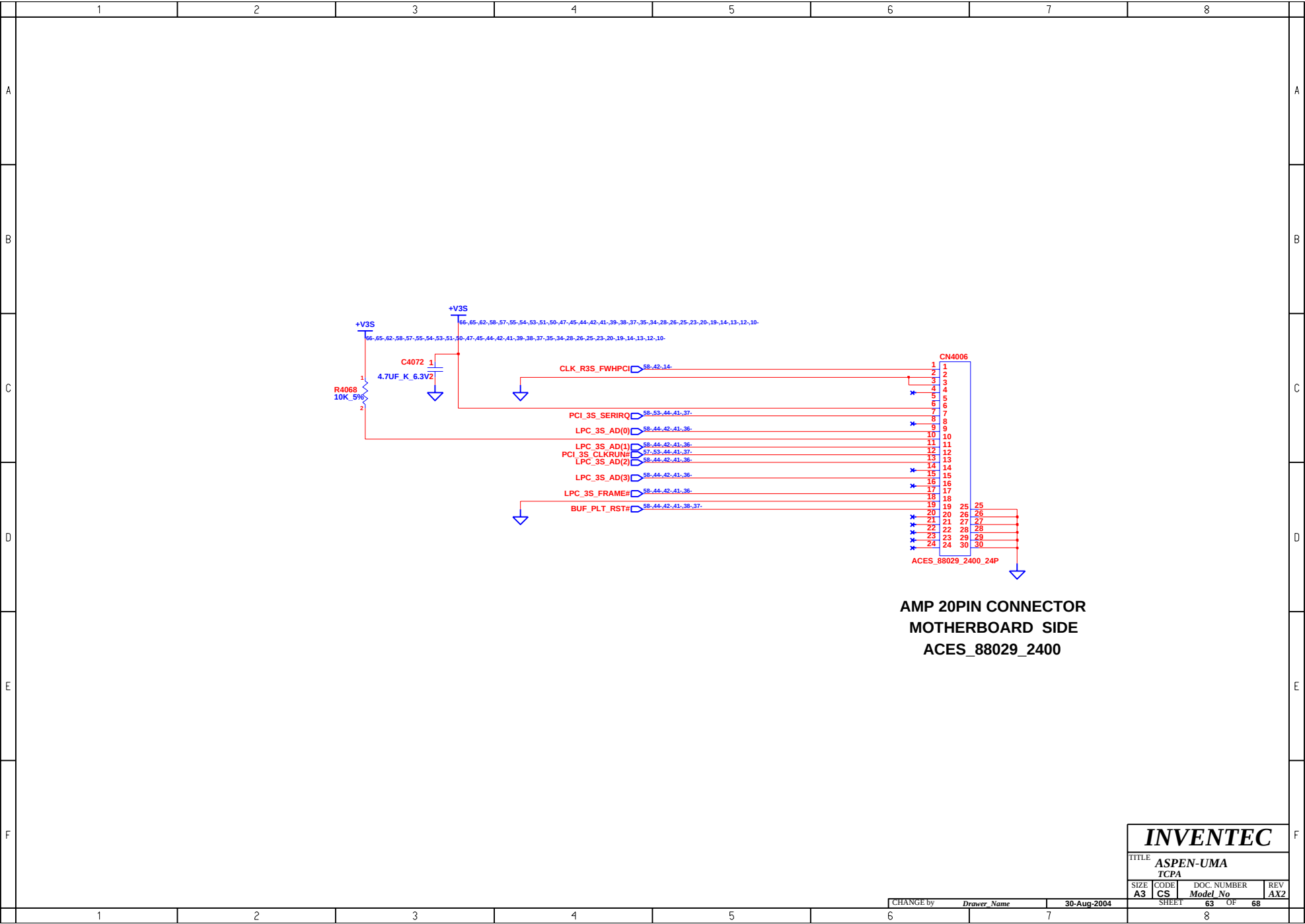
CHANGE by Drawer Name 4-Nov-2004

SHEET 59 OF 68









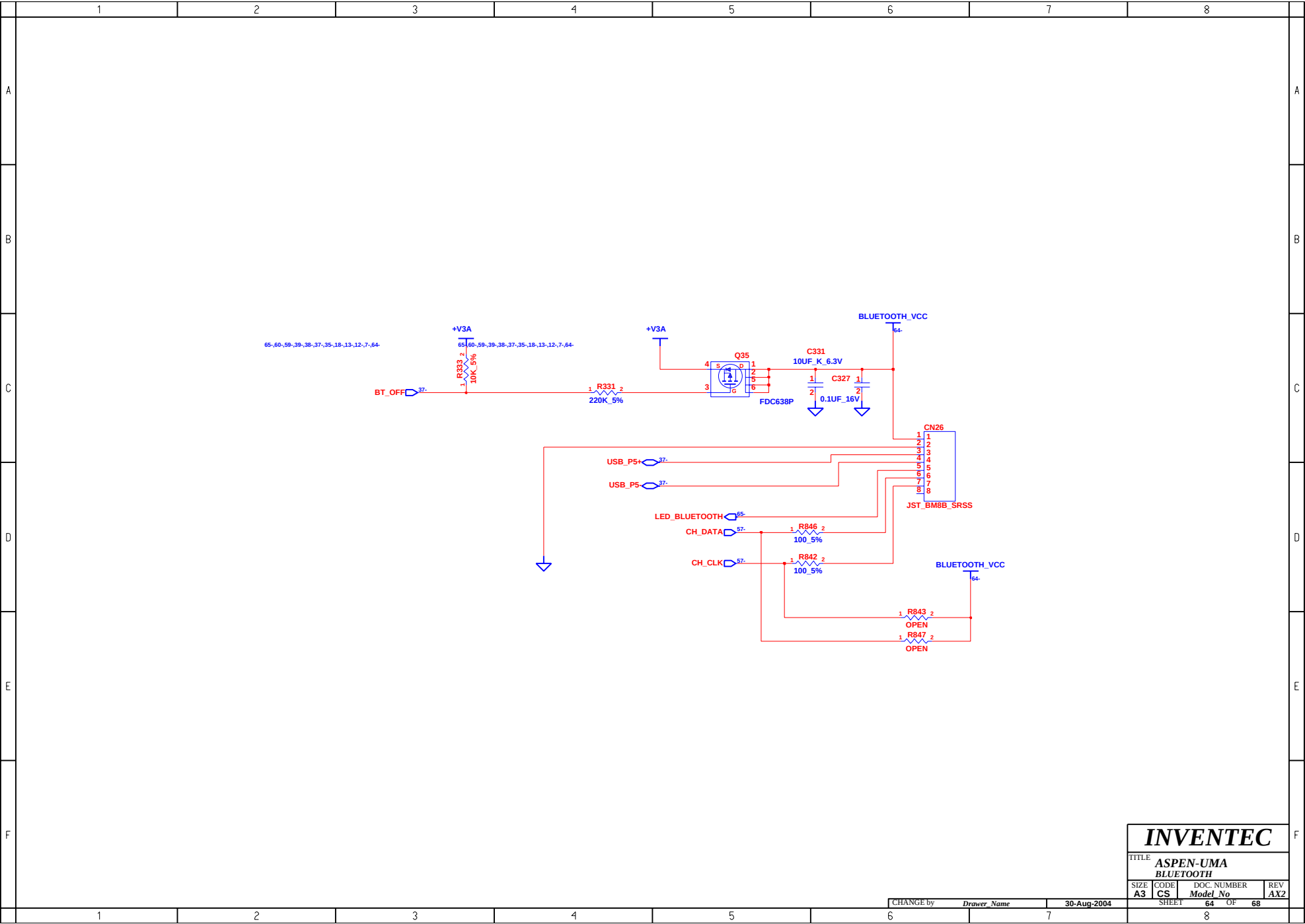
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TITLE
ASPEN-UMA
TCPA

SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
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SHEET 63 OF 68

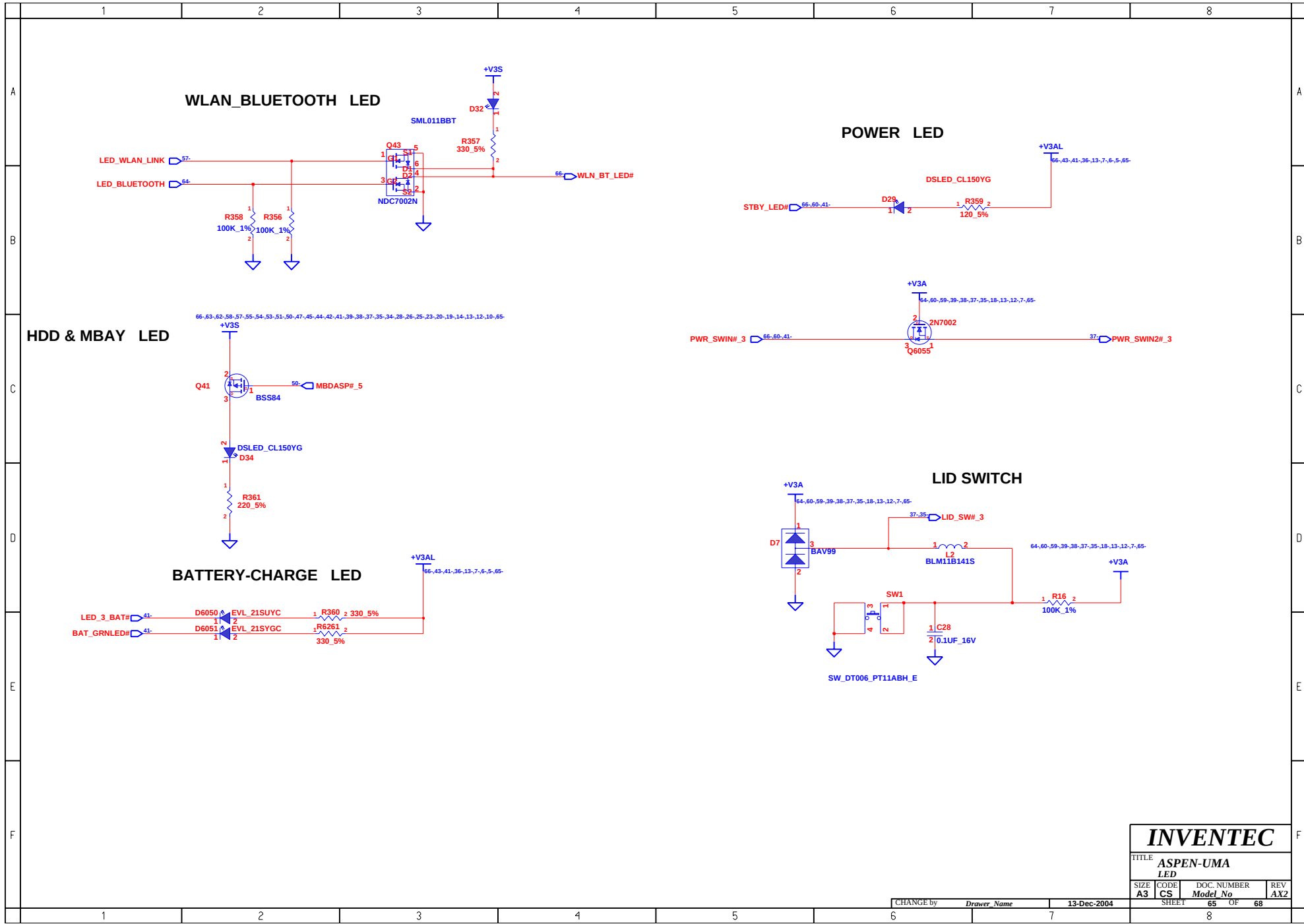
CHANGE by *Drawn Name* 30-Aug-2004



INVENTEC

TITLE			
ASPEN-UMA BLUETOOTH			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

CHANGE by	Drawn Name	30-Aug-2004	SHEET	64	OF	68
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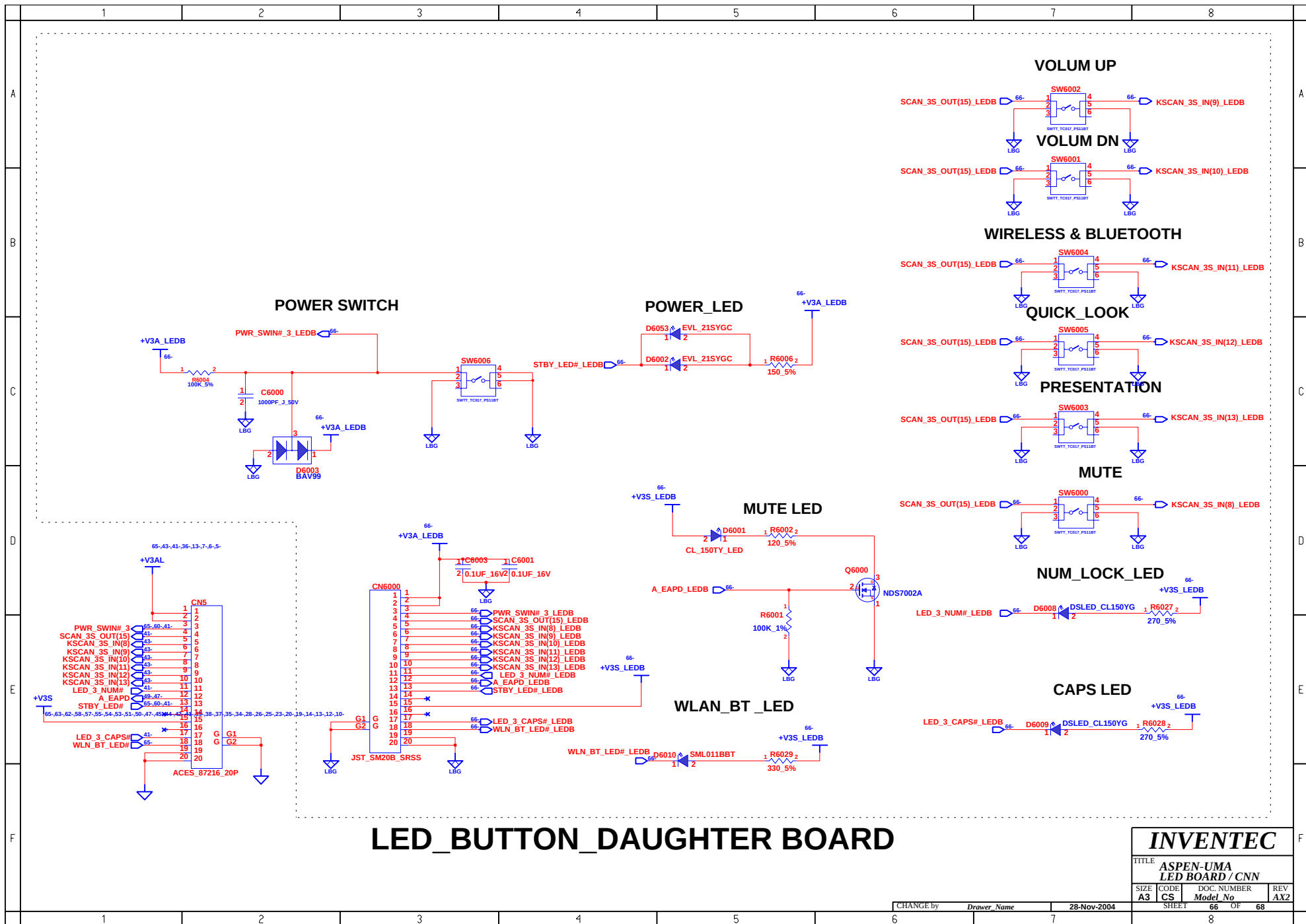


INVENTEC

TITLE
**ASPEN-UMA
LED**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

CHANGE by *Drawn Name* 13-Dec-2004
SHEET 65 OF 68



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IEC / HP Aspen_UMA MV Schematic Changes								December 10, 2004
HP Confidential Information						Open Items		
Item #	Project	Component / Subsection	PG. #	Description of Requested Change	Date Open+F7 / Closed	Requested By / Owner	Status / Aspen	Aspen FBK/Date
1	UMA	V2.5L	8	Improve the RGB signal waveform on CRT monitor R6247 from 909k change to 1M_1% 60130B10049T	2004/11/17	IEC	Open	sch1117 done
2	UMA	ALVISO	23	Improve the S-Video & RGB signal waveform for S-Video & RGB monitor. 1.L5014 pin 1 C6059 change to 47UF capacitor (OXi cap). 2.L5015 pin 1 C6063 change to 47UF capacitor (OXi cap.) 3.C216 change to 47UF capacitor (OXi cap.) 4.Remove C6068.	2004/11/29	IEC	Open	sch1129 done
3	UMA	Power	11	Change U6064 +V0.9 limit regulator circuit for DDR2. Remove R640 , C734 , C195 , C735 , C196 , C193 add C6176 1uf , C6172 , C6173 , C6174 , C6175 10uf.	2004/11/29	HP	Open	sch1129 done
4	UMA	LED	66	1.Add LED D6053 EVL_21SYGC for machine ID recommend. 2.Change D6002 to EVL_21SYGC.	2004/11/29	HP	Open	sch1129 done
5	UMA	Switch Board	66	Change switch Board trace from Top side & Bottom side to internal layer a surface used GND to Cover it this change for safty ESD recommend.	2004/11/29	IEC	Open	sch1129 done
6	UMA	serial Board	45	Change Serial Board trace from Top side & Bottom side to internal layer a surface used GND to cover it this change for Safty ESD recommend.	2004/11/29	IEC	Open	sch1129 done
7	UMA	Power	7	Move D6048 , C6162 , R6283 placement this change for machine recommend.	2004/11/29	IEC	Open	sch1129 done
8	UMA	ICH-6	37	Change C882,C883 from 10pF to 18pF for RTC accuracy	2004/11/29	IEC	Open	sch1129 done
9	UMA	ALVISO ICH-6	23 23 36	For EMI recommend: 1.Add C6188,C6189,C6190 0.01uF_16V connect +V1.5S for EMI improve +V1.5S power plane. 2.Add C6185,C6186,C6187 0.01uF_16V connect +V1.8 for EMI improve +V1.8 power plane 3.Add C6191 0.01uF_16V connect CN7 RTCBAT to improve EMI noise.	2004/12/2	IEC	Open	sch1202 done
10	UMA	Power	11	Add two power via 30_16 on +V0.9S power rail.	2004/12/3	IEC	open	sch1203 done
11	UMA	Power	13	Aspen UMA uses a 2.5V rail,R6150 should be changed to 107K_1%, and R6152 should be changed to 57.6K_1%.	2004/12/10	HP	open	sch1210 done