

HSF Property:ROHS or Halogen-Free

2012.03.23

21-OCT-2002		
DATE	CHANGE NO.	REV

DRAWER		EE	DATE	POWER	DATE	INVENTEC				
DESIGN		LOBEN	2012.03.23	EDISON	2012.03.23					
CHECK		LOBEN	2012.03.23	EDISON	2012.03.23					
RESPONSIBLE		BERVAN	2012.03.23	RY	2012.03.23					
TITLE						MODEL/PROJECT FUNCTION DAKAR10/FG MAIN BOARD				
SIZE/				VER		SIZE		CODE	D/C NUMBER	REV
FILE NAME:								C/S	1310A00A.X01-G-MTR	X01
P/N:		~~~~~						SHEET	70	

TABLE OF CONTENTS

PAGE	PAGE	PAGE
1. COVER PAGE	26. CARDREADER	51. PCH 5 USB
2. INDEX	27. MINI1 WLAN/DEBUG CARD	52. PCH 6 MISC
3. BLOCK DIAGRAM	28. MINI2 3G/LTE	53. PCH 7 POWER
4. POWER MAP	29. SATA HDD/ODD CONN	54. PCH 8 POWER
5. POWER CHARGER	30. USB 2.0 CONN	55. PCH 9 GND
6. POWER +V3LA/+V3A/+5A	31. USB 3.0 CONTROLLER(REMOVE)	56. VGA 1
7. POWER +V1.5/+V0.75	32. USB 3.0 CONN W/ S&C	57. VGA 2
8. POWER +V1.8S	33. USB 3.0 CONN	58. VGA 3
9. POWER VCCIO	34. LCM CONN	59. VGA 4
10. POWER VCCSA	35. CRT CONN	60. VGA 5
11. POWER VCORE	36. HDMI CONN	61. VGA 6
12. POWER VGFX	37. HDMI CEC	62. VRAM 1
13. POWER VCORE_DGPU	38. DDR3 DIMM0	63. VRAM 2
14. ENABLE PIN	39. DDR3 DIMM1	64. VRAM 3
15. LOAD SWITCH-1	40. FAN & THERMAL SENSOR	65. VRAM 4
16. LOAD SWITCH-2	41. CPU 1	66. USB BOARD
17. PCB SCREW	42. CPU 2	67. PICK BUTTON BOARD(4 PIN)
18. HALL SENSOR	43. CPU 3 DRAM	68. POWER BUTTON BOARD
19. LED	44. CPU 4 POWER	69. EMI
20. K/B & TP/B CONN	45. CPU 5 POWER	70. PICK BUTTON BOARD(8 PIN)
21. EC	46. CPU 6 GND	
22. LAN	47. PCH 1	
23. RJ45 & TRANSFORMER	48. PCH 2	
24. AUDIO CODEC	49. PCH 3	
25. SPEAKER/HP JACK/MIC JACK	50. PCH 4 AXG	

INVENTEC

TITLE			
MODEL PROJECT,FUNCTION Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX

DATE 21-OCT-2002

SHEET 2 of 70

D

C

B

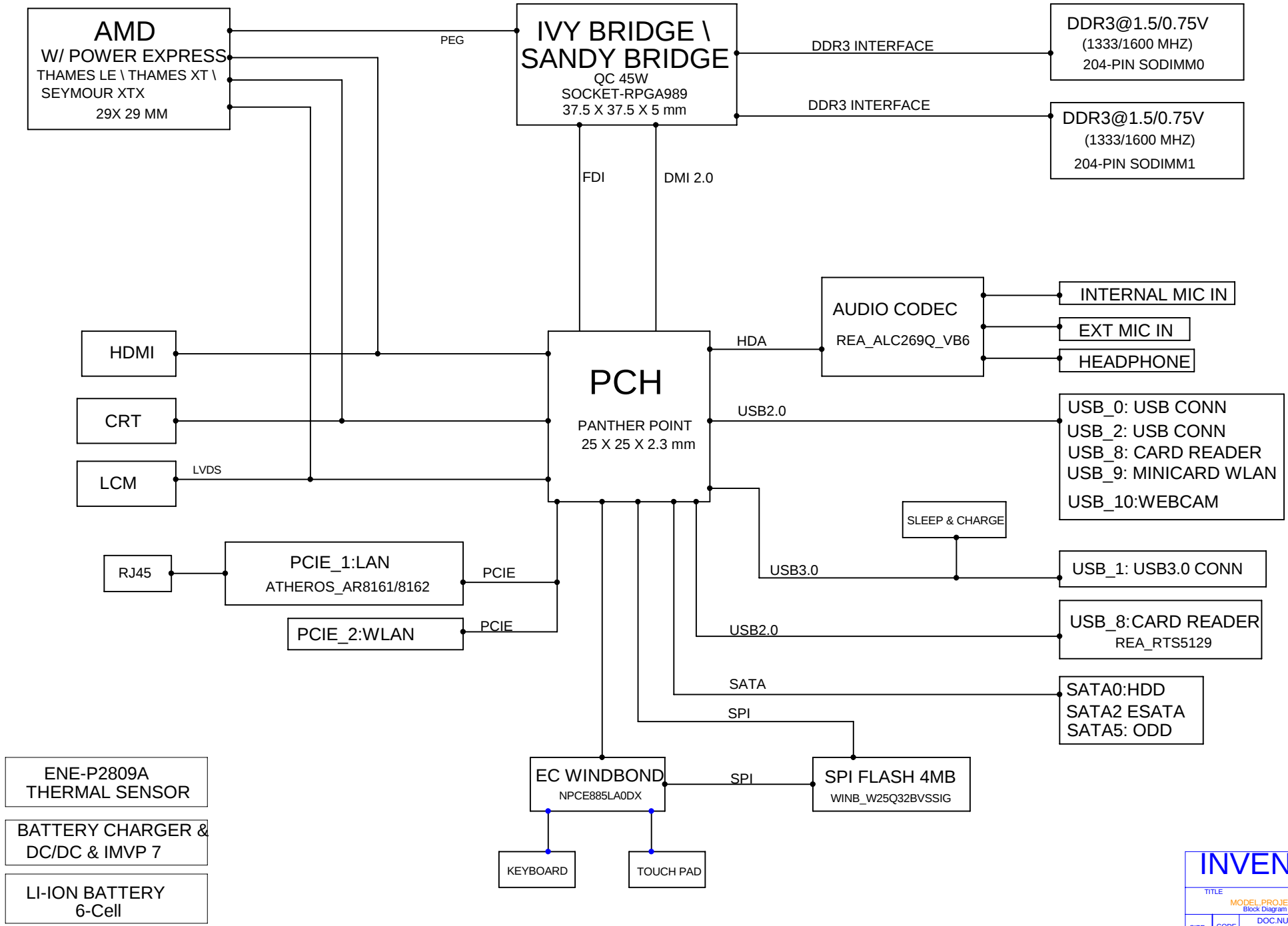
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D

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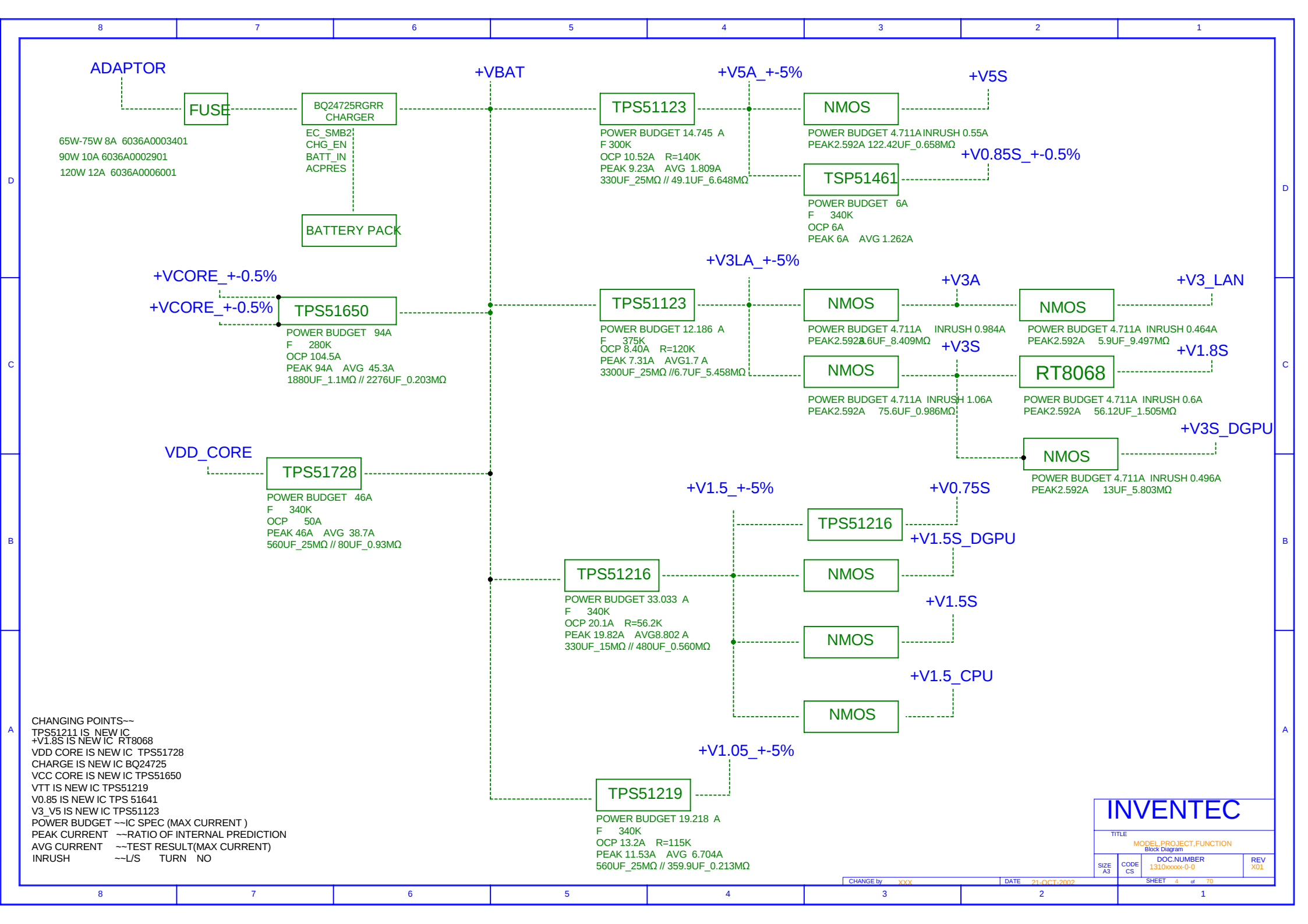
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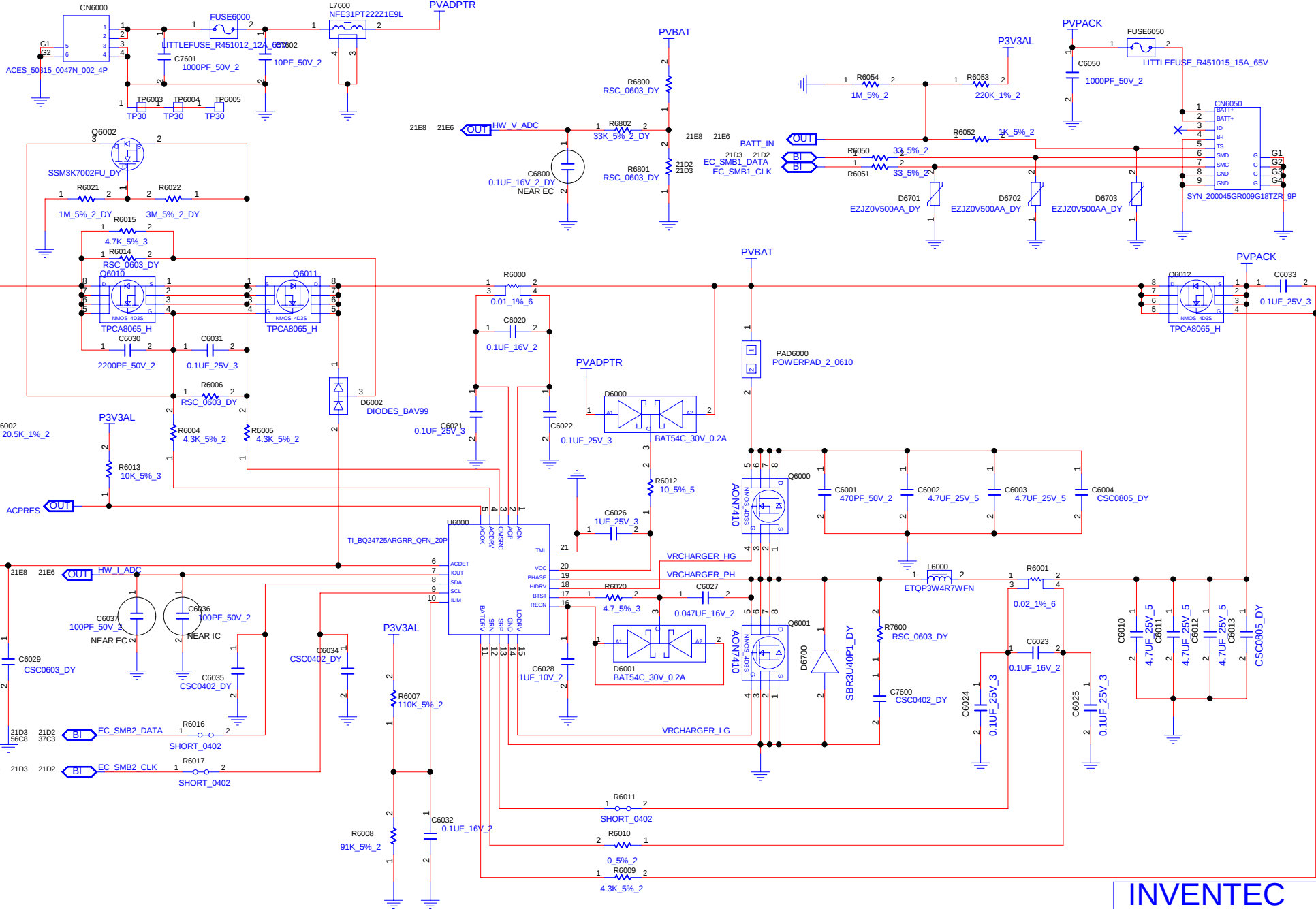


INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01



FUSE6000
65W-75W 8A(6036A0003401)
90W 10A(6036A0002901)
120W 12A(6036A0006001)

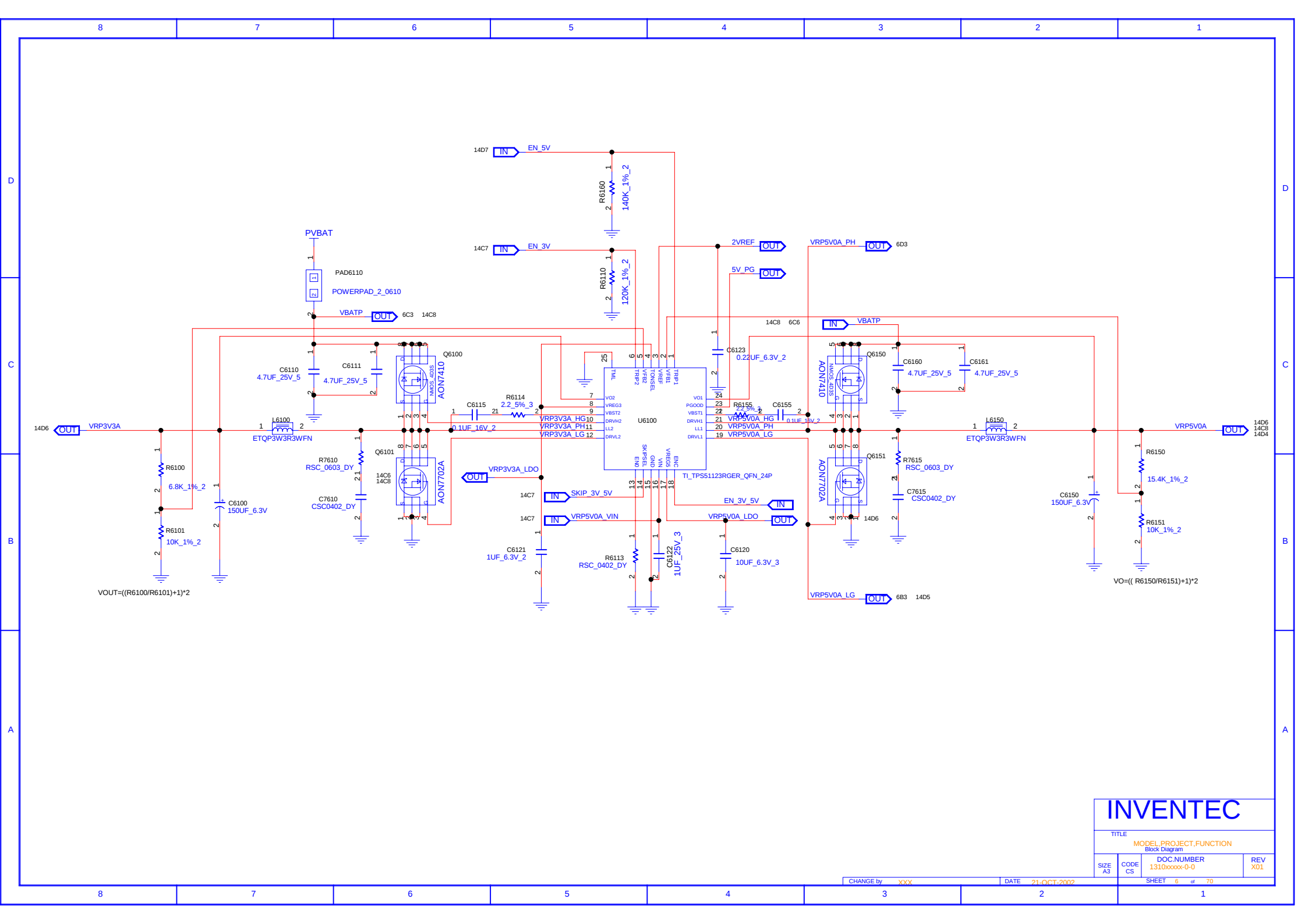


INVENTEC

TITLE
MODEL PROJECT FUNCTION
Block Diagram
DOC NUMBER
1310xxxx-0-0
REV
X01

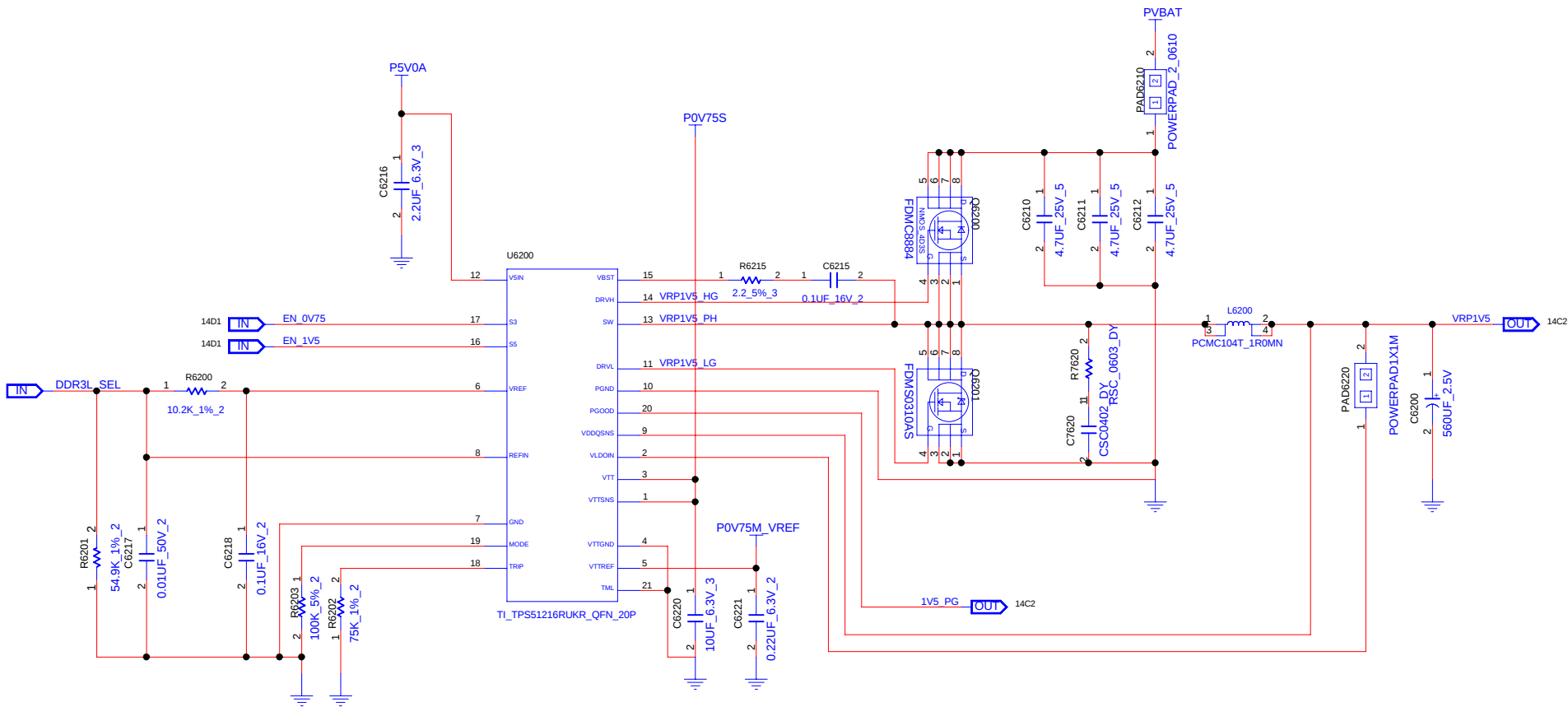
CHANGE by XXXX DATE 21-OCT-2002

SHEET 5 of 70



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01



VOUT=REFIN=1.8*(R6201/(R6200+R6201))
 MODE=100KOHM:TRACKING DISCHARGE

STATE	S3	S5	VREF	VDDQ	VTTREF	VTT
S0	HI	HI	ON	ON	ON	ON
S3	LO	HI	ON	ON	ON	OFF(High-Z)
S4/S5	LO	LO	OFF	OFF(Discharge)	OFF(Discharge)	OFF(Discharge)

INVENTEC

TITLE

MODEL,PROJECT,FUNCTION

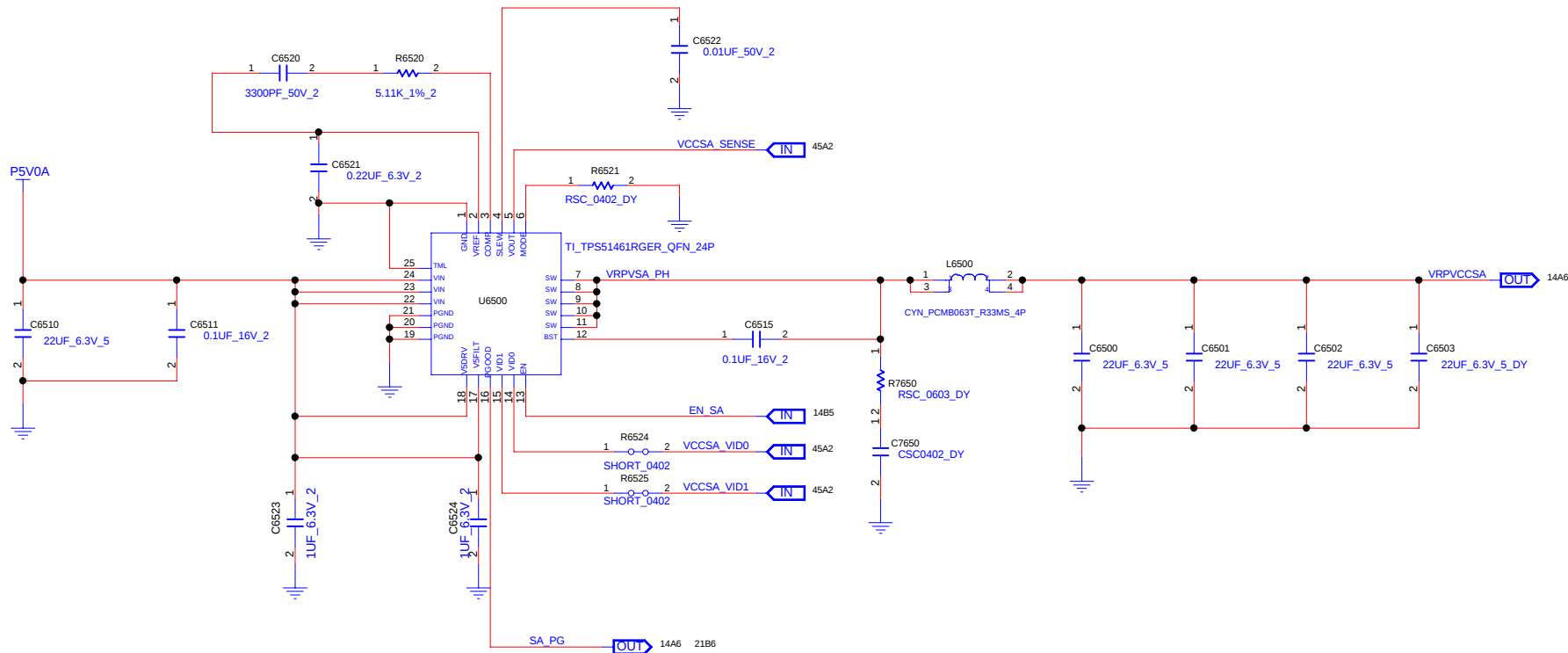
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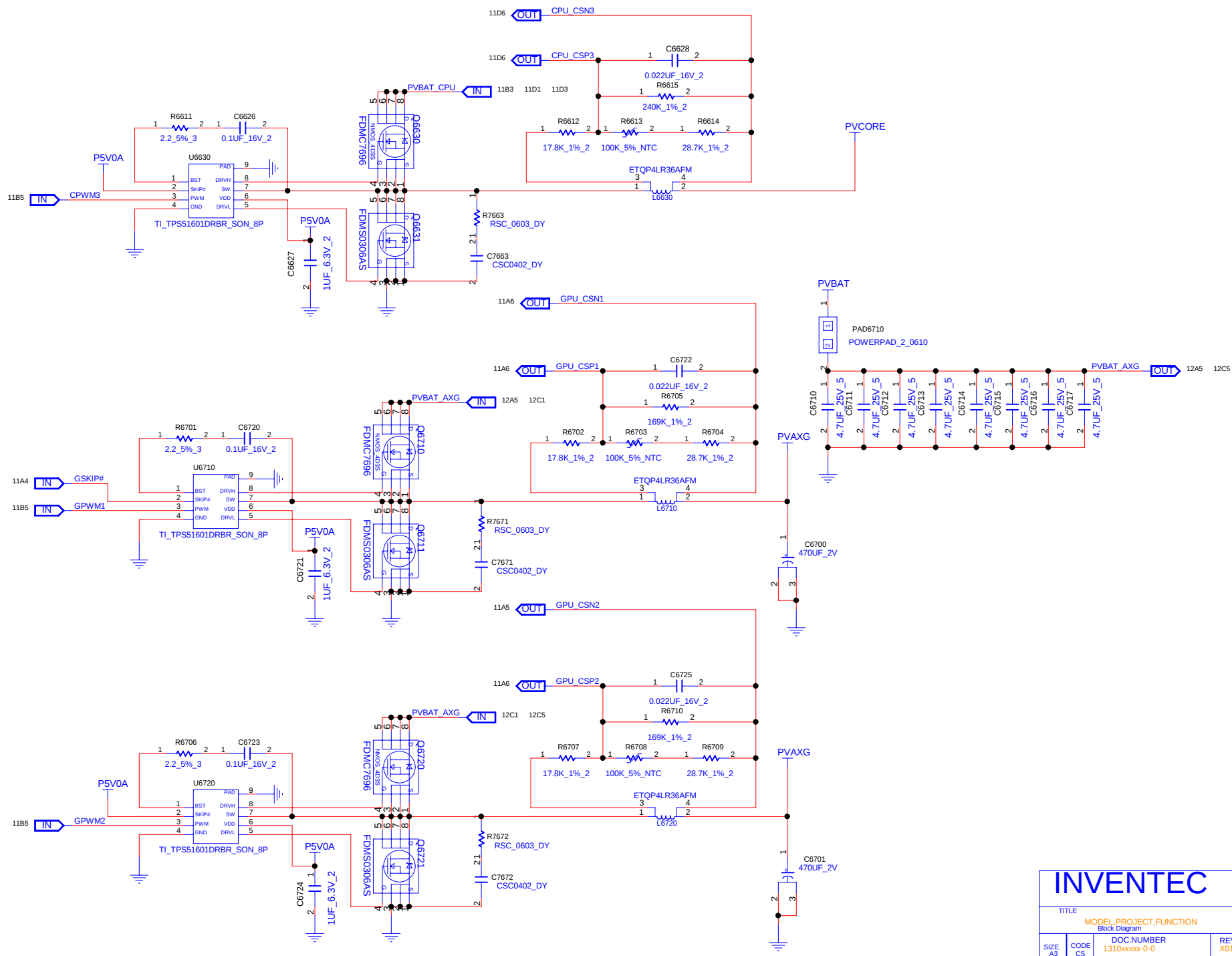
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REV X01



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01



INVENTEC

TITLE

MODEL PROJECT FUNCTION

Block Diagram

DOC NUMBER

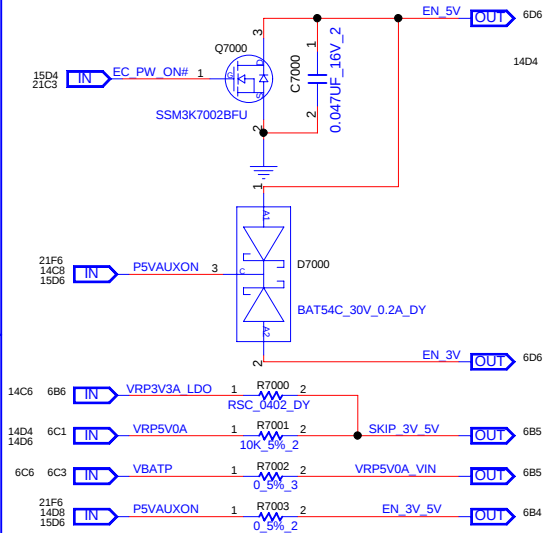
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REV

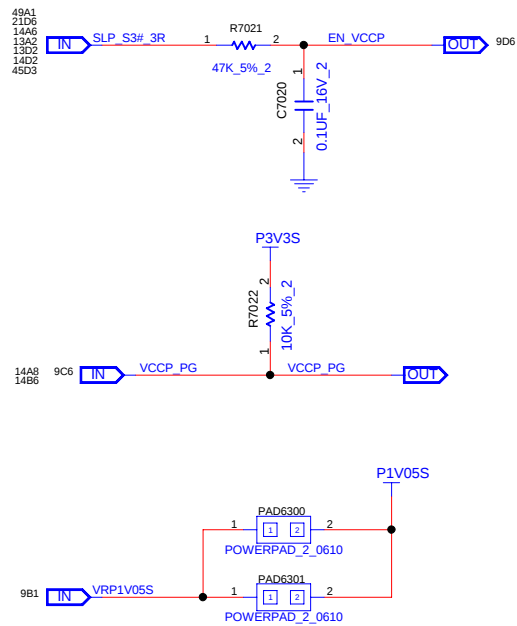
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SIZE	A3	CODE	CS
CHANGE by		XXX	DATE
21-OCT-2002		SHEET	
12		of 70	

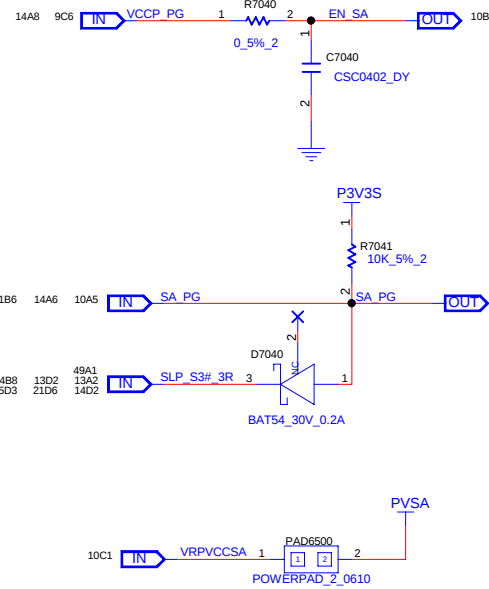
3V & 5V



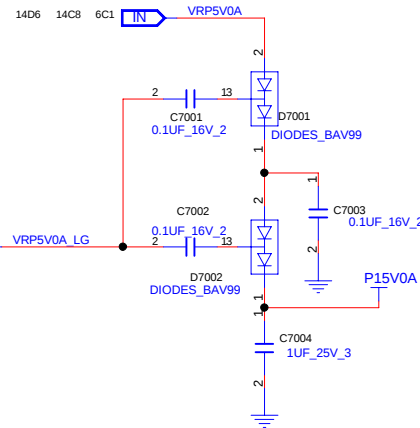
VCCIO



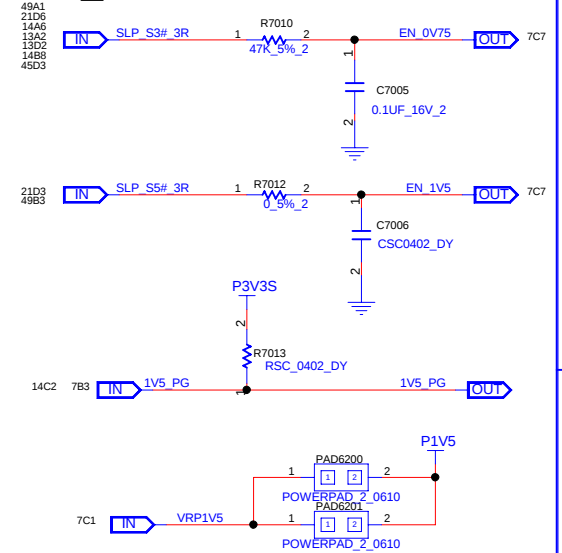
VCCSA



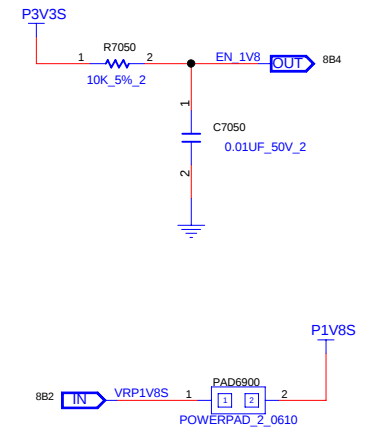
DGPU



DDR_P1V5

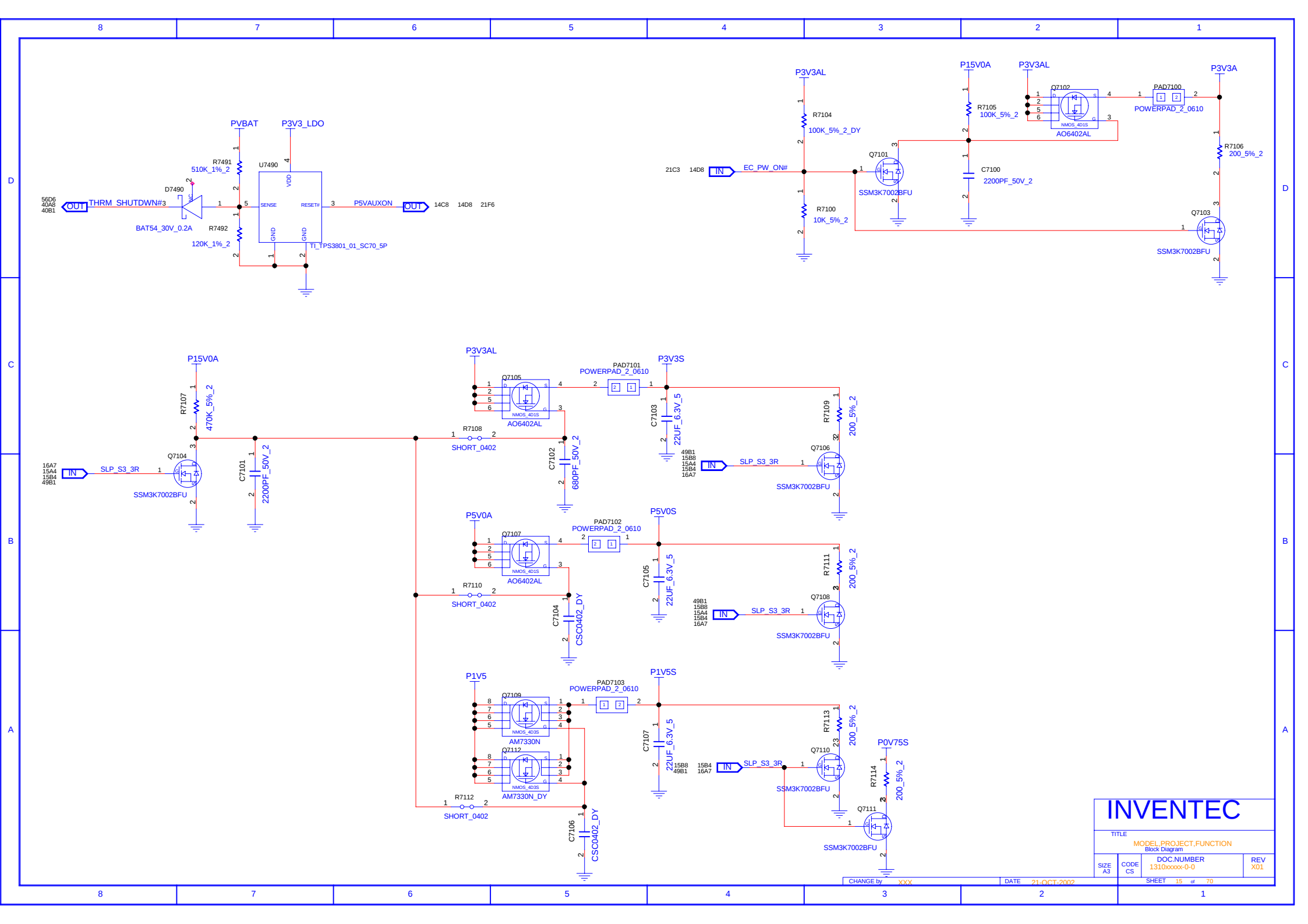


P1V8S



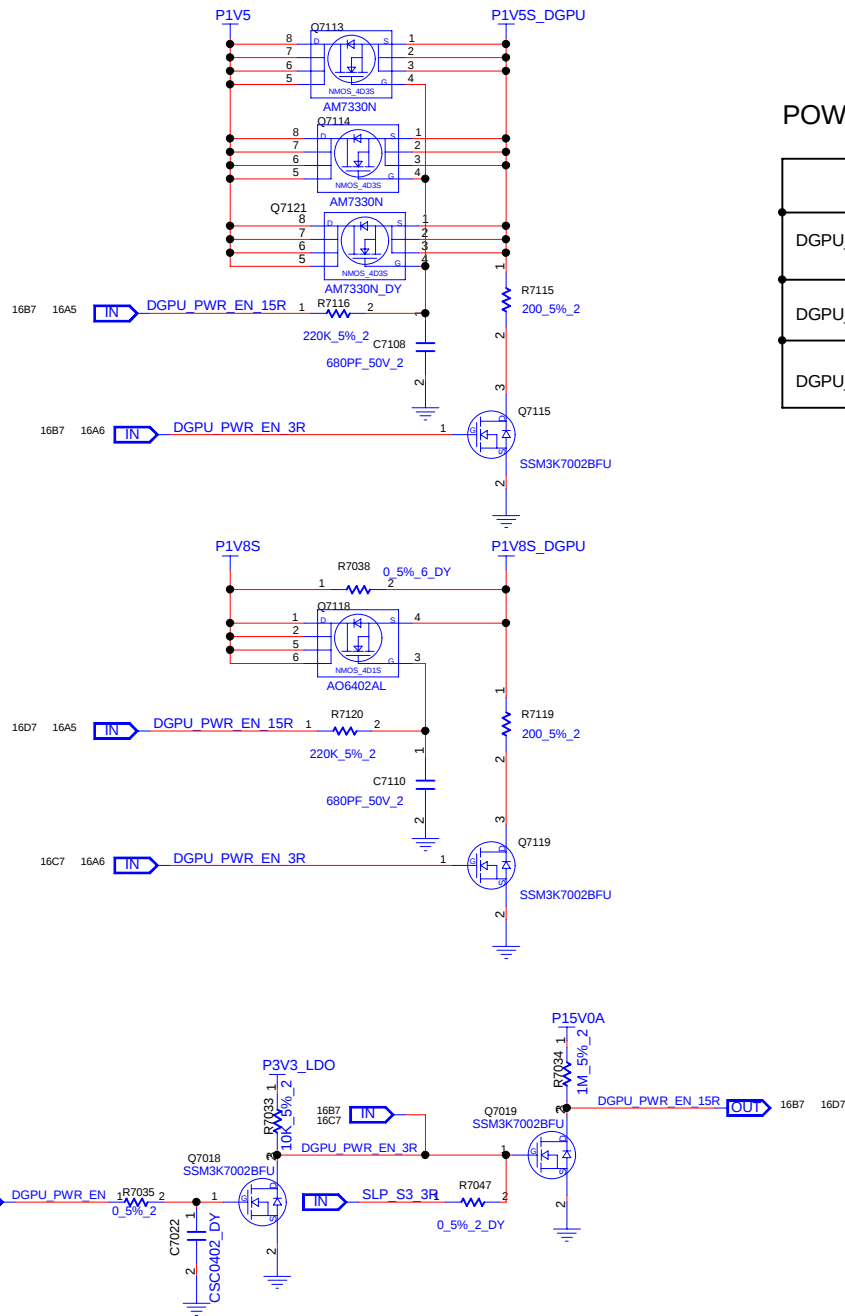
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MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01



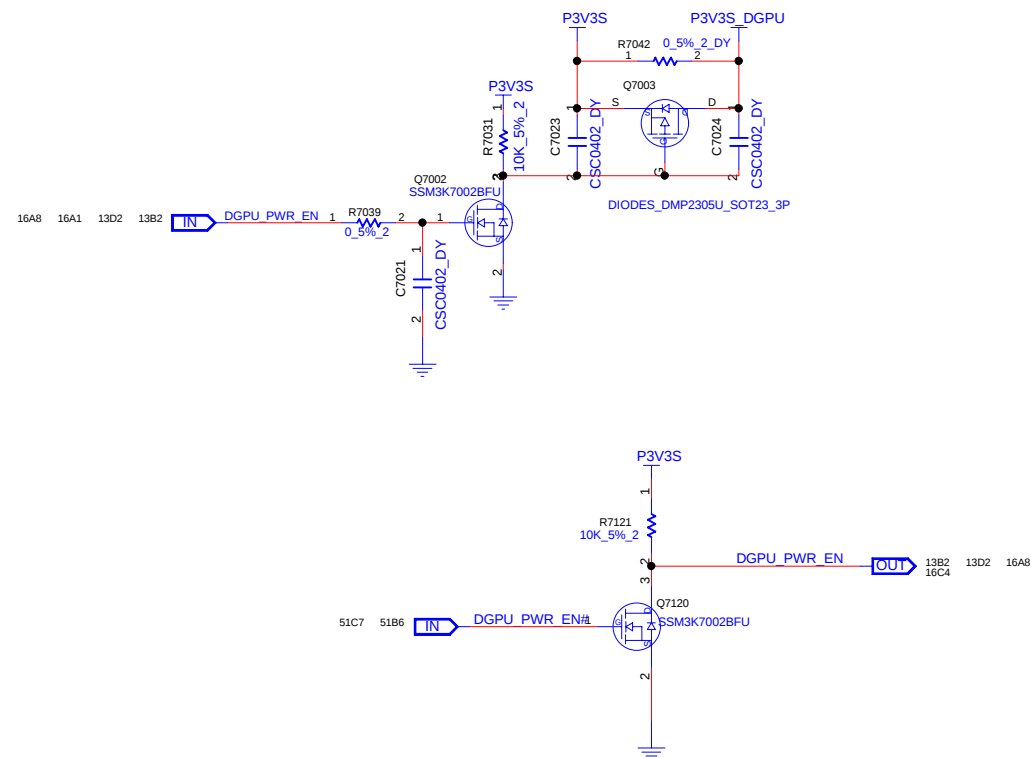
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MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01



POWER EXPRESS

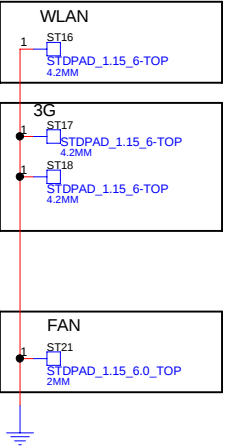
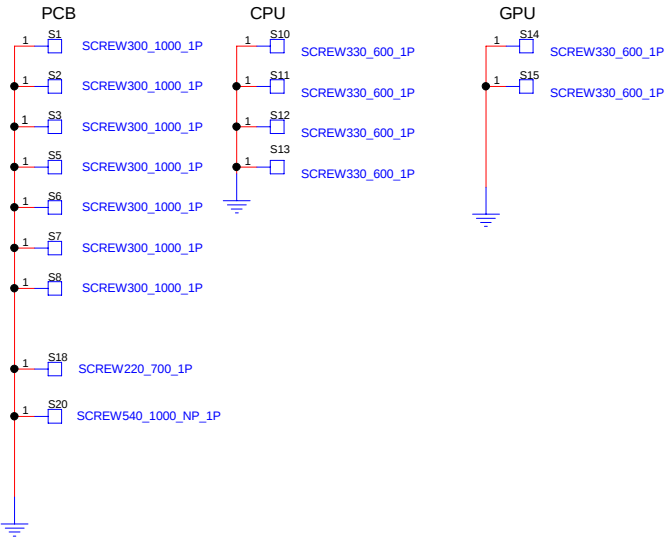
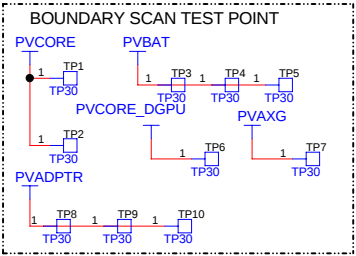
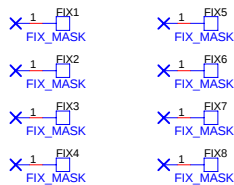
	DURING RESET	AFTER RESET	
DGPU_PWR_EN#	HIGH	HIGH	0 : DGPU POWER SWITCH TURNED ON 1 : POWER SWITCH TURNED OFF
DGPU_PWRGD			0 : DGPU POWER IS NOT STABLE 1 : DGPU POWER IS STABLE
DGPU_HOLD_RST#	LOW	LOW	0 : KEEP DGPU IN RESET 1 : RESET IS RELEASED



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

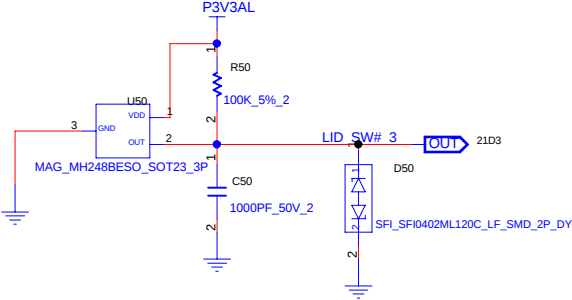
REFERENCE 0~49(PCB SCREW)



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

REFERENCE 50-99(HALL SENSOR)



INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

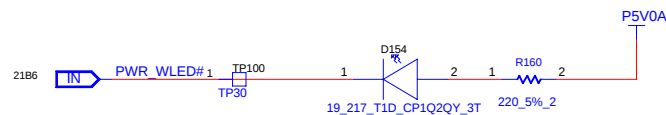
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CHANGE by XXX DATE 21-OCT-2002 SHEET 18 of 70

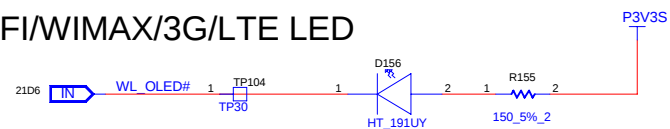
REFERENCE 100~199(LED)

POWER ON LED

D154 BRIGHT WHEN SYSTEM IS POWER ON
D154 BLINK WHEN SYSTEM GO TO SLEEP

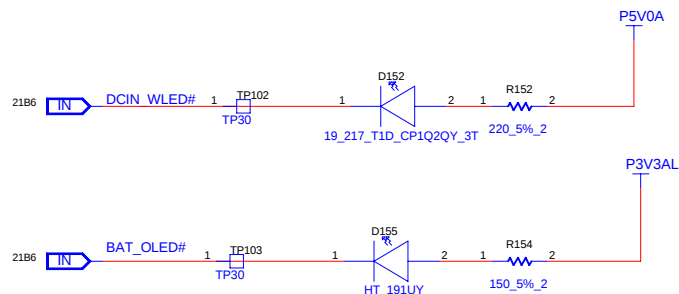


WIFI/WIMAX/3G/LTE LED



DC IN / BATTERY CHARGE LED

D152 BRIGHT: BOTH AC-ADAPTER IS PLUGGED IN AND BATTERY IS FULL CHARGED
D155 BRIGHT: WHILE CHARGING BATTERY FROM AC-ADAPTER
BLINK: LOW BATTERY



INVENTEC

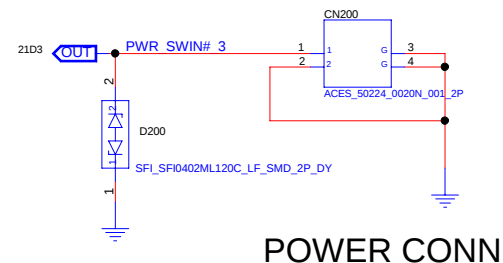
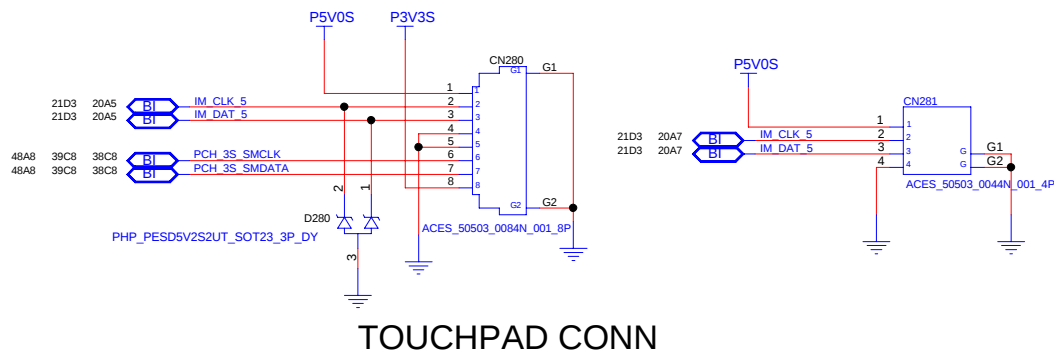
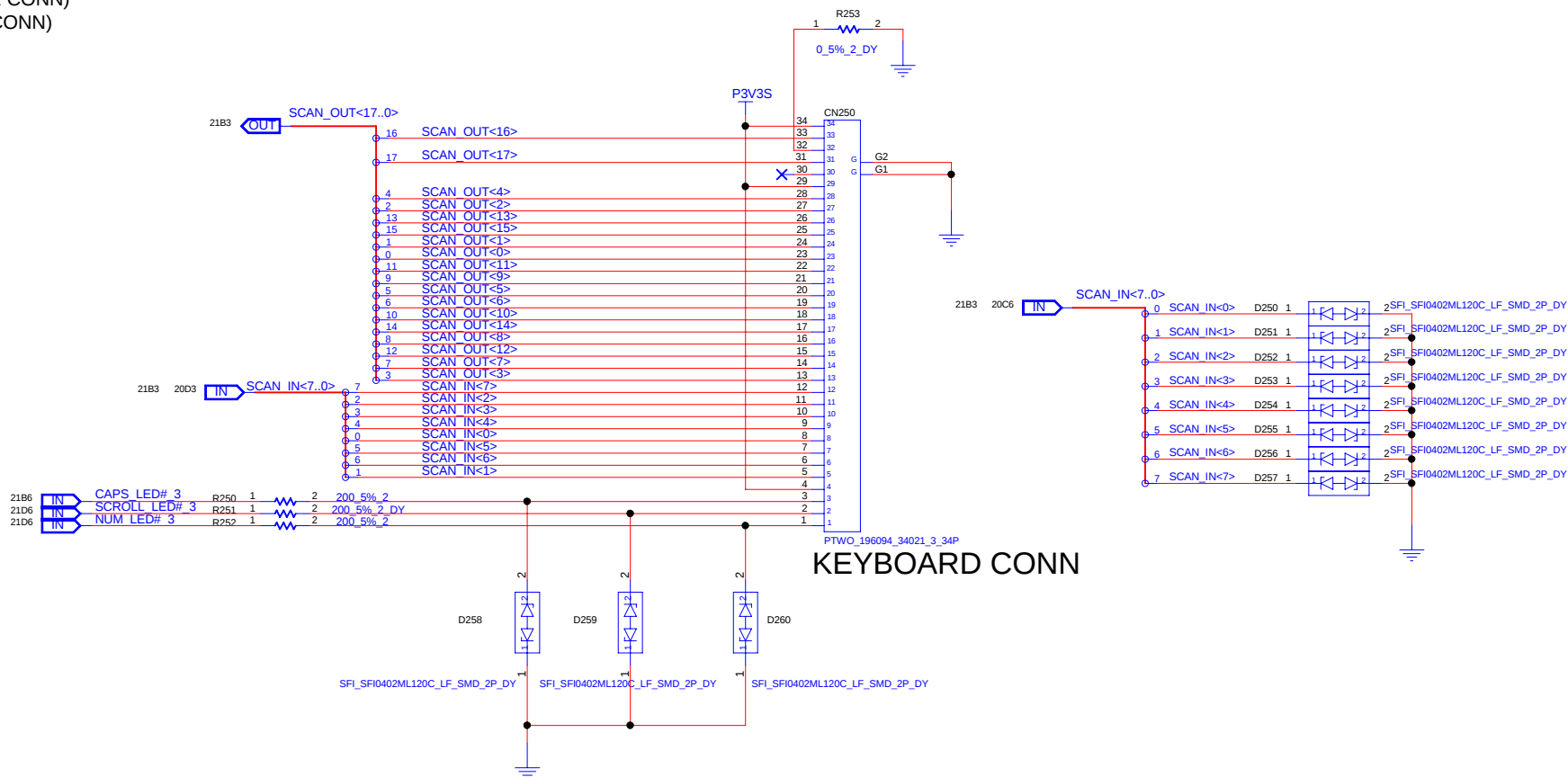
TITLE
MODEL PROJECT FUNCTION
Block Diagram

SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01
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CHANGE by XXX | DATE 21-OCT-2002

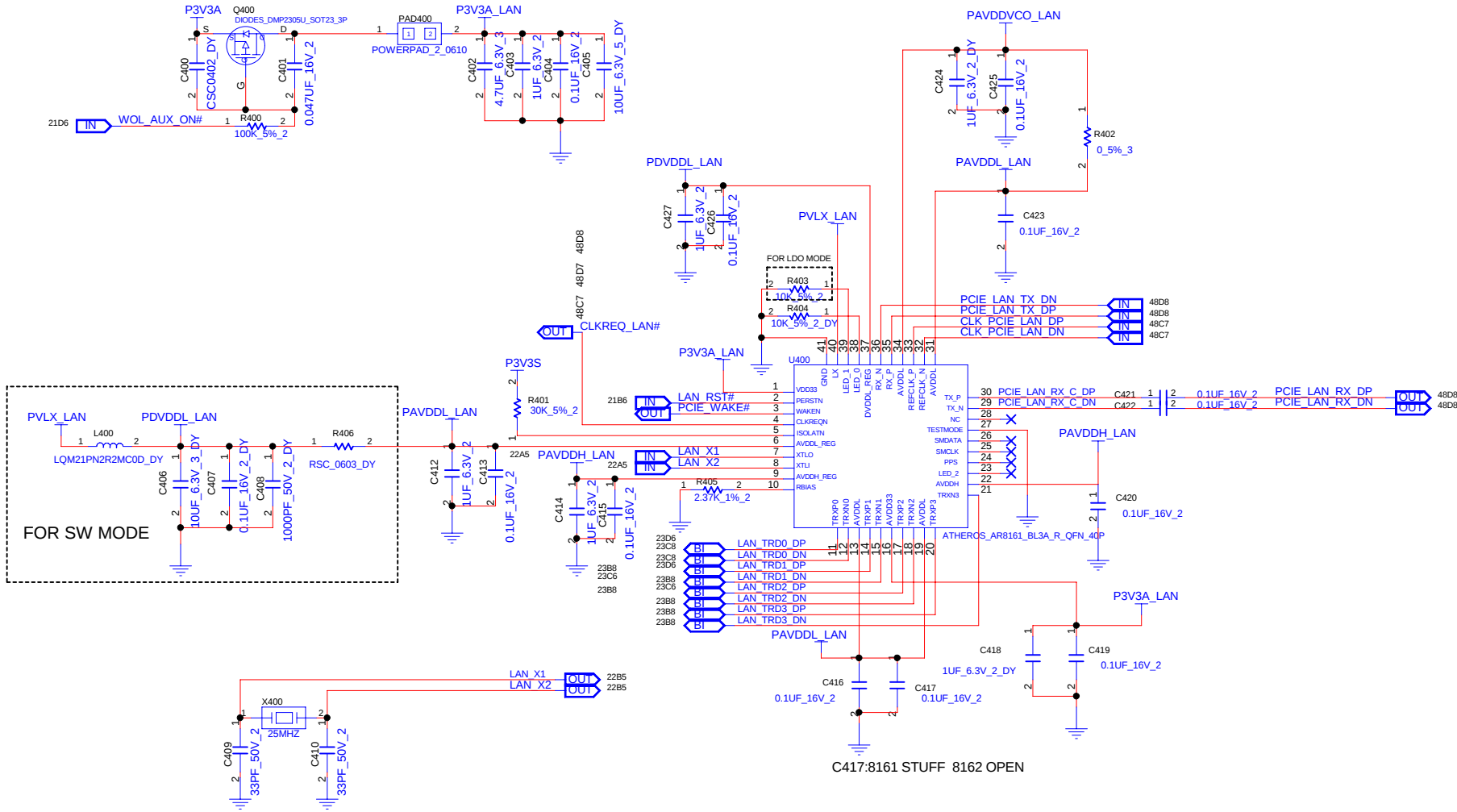
SHEET 19 of 70

REFERENCE 200~249(POWER CONN)
REFERENCE 250~299(KB/TP CONN)



INVENTEC

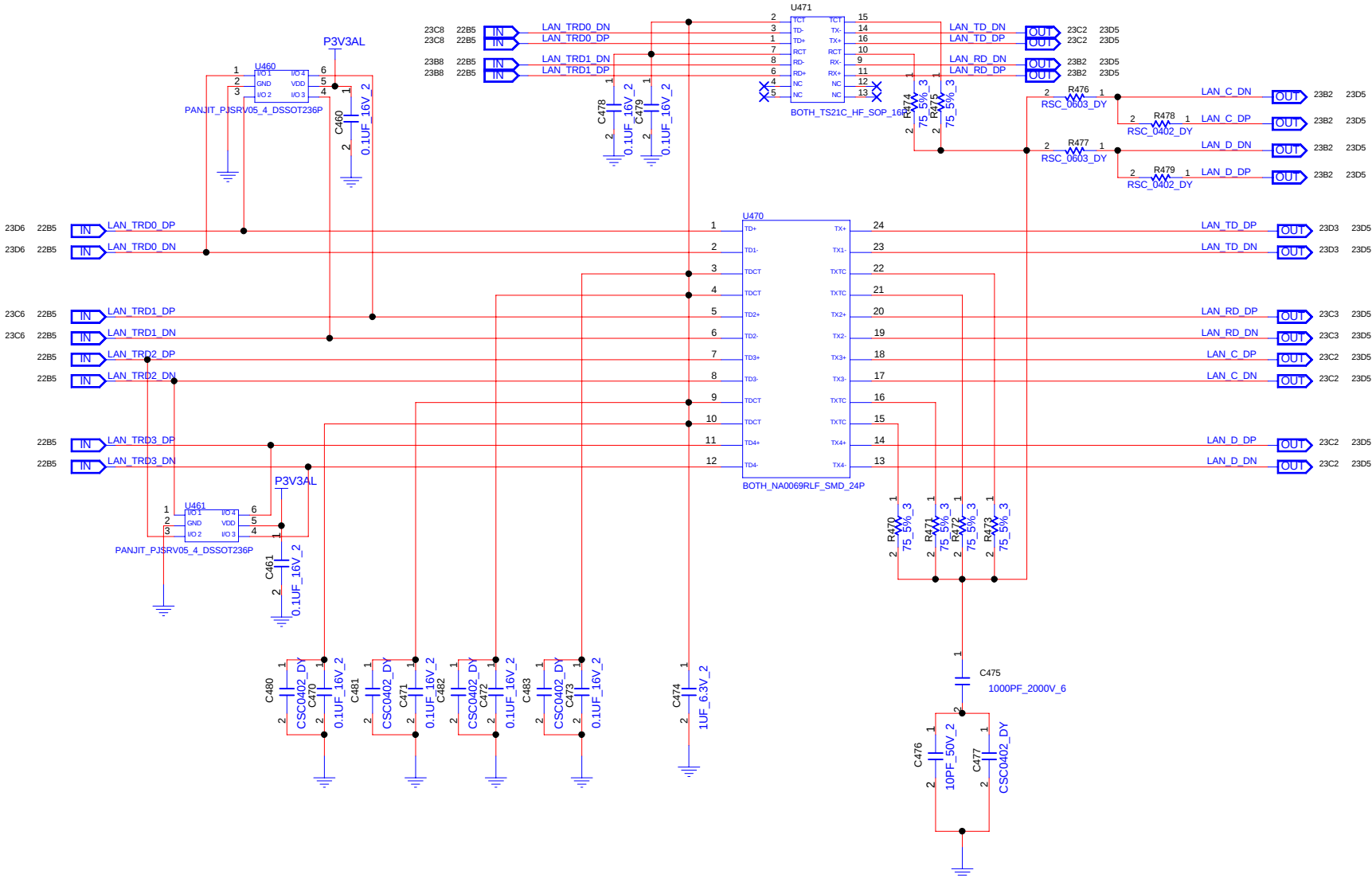
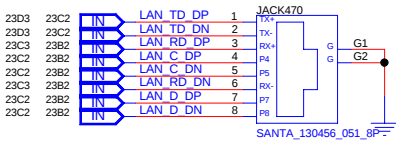
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MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01



INVENTEC

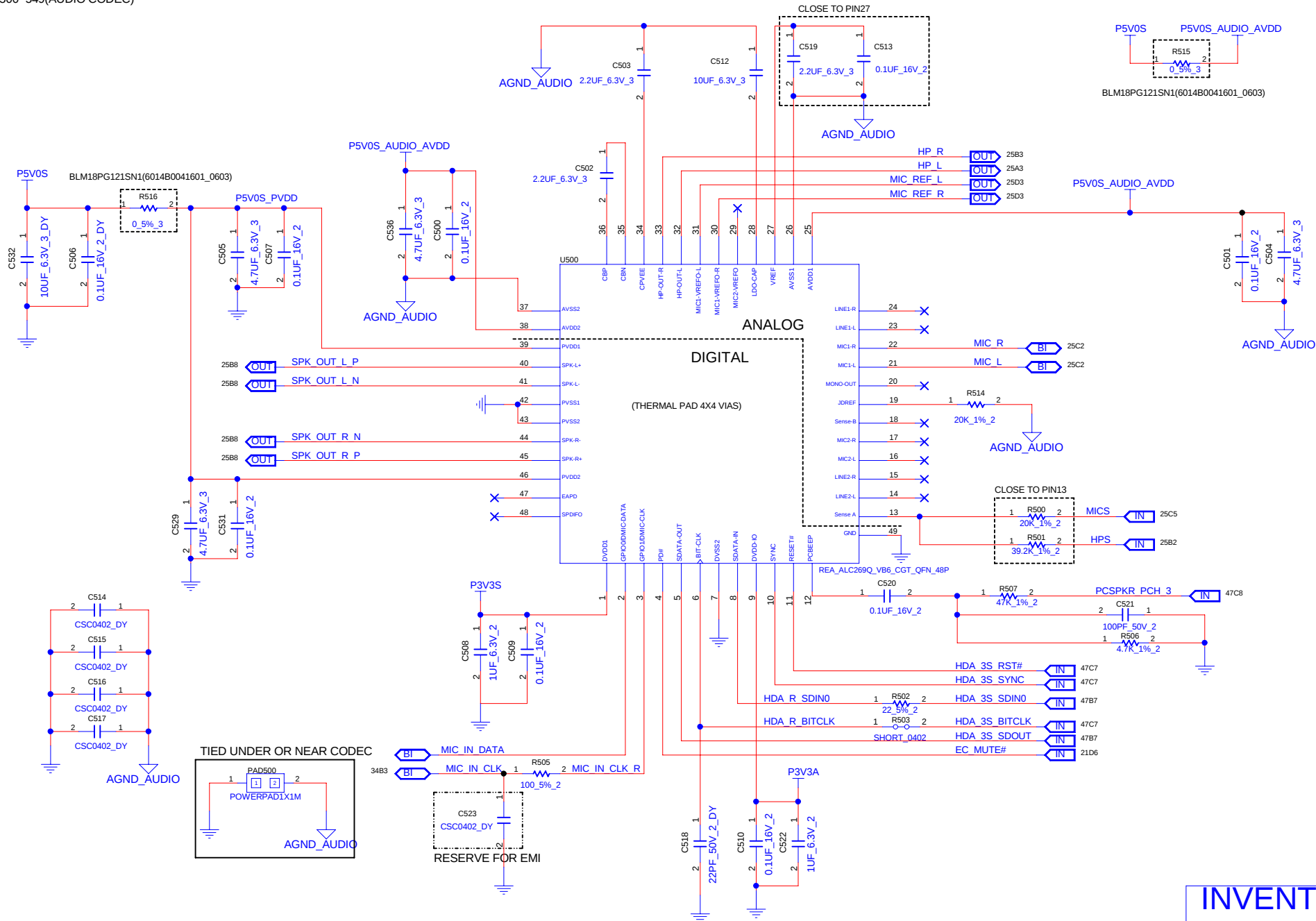
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MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

REFERENCE 400~499(LAN)



INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
CHANGE by		DATE	SHEET
XXX		21-OCT-2002	23 of 70

REFERENCE 500-549(AUDIO CODEC)



INVENTEC

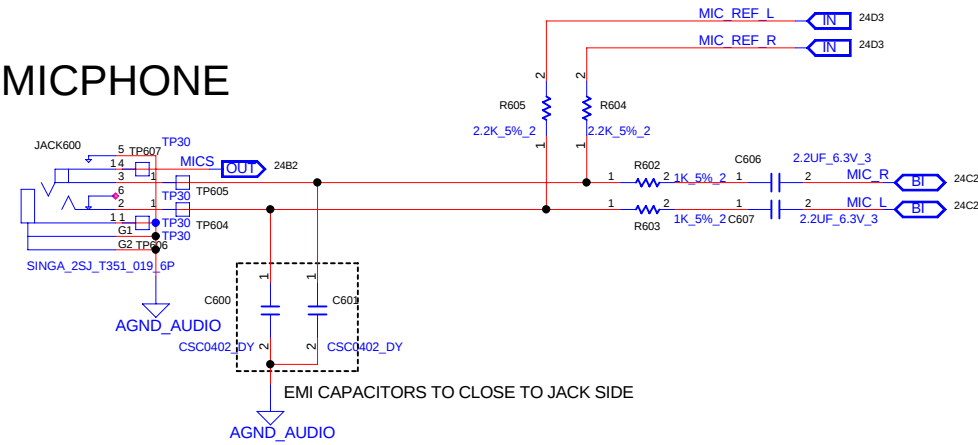
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Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

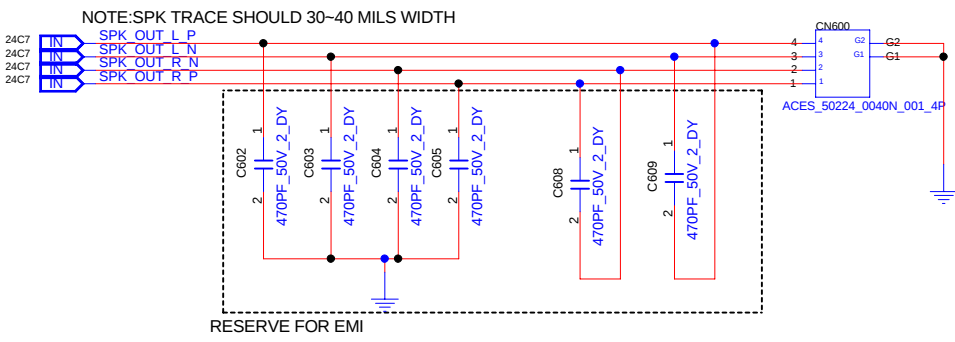
SHEET 24 of 70

AUDIO JACKS

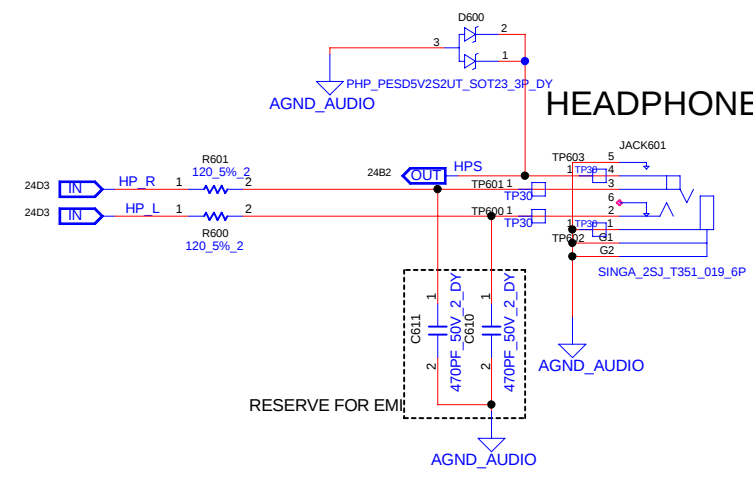
MICPHONE



INTERNAL SPEAKERS

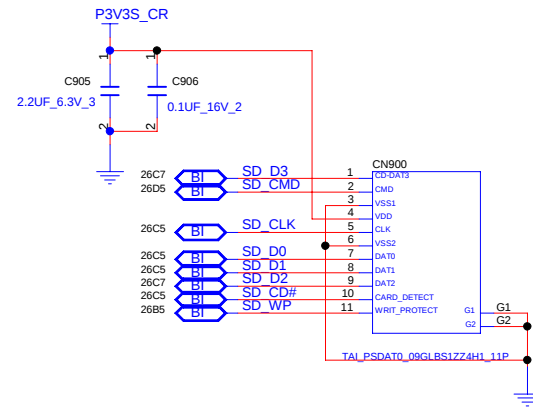
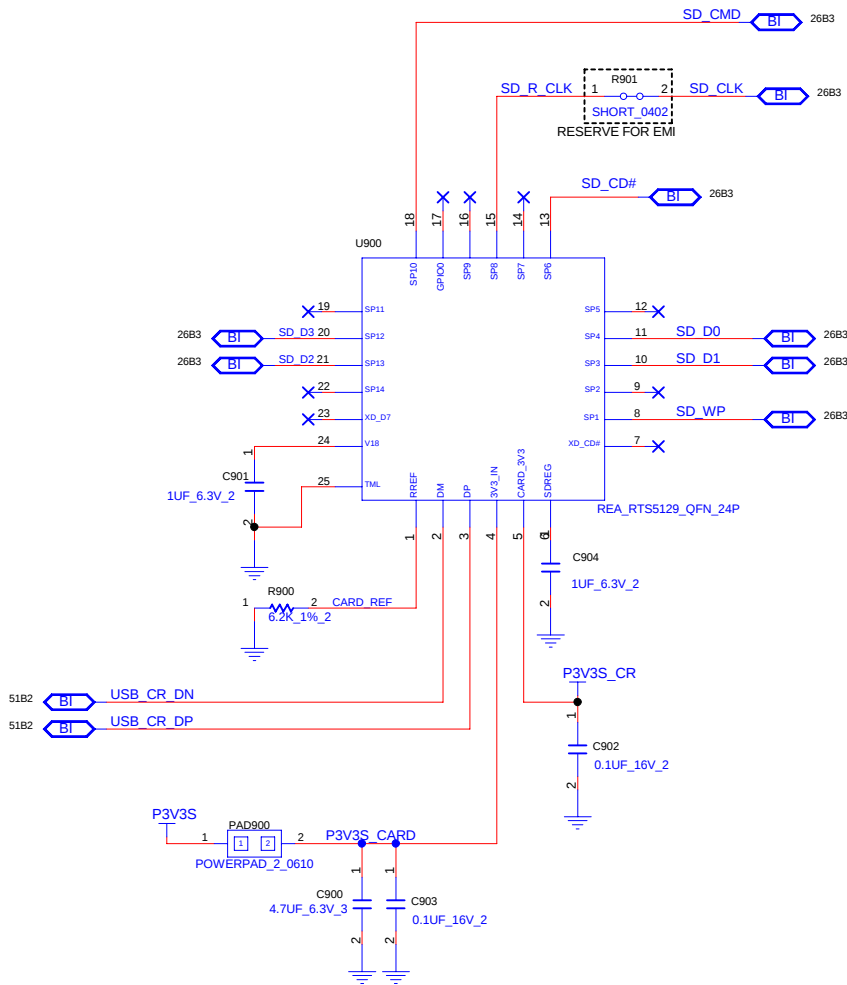


HEADPHONE



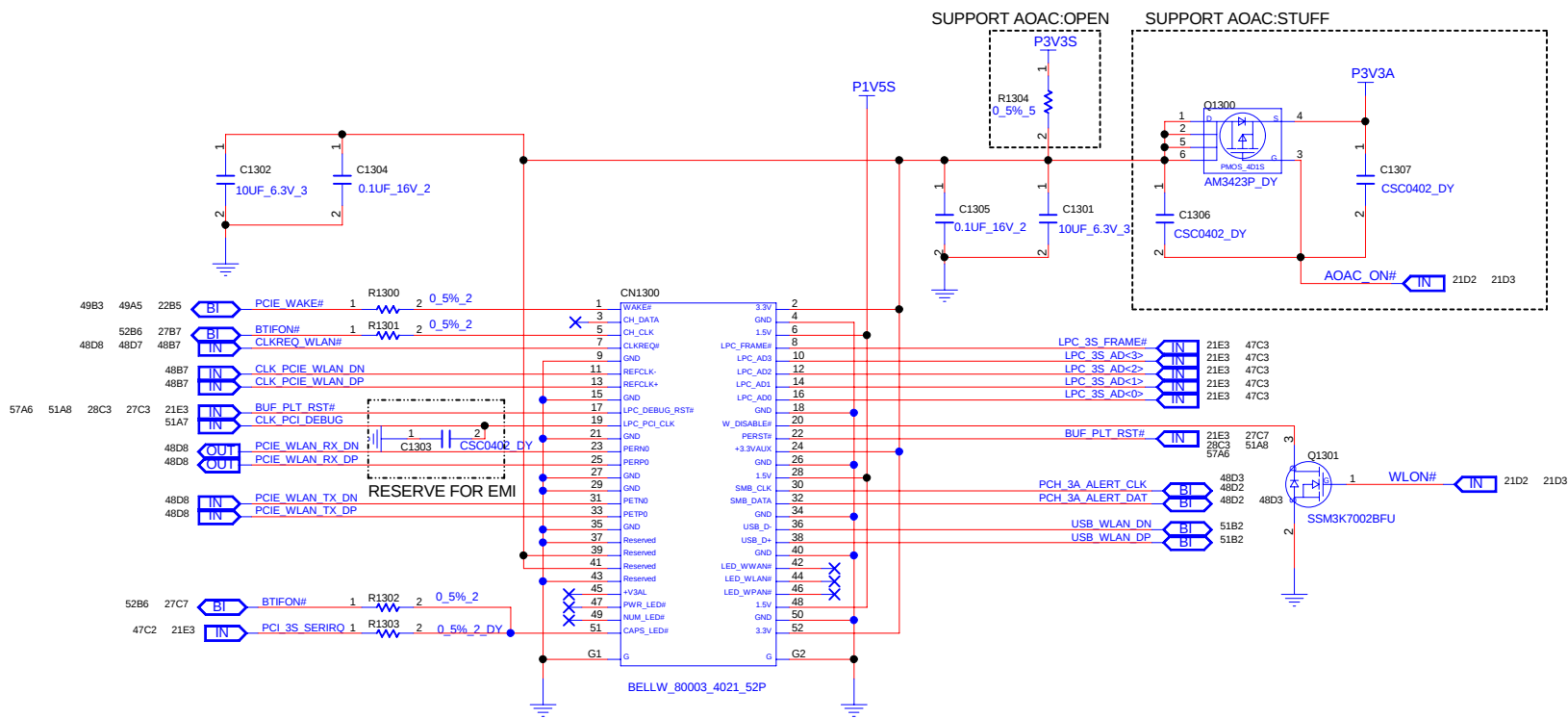
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TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE A3		DOC NUMBER 1310xxxx-0-0	REV X01
CODE CS	SHEET 25 of 70		

REFERNCE 900~999(CARDREADER)



INVENTEC

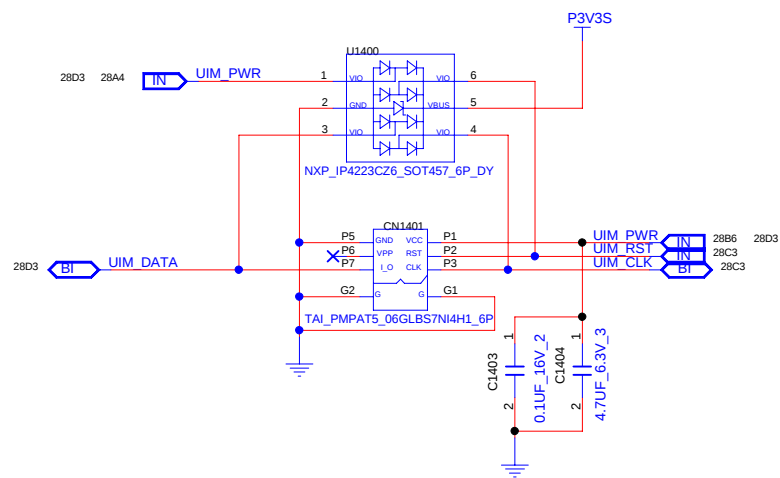
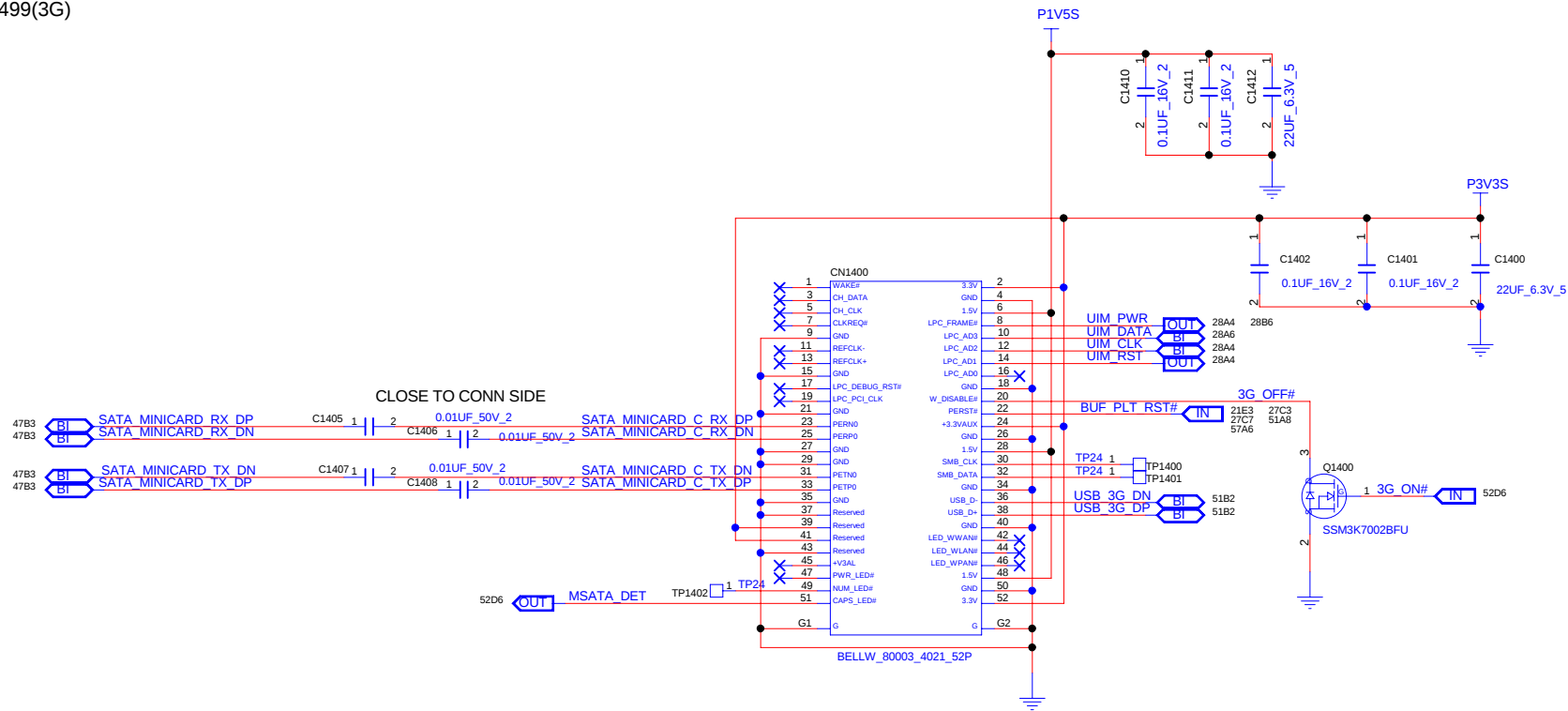
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MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01



MINI CARD 1(WLAN)

INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

REFERENCE 1400~1499(3G)

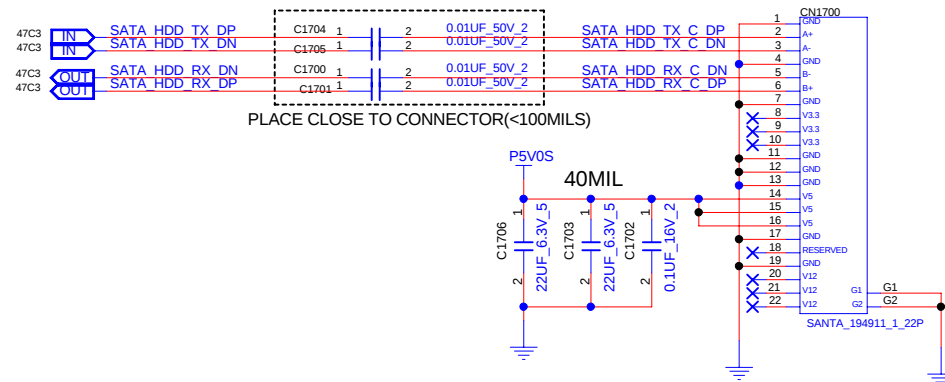


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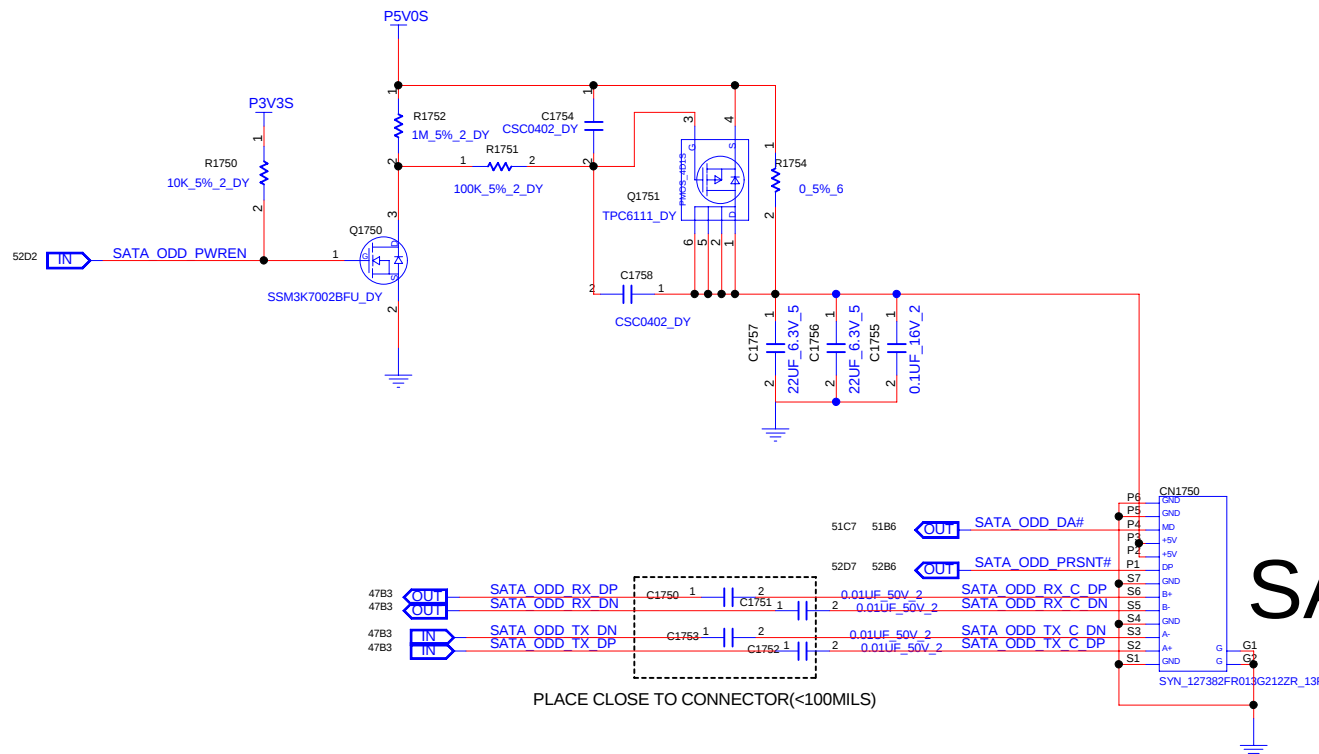
TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

REFERENCE 1700~1749(HDD)
REFERENCE 1750~1799(ODD)

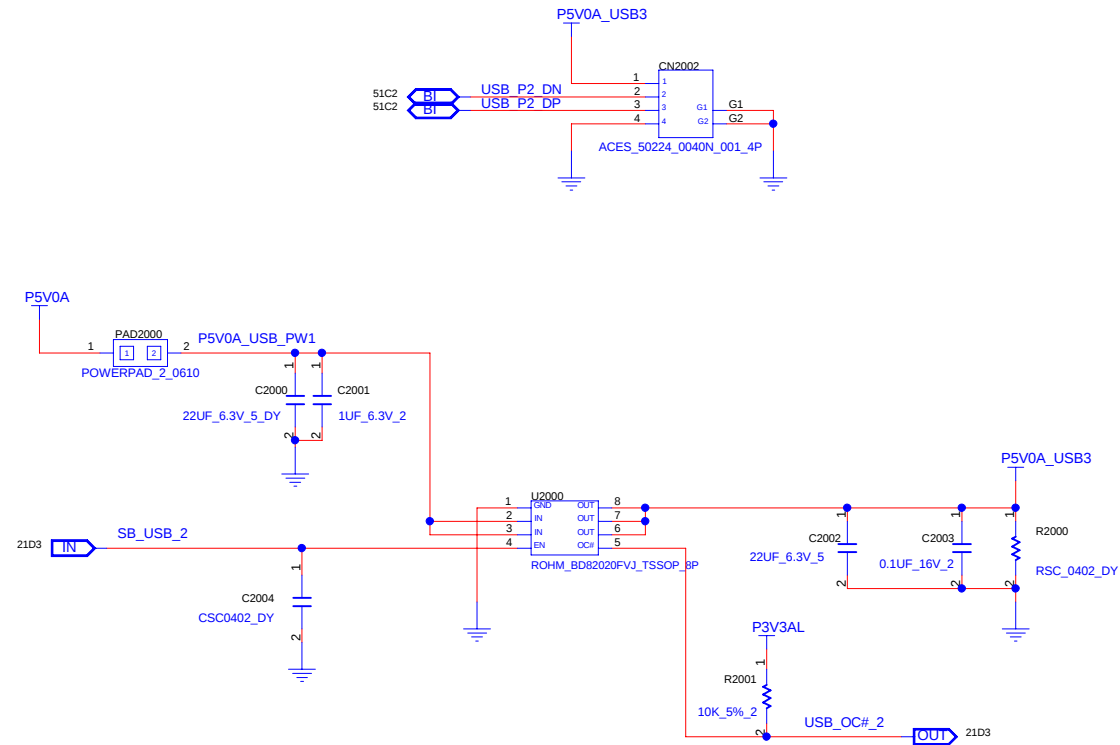
SATA HDD



SATA ODD



INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 29 of 70			



INVENTEC

TITLE	MODEL PROJECT FUNCTION
1. Project Overview	1. Project Overview
2. Project Objectives	2. Project Objectives
3. Project Scope	3. Project Scope
4. Project Organization	4. Project Organization
5. Project Schedule	5. Project Schedule
6. Project Budget	6. Project Budget
7. Project Risks	7. Project Risks
8. Project Communication	8. Project Communication
9. Project Monitoring and Control	9. Project Monitoring and Control
10. Project Closure	10. Project Closure

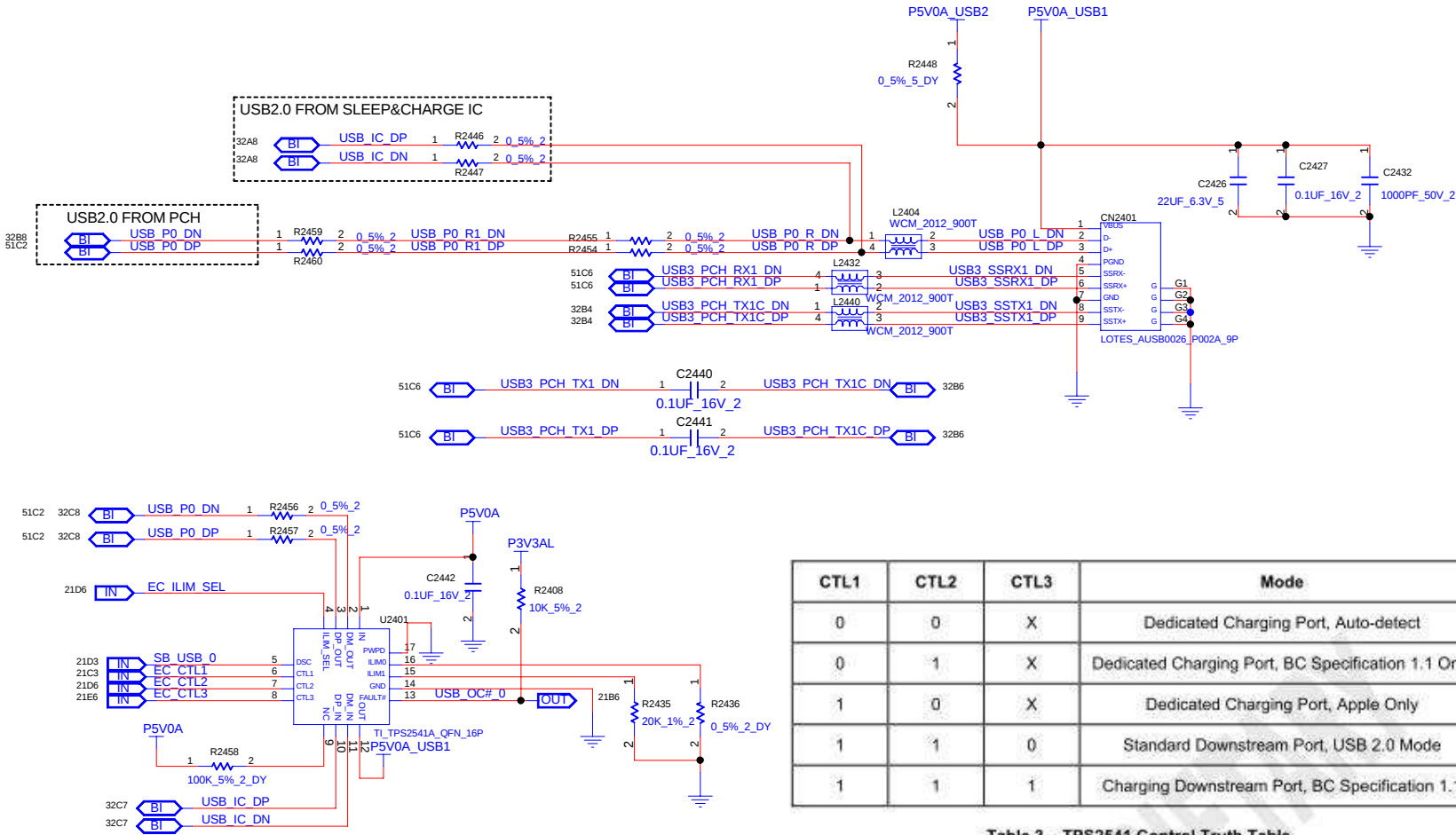
SIZE A3	CODE CS	DOC. NUMBER 1310xxxxx-0-0	REV X01
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SHEET 30 of 70

REFERENCE 2400~2499

REMOVE USB3.0 CONTROLLER IC

INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01



CTL1	CTL2	CTL3	Mode
0	0	X	Dedicated Charging Port, Auto-detect
0	1	X	Dedicated Charging Port, BC Specification 1.1 Only
1	0	X	Dedicated Charging Port, Apple Only
1	1	0	Standard Downstream Port, USB 2.0 Mode
1	1	1	Charging Downstream Port, BC Specification 1.1

Table 3 – TPS2541 Control Truth Table

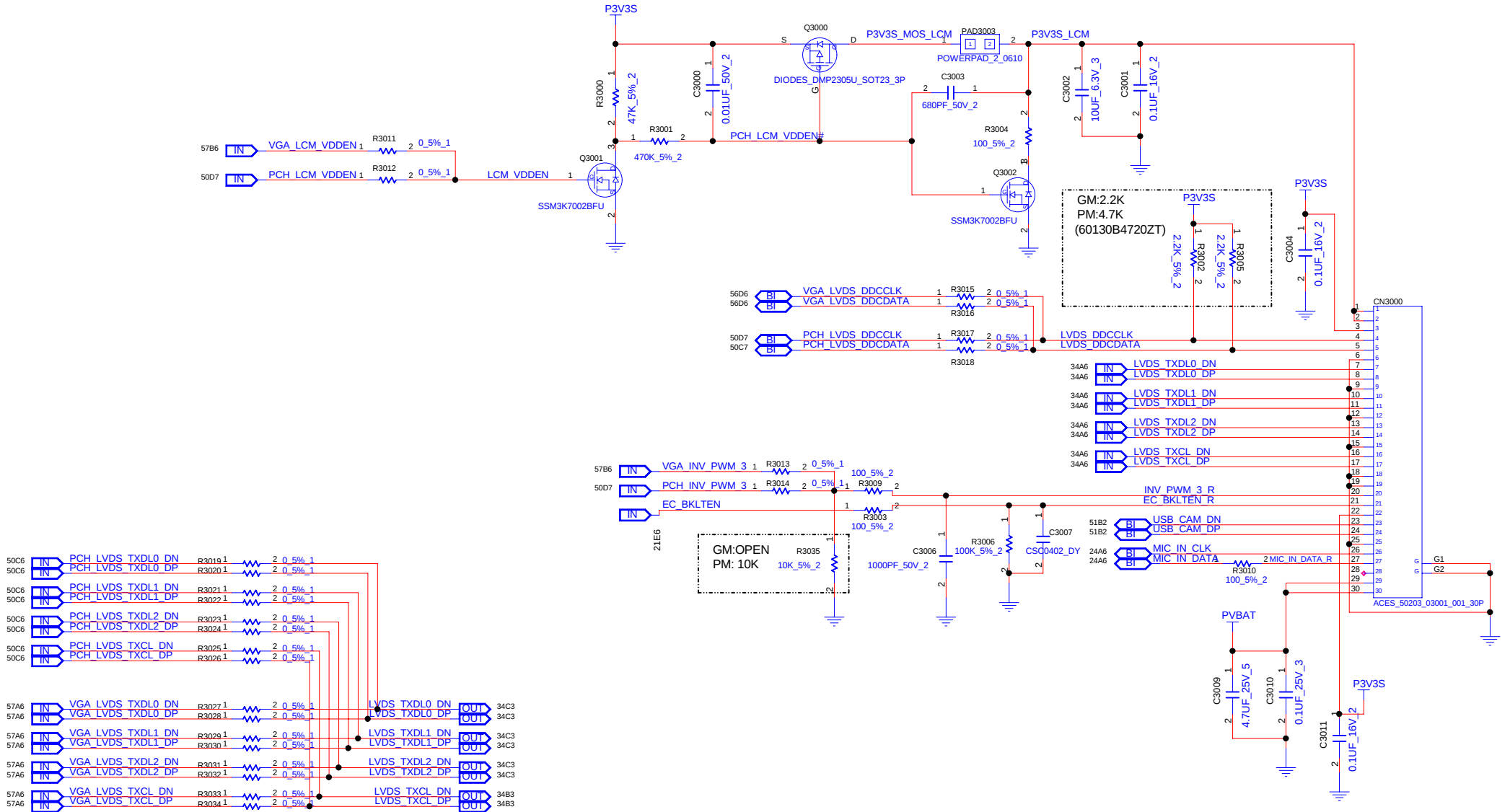
INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

A



REFERENCE 3000~3049(LCM)



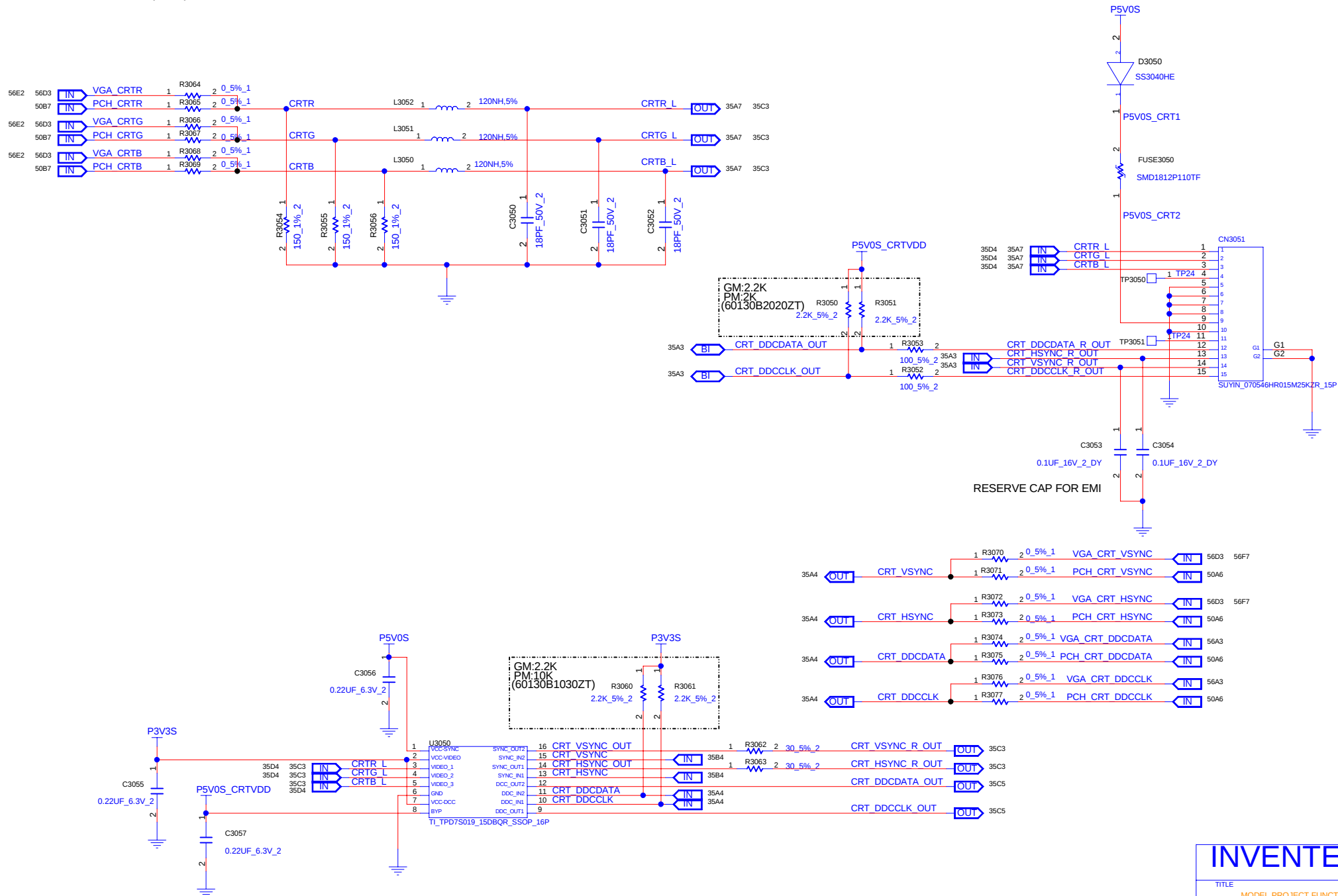
INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE		CODE	
A3	CS	DOC NUMBER	REV
1310xxxx-0-0		X01	

CHANGE by XXX DATE 21-OCT-2002

SHEET 34 of 70

REFERENCE 3050~3099(CRT)

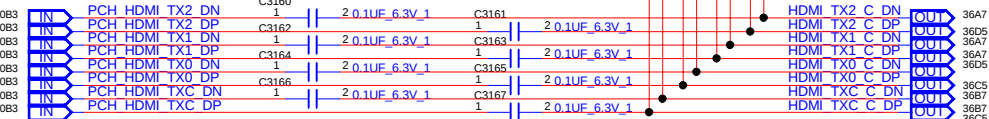
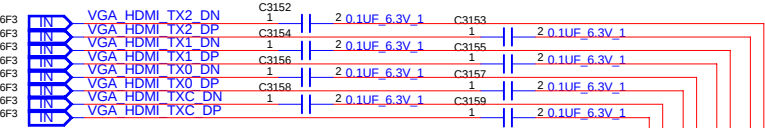
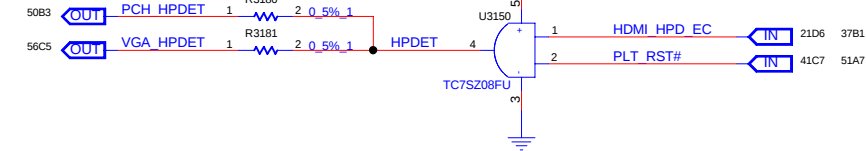
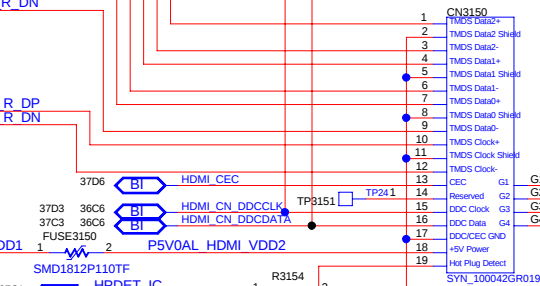
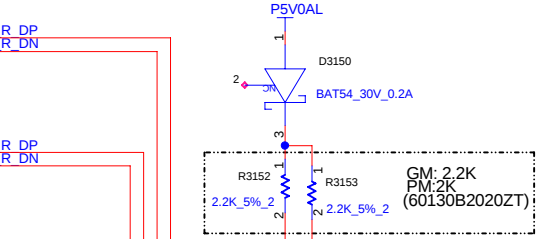
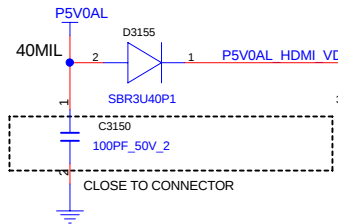
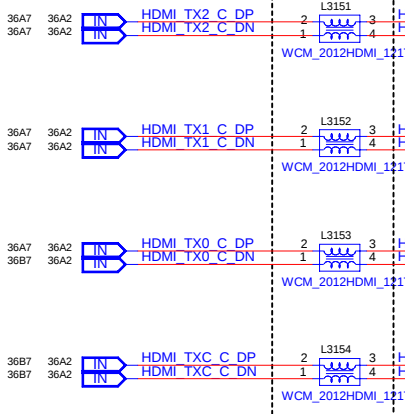
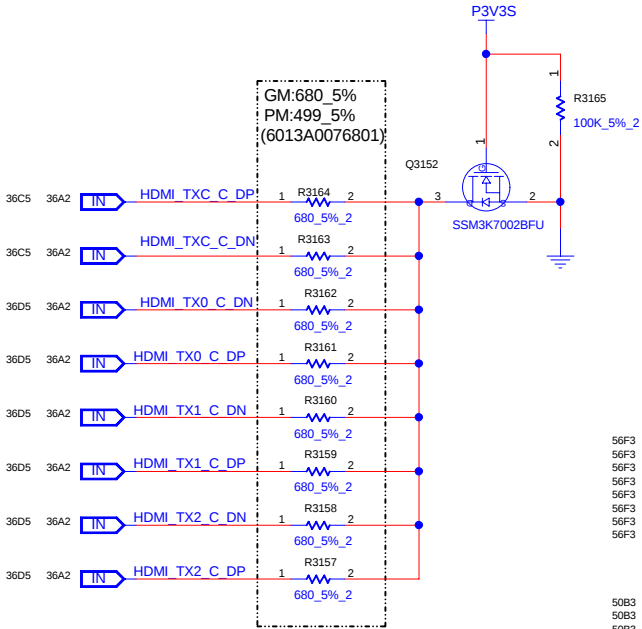
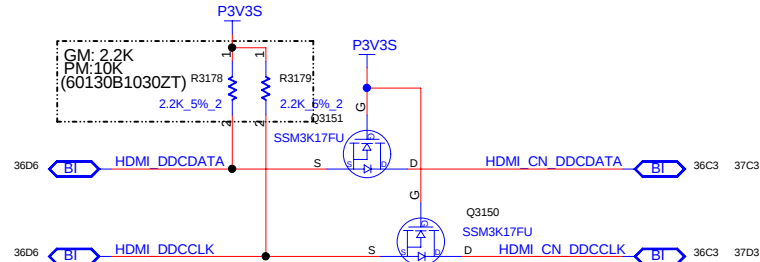
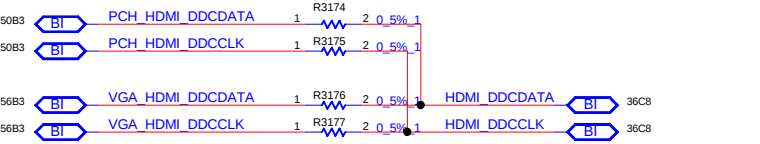


INVENTEC

TITLE		
MODEL,PROJECT,FUNCTION Block Diagram		
SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0

REFERENCE 3150~3199(HDMI)

PLACE CLOSE TO CONNECTOR



INVENTEC

36A7 36D5

TITLE

MODEL,PROJECT,FUNCTION

Block Diagram

DOC NUMBER

1310xxxx-0-0

REV

X01

SIZE

A3

CODE

CS

CHANGE by

xxx

DATE

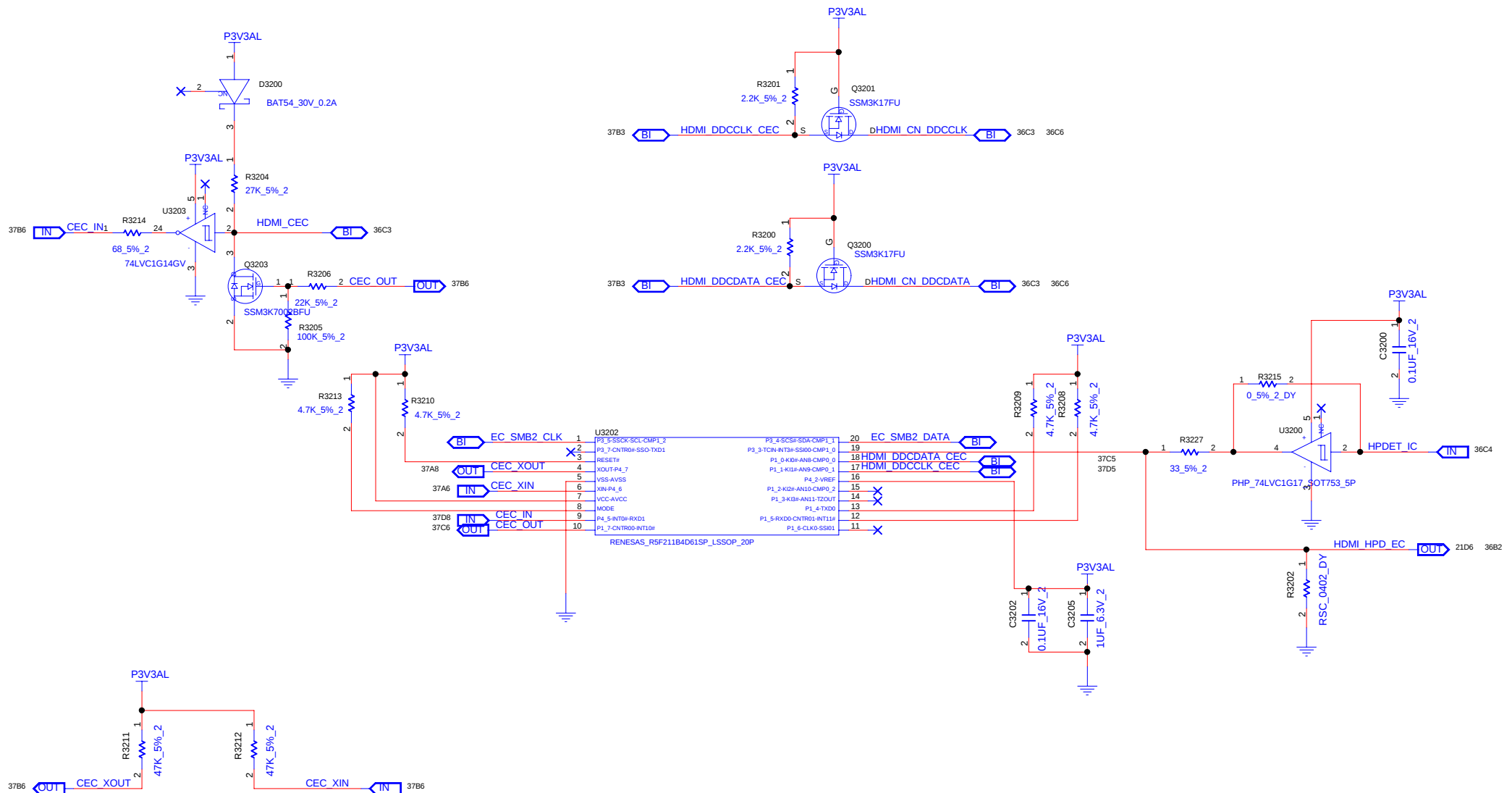
21-OCT-2002

SHEET

36

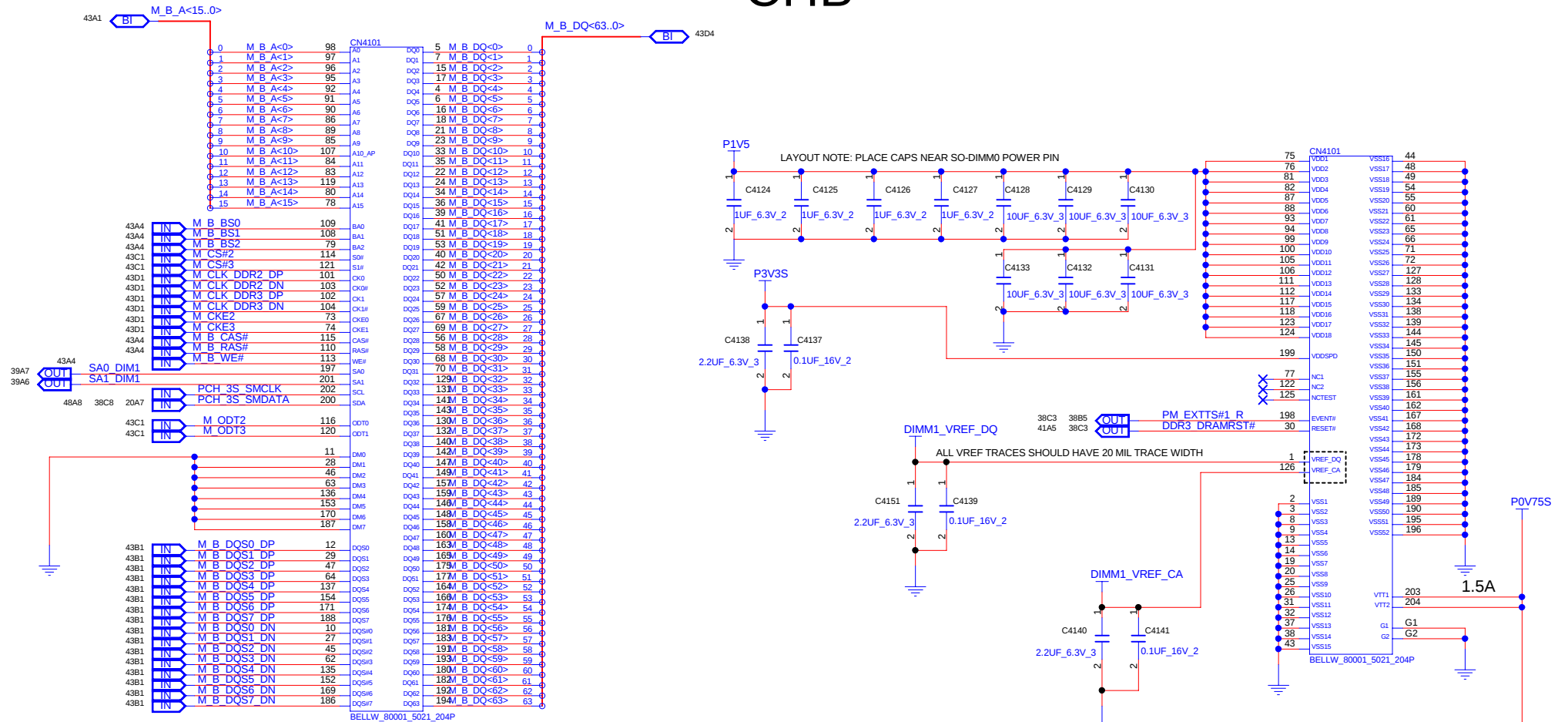
of

70

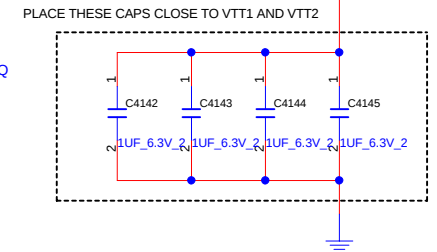
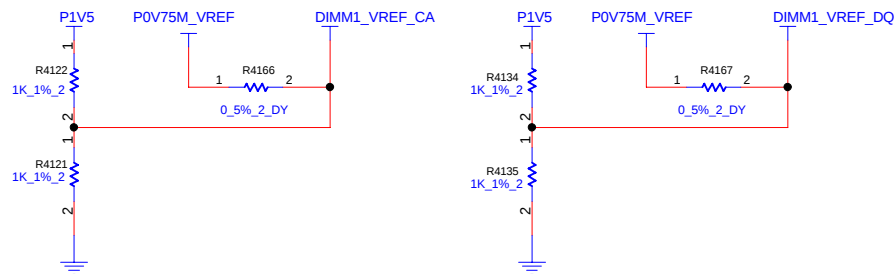
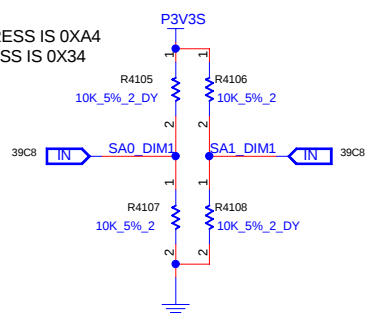


INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01
SHEET 37 of 70			

CHB



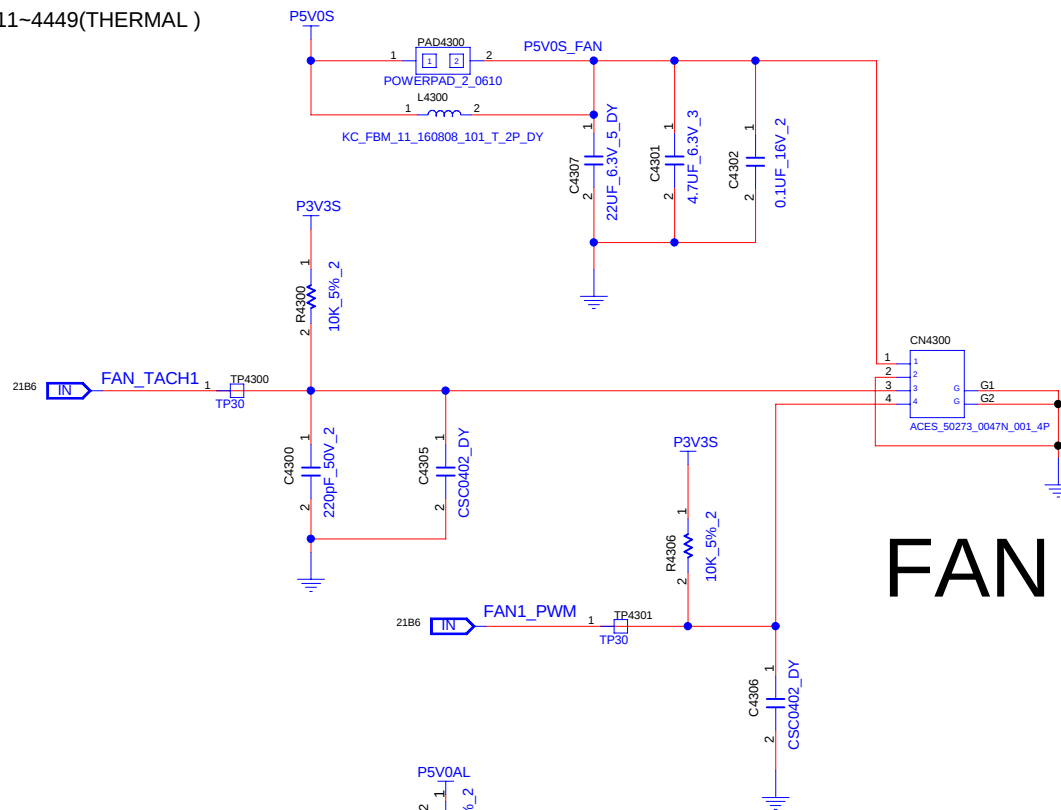
NOTE:
SO-DIMMB SPD ADDRESS IS 0XA4
SO-DIMMB TS ADDRESS IS 0X34



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

REFERENCE 4300~4349(FAN)
REFERENCE 4411~4449(THERMAL)



FAN CN



THERMAL SHUTDOWN	
GM THERMAL SHUTDOWN CPU SIDE AT 68.6 C	GM THERMAL SHUTDOWN VGA SIDE AT 63.4 C
PM THERMAL SHUTDOWN CPU SIDE AT 71.9 C	PM THERMAL SHUTDOWN VGA SIDE AT 80.2 C

	10F	10FG
R4442	48.7K	43.2K
R4443	39K	30.1K
R4446	59K	32.4K
R4441	75K	18.2K

INVENTEC

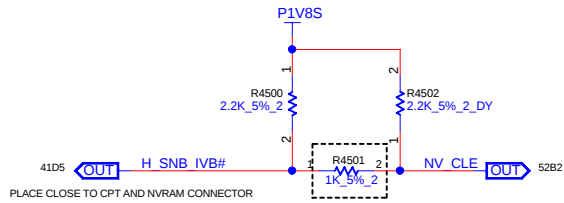
TITLE	MODEL PROJECT FUNCTION
1. Project Overview	1. Project Overview
2. Project Objectives	2. Project Objectives
3. Project Scope	3. Project Scope
4. Project Organization	4. Project Organization
5. Project Schedule	5. Project Schedule
6. Project Budget	6. Project Budget
7. Project Risks	7. Project Risks
8. Project Communication	8. Project Communication
9. Project Monitoring and Control	9. Project Monitoring and Control
10. Project Closure	10. Project Closure

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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CHANGE by	XXX	DATE	21-OCT-2002
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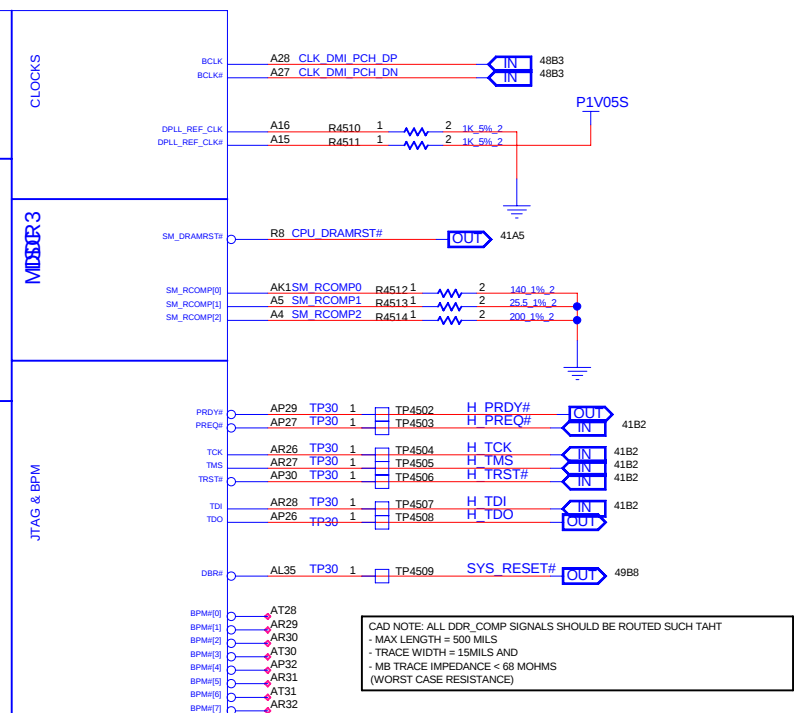
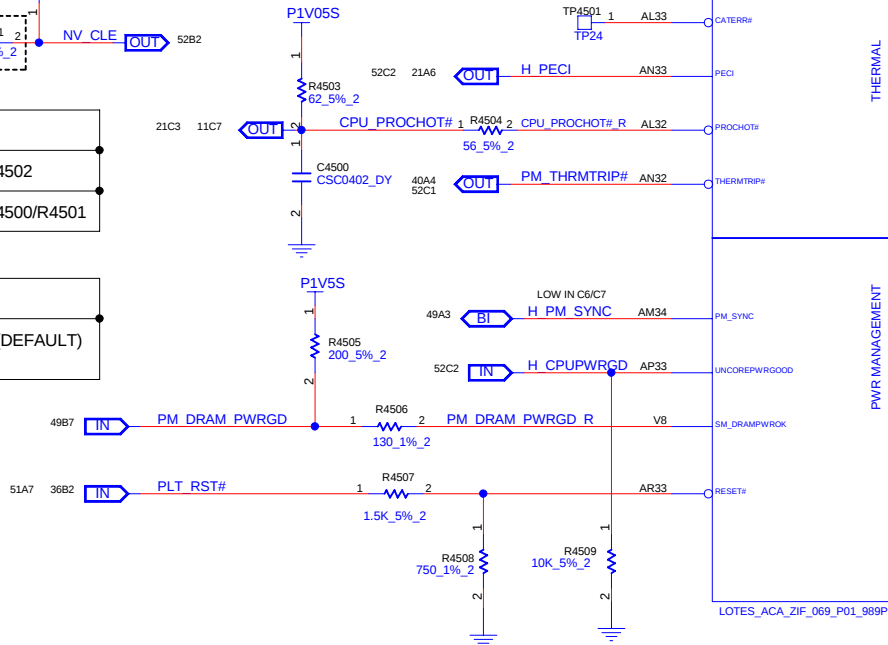
SHEET 40 of 70

REFERENCE 4500~4699(CPU)

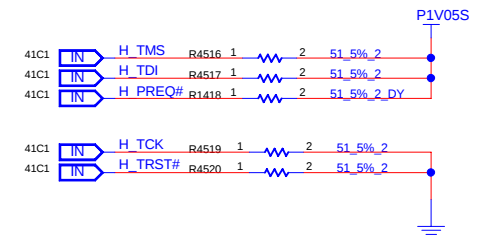


PROCESS STRAP SETTING	
SANDY BRIDGE ONLY	STUFF R4502
SANDY BRIDGE/IVY BRIDGE	STUFF R4500/R4501

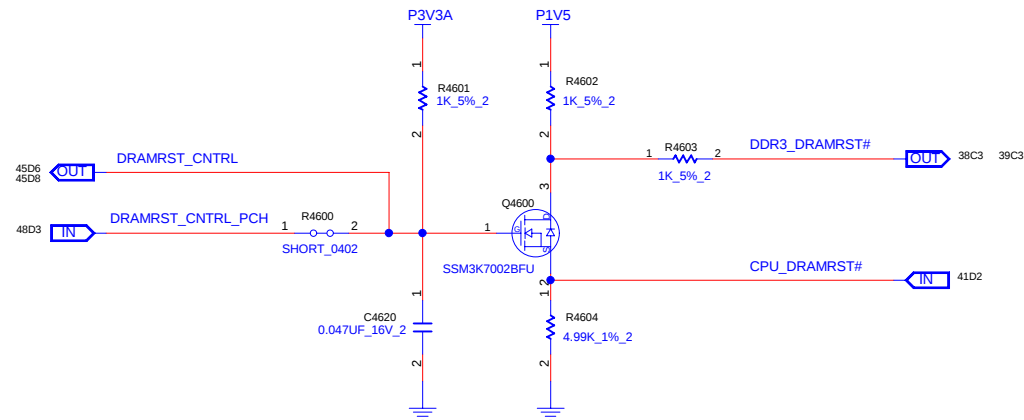
DMI&FDI TERMINATIONVOLTAGE	
NV_CLE	SET TOVSS WHEN LOW (DEFAULT) SET TOVCC WHEN HIGH



CAD NOTE: ALL DDR_COMP SIGNALS SHOULD BE ROUTED SUCH THAT
- MAX LENGTH = 500 MILS
- TRACE WIDTH = 15MILS AND
- MB TRACE IMPEDANCE < 68 MOHMS
(WORST CASE RESISTANCE)



S3 CIRCUIT: DRAM_RST# TO MEMORY SHOULD BE HIGH DURING S3



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01

REFERENCE 4500~4699(CPU)

CAD NOTE: DP_COMPIO AND ICOMPO SIGNALS
SHOULD BE SHORTED NEAR BALLS AND ROUTED WITH
- TYPICAL IMPEDANCE < 25 MOHMS

CAD NOTE: PEG_ICOMPI AND RCOMPO SIGNALS
SHOULD BE SHORTED AND ROUTED WITH
- MAX LENGTH = 500 MILS
- TYPICAL IMPEDANCE = 43 MOHMS

PEG_ICOMPO SIGNALS SHOULD BE ROUTED WITH
- MAX LENGTH = 500 MILS
- TYPICAL IMPEDANCE = 14.5 MOHMS

CLOSE TO CPU

INVENTEC

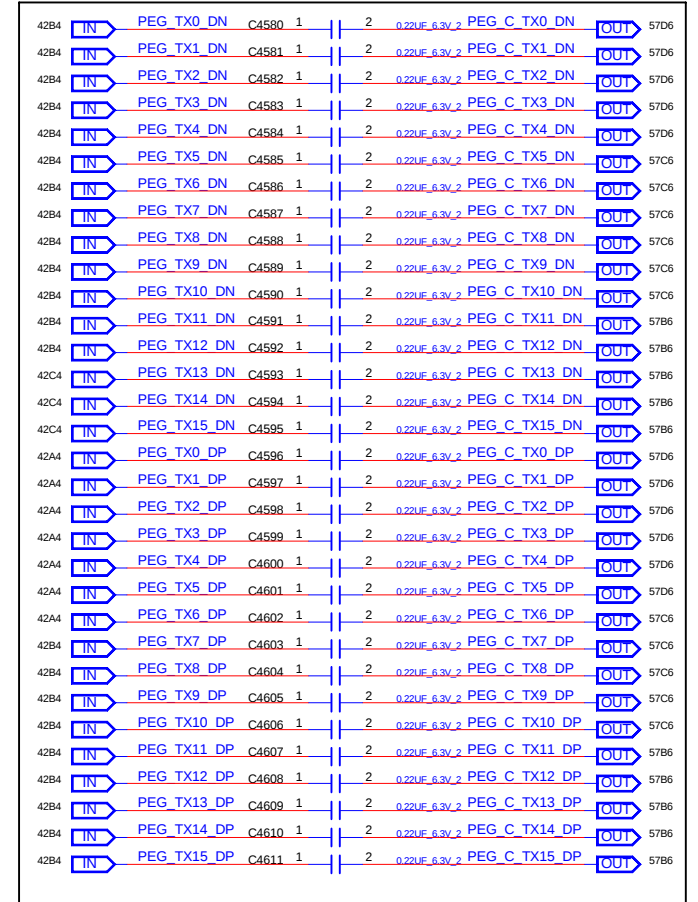
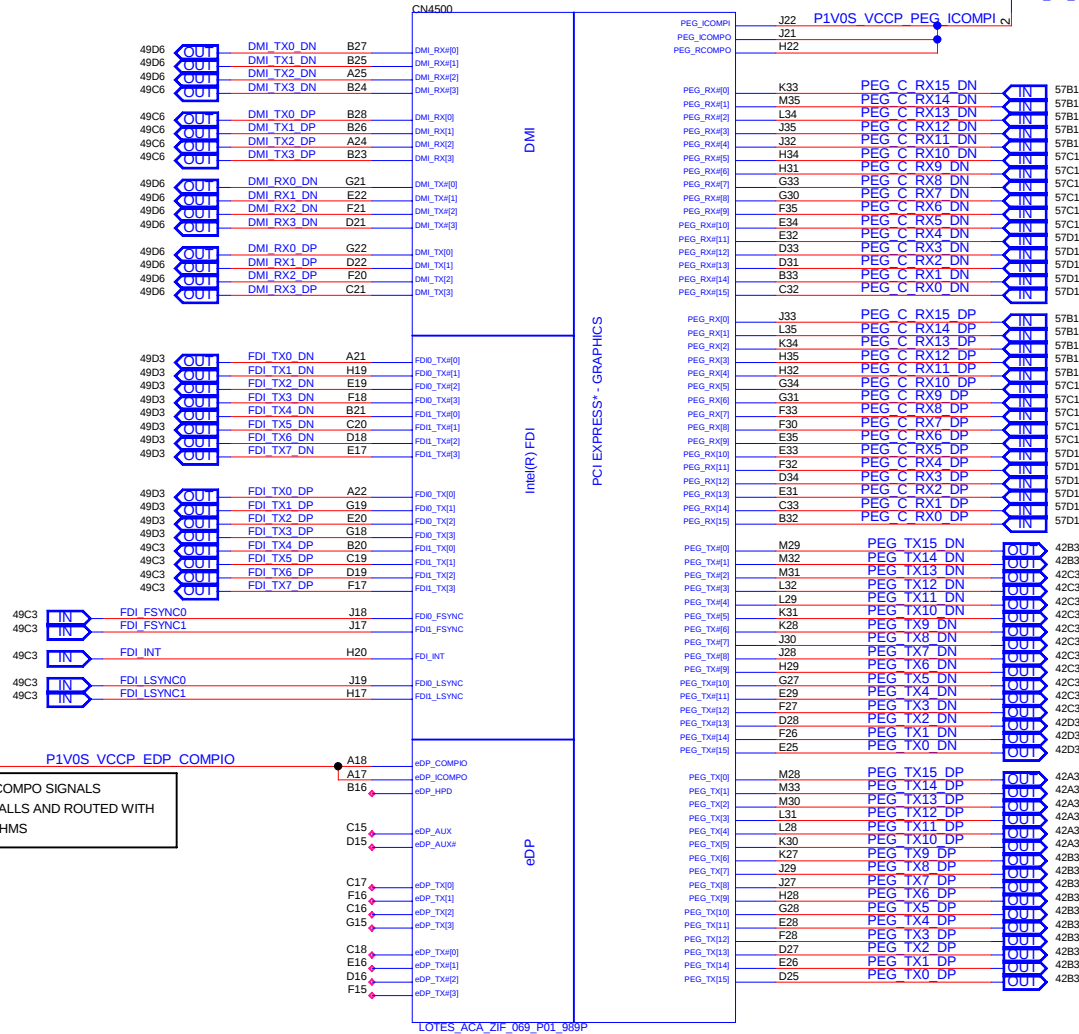
TITLE
MODEL PROJECT FUNCTION
Block Diagram

DOC NUMBER
1310xxxx-0-0

REV
X01

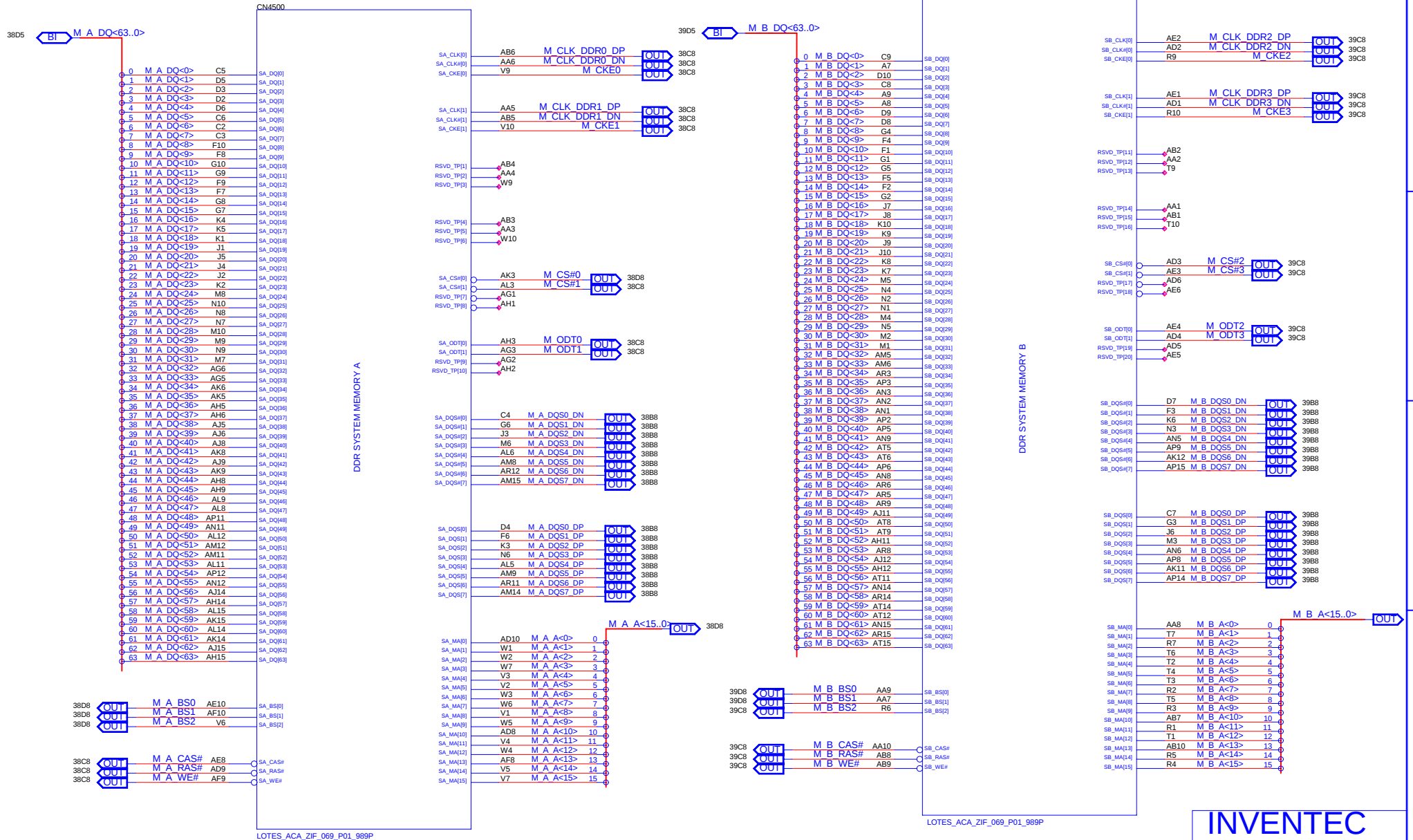
CHANGE by XXX DATE 21-OCT-2002

SHEET 42 of 70



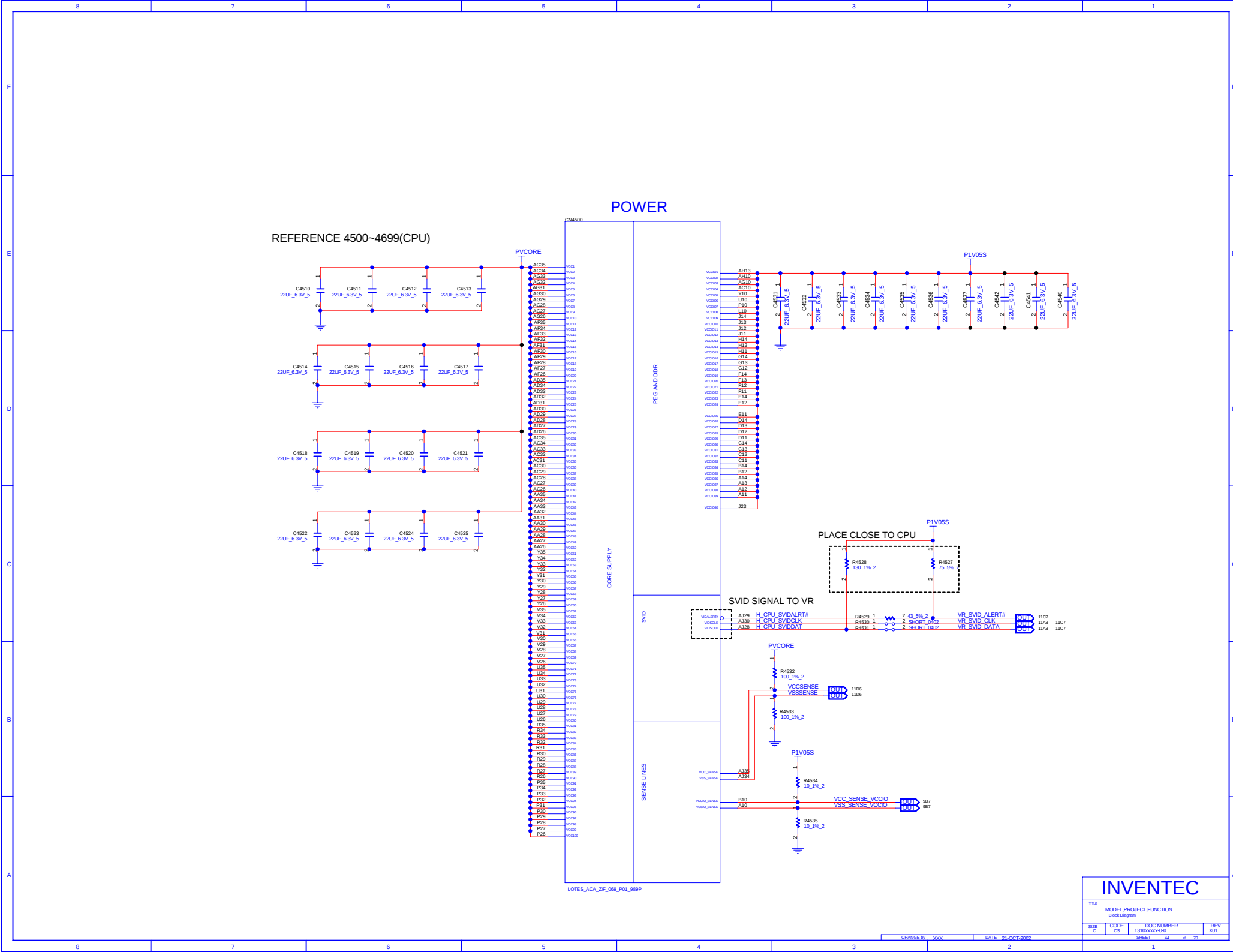
REFERENCE 4500~4699(CPU)

SOCKET,CPU,989P,TIN,3.0MM,STR,SMD,TR

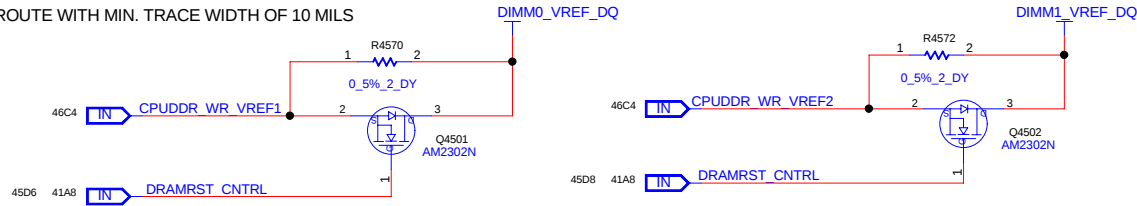


INVENTEC

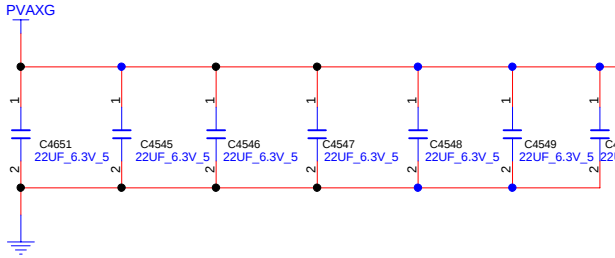
TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
DOC NUMBER			
1310xxxx-0-0			
REV			
X01			
SHEET	43	of	70



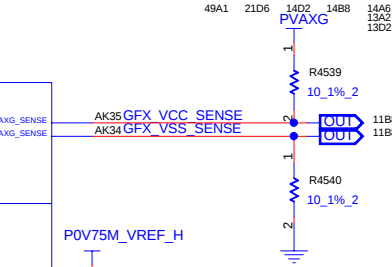
PROCESSOR DRIVEN VREF PATH WAS STUFFED BY DEFAULT:
ROUTE WITH MIN. TRACE WIDTH OF 10 MILS



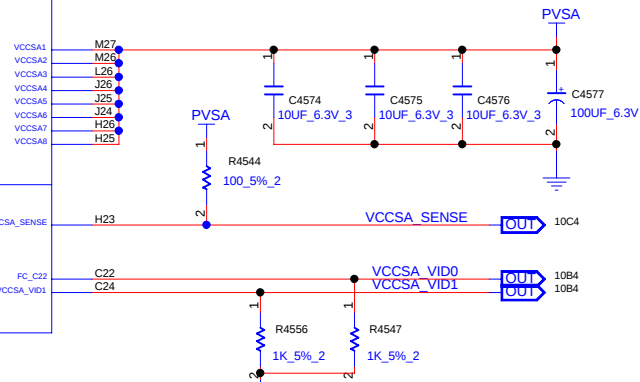
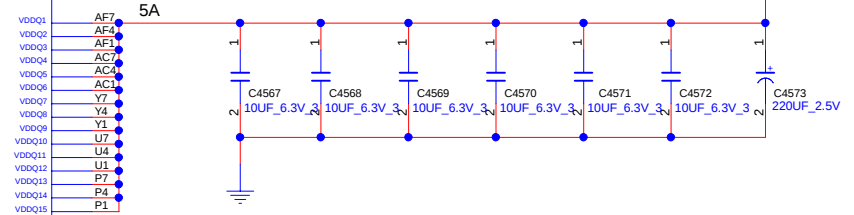
POWER



LOTES_ACA_ZIF_069_P01_989P



NOTE : DDR_WR_VREF SHOULD HAVE 20/20 MIL WHEREVER POSSIBLE



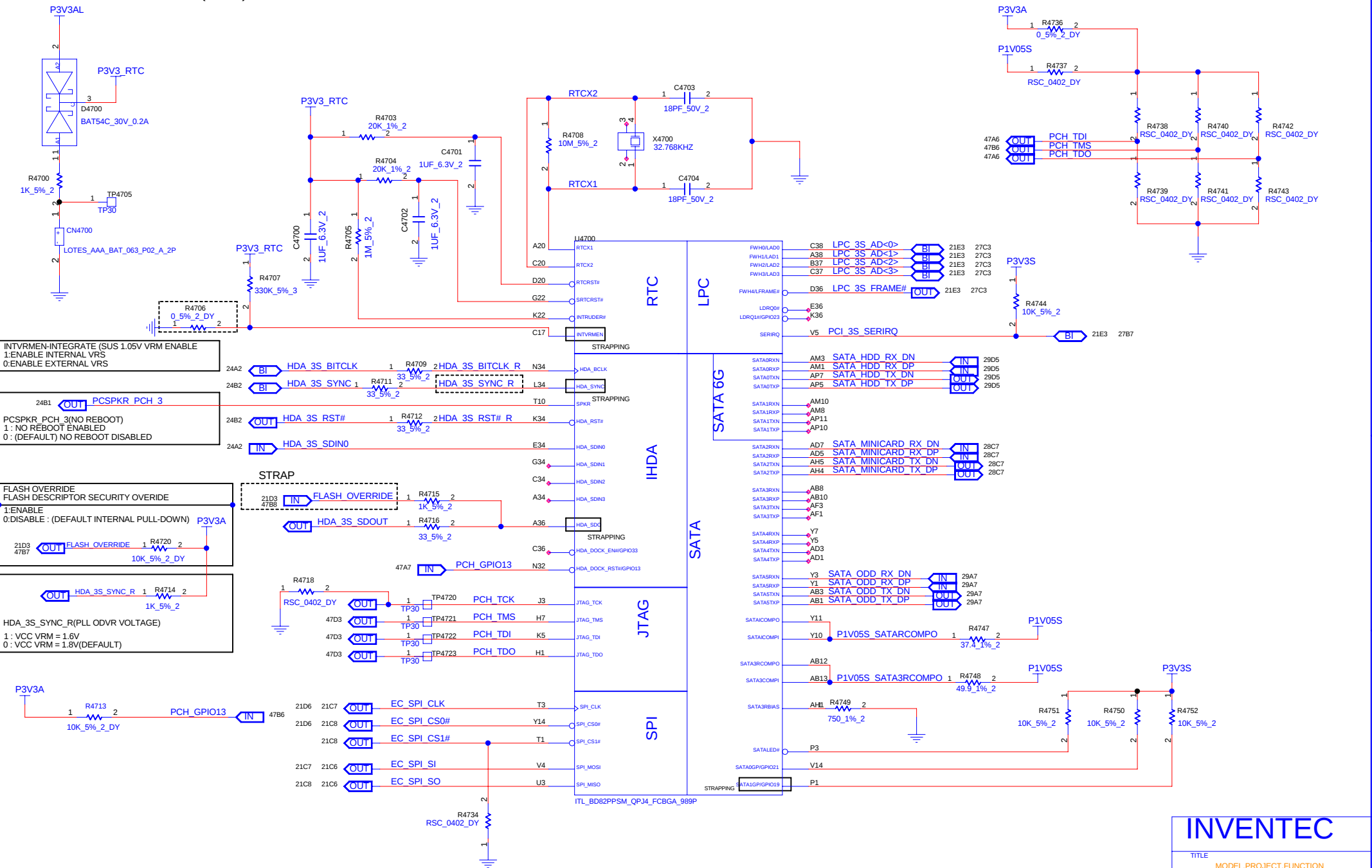
INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 45 of 70

REFERENCE 4700~4949(PCH)



INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
DOC NUMBER			
1310xxxx-0-0			
REV			
X01			
SIZE	CODE	SHEET	
A3	CS	47 of 70	

D

C

B

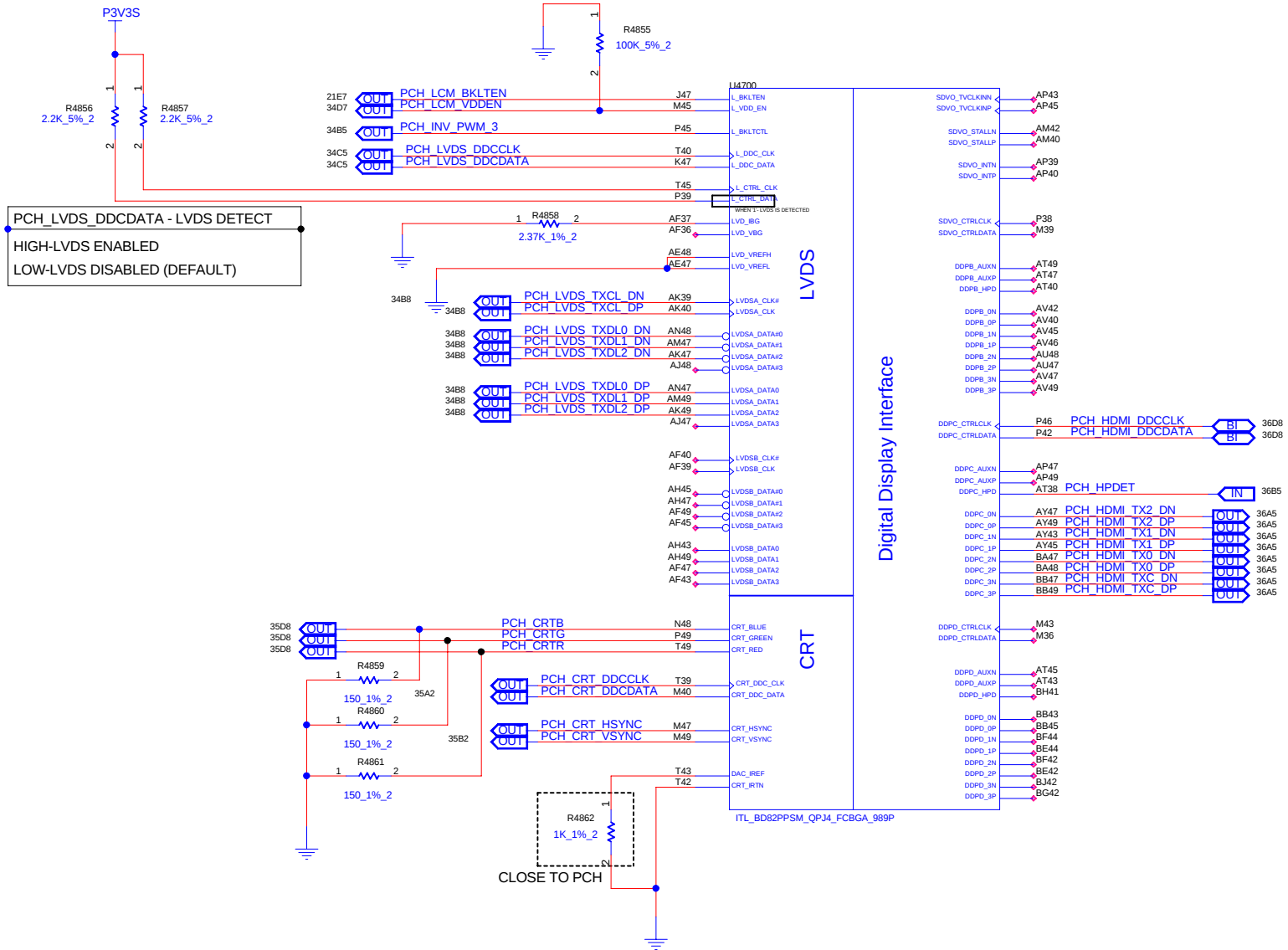
A

D

C

B

A



INVENTEC

TITLE

MODEL PROJECT FUNCTION

Block Diagram

SIZE A3

CODE CS

DOC NUMBER 1310xxxx-0-0

REV X01

CHANGE by XXX

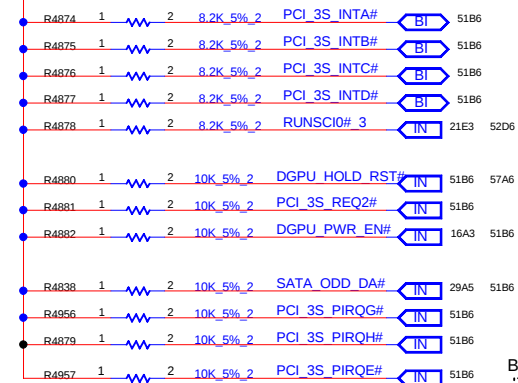
DATE 21-OCT-2002

SHEET 50 of 70

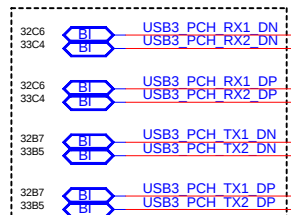
REFERENCE 4700-4949(PCH)

GPIO51 BBS_BIT1	GPIO19 BBS_BIT0	BOOT BIOS DESTINATION
0	1	RESERVED(NAND)
1	0	-----
1	1	SPI (DEFAULT)
0	0	LPC

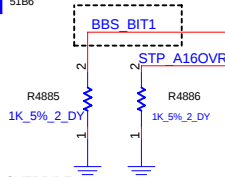
P3V3S



ROUTE WITH 90 OHMS IMPEDANCE
TOTAL LENGTH NO LONGER THAN 11 INCHES



BBS STRAPPING



LOW=A16 SWAP OVERRIDE

STP_A160VR
TOP-BLOCK SWAP OVERRIDE
HIGH=DEFAULT

P3V3A

10K_5%_2_DY

P3V3A PME#

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

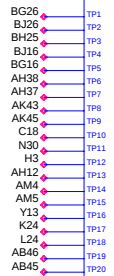
10K_5%_2_DY

MACHINE ID 0: USB ID0
MACHINE ID 1: POWER EXPRESS (YES:1 NO:0)
MACHINE ID 2: UMA:1 / DIS:0
MACHINE ID 3: MAINSTREAM:1 / ENTRY:0
MACHINE ID 4: HDMI:1 / NO HDMI:0
MACHINE ID 5: SLEEP&CHARGE&DOLBY:(YES1 / NO 0)
MACHINE ID 6: USB ID1
MACHINE ID 1_DB : 35W CPU ONLY :1/ 35W&45W CPU:0

USB ID0	USB ID1	USB ID2	USB ID3	USB ID4	USB ID5	USB ID6	USB ID7	USB ID8	USB ID9
1	1	0	0	0	0	0	0	0	0

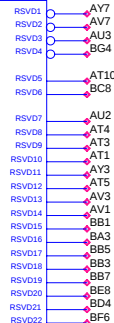
NOTE:10K_5%(60130B1030ZT)

U4700



RSVD

NVRAM

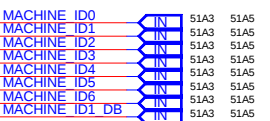


NOTE:
USB2.0/3.0 COMBO-USB2.0 PORT 0,1 MAPPED USB3.0 PORT 1,2

P0.P1 RESERVER FOR USB3.0

CARD READER
WLAN
WEBCAM
3G

22.6 1% 3
CLOSE TO PCH



TO BE USED AS GPIO

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

NOTE:10K_5%(60130B1030ZT)

INVENTEC

TITLE
MODEL PROJECT.FUNCTION
Block Diagram

DOC NUMBER
1310xxxx-0-0

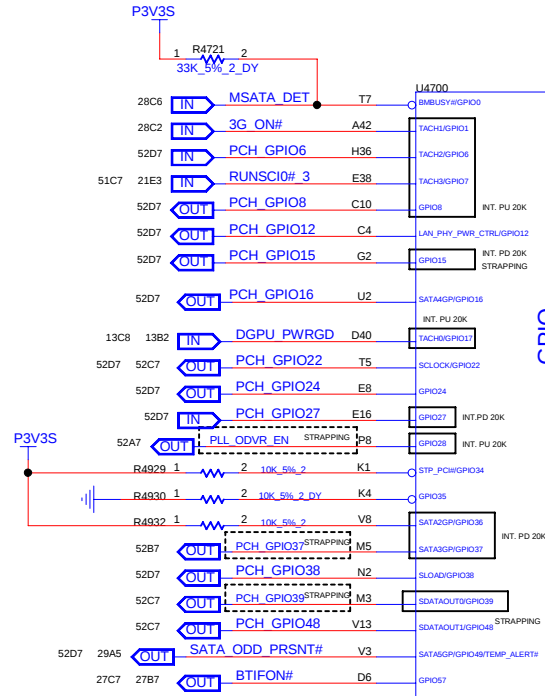
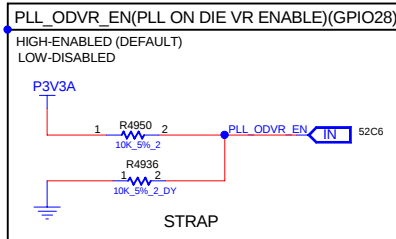
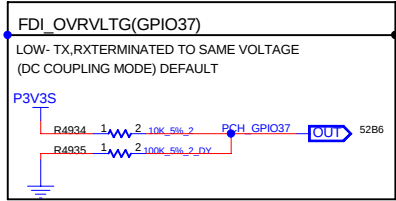
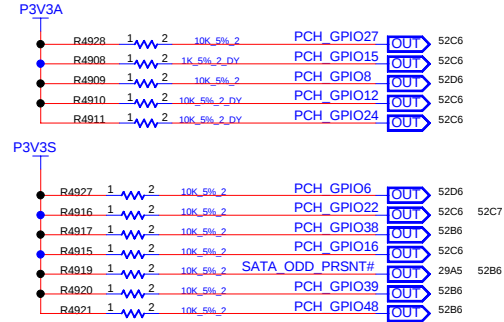
REV
X01

SHEET 51 of 70

CHANGE by XXX

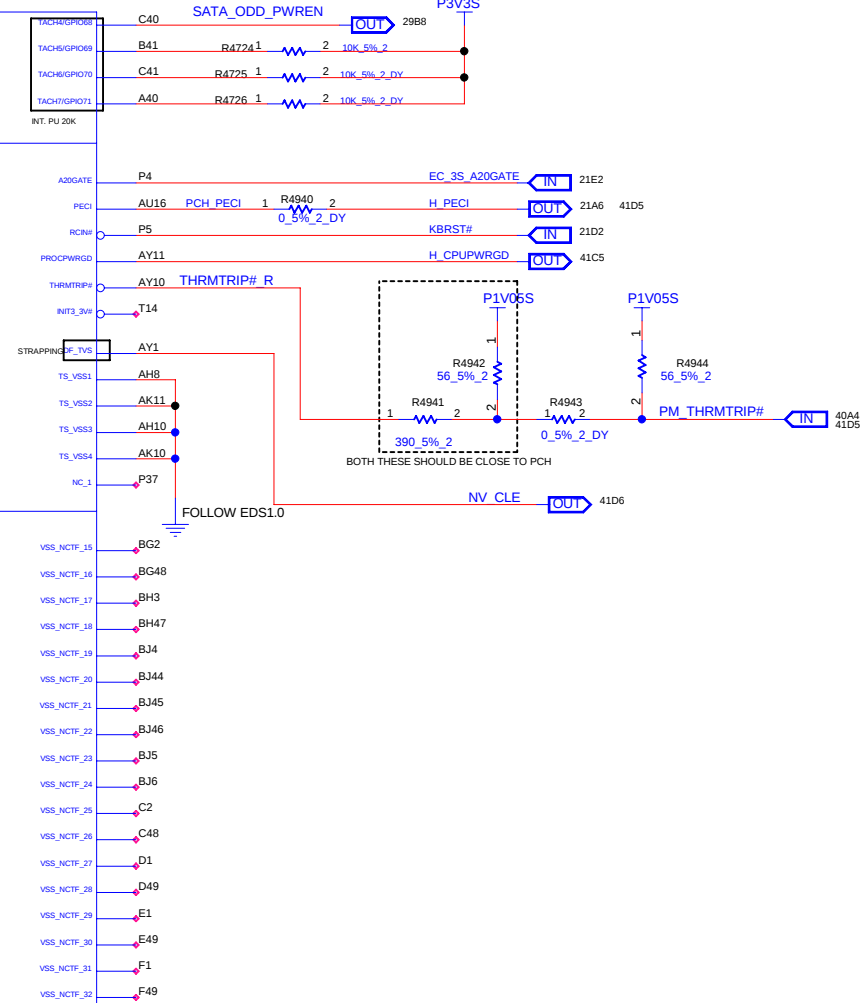
DATE 21-OCT-2002

REFERENCE 4700~4949(PCH)



GPIO

NCTF



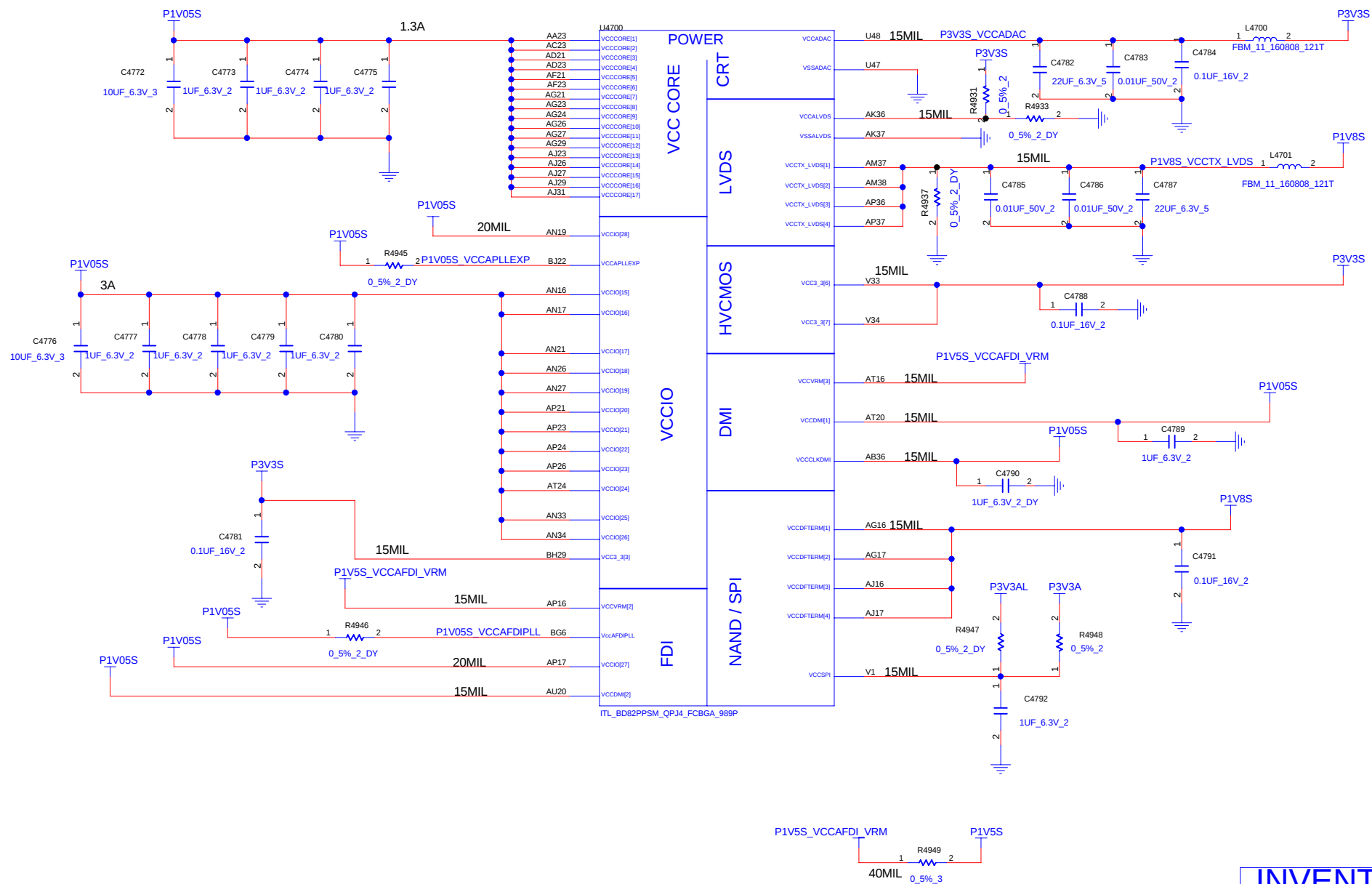
INVENTEC

TITLE MODEL PROJECT FUNCTION

SIZE A3 CODE CS DOC NUMBER 1310xxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2007 SHEET 52 of 70

REFERENCE 4700~4949(PCH)



INVENTEC

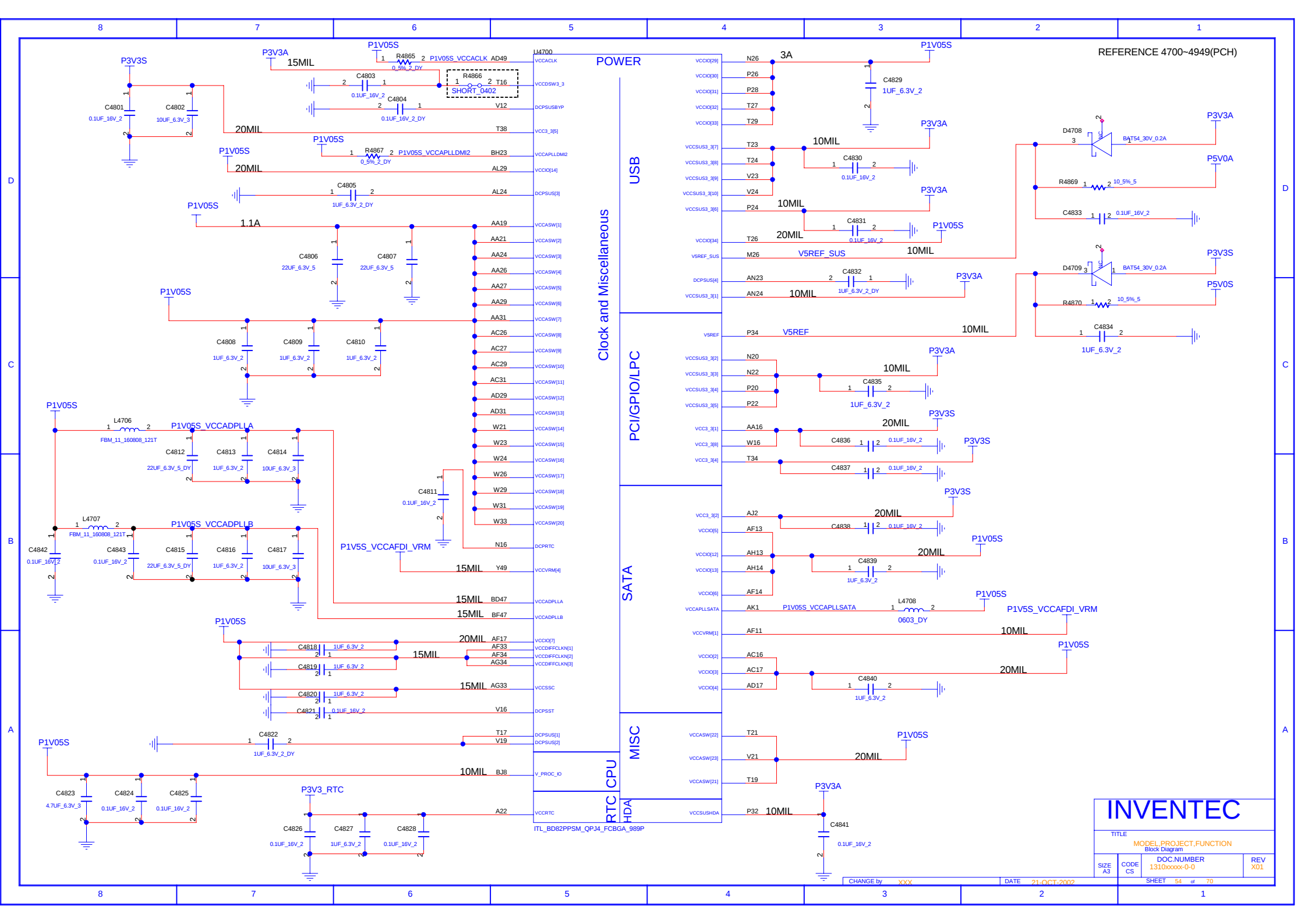
TITLE	MODEL PROJECT FUNCTION

MODEL, PROJECT, FUNCTION		
Block Diagram		
		DOC. NUMBER

SIZE A3	CODE CS	1310xxxxx-0-0
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CHANGE by	XXX	DATE	21-OCT-2002
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SHEET 53 of 70



REFERENCE 4700-4949(PCH)

INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
DOC NUMBER			
1310xxxx-0-0			
SIZE	CODE	REV	
A3	CS	X01	

8

7

6

5

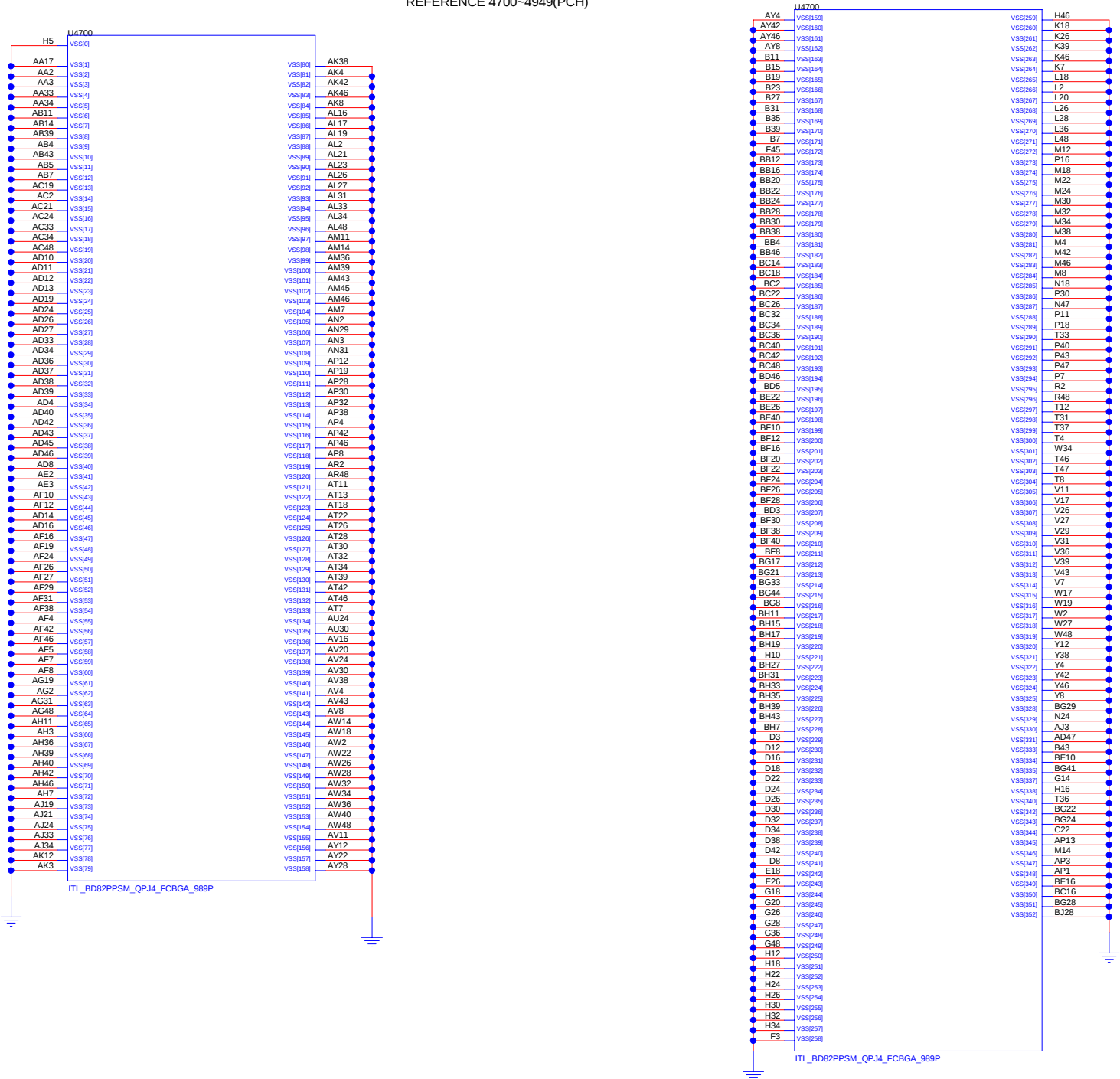
4

3

2

1

REFERENCE 4700~4949(PCH)



INVENTEC

TITLE
MODEL PROJECT FUNCTION
Block DiagramSIZE
A3CODE
CSDOC NUMBER
1310xxxx-0-0REV
X01

CHANGE by XXX

DATE 21-OCT-2002

SHEET 55 of 70

8

7

6

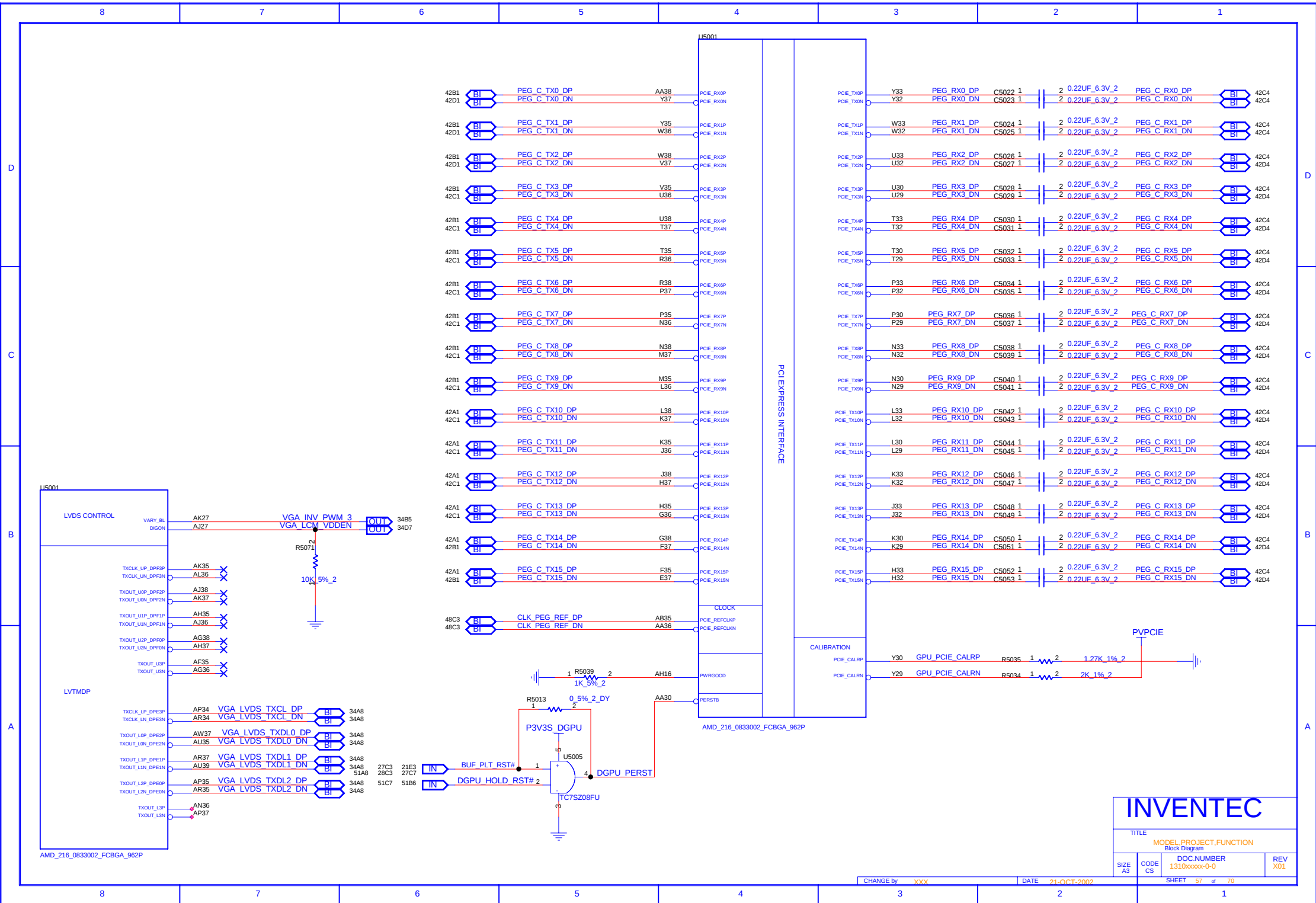
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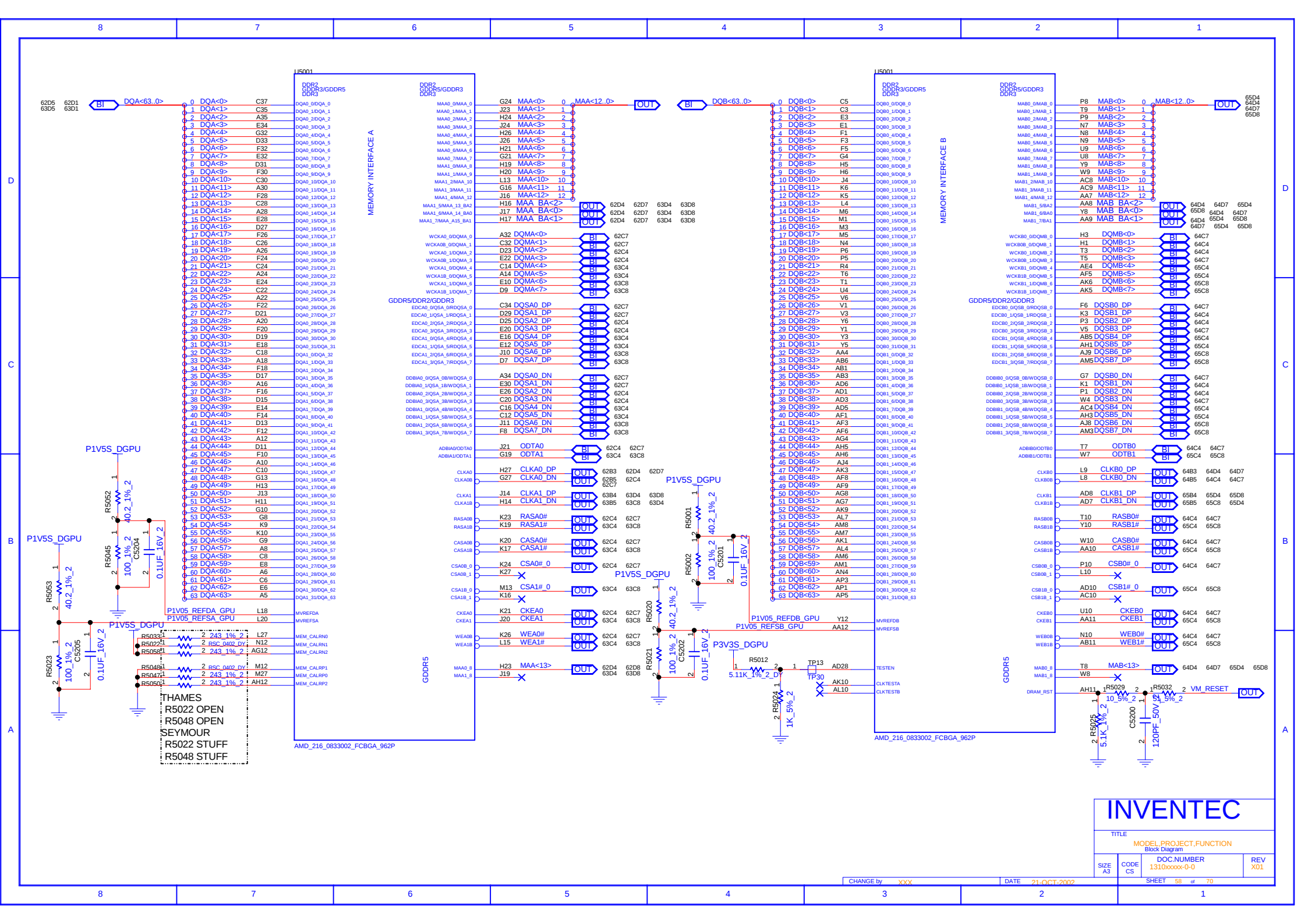
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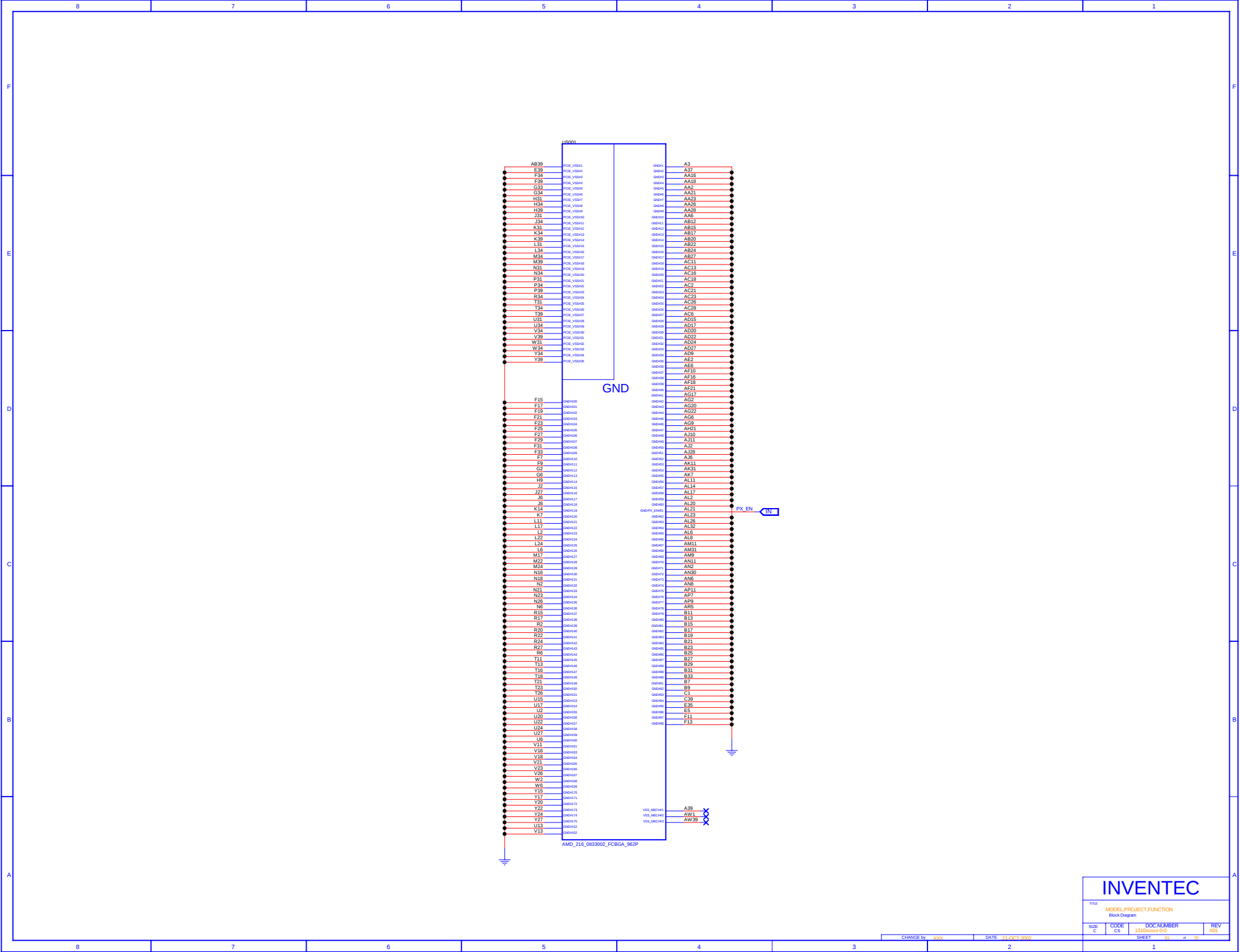
3

2

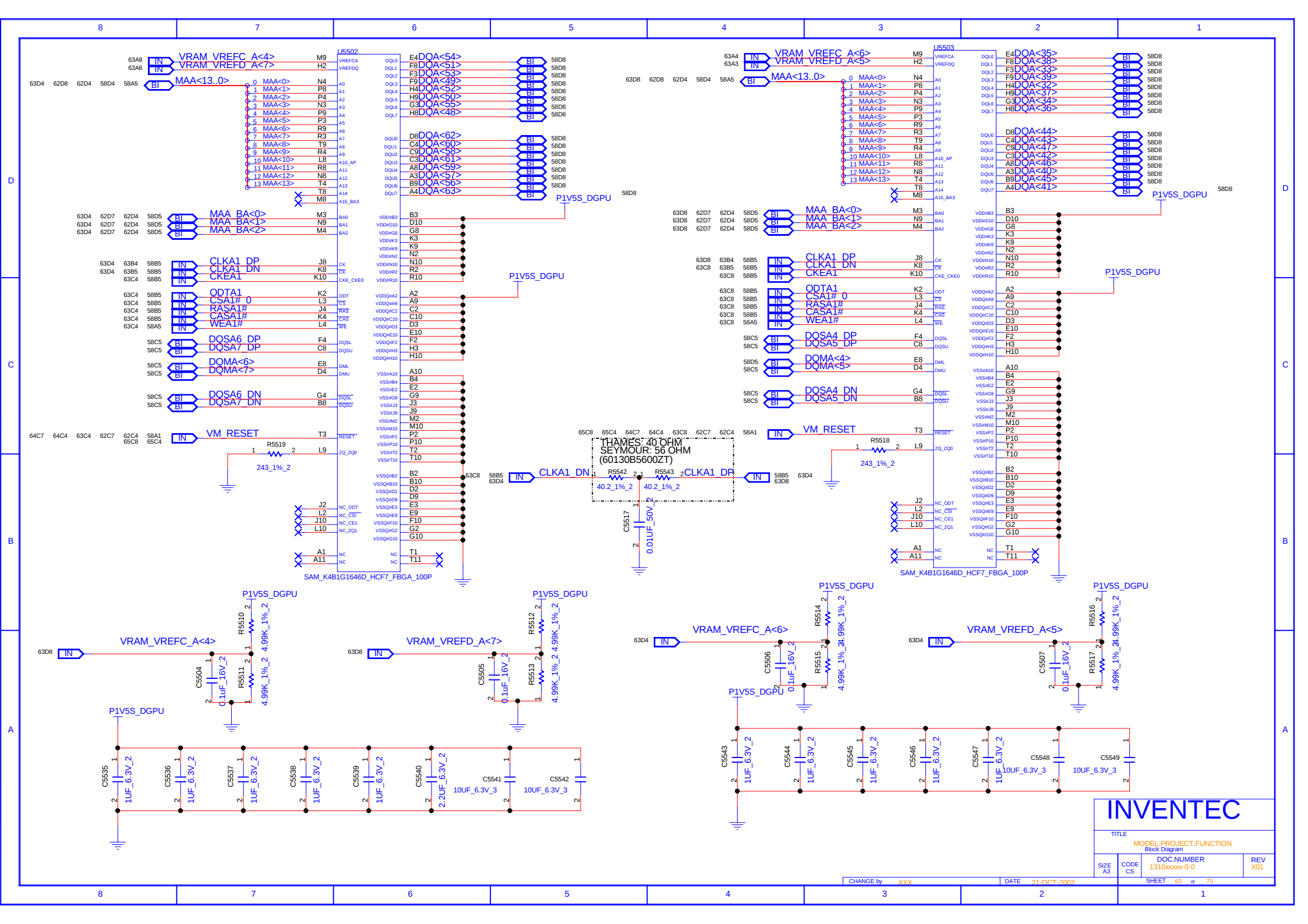
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INVENTEC

TITLE

MODEL PROJECT FUNCTION

Block Diagram

DOC NUMBER

1310xxxx-0-0

REV

X01

SIZE

A3

CODE

CS

CHANGE by

xxx

DATE

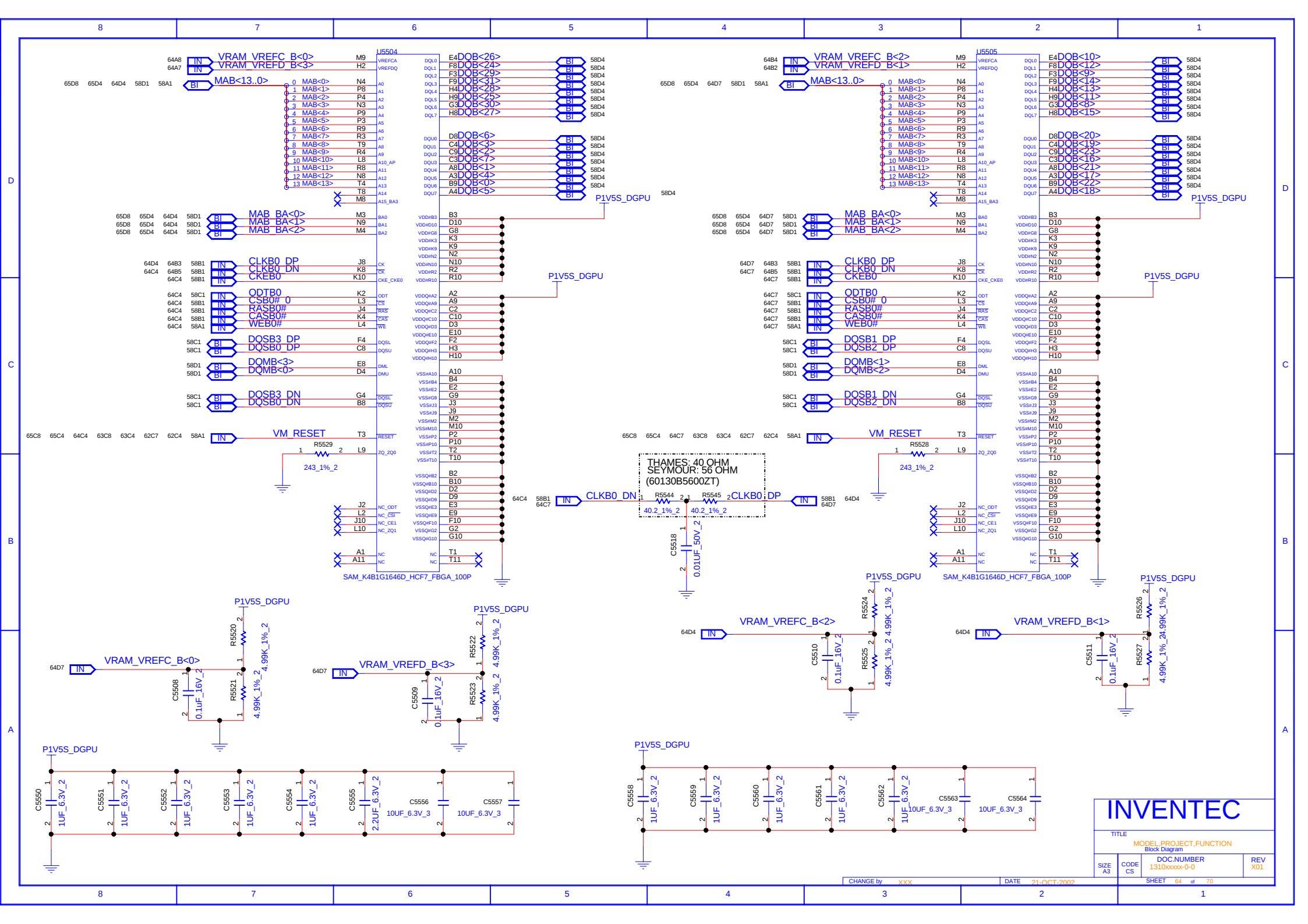
21-OCT-2002

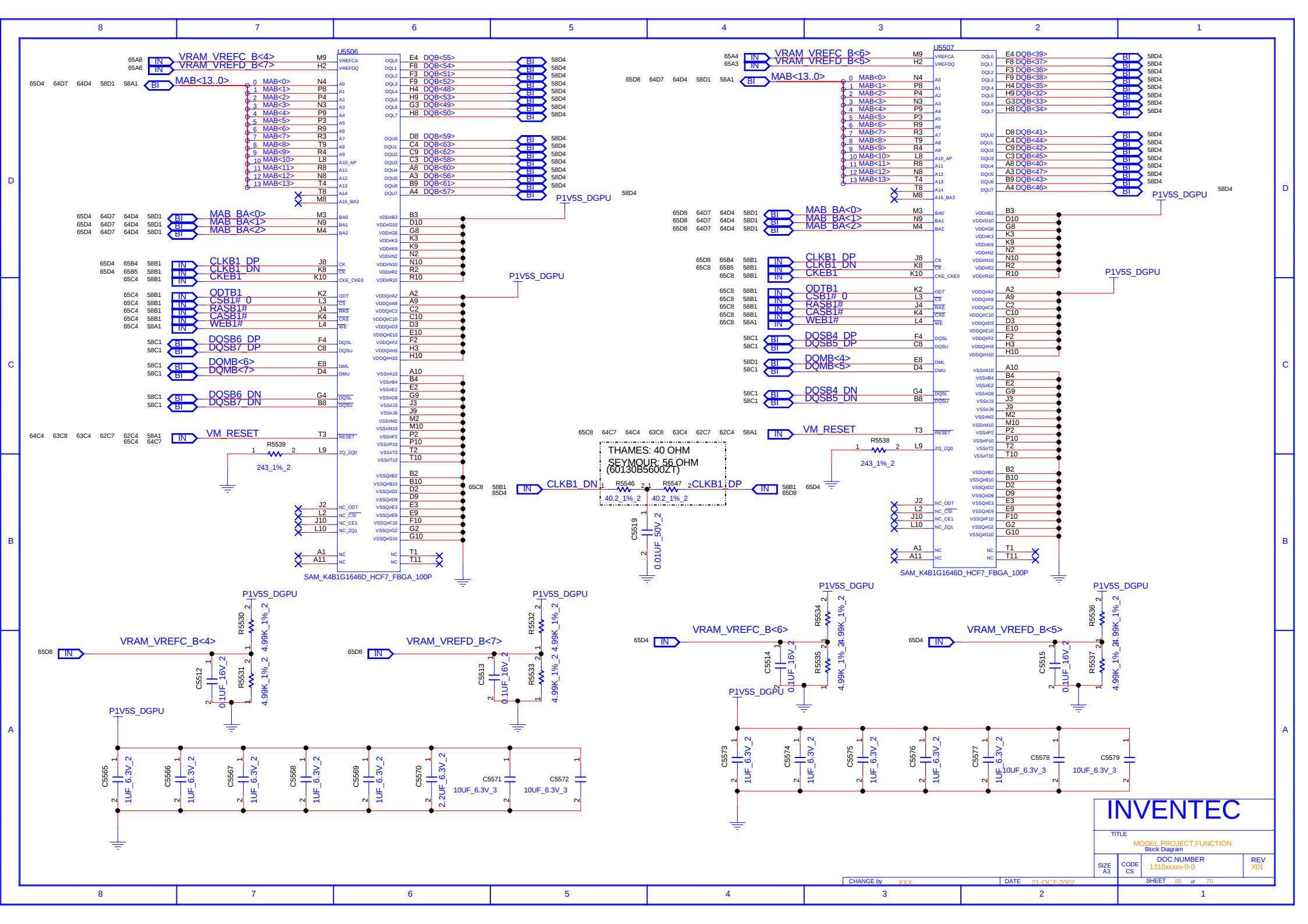
SHEET

63

of

70





INVENTEC

TITLE

MODEL PROJECT FUNCTION

Block Diagram

DOC NUMBER

1310xxxx-0-0

REV

X01

SIZE

A3

CODE

CS

CHANGE by

xxx

DATE

21-OCT-2002

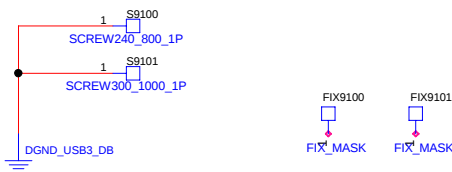
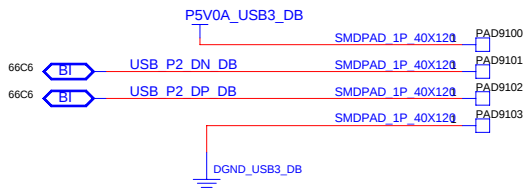
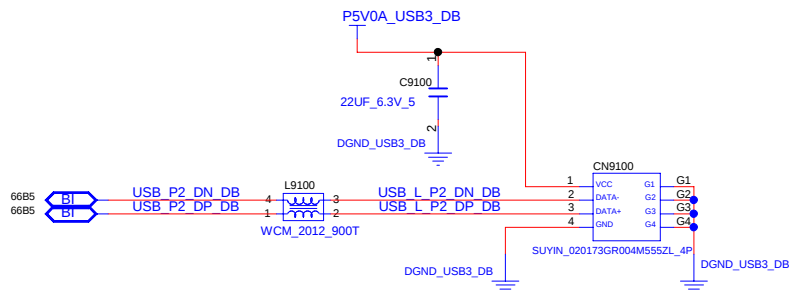
SHEET

65

of

70

USB BOARD



INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

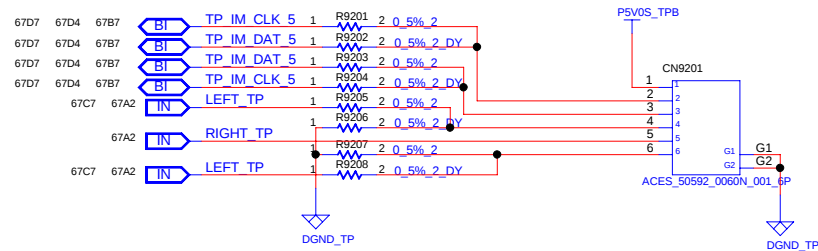
SIZE A3 CODE CS DOC NUMBER 1310xxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002

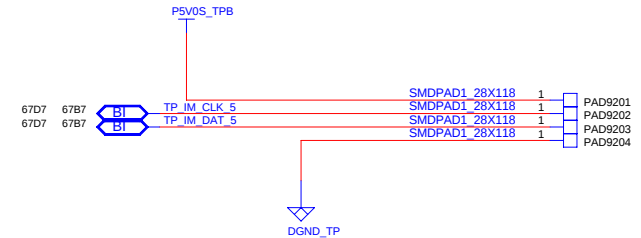
SHEET 66 of 70

TOUCH PAD BOARD 4 PIN

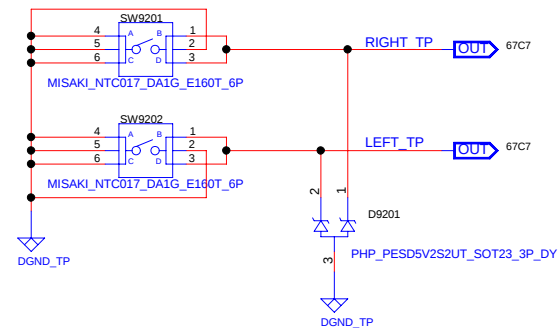
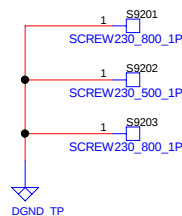
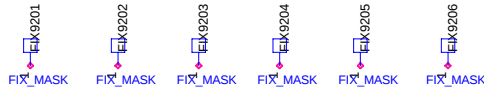
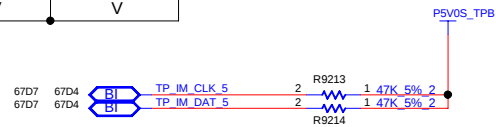
TOUCHPAD TO MODULE CONN



TOUCHPAD TO MB CONN



4PIN	IMR/METAL	TEXTURE
R9201		V
R9202	V	
R9203		V
R9204	V	
R9205		V
R9206	V	
R9207		V
R9208	V	
R9213	V	V
R9214	V	V

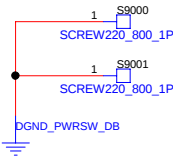
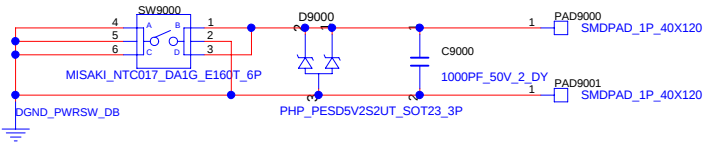


INVENTEC

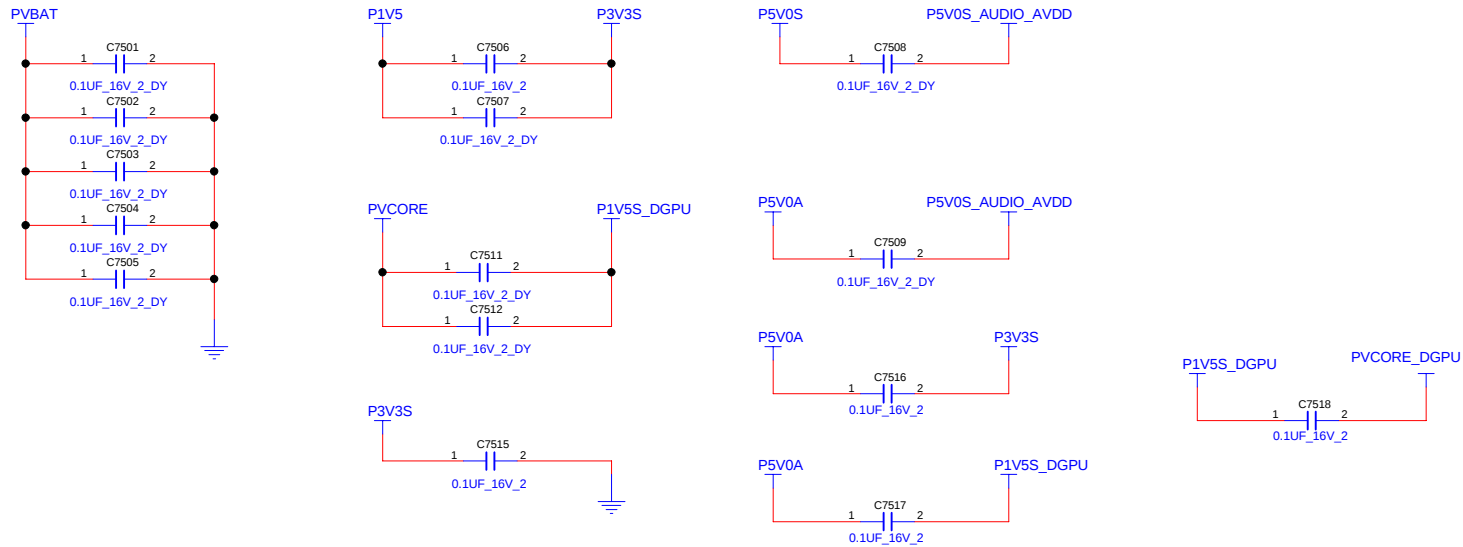
TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

REFERENCE 9000~9999(SMALL BOARD)

POWER BUTTON



INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
CHANGE by XXX			
DATE 21-OCT-2002			
SHEET 68 of 70			



INVENTEC

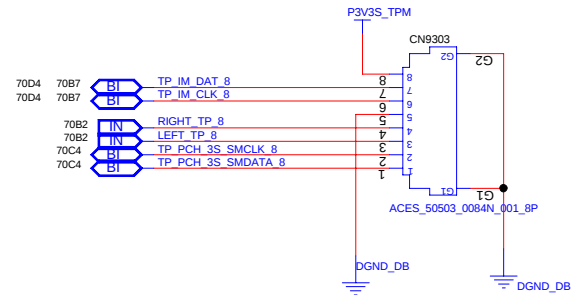
TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01
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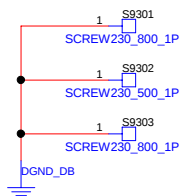
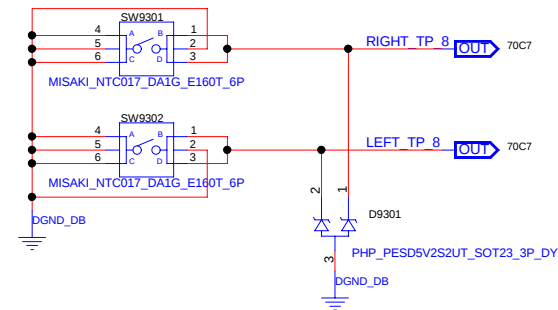
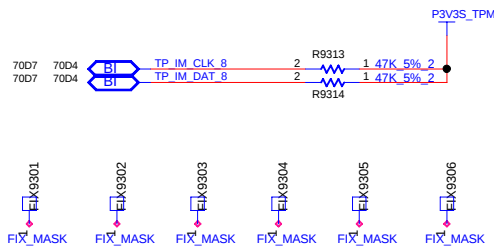
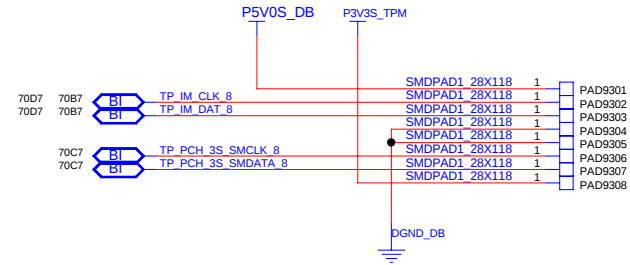
CHANGE by XXX DATE 21-OCT-2002 SHEET 69 of 70

TOUCH PAD BOARD 8 PIN

TOUCHPAD TO MODULE CONN



TOUCHPAD TO MB CONN



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 70 of 70