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HSF Property: ROHS or Halogen-Free(5L3?)

GL10FG

2011.08..31

21-OCT-2002		
DATE	CHANGE NO.	REV

	EE	DATE	POWER	DATE	INVENTEC			
DRAWER								
DESIGN								
CHECK								
RESPONSIBLE								
SIZE				VER	SIZE	CODE	DOC. NUMBER	REV
FILE NAME					C	CS	13100000X-0-0	X01
PIN	XXX				SHEET 1 of 35			

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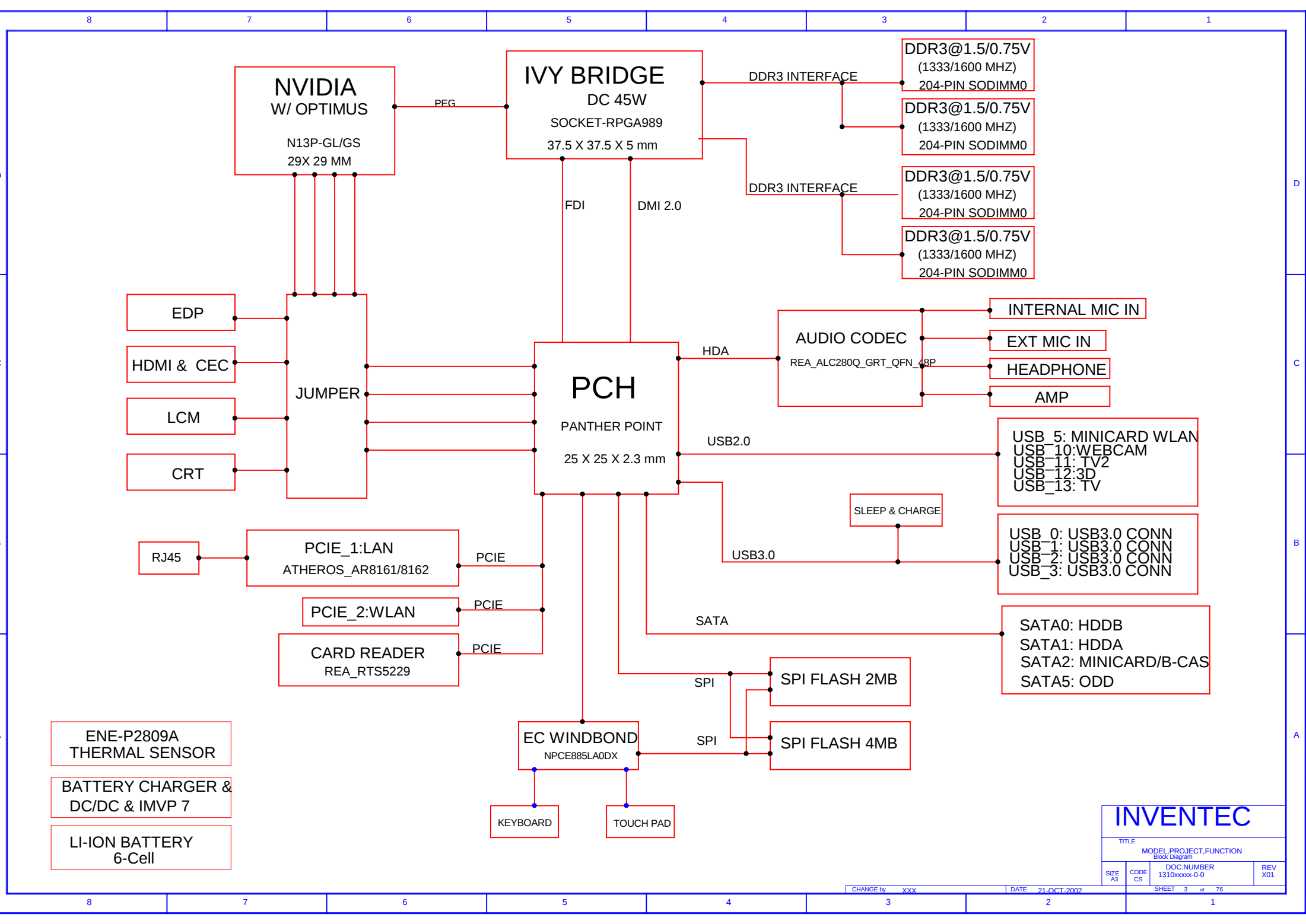
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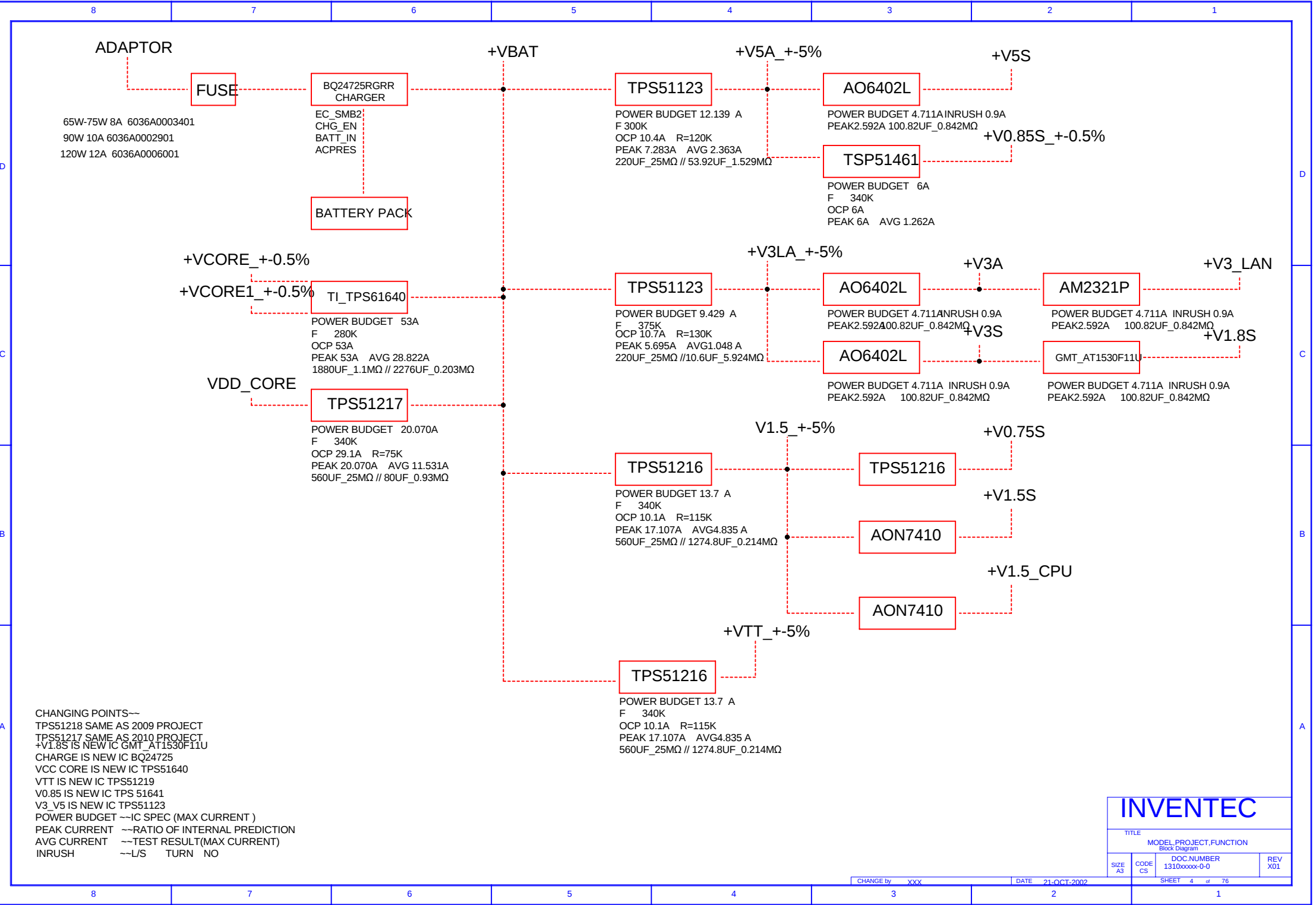
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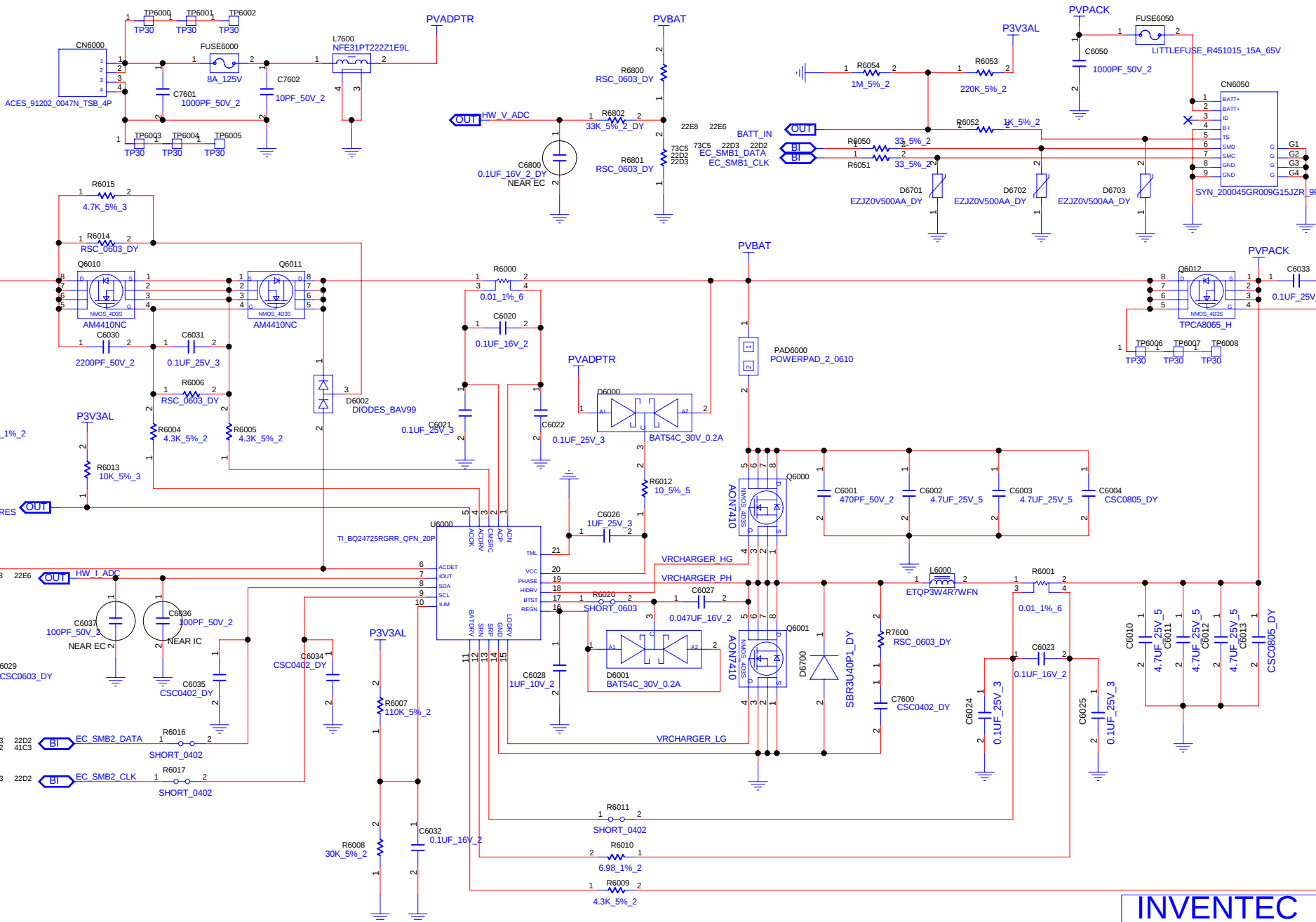
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MODEL,PROJECT,FUNCTION

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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CHANGE by: XXX | DATE: 21-OCT-2002 | SHEET: 2 of 76



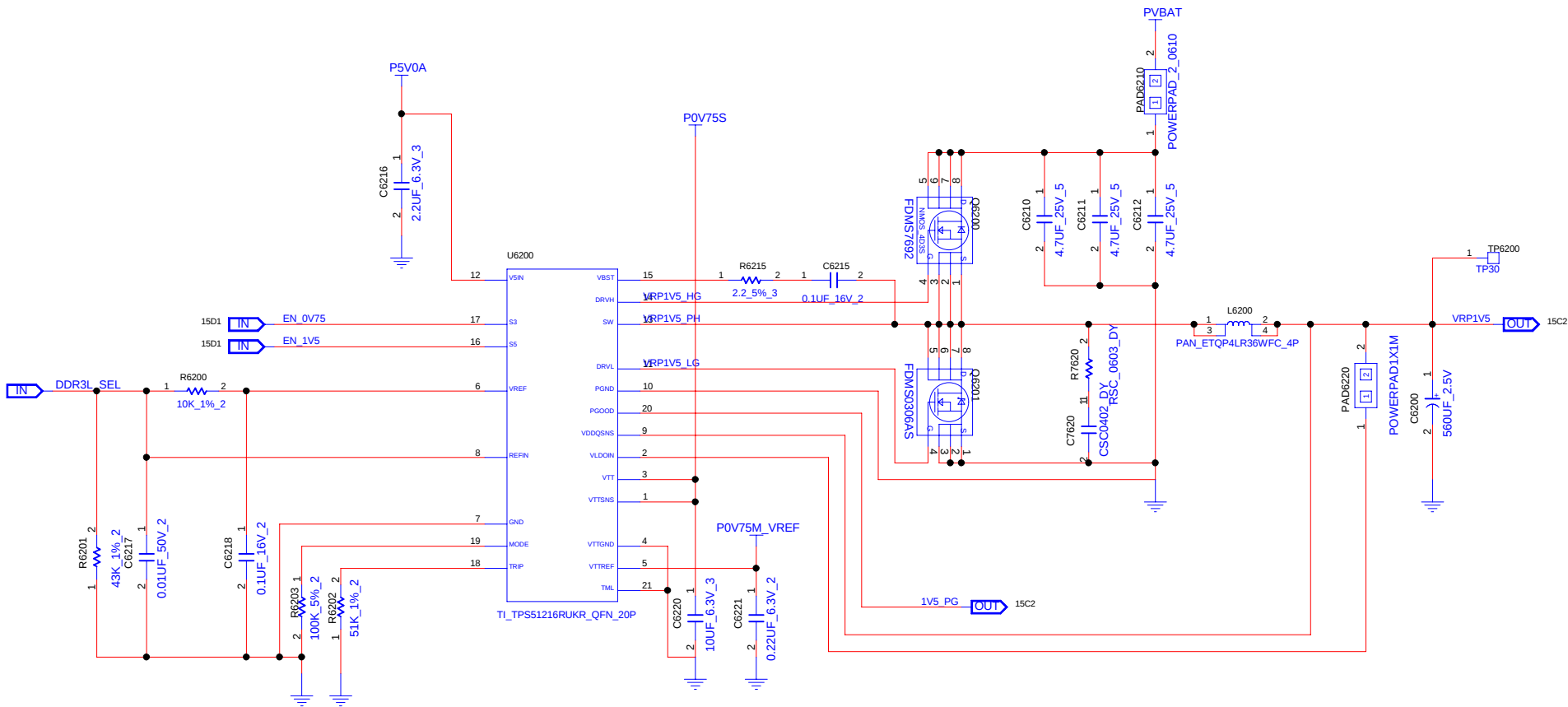




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CHANGE by	XXX	DATE	21-OCT-2002
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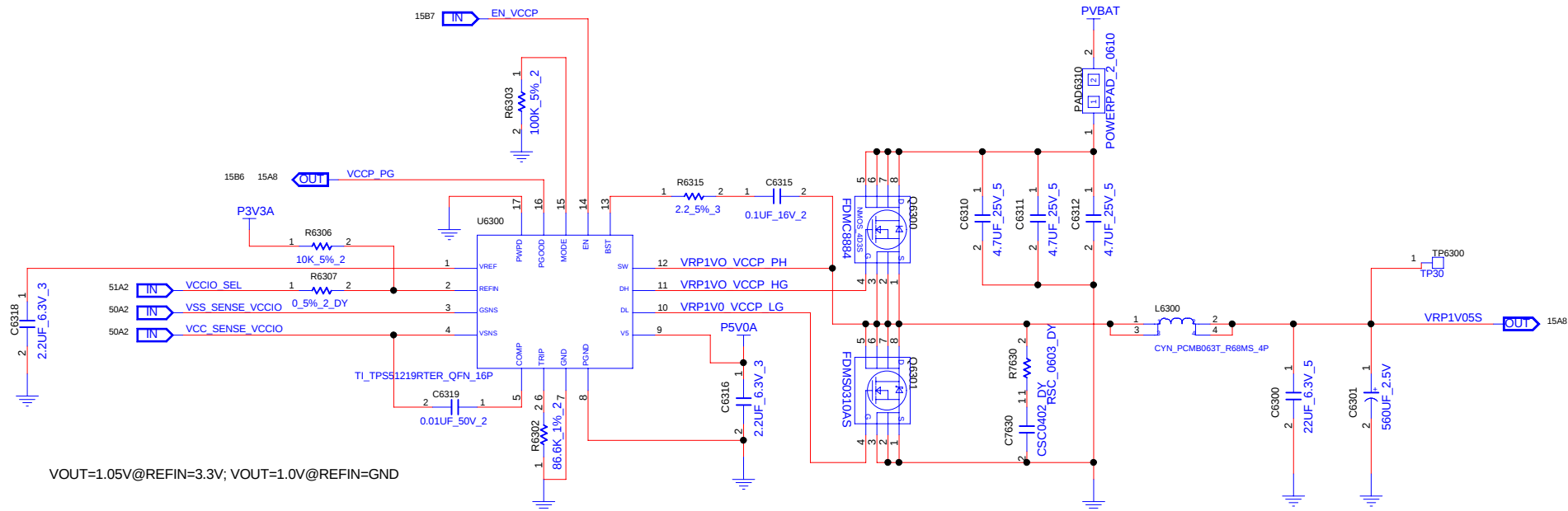


VOUT=REFIN=1.8*(R6201/(R6200+R6201))
MODE=100KOHM:TRACKING DISCHARGE

STATE	S3	S5	VREF	VDDQ	VTTREF	VTT
S0	HI	HI	ON	ON	ON	ON
S3	LO	HI	ON	ON	ON	OFF(High-Z)
S4/S5	LO	LO	OFF	OFF(Discharge)	OFF(Discharge)	OFF(Discharge)

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

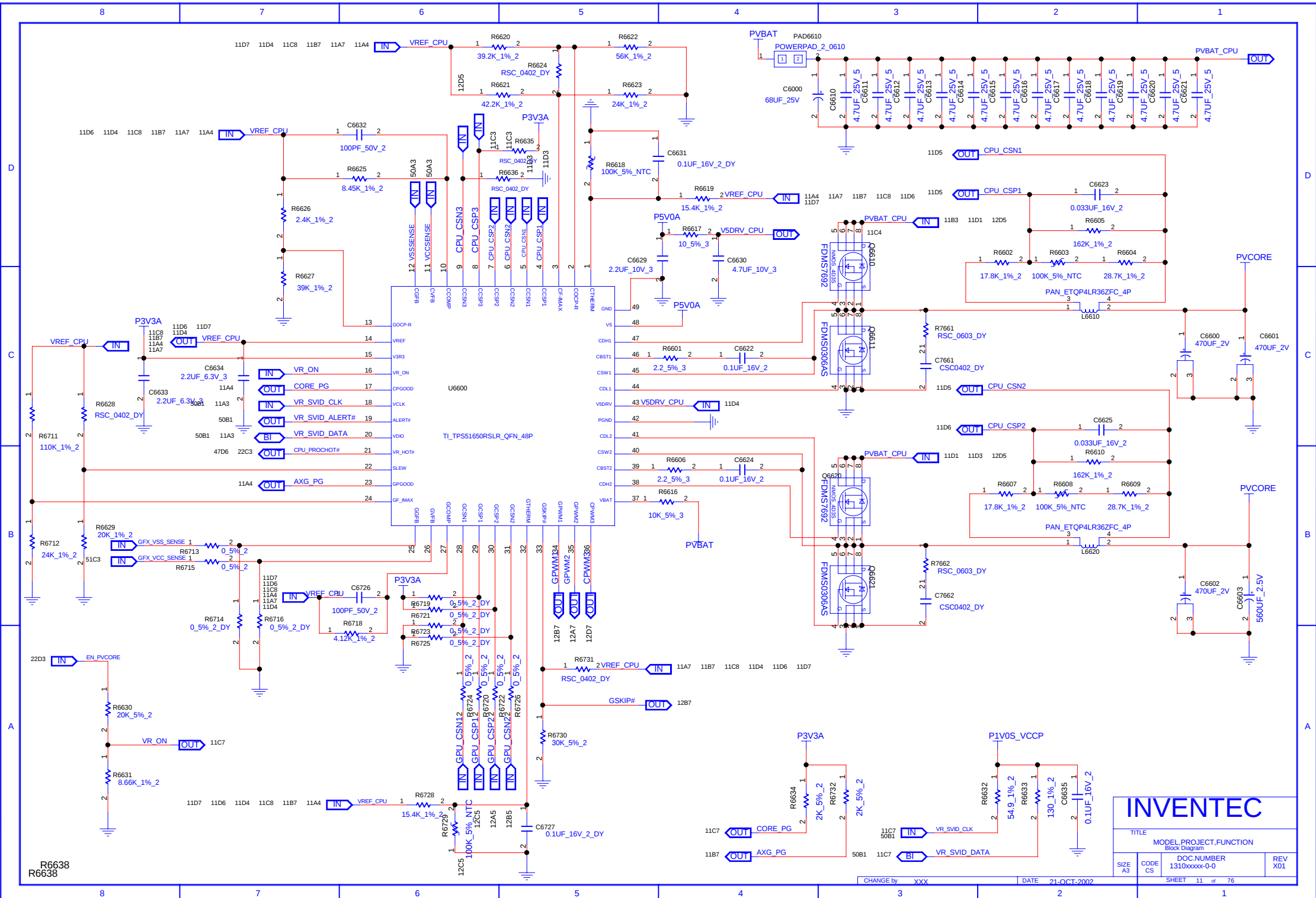


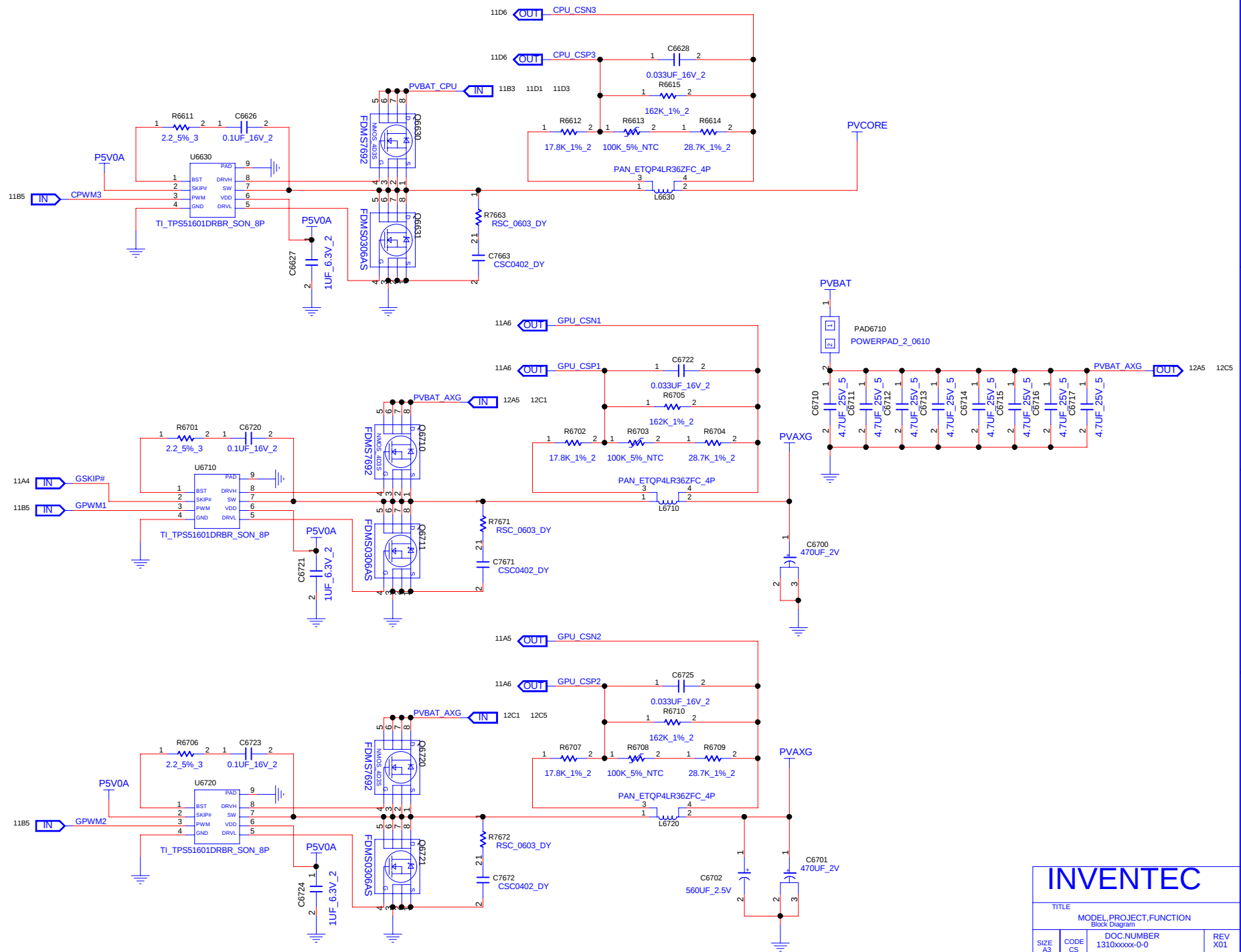
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		REV X01	

CHANGE by XXX DATE 21-OCT-2002

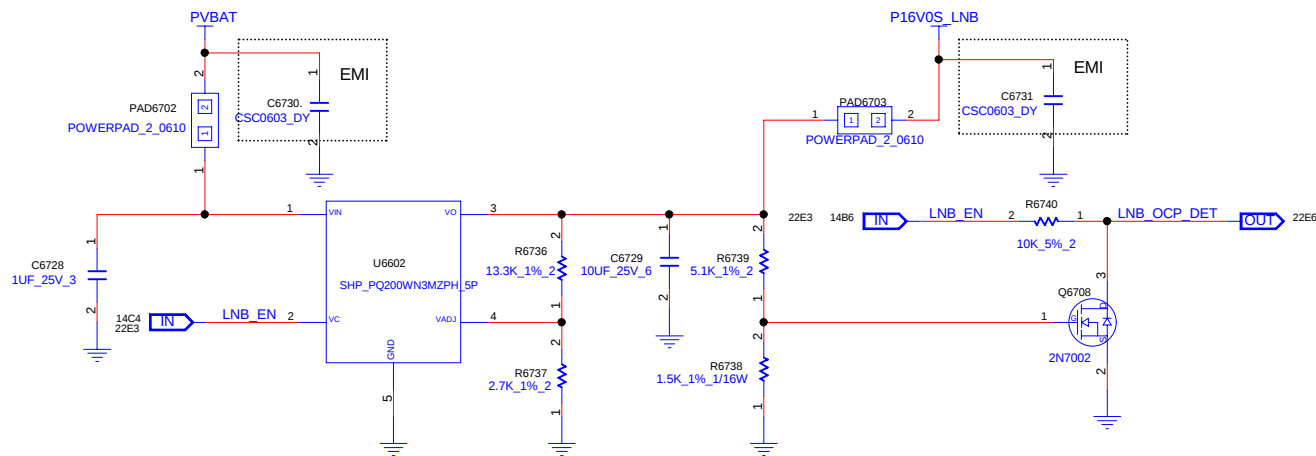
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MODEL PROJECT FUNCTION			
Block Diagram			
DOC NUMBER			
1310xxxx-0-0			
REV			
X01			
SIZE	CODE	DATE	
A3	CS	21-OCT-2002	
CHANGE by		XXX	
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LNB POWER



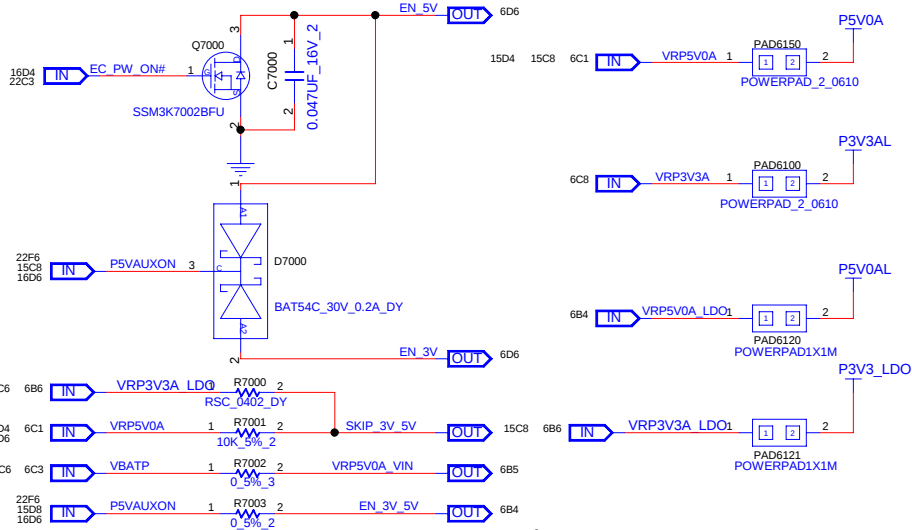
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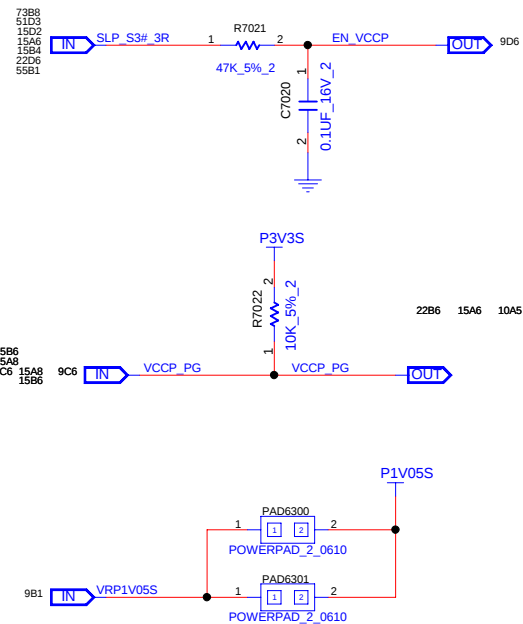
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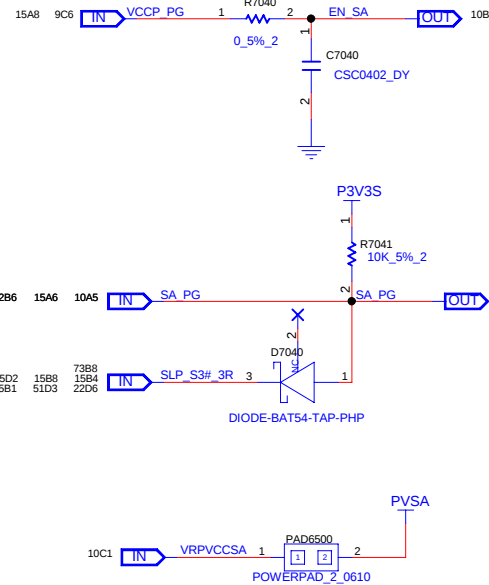
3V & 5V



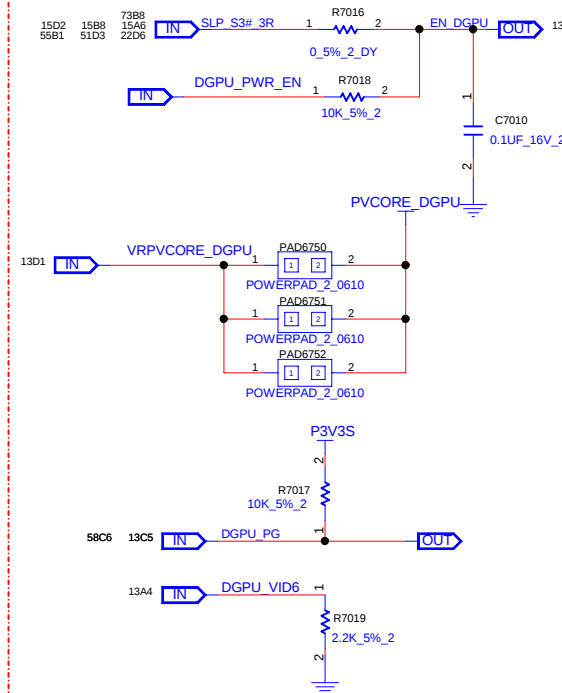
VCCIO



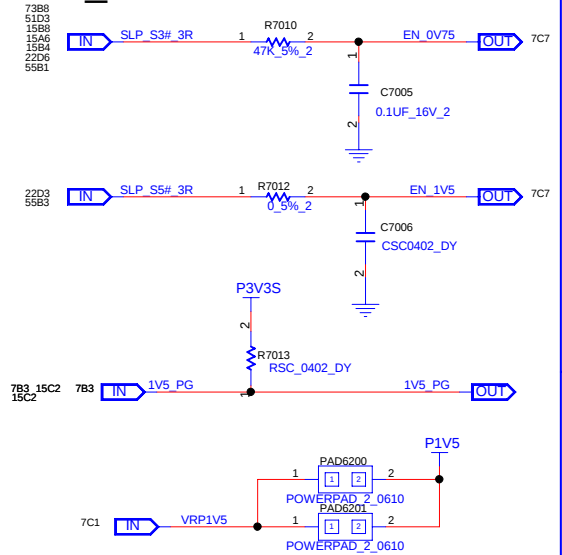
VCCSA



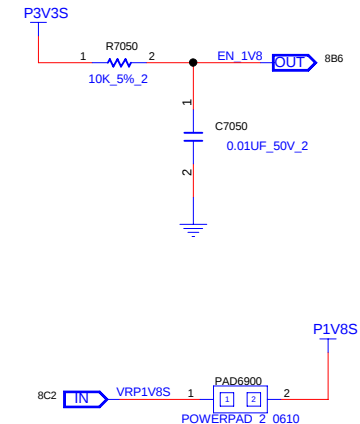
DGPU



DDR_P1V5

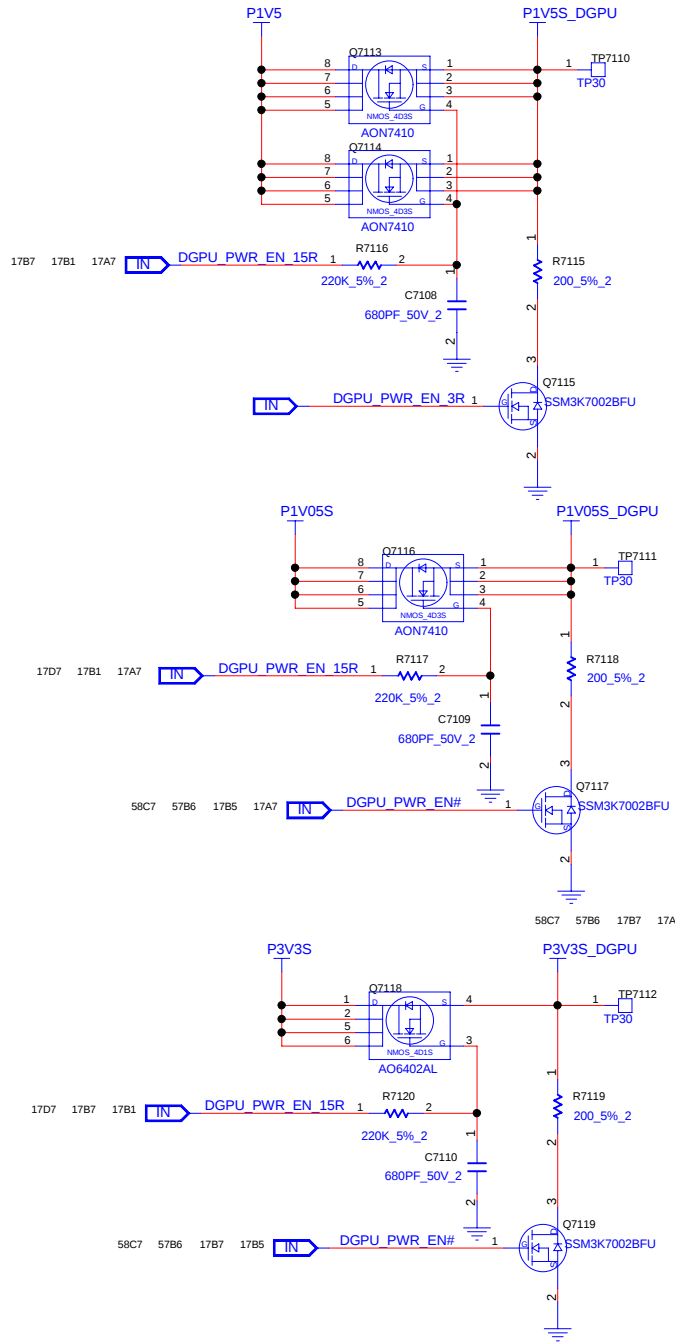


P1V8S



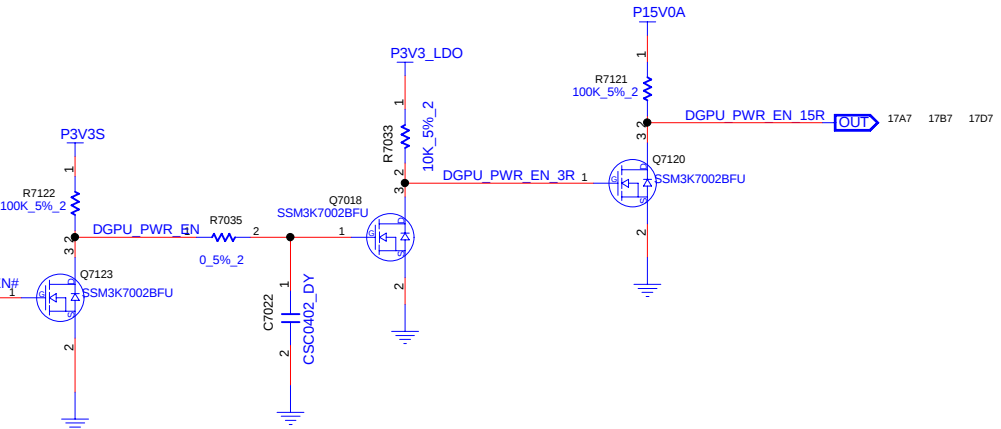
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TITLE			
MODEL PROJECT FUNCTION			
DOC NUMBER			
REV X01			



NVIDIA OPTIMUS

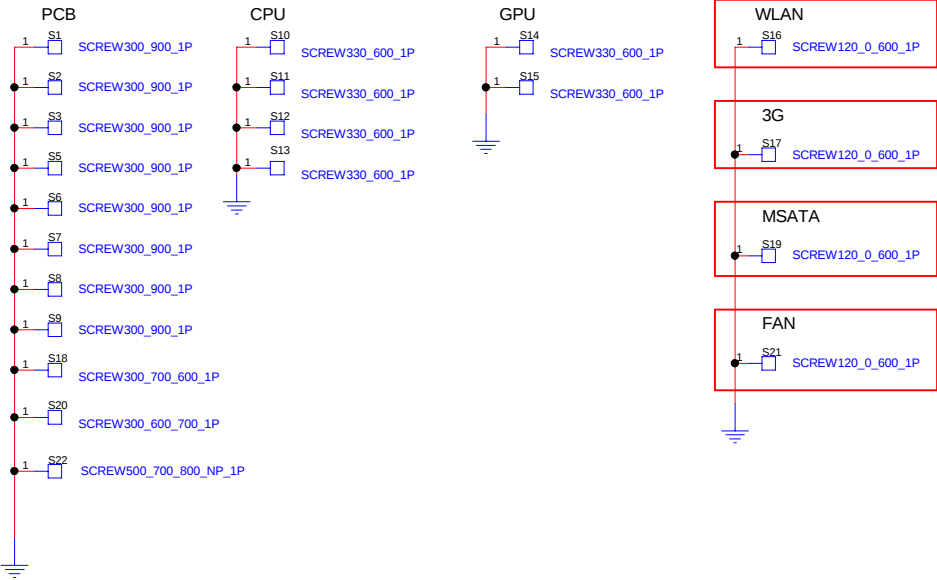
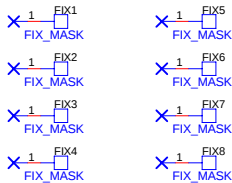
	DURING RESET	AFTER RESET	
DGPU_PWR_EN#	HIGH	HIGH	0 : DGPU POWER SWITCH TURNED ON 1 : POWER SWITCH TURNED OFF
DGPU_PG			0 : DGPU POWER IS NOT STABLE 1 : DGPU POWER IS STABLE
DGPU_HOLD_RST#	LOW	LOW	0 : KEEP DGPU IN RESET 1 : RESET IS RELEASED



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

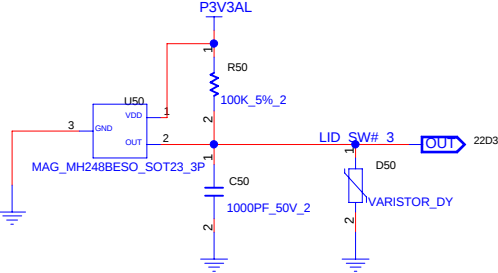
REFERENCE 0~49(PCB SCREW)



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
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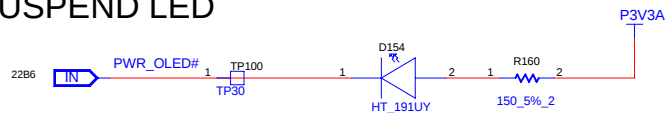
REFERENCE 50-99(HALL SENSOR)



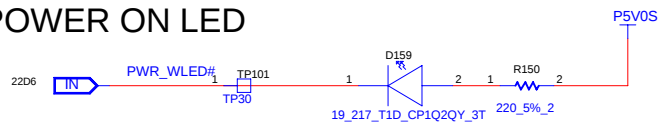
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TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01

SUSPEND LED

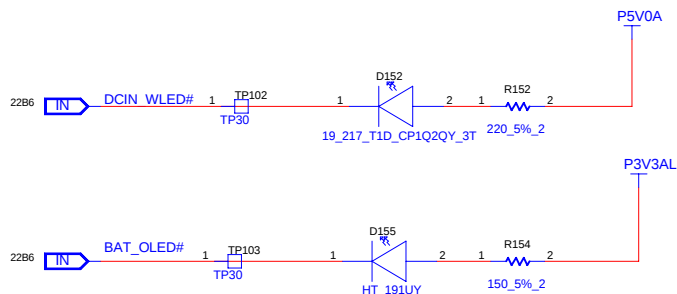


POWER ON LED

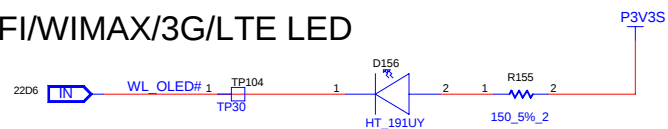


DC IN / BATTERY CHARGE LED

D152 BRIGHT:BOTH AC-ADAPTER IS PLUGGED IN AND BATTERY IS FULL CHARGED
D155 BRIGHT:WHILE CHARGING BATTERY FROM AC-ADAPTER
BLINK:LOW BATTERY



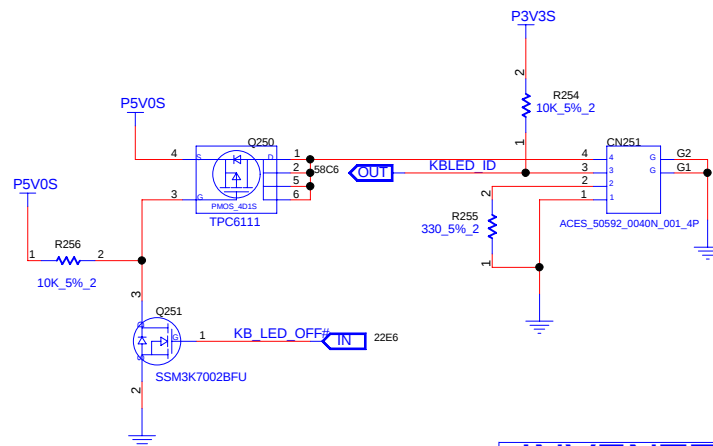
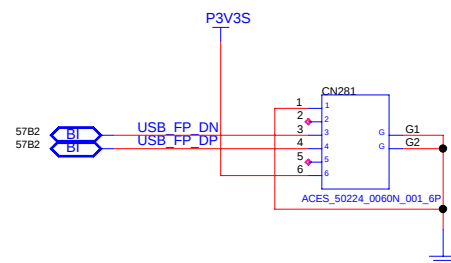
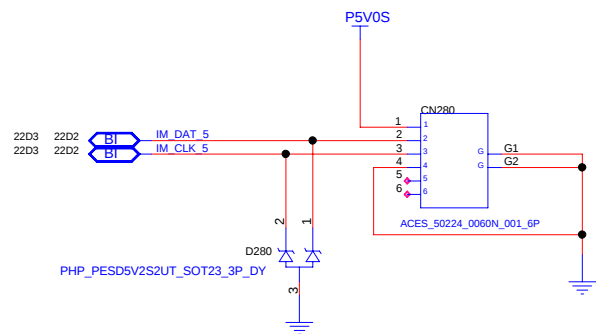
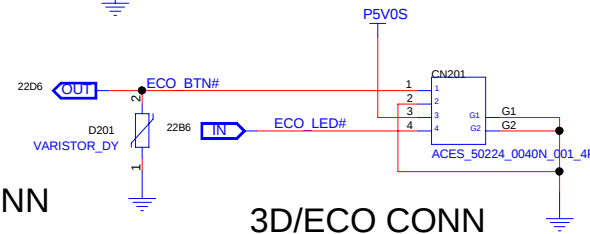
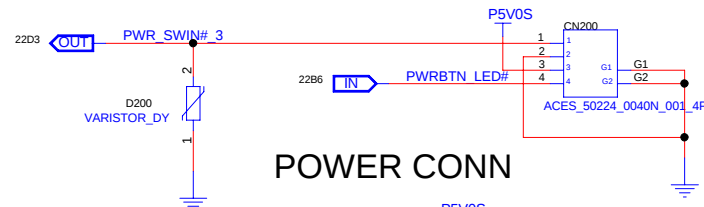
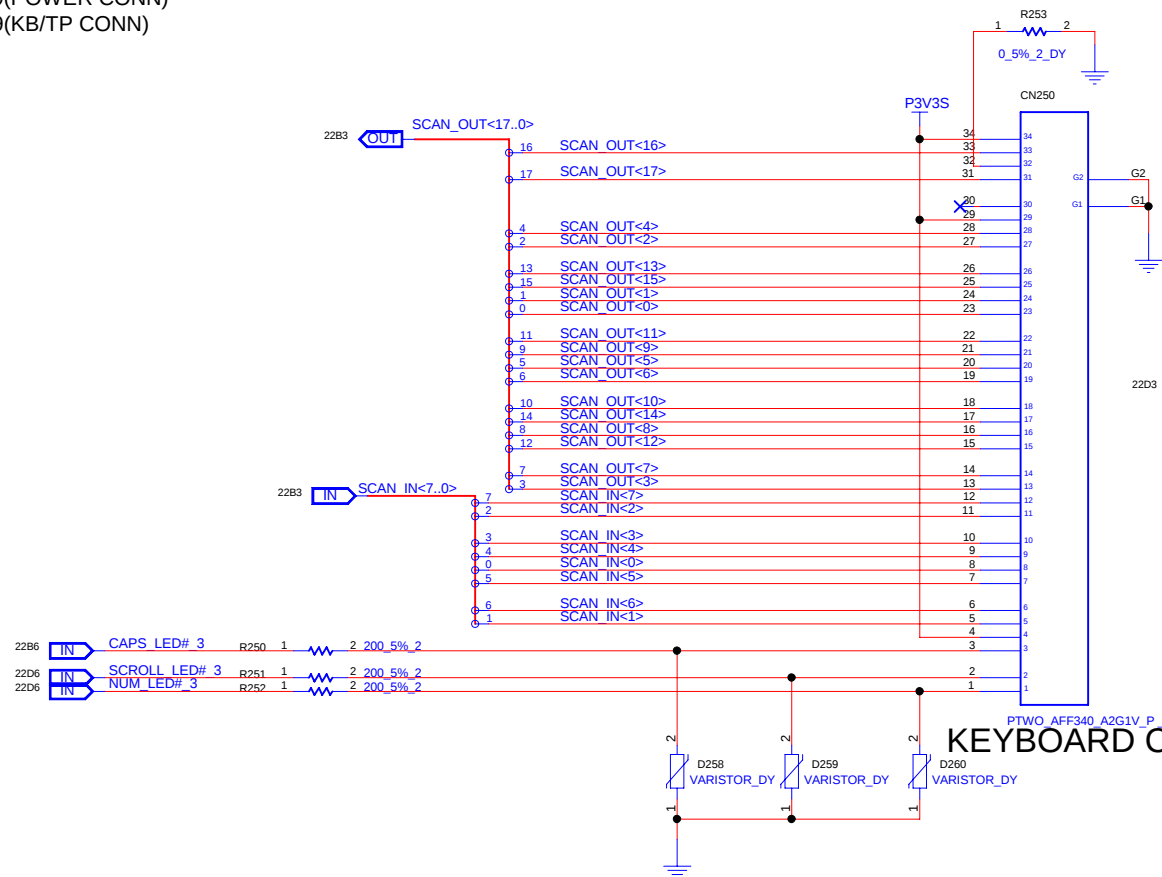
WIFI/WIMAX/3G/LTE LED



INVENTEC

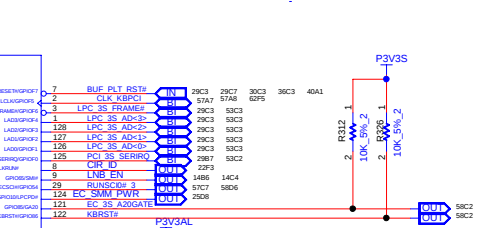
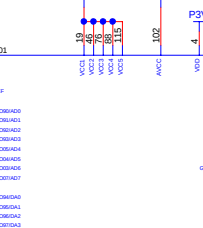
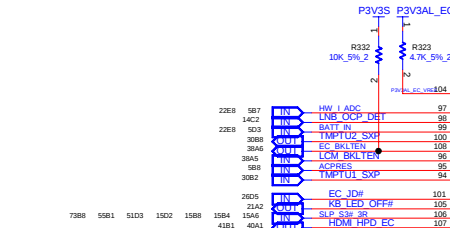
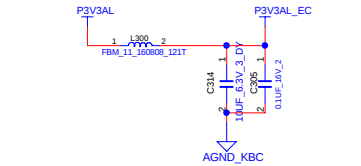
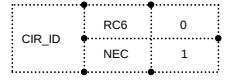
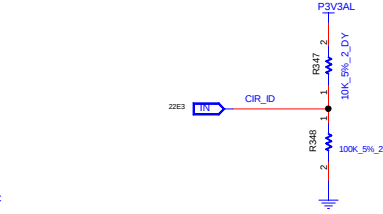
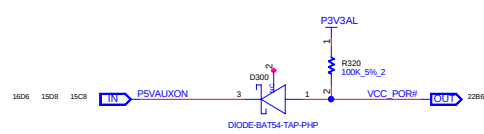
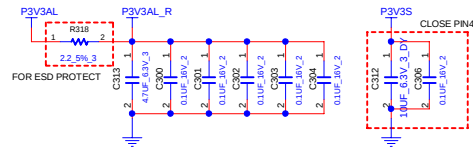
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SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01

REFERENCE 200~249(POWER CONN)
REFERENCE 250~299(KB/TP CONN)

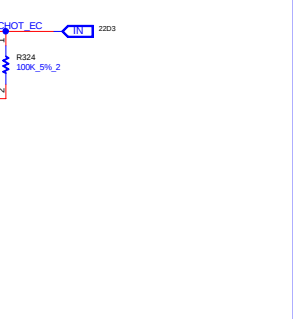
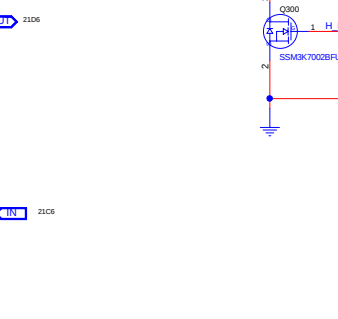
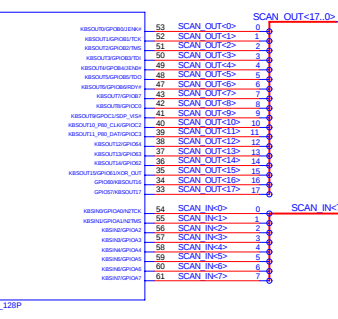
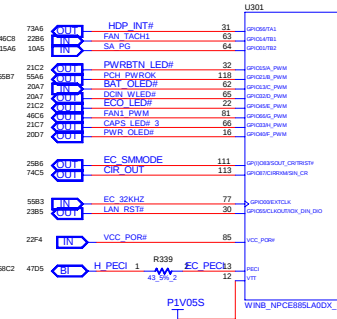
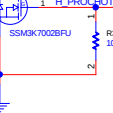
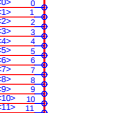
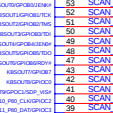
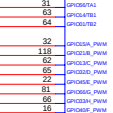
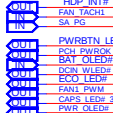
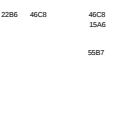
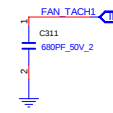
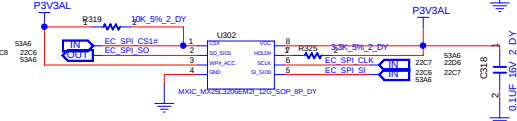
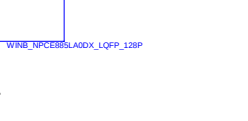
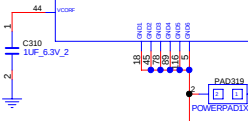
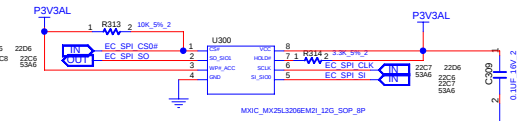
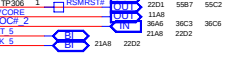
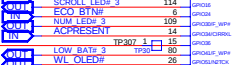
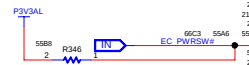


INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
DOC NUMBER			
1310xxxx-0-0			
REV			
X01			

REFERENCE 300-389(KBC)



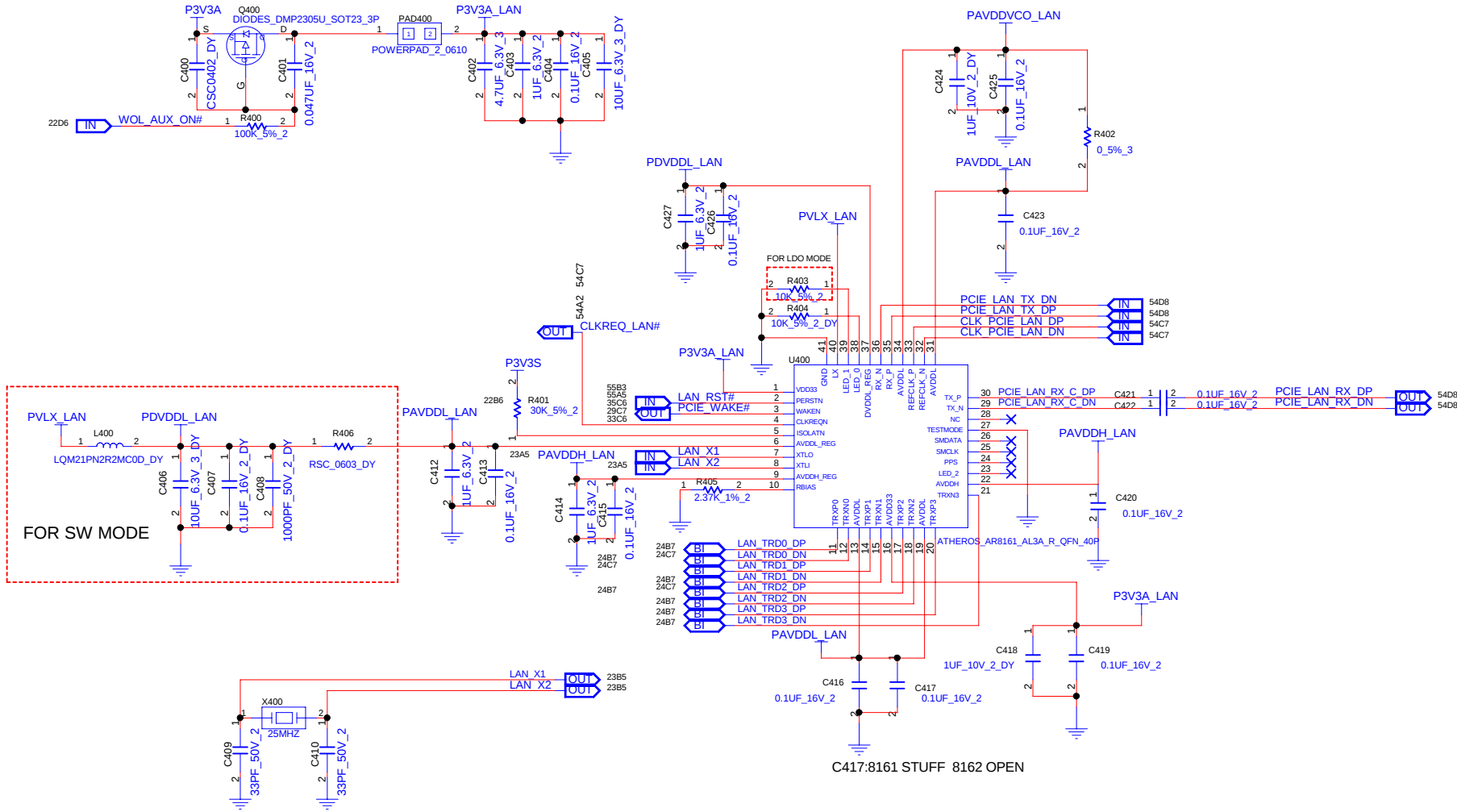
EC_SMB1	EC_SMB2	EC_SMB3
1.BATTERY	1.CHARGE	
2.3D SENSOR	2.GPU THERMAL	
3.CEC		



INVENTEC

TITLE	MODEL/PROJECT/FUNCTION	DOC NUMBER	REV
SIZE	CODE	131000000-03	X01
C	CS	SHEET	22

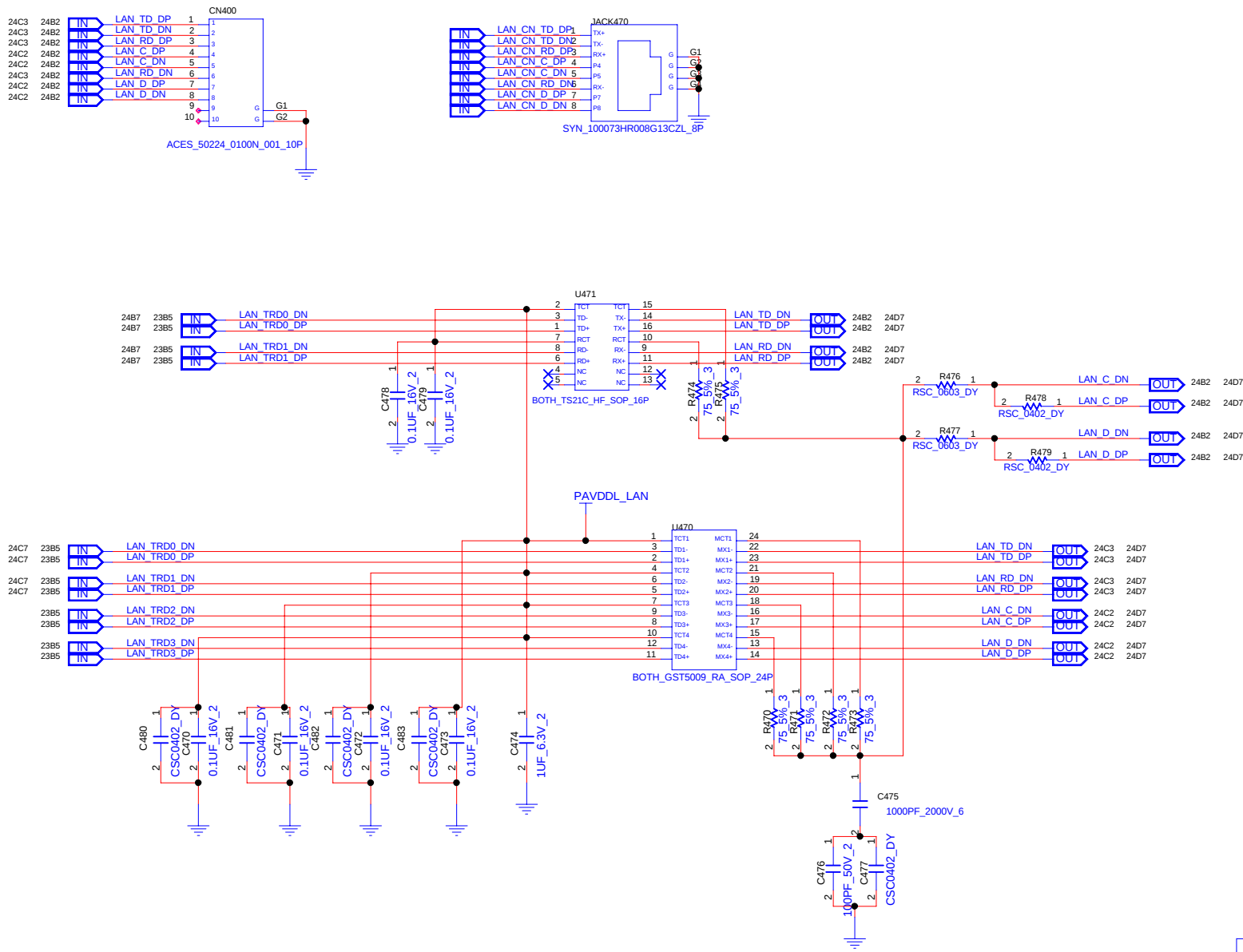
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Block Diagram			
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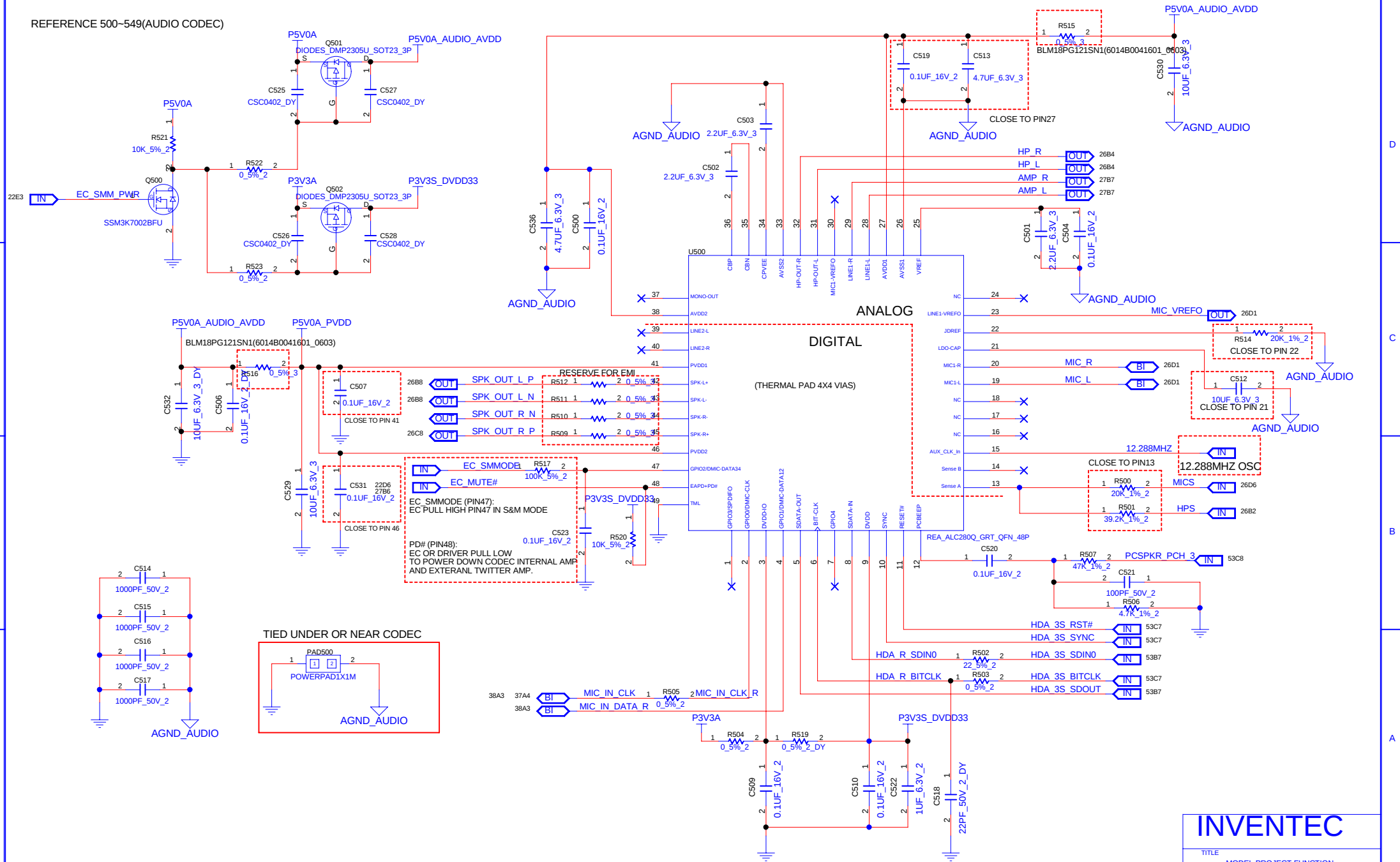
REFERENCE 400~499(LAN)



INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

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REFERENCE 500-549(AUDIO CODEC)

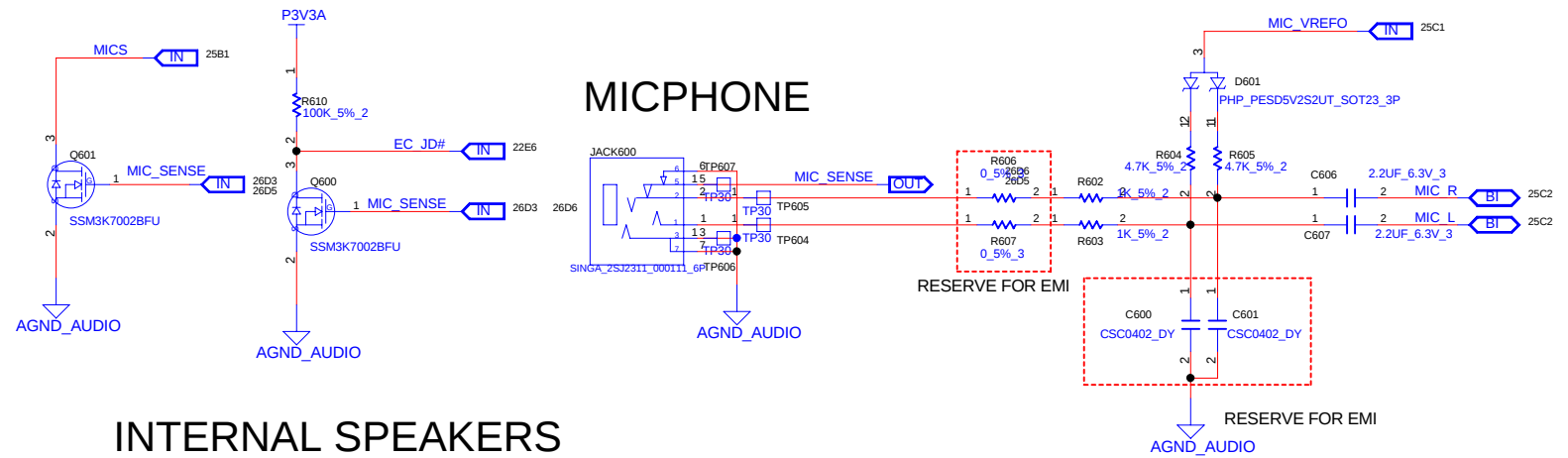


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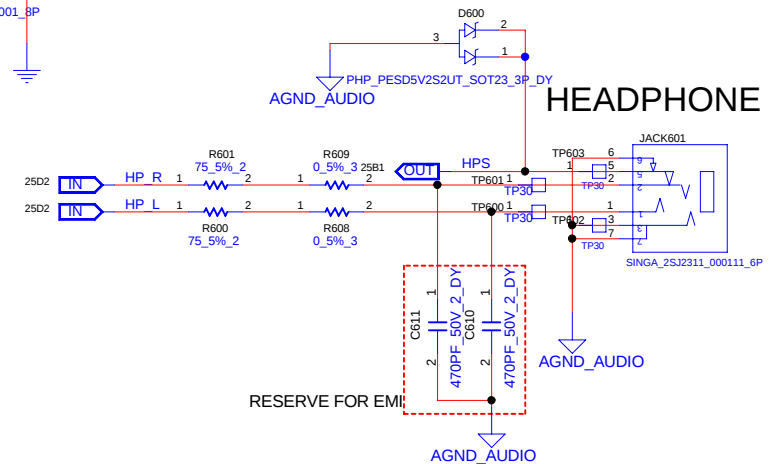
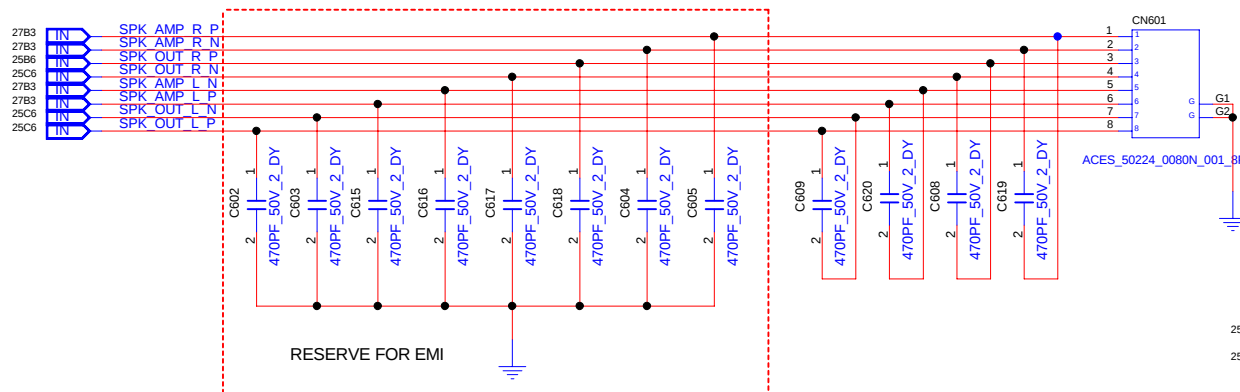
REFERCE 600-649(JACK/MIC/SPEAKER)

AUDIO JACKS



INTERNAL SPEAKERS

NOTE:SPK TRACE SHOULD 30~40 MILS WIDTH



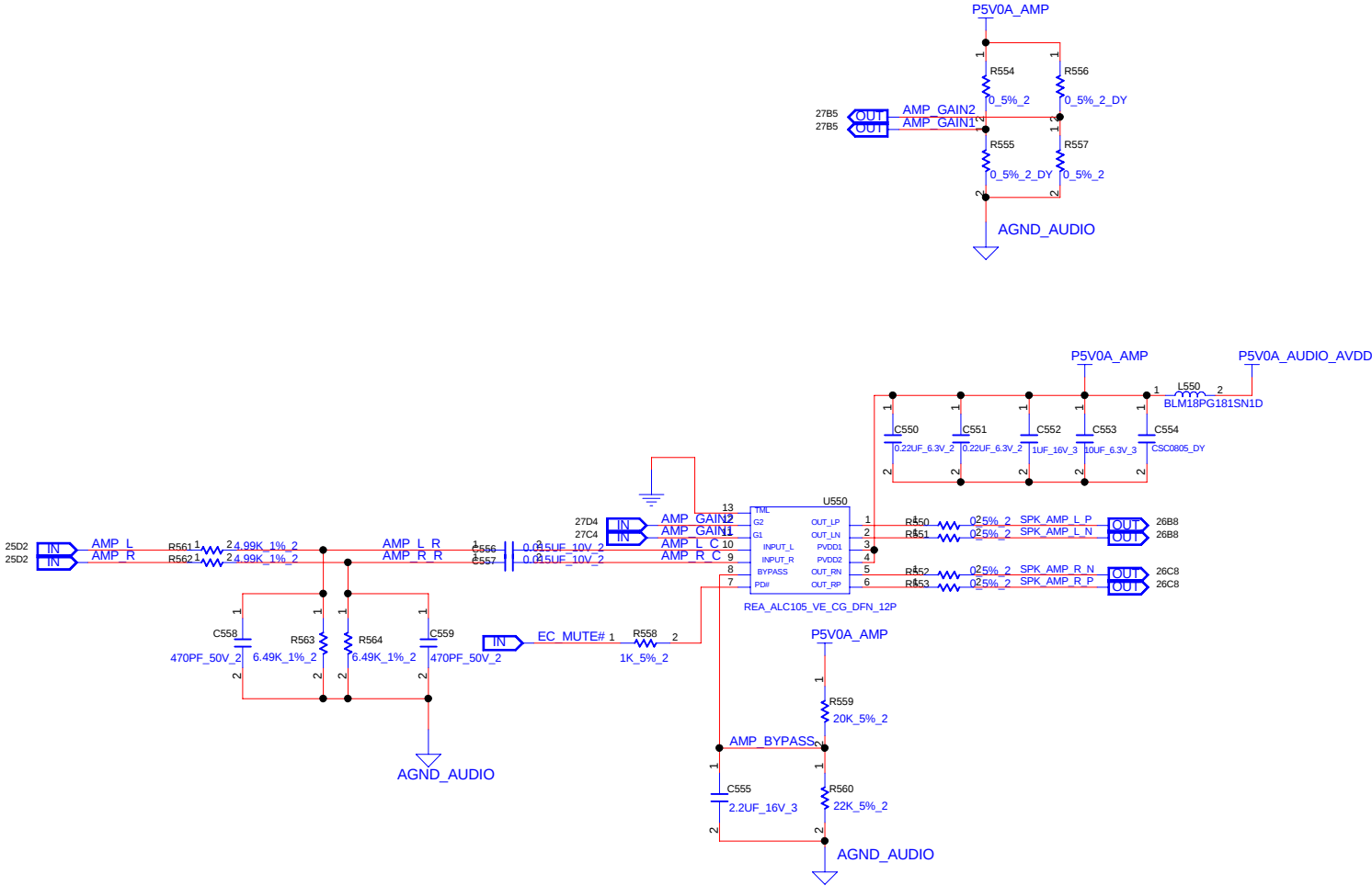
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Block Diagram
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CHANGE by XXX DATE 21-OCT-2002

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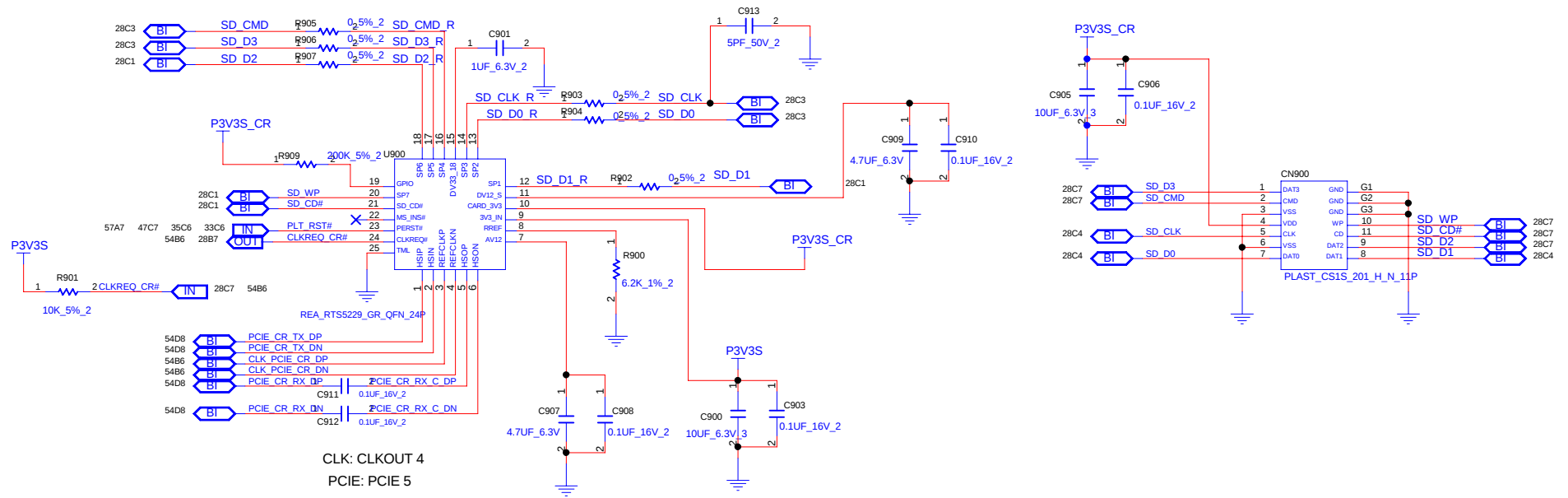
GAIN SETTING		
G2	G1	AMP
0	0	11DB
0	1	14DB
1	0	19DB
1	1	25DB



INVENTEC

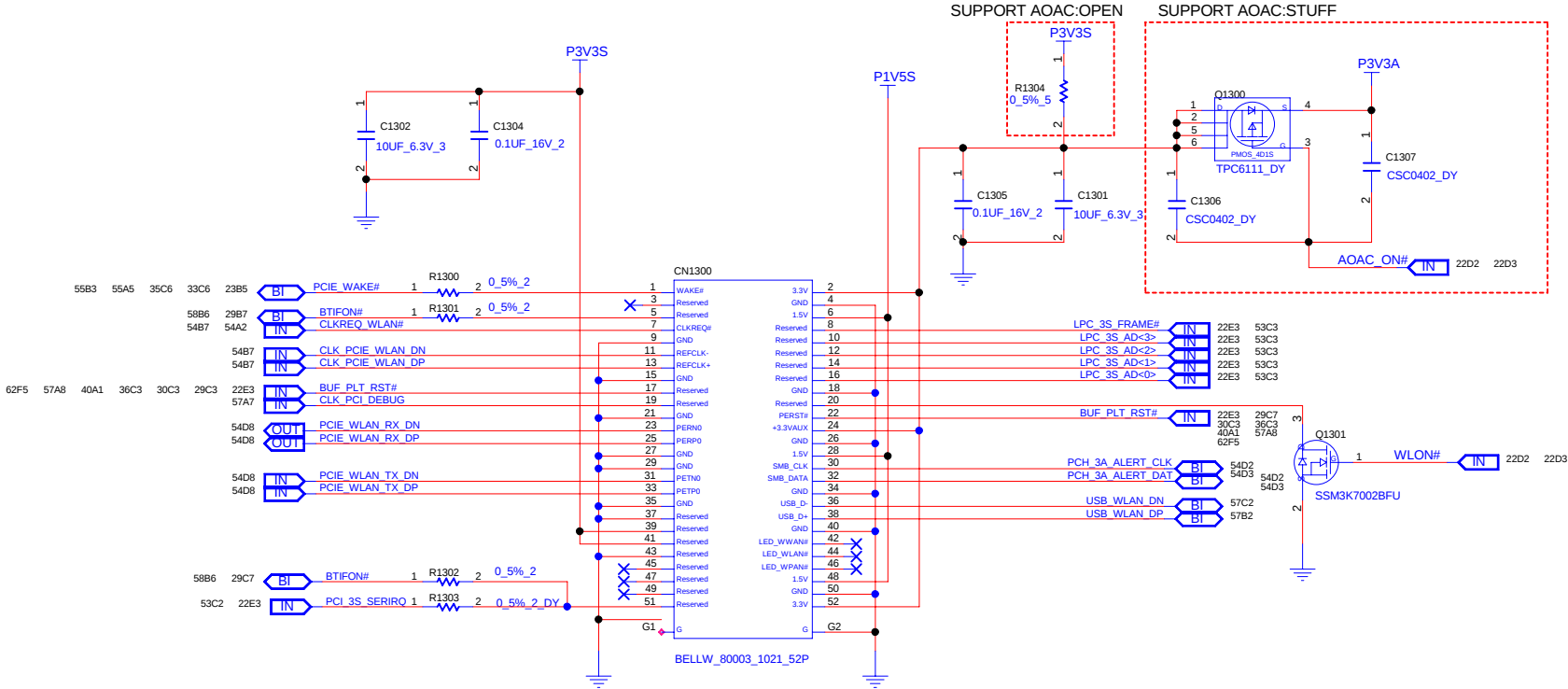
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SIZE A3	CODE CS	REV X01

REFERNCE 900~999(CARDREADER)



INVENTEC

TITLE			
MODEL.PROJECT.FUNCTION Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

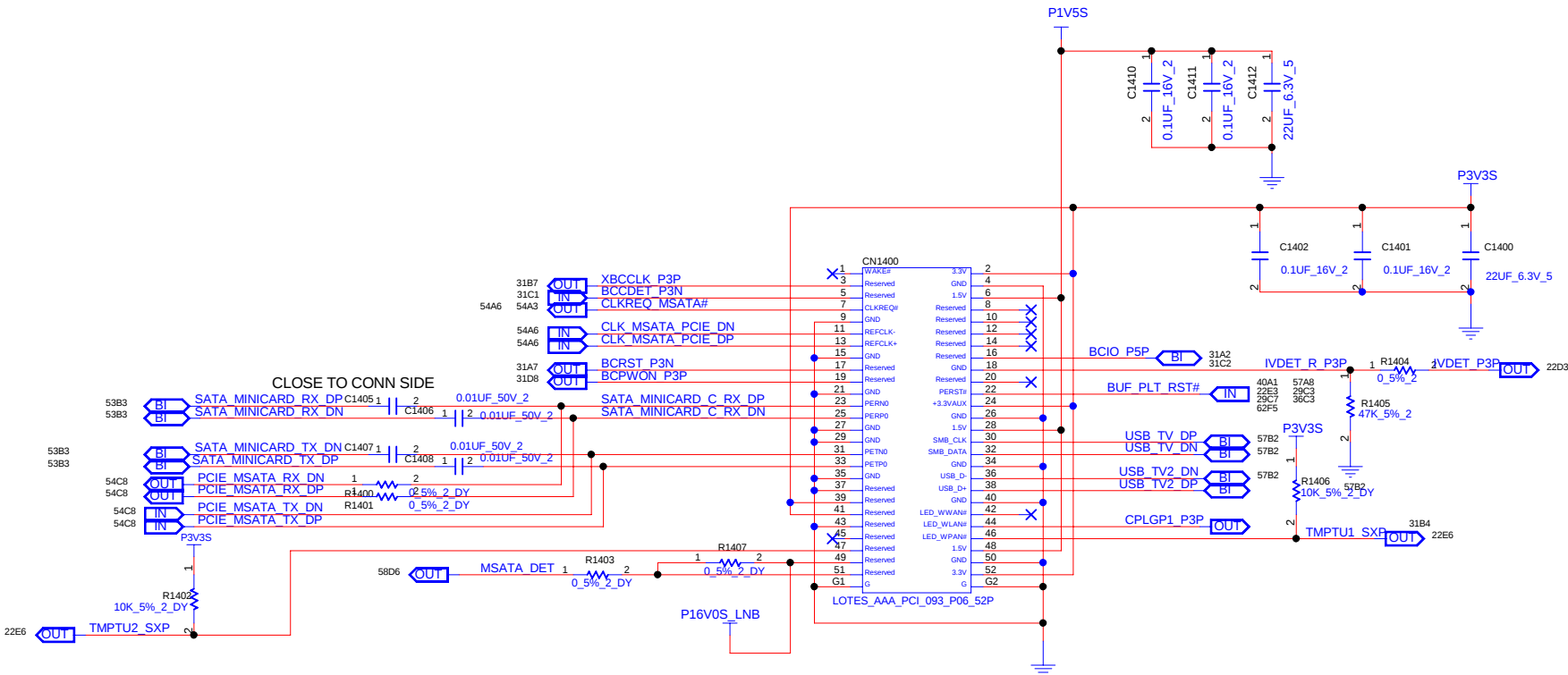


MINI CARD 1(WLAN)

INVENTEC

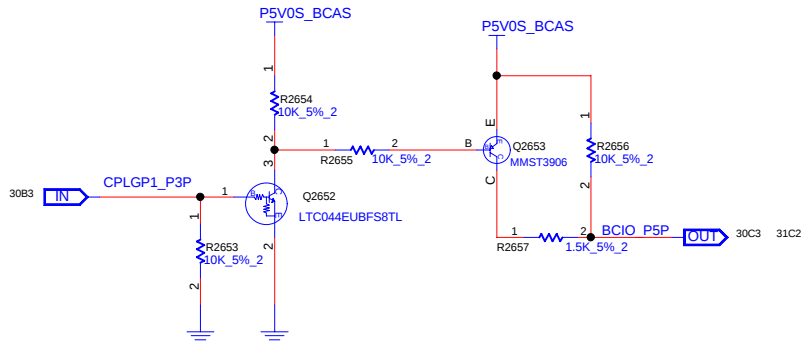
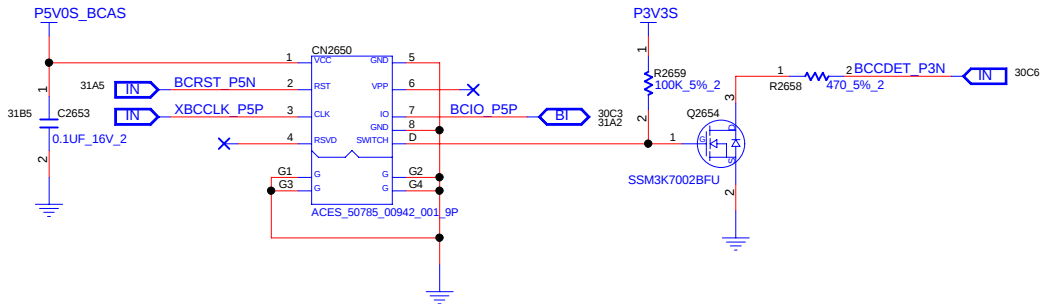
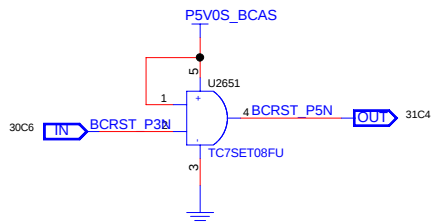
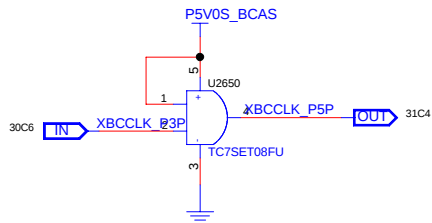
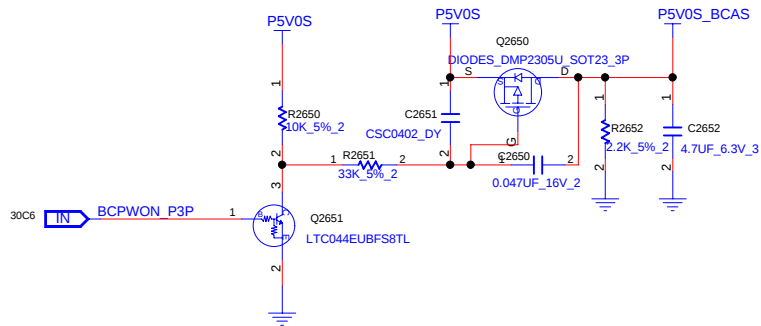
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

REFERENCE 1400~1499(3G)



INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

REFERENCE 2650~2999(RESERVE)



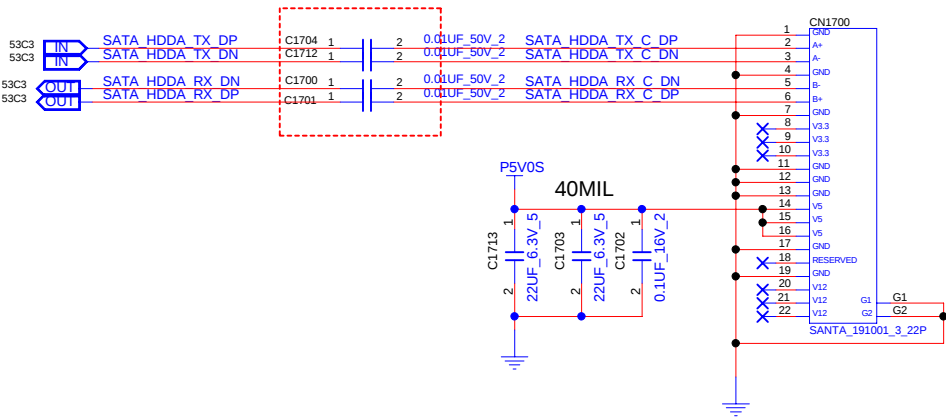
INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE		REV	
A3	CS	1310xxxxx-0-0	X01
CHANGE by XXXX			
DATE 21-OCT-2002			
SHEET 31 of 76			

REFERENCE 1700~1749(HDD)
REFERENCE 1750~1799(ODD)

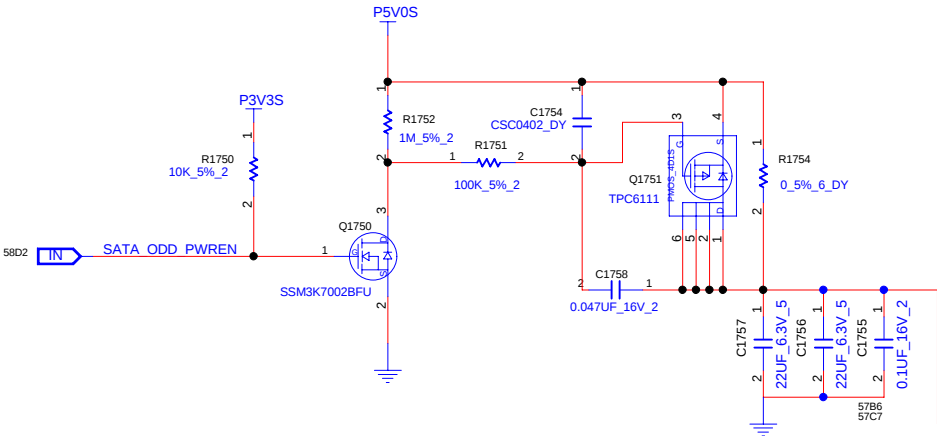
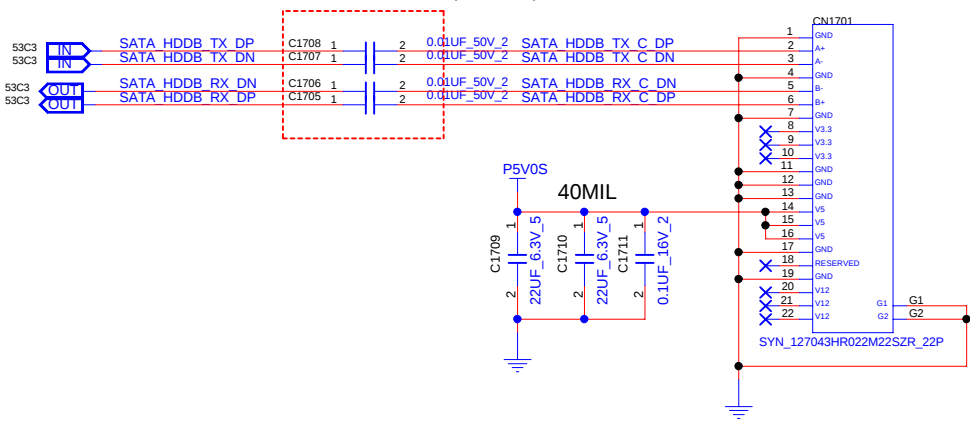
SATA HDD A

SATA HDD B

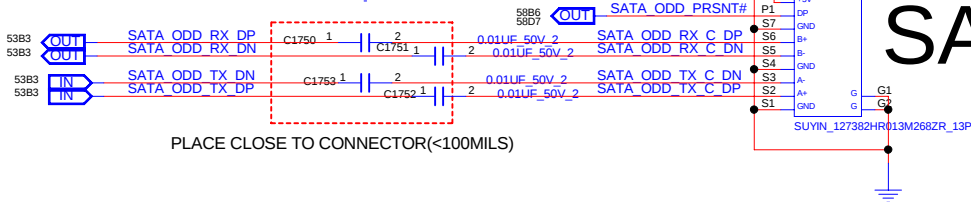
PLACE CLOSE TO CONNECTOR(<100MILS)



PLACE CLOSE TO CONNECTOR(<100MILS)



PLACE CLOSE TO CONNECTOR(<100MILS)



SATA ODD

INVENTEC

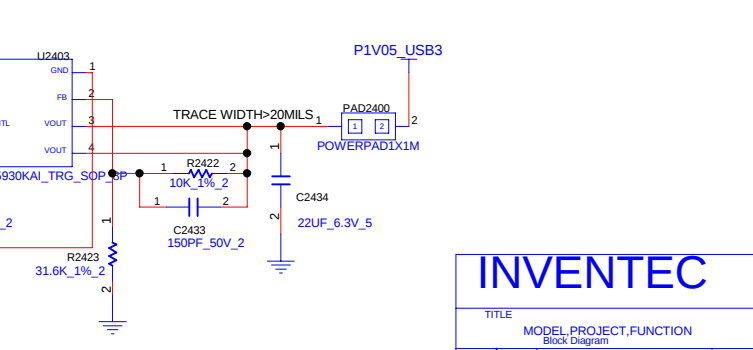
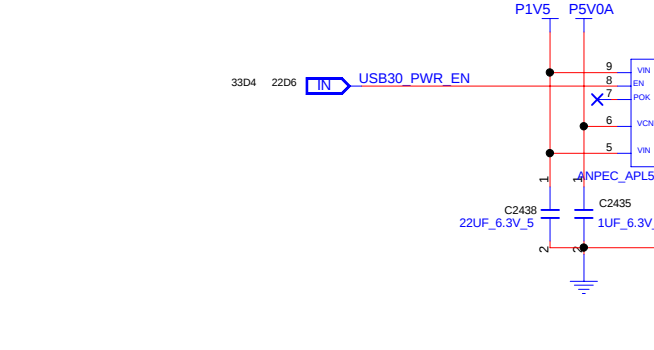
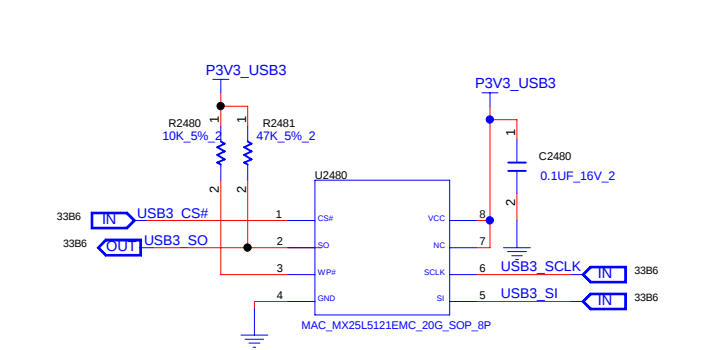
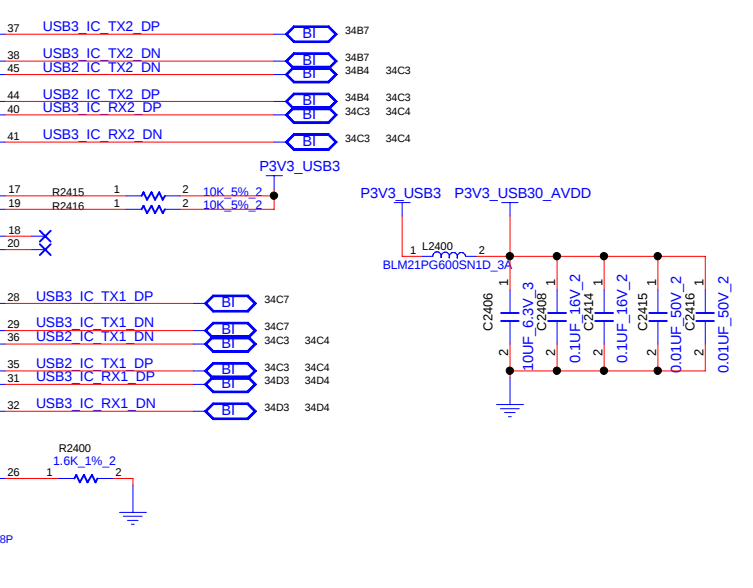
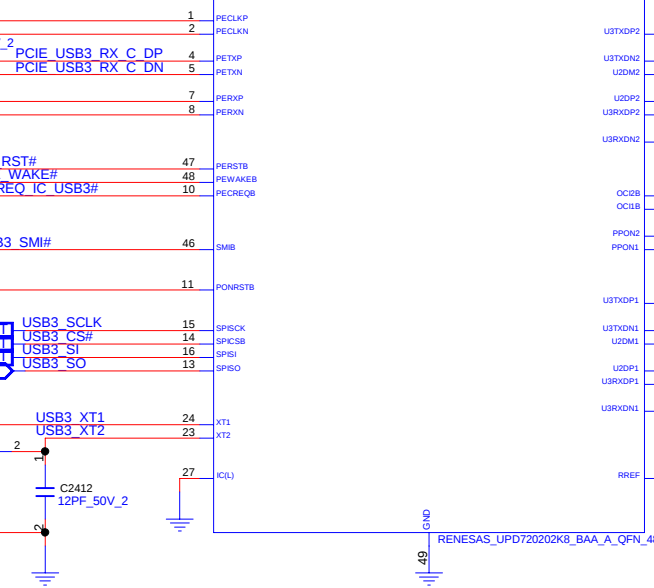
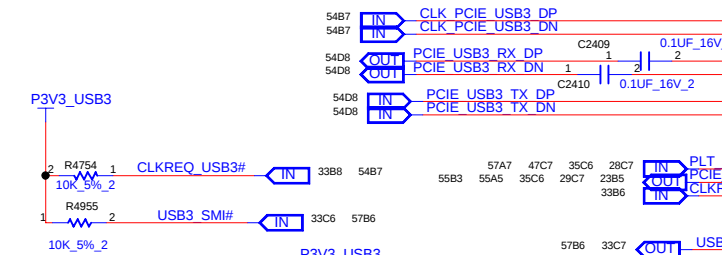
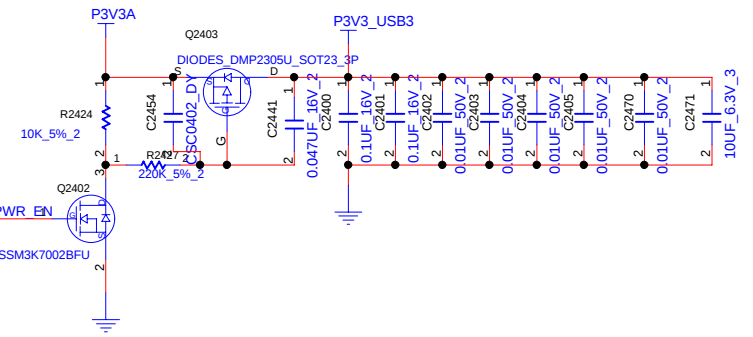
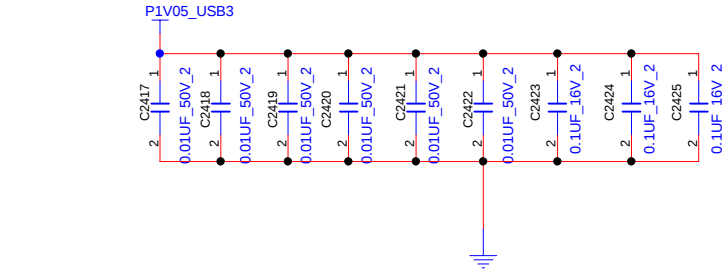
TITLE
MODEL PROJECT, FUNCTION
Block Diagram

SIZE A3 CODE CS DOC NUMBER 1310xxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002

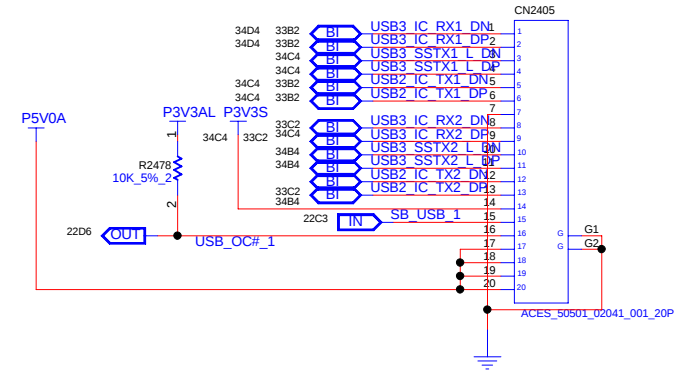
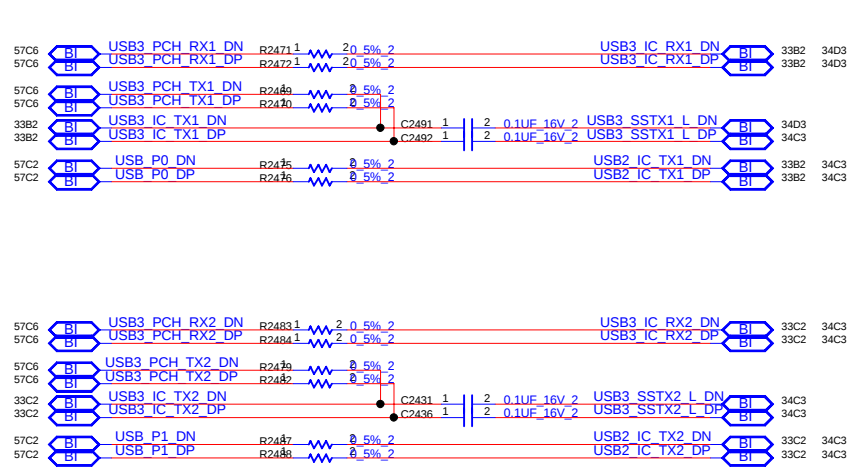
SHEET 32 of 76

REFERENCE 2000~2099(USB)



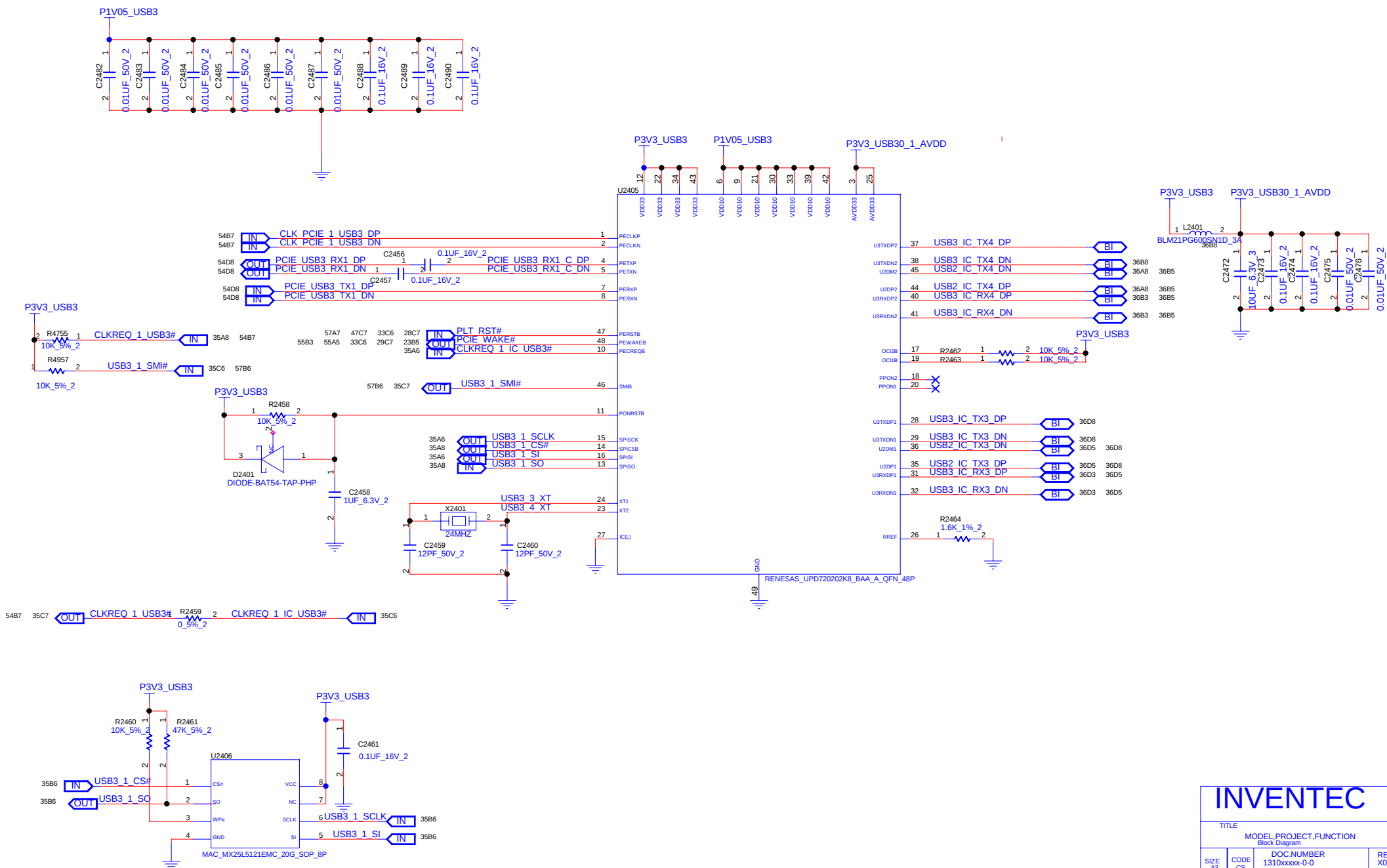
INVENTEC				
TITLE		MODEL,PROJECT,FUNCTION		
BLOCK DIAGRAM		DOC. NUMBER		
SIZE A3		CODE CS		REV X01
1310xxxxx-0-0		SHEET 33 of 76		

REFERENCE 2400~2499(USB3.0)



USB3.0

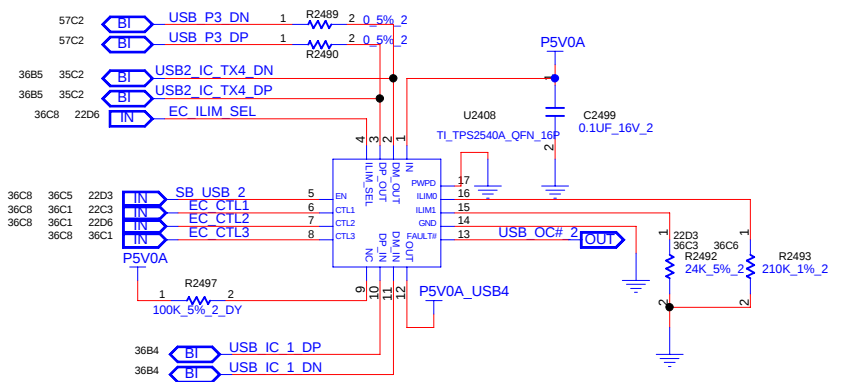
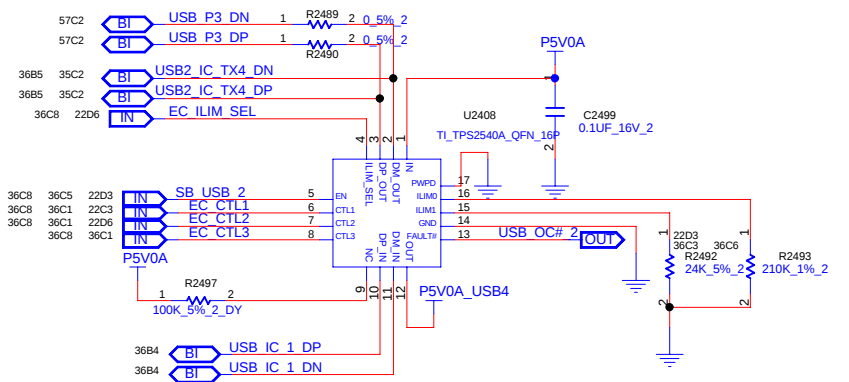
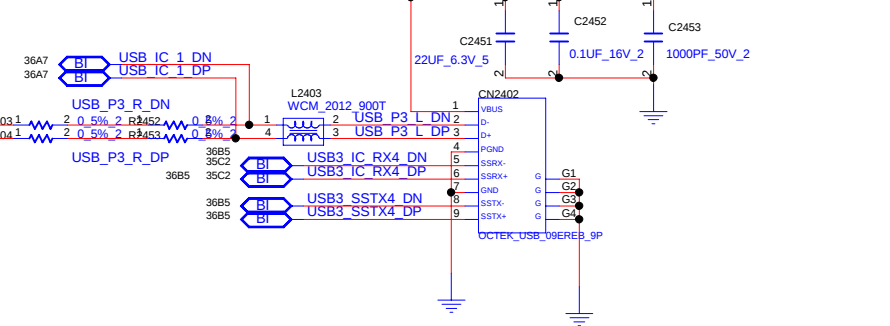
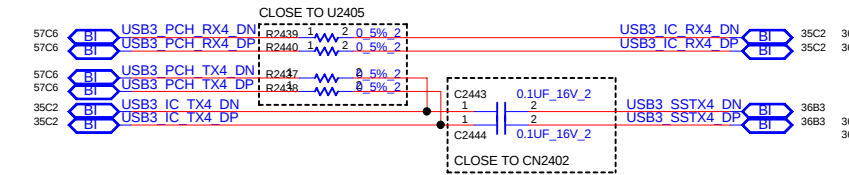
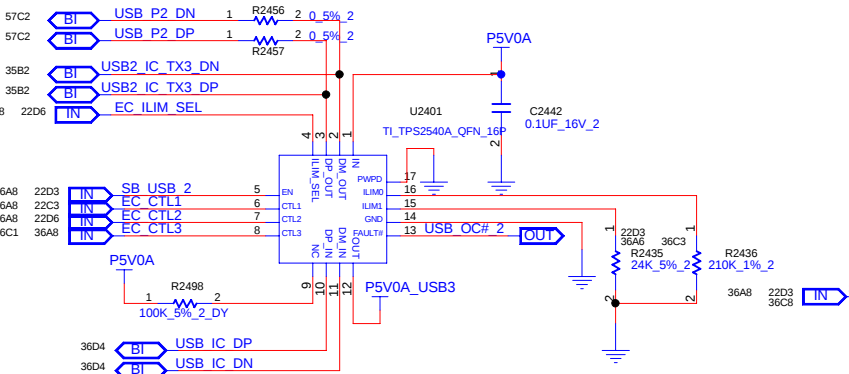
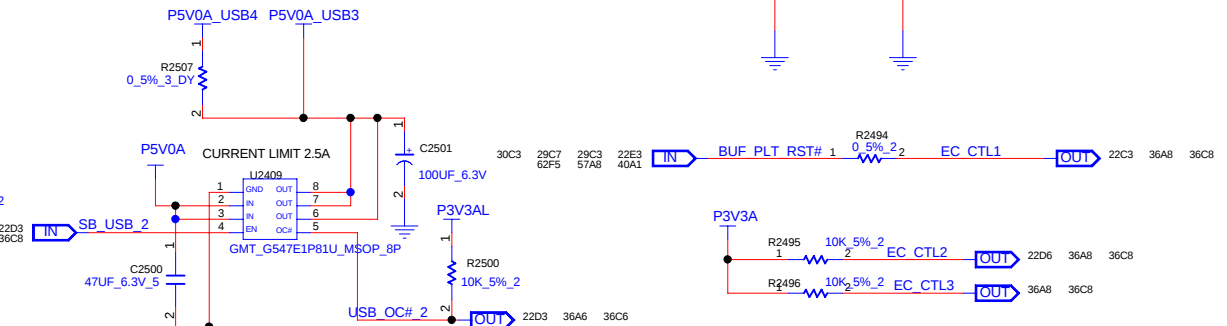
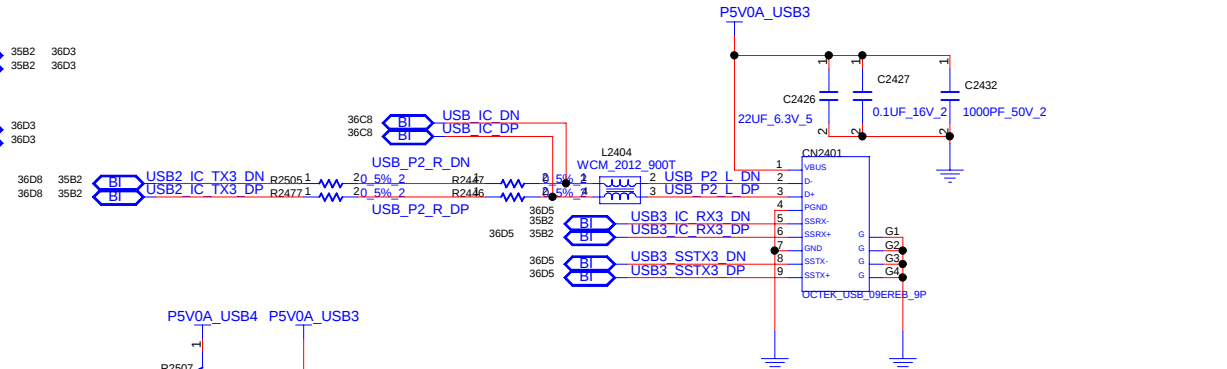
INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

Diagram illustrating the USB3 IC RX3 and TX3 signal paths. The RX3 path (pins 57C6, 57C6, 57C6, 35B2, 35B2) connects to USB3 IC RX3 DN and DP pins (35B2, 36D3). The TX3 path (pins 57C6, 57C6, 35B2, 35B2) connects to USB3 IC TX3 DN and DP pins (36D3, 36D3). The RX3 path includes a 20 ohm resistor (R2432, R2433) and a 0.1uF capacitor (C2447, C2448) connected to ground. The TX3 path includes a 20 ohm resistor (R2430, R2431) and a 0.1uF capacitor (C2447, C2448) connected to ground. The diagram is labeled "CLOSE TO U2405" and "CLOSE TO CN2401".



TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC. NUMBER 1310xxxx-0-0	FIG. NO. X

D

C

B

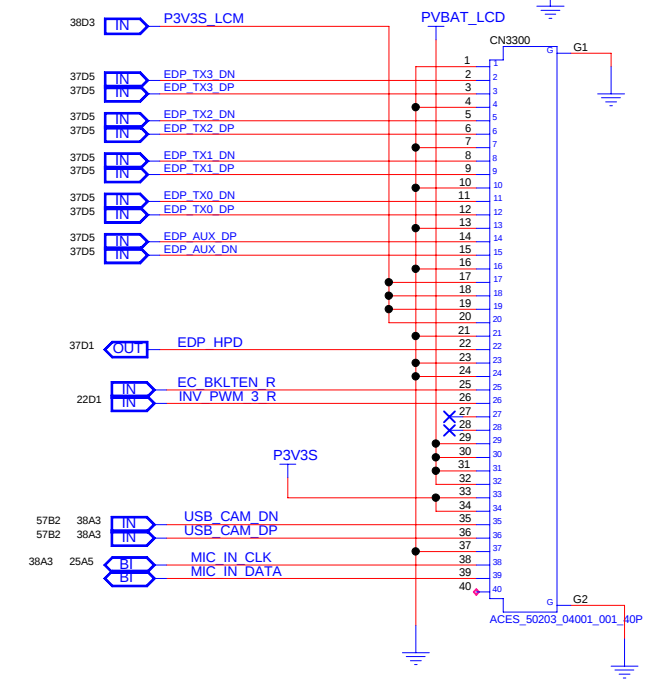
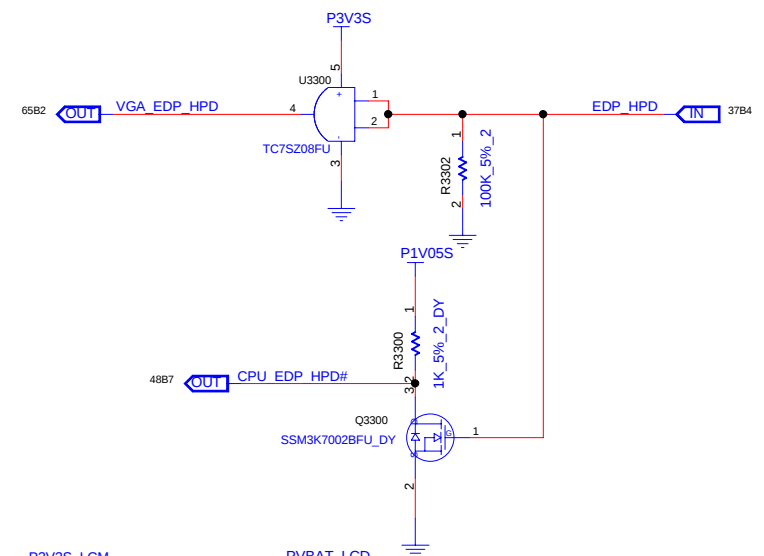
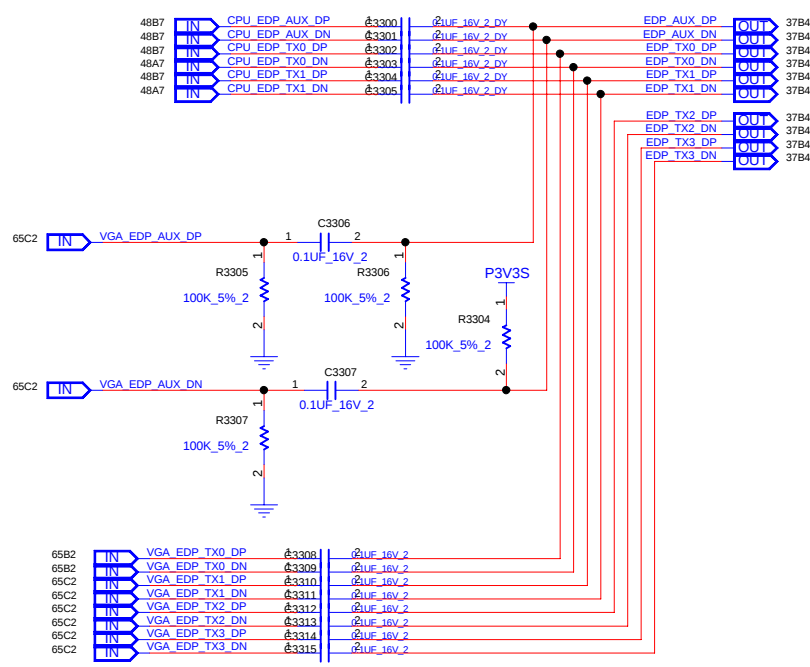
A

D

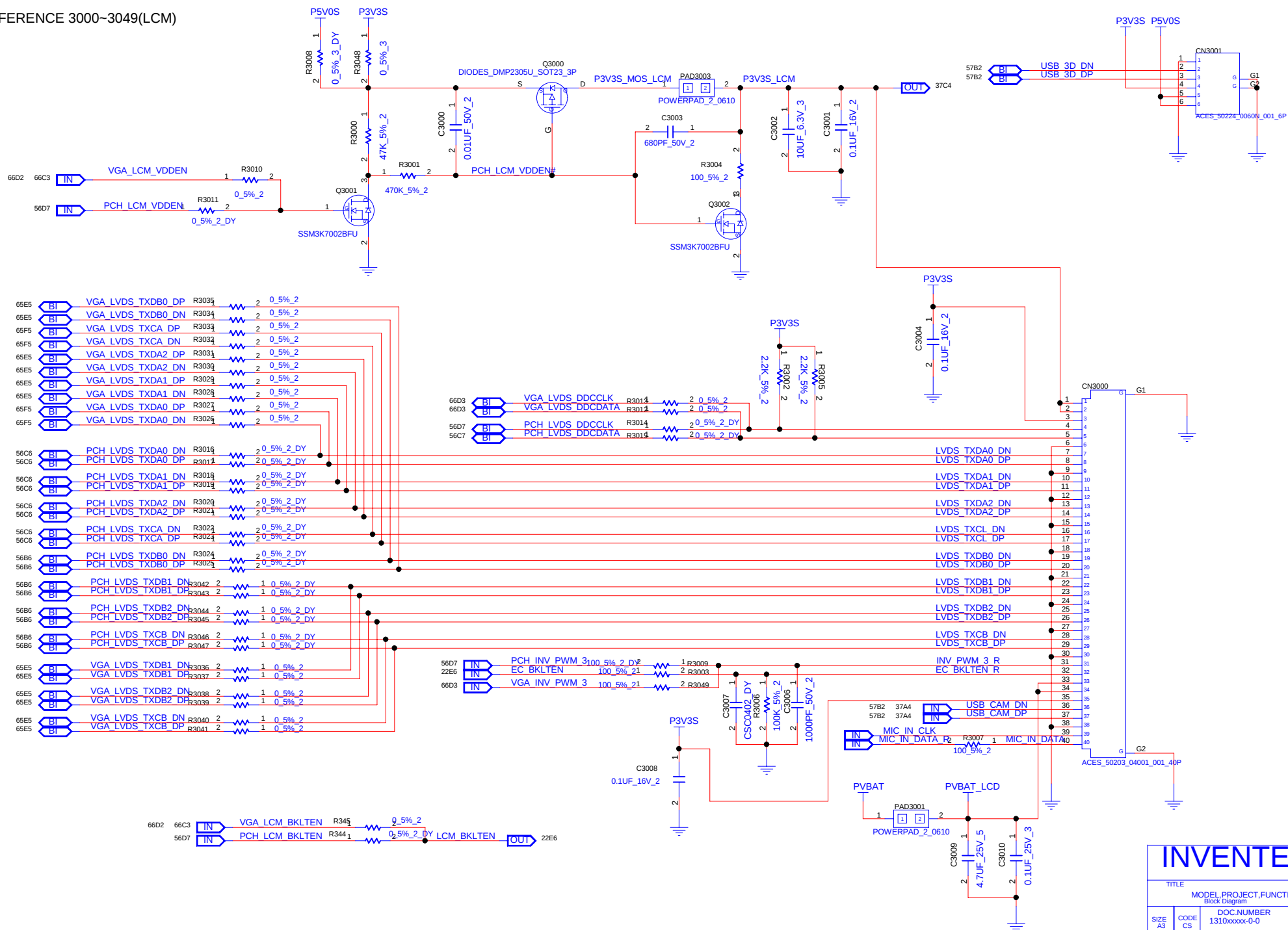
C

B

A



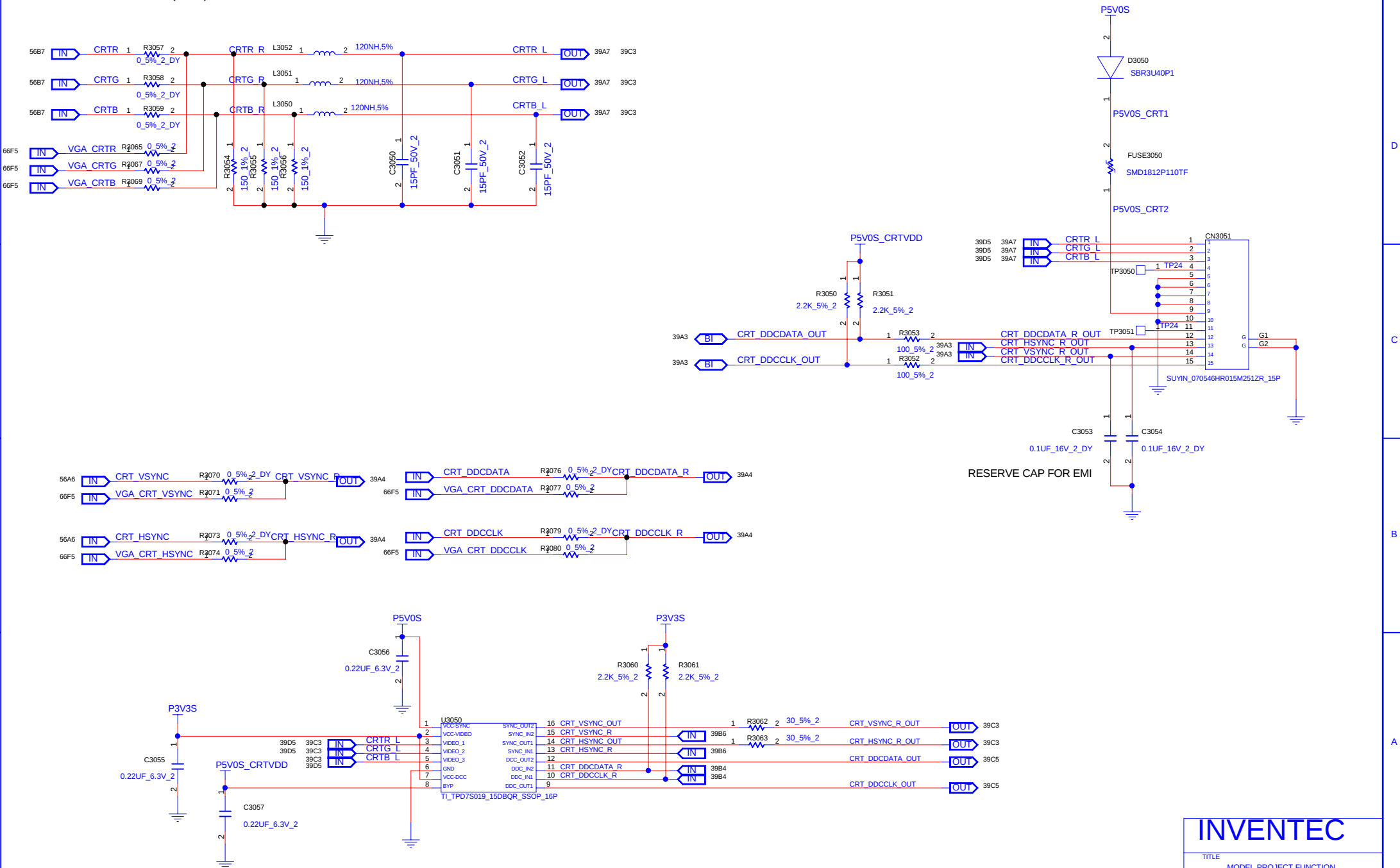
INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01



INVENTEC

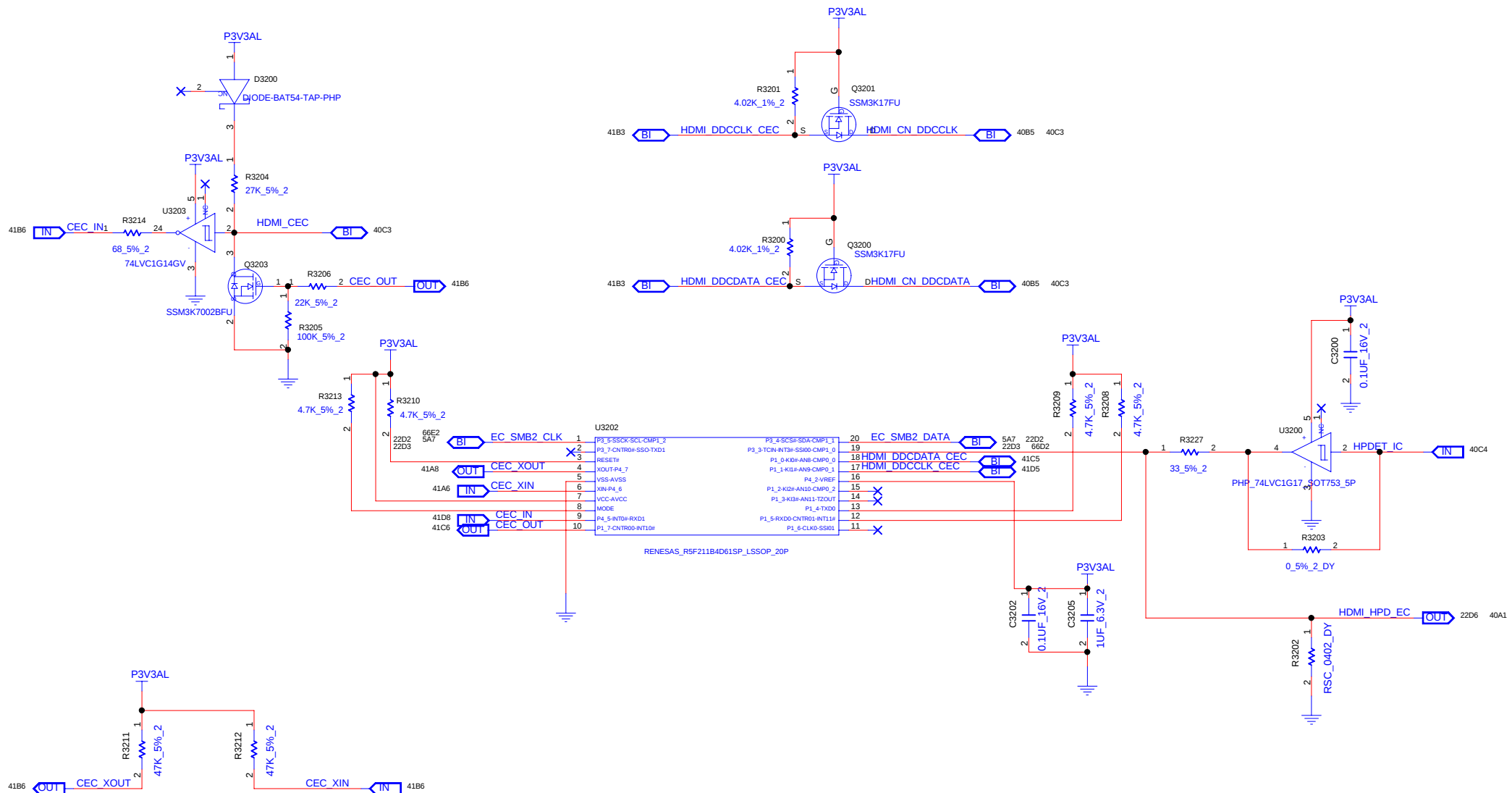
TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01

REFERENCE 3050~3099(CRT)



INVENTEC

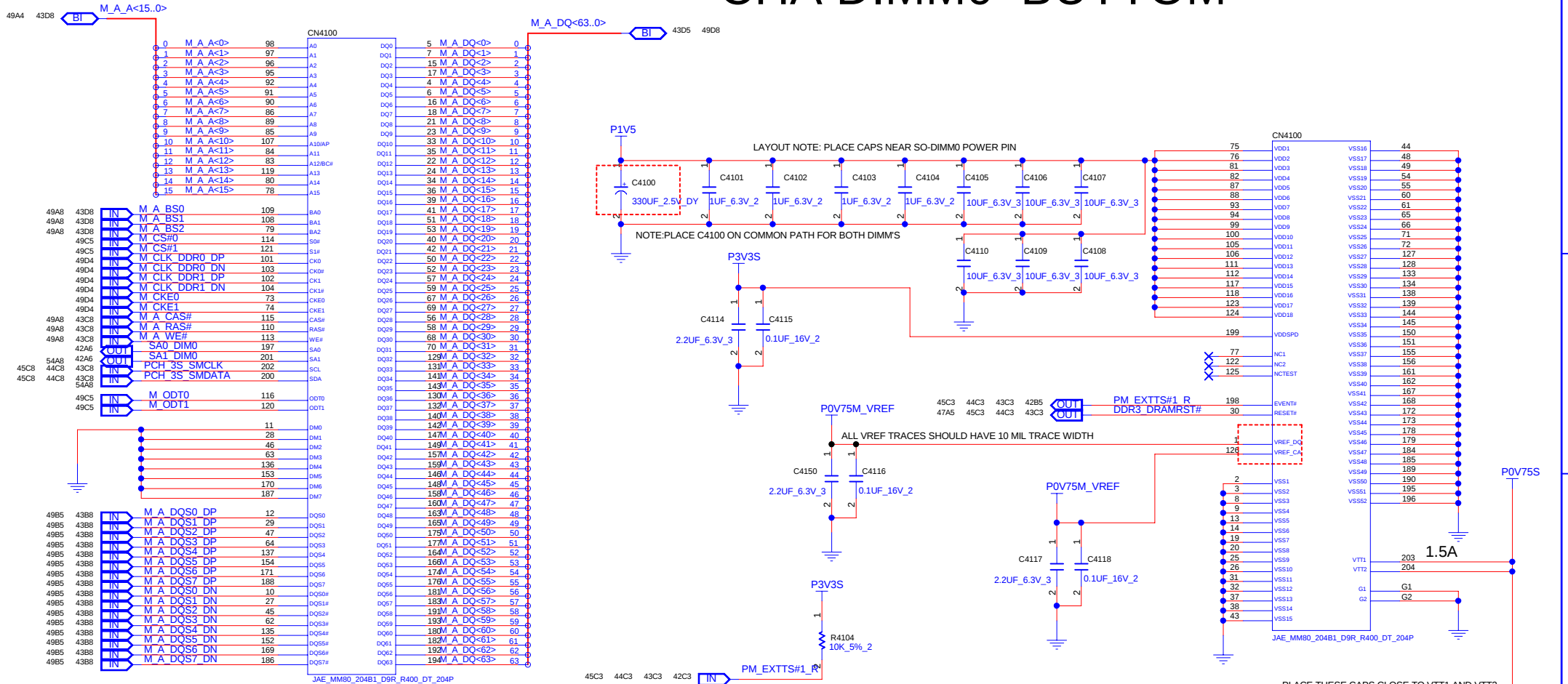
TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01



INVENTEC			
TITLE			
MODEL.PROJECT.FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01

REFERENCE 4100-4199(DDR)

CHA DIMM0 BUTTOM



NOTE:

IF SA0_DIM0=1, SA1_DIM0=0
SO-DIMMA SPD ADDRESS IS 0XA2
SO-DIMMA TS ADDRESS IS 0X32

IF SA0_DIM0=0, SA1_DIM0=0
SO-DIMMA SPD ADDRESS IS 0XA0
SO-DIMMA TS ADDRESS IS 0X30

INVENTEC

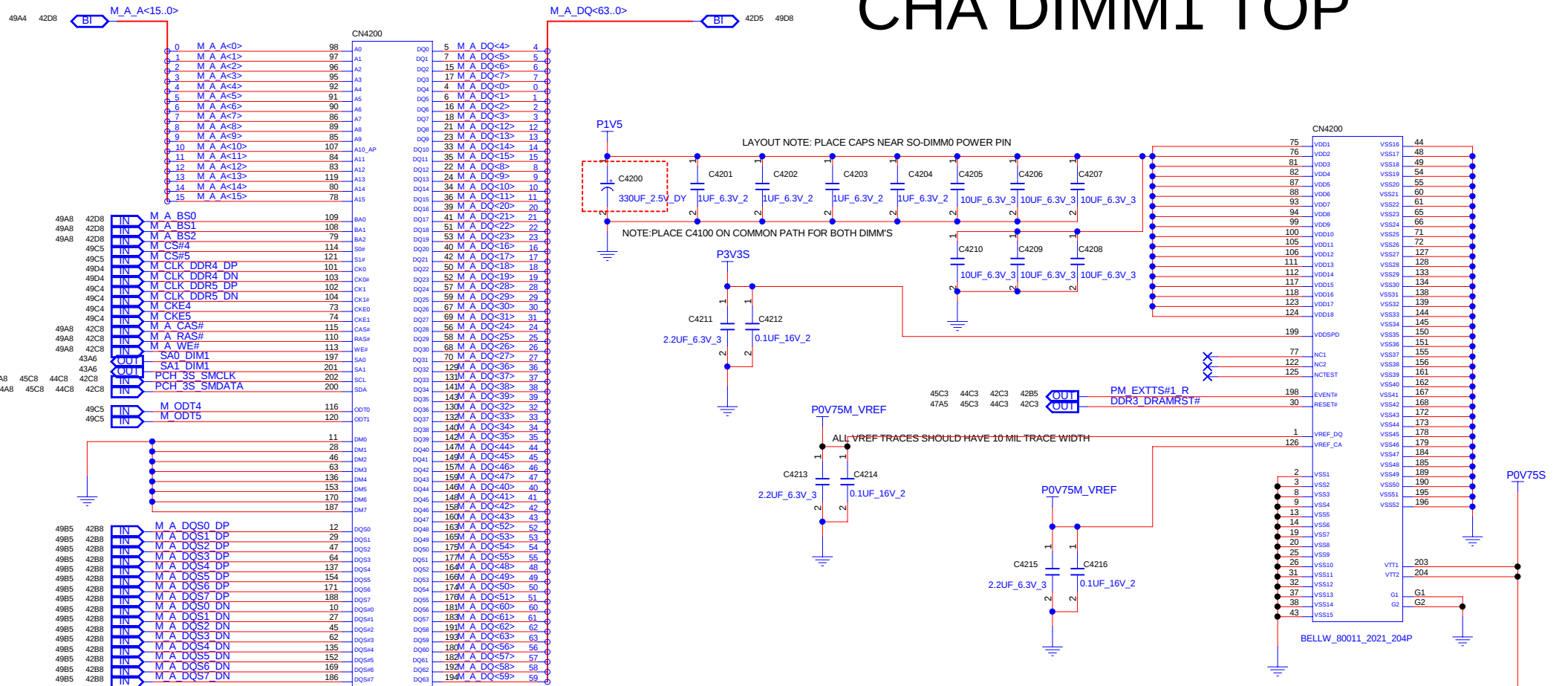
TITLE			
MODEL PROJECT FUNCTION			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 42 of 76

REFERENCE 4200~4299(DDR)

CHA DIMM1 TOP

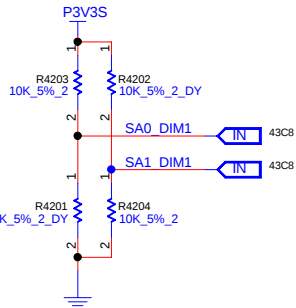


BELLW_80011_2021_204P

NOTE:

IF SA0_DIM0=1, SA1_DIM0=0
SO-DIMMA SPD ADDRESS IS 0XA2
SO-DIMMA TS ADDRESS IS 0X32

IF SA0_DIM0=0, SA1_DIM0=0
SO-DIMMA SPD ADDRESS IS 0XA0
SO-DIMMA TS ADDRESS IS 0X30



INVENTEC

TITLE
MODEL PROJECT FUNCTION

SIZE A3 CODE CS DOC NUMBER 1310xxxx-0-0 REV X01

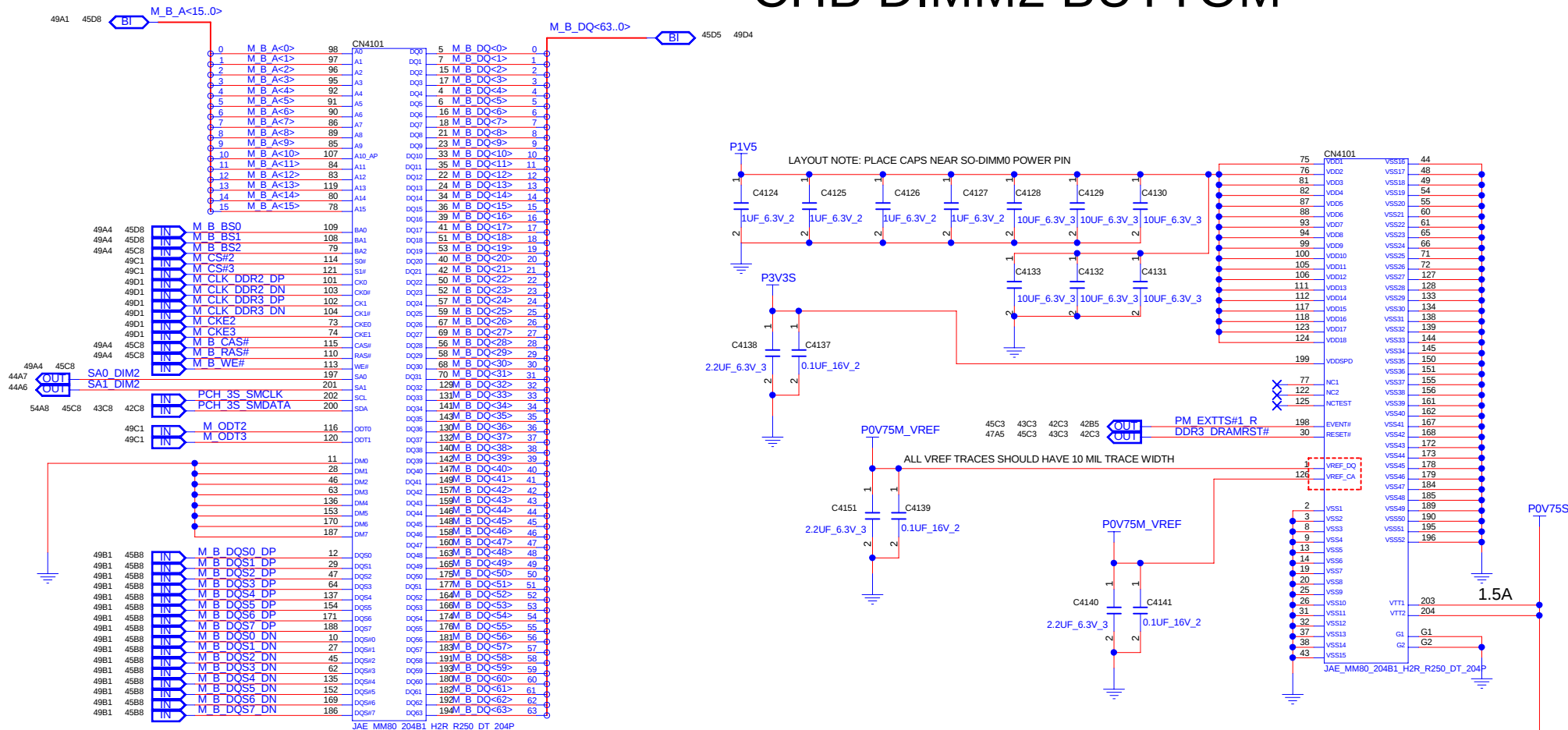
CHANGE by XXX

DATE 21-OCT-2002

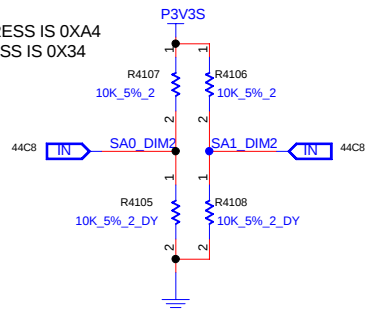
SHEET 43 of 76

REFERENCE 4100~4199(DDR)

CHB DIMM2 BUTTOM



NOTE:
SO-DIMMB SPD ADDRESS IS 0XA4
SO-DIMMB TS ADDRESS IS 0X34



INVENTEC

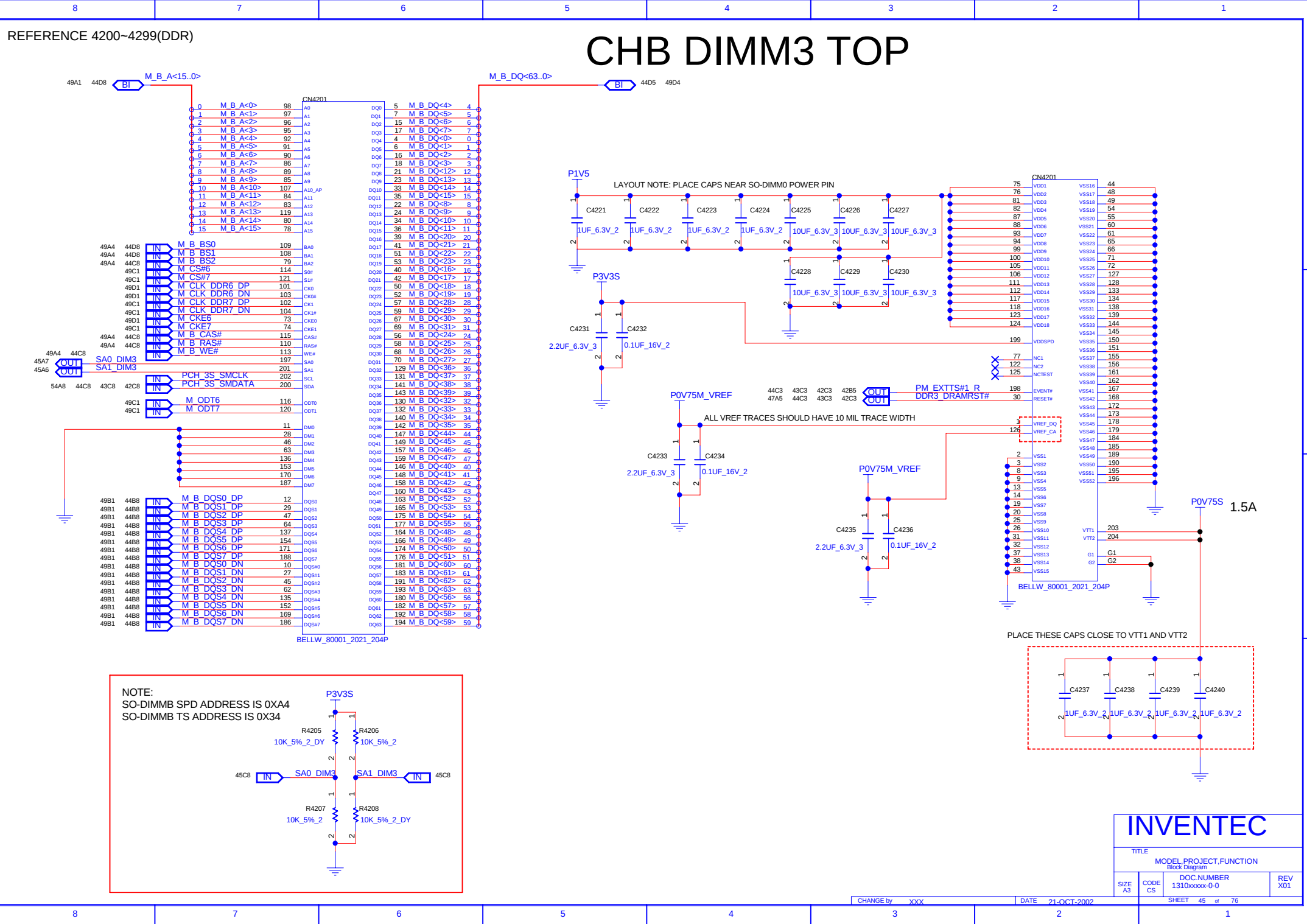
TITLE			
MODEL PROJECT FUNCTION			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX

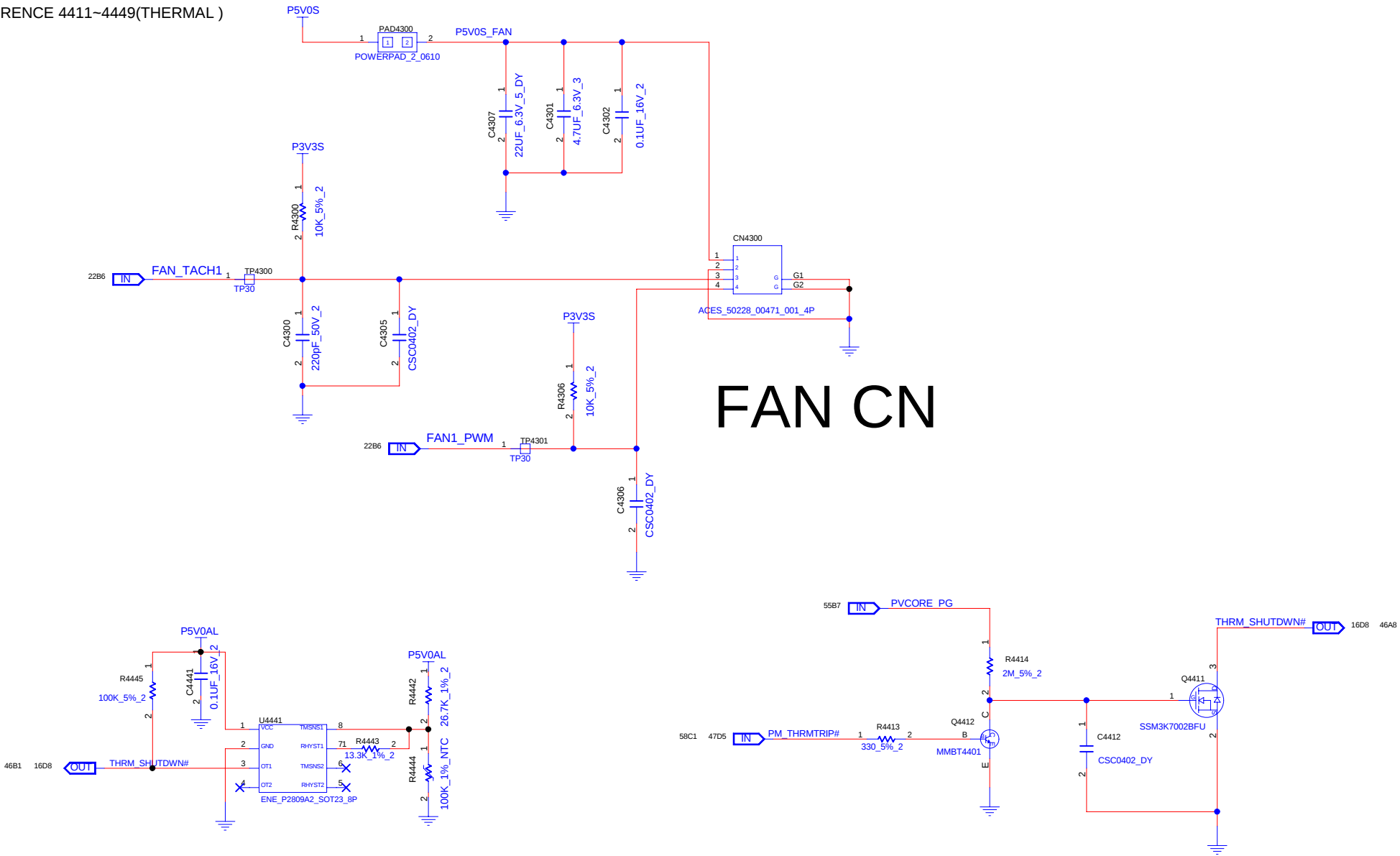
DATE 21-OCT-2002

SHEET 44 of 76

8	7	6	5	4	3	2	1
---	---	---	---	---	---	---	---



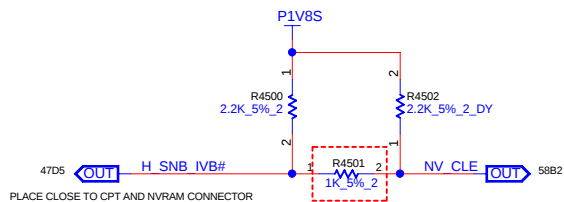
REFERENCE 4300~4349(FAN)
REFERENCE 4411~4449(THERMAL)



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
DOC NUMBER		REV	
1310xxxxx-0-0		X01	
SIZE	CODE		
A3	CS		

REFERENCE 4500~4699(CPU)

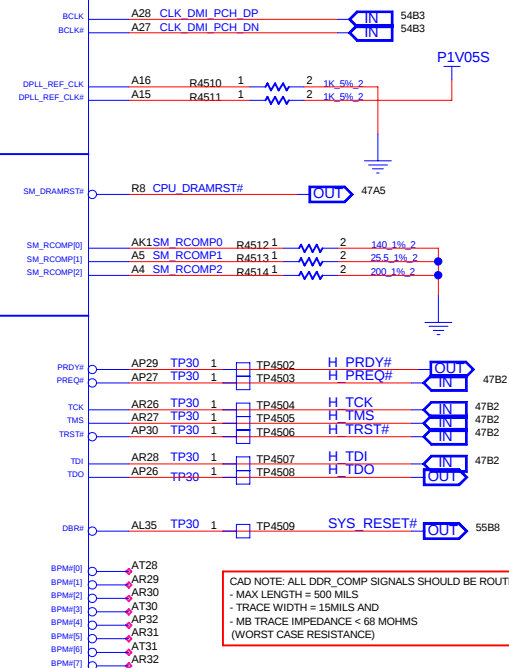
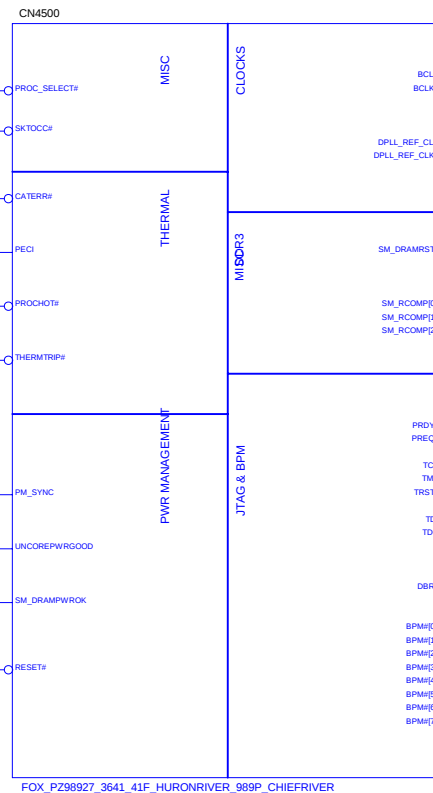
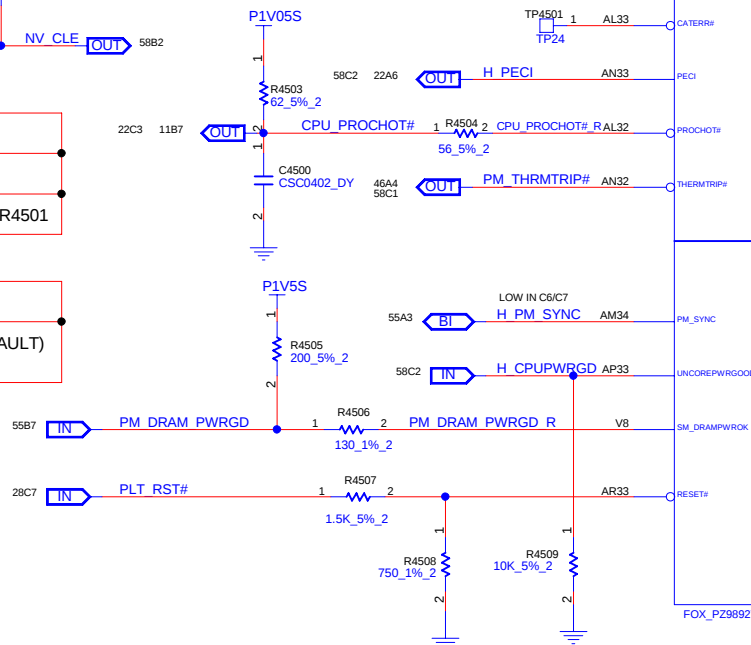


PROCESS STRAP SETTING

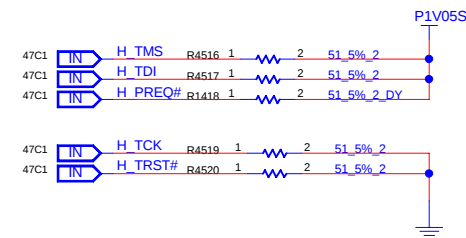
SANDY BRIDGE ONLY	STUFF R4502
SANDY BRIDGE/IVY BRIDGE	STUFF R4500/R4501

DMI&FDI TERMINATIONVOLTAGE

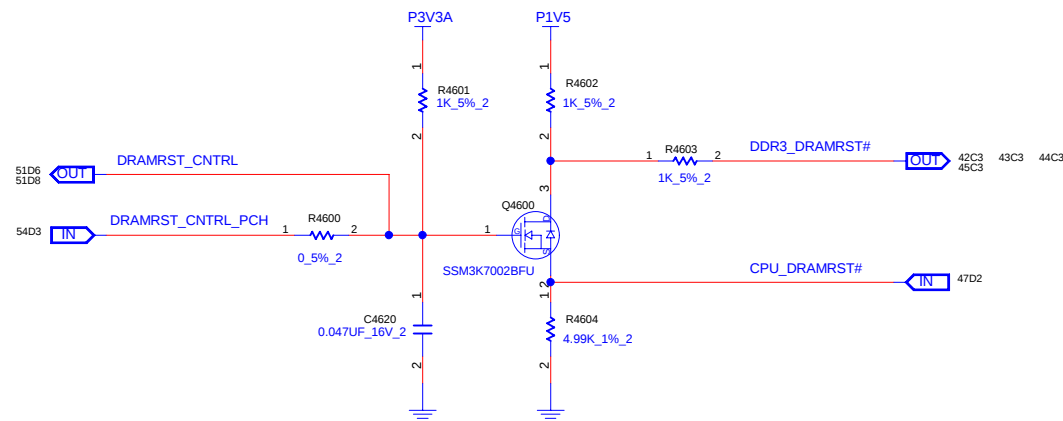
NV_CLE	SET TOVSS WHEN LOW (DEFAULT) SET TOVCC WHEN HIGH
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CAD NOTE: ALL DDR_COMP SIGNALS SHOULD BE ROUTED SUCH THAT
- MAX LENGTH = 500 MILS
- TRACE WIDTH = 15MILS AND
- MB TRACE IMPEDANCE < 68 MOHMS
(WORST CASE RESISTANCE)



S3 CIRCUIT: DRAM_RST# TO MEMORY SHOULD BE HIGH DURING S3



INVENTEC

TITLE		
MODEL,PROJECT,FUNCTION Block Diagram		
SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0

D



D



FOX PZ98927 3641 41F HURONRIVER 989P CHIEFRIVER



CLOSE TO CPU									
48B4	IN	PEG TX0 DN	C4580	1	2.22UF_6.3V_2	PEG C TX0 DN	OUT	62E5	
48B4	IN	PEG TX1 DN	C4581	1	2.22UF_6.3V_2	PEG C TX1 DN	OUT	62E5	
48B4	IN	PEG TX2 DN	C4582	1	2.22UF_6.3V_2	PEG C TX2 DN	OUT	62E5	
48B4	IN	PEG TX3 DN	C4583	1	2.22UF_6.3V_2	PEG C TX3 DN	OUT	62D5	
48B4	IN	PEG TX4 DN	C4584	1	2.22UF_6.3V_2	PEG C TX4 DN	OUT	62D5	
48B4	IN	PEG TX5 DN	C4585	1	2.22UF_6.3V_2	PEG C TX5 DN	OUT	62D5	
48B4	IN	PEG TX6 DN	C4586	1	2.22UF_6.3V_2	PEG C TX6 DN	OUT	62D5	
48B4	IN	PEG TX7 DN	C4587	1	2.22UF_6.3V_2	PEG C TX7 DN	OUT	62D5	
48B4	IN	PEG TX8 DN	C4588	1	2.22UF_6.3V_2	PEG C TX8 DN	OUT	62C5	
48B4	IN	PEG TX9 DN	C4589	1	2.22UF_6.3V_2	PEG C TX9 DN	OUT	62C5	
48B4	IN	PEG TX10 DN	C4590	1	2.22UF_6.3V_2	PEG C TX10 DN	OUT	62C5	
48B4	IN	PEG TX11 DN	C4591	1	2.22UF_6.3V_2	PEG C TX11 DN	OUT	62C5	
48B4	IN	PEG TX12 DN	C4592	1	2.22UF_6.3V_2	PEG C TX12 DN	OUT	62B5	
48C4	IN	PEG TX13 DN	C4593	1	2.22UF_6.3V_2	PEG C TX13 DN	OUT	62B5	
48C4	IN	PEG TX14 DN	C4594	1	2.22UF_6.3V_2	PEG C TX14 DN	OUT	62B5	
48C4	IN	PEG TX15 DN	C4595	1	2.22UF_6.3V_2	PEG C TX15 DN	OUT	62B5	
48A4	IN	PEG TX0 DP	C4596	1	2.22UF_6.3V_2	PEG C TX0 DP	OUT	62E5	
48A4	IN	PEG TX1 DP	C4597	1	2.22UF_6.3V_2	PEG C TX1 DP	OUT	62E5	
48A4	IN	PEG TX2 DP	C4598	1	2.22UF_6.3V_2	PEG C TX2 DP	OUT	62E5	
48A4	IN	PEG TX3 DP	C4599	1	2.22UF_6.3V_2	PEG C TX3 DP	OUT	62E5	
48A4	IN	PEG TX4 DP	C4600	1	2.22UF_6.3V_2	PEG C TX4 DP	OUT	62D5	
48A4	IN	PEG TX5 DP	C4601	1	2.22UF_6.3V_2	PEG C TX5 DP	OUT	62D5	
48A4	IN	PEG TX6 DP	C4602	1	2.22UF_6.3V_2	PEG C TX6 DP	OUT	62D5	
48B4	IN	PEG TX7 DP	C4603	1	2.22UF_6.3V_2	PEG C TX7 DP	OUT	62D5	
48B4	IN	PEG TX8 DP	C4604	1	2.22UF_6.3V_2	PEG C TX8 DP	OUT	62C5	
48B4	IN	PEG TX9 DP	C4605	1	2.22UF_6.3V_2	PEG C TX9 DP	OUT	62C5	
48B4	IN	PEG TX10 DP	C4606	1	2.22UF_6.3V_2	PEG C TX10 DP	OUT	62C5	
48B4	IN	PEG TX11 DP	C4607	1	2.22UF_6.3V_2	PEG C TX11 DP	OUT	62C5	
48B4	IN	PEG TX12 DP	C4608	1	2.22UF_6.3V_2	PEG C TX12 DP	OUT	62C5	
48B4	IN	PEG TX13 DP	C4609	1	2.22UF_6.3V_2	PEG C TX13 DP	OUT	62B5	
48B4	IN	PEG TX14 DP	C4610	1	2.22UF_6.3V_2	PEG C TX14 DP	OUT	62B5	
48B4	IN	PEG TX15 DP	C4611	1	2.22UF_6.3V_2	PEG C TX15 DP	OUT	62B5	

SHEET 48 of 76

REFERENCE 4500~4699(CPU)

SOCKET,CPU,989P,TIN,3.0MM,STR,SMD,TR

CN4500

DDR SYSTEM MEMORY A

DDR SYSTEM MEMORY B

INVENTEC

TITLE
MODEL,PROJECT,FUNCTION

DOC NUMBER
1310xxxxx-0-0

REV
X01

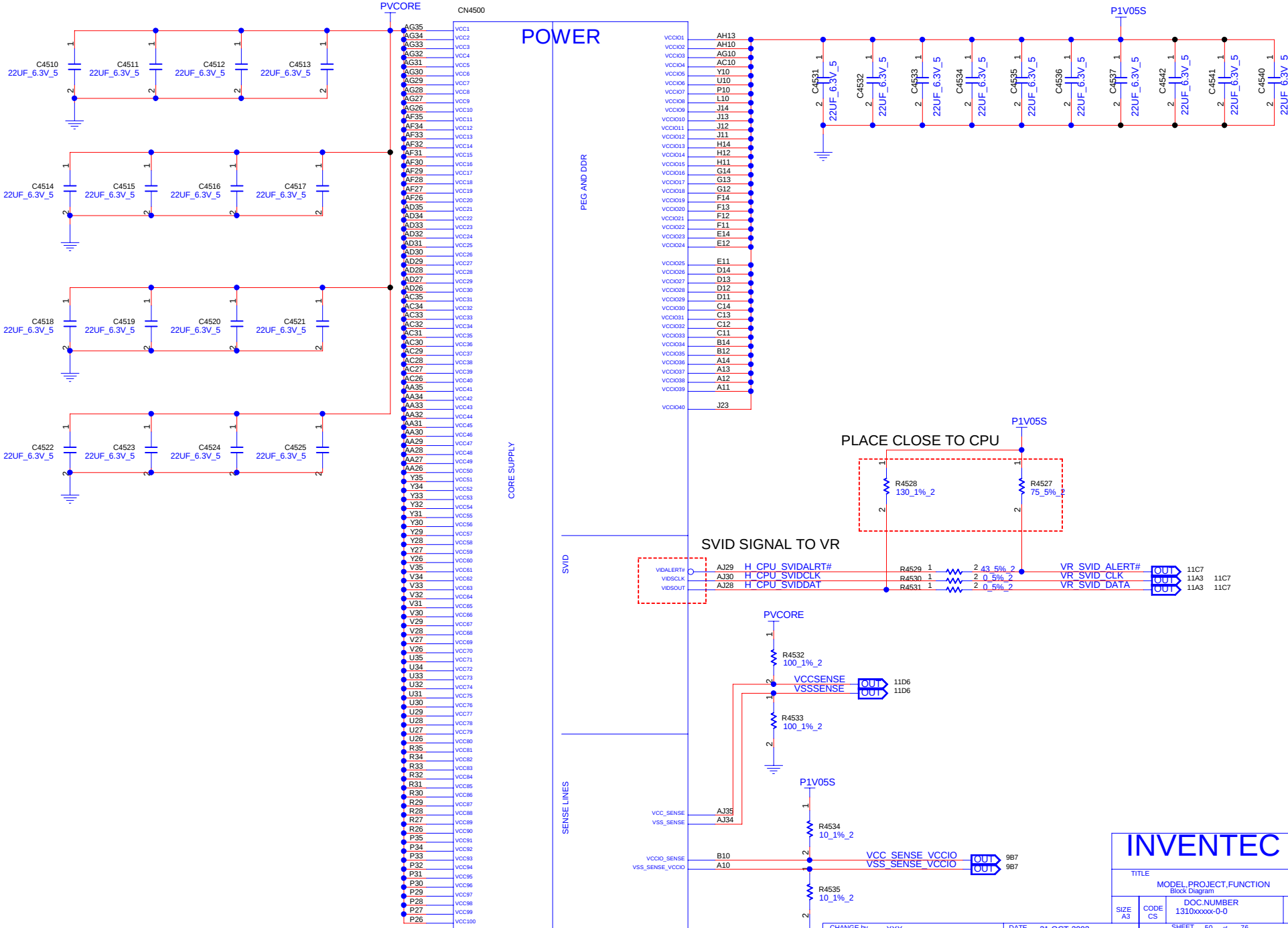
SHEET 49 of 76

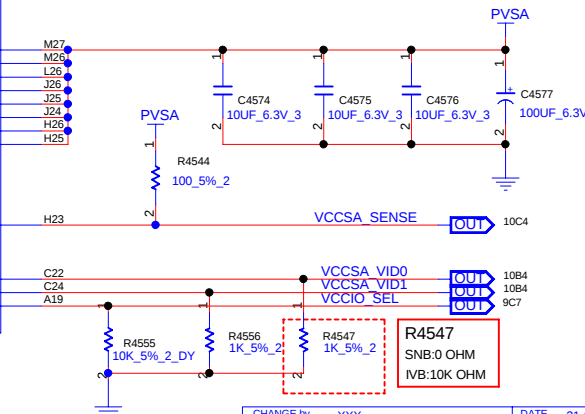
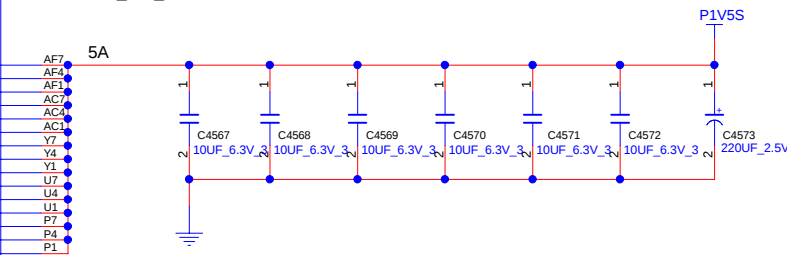
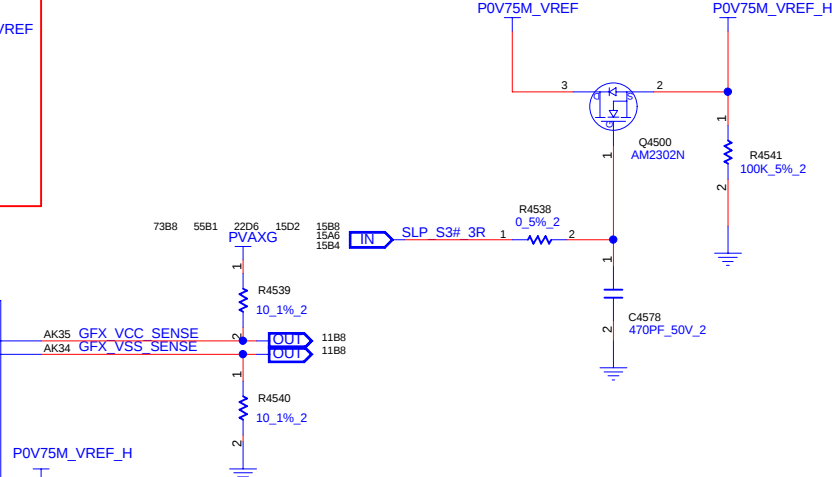
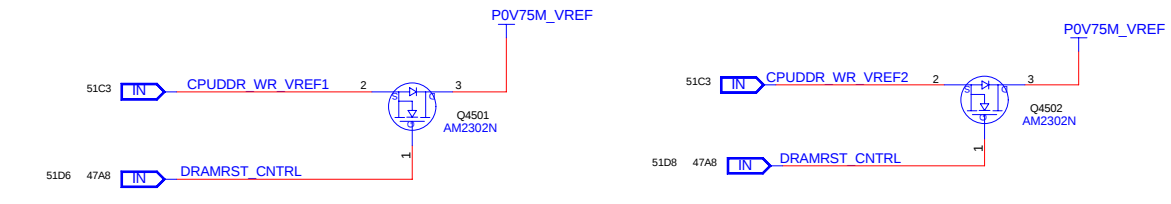
CHANGE by XXX DATE 21-OCT-2002

FOX_PZ98927_3641_41F_HURONRIVER_989P_CHIEFRIVER

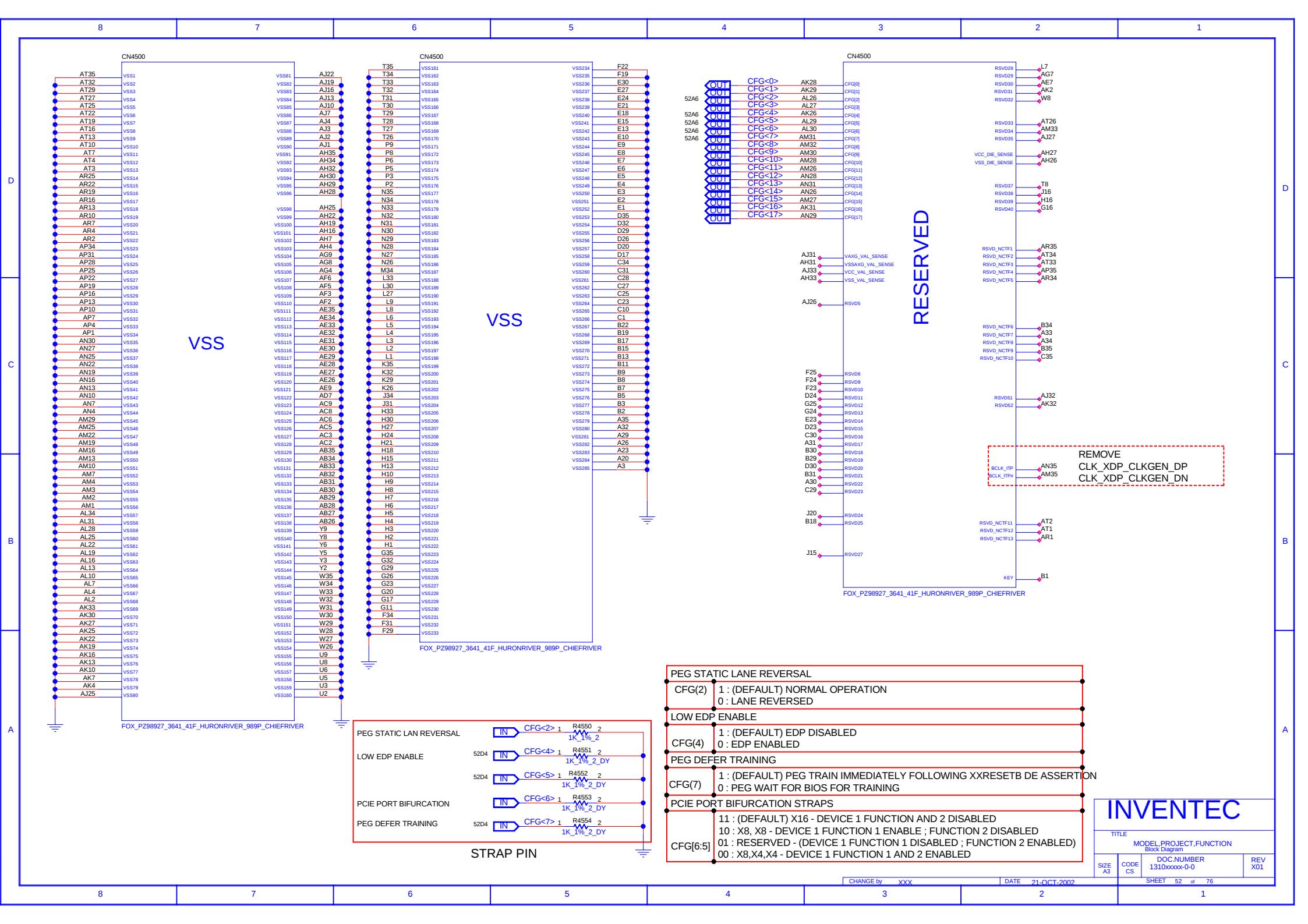
FOX_PZ98927_3641_41F_HURONRIVER_989P_CHIEFRIVER

REFERENCE 4500~4699(CPU)



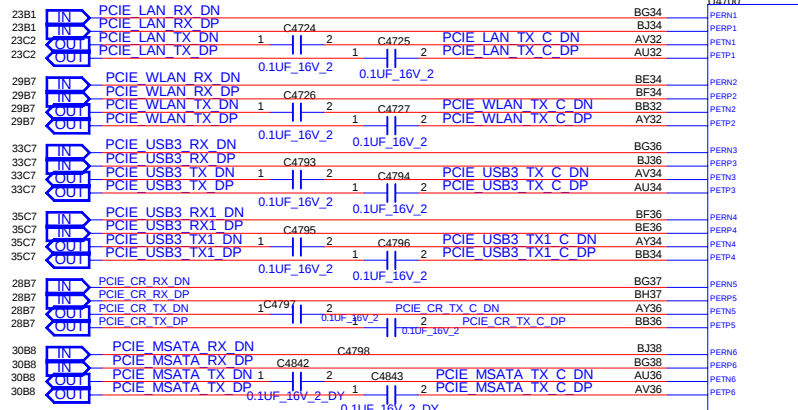


INVENTEC			
TITLE MODEL,PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC. NUMBER 1310xxxxx-0-0 SHEET 51 of 76	REV X01

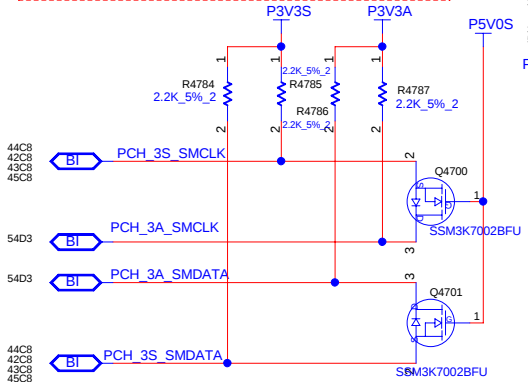
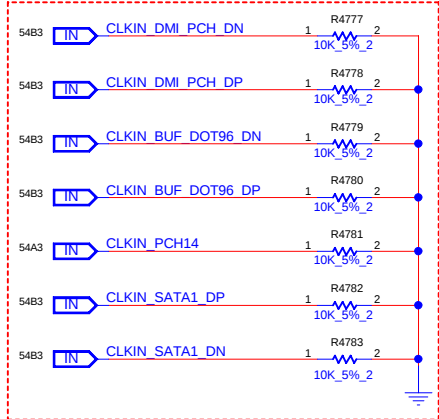


SHEET 53 of 76

REFERENCE 4700~4949(PCH)



CLOCK TERMINATION FOR FICM STUFF FOR INTEGRATED CLK



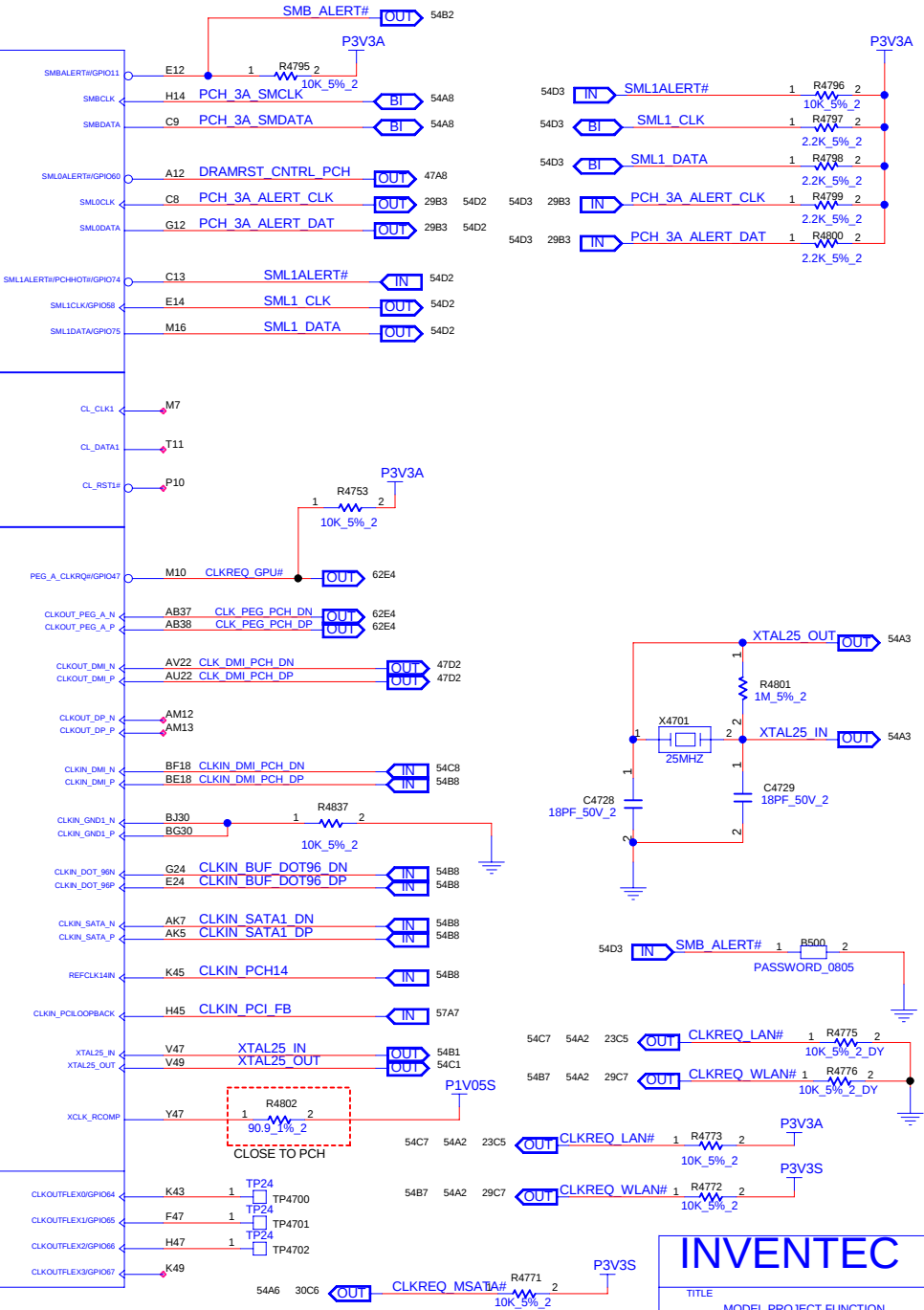
PCIE-E*

CLOCKS

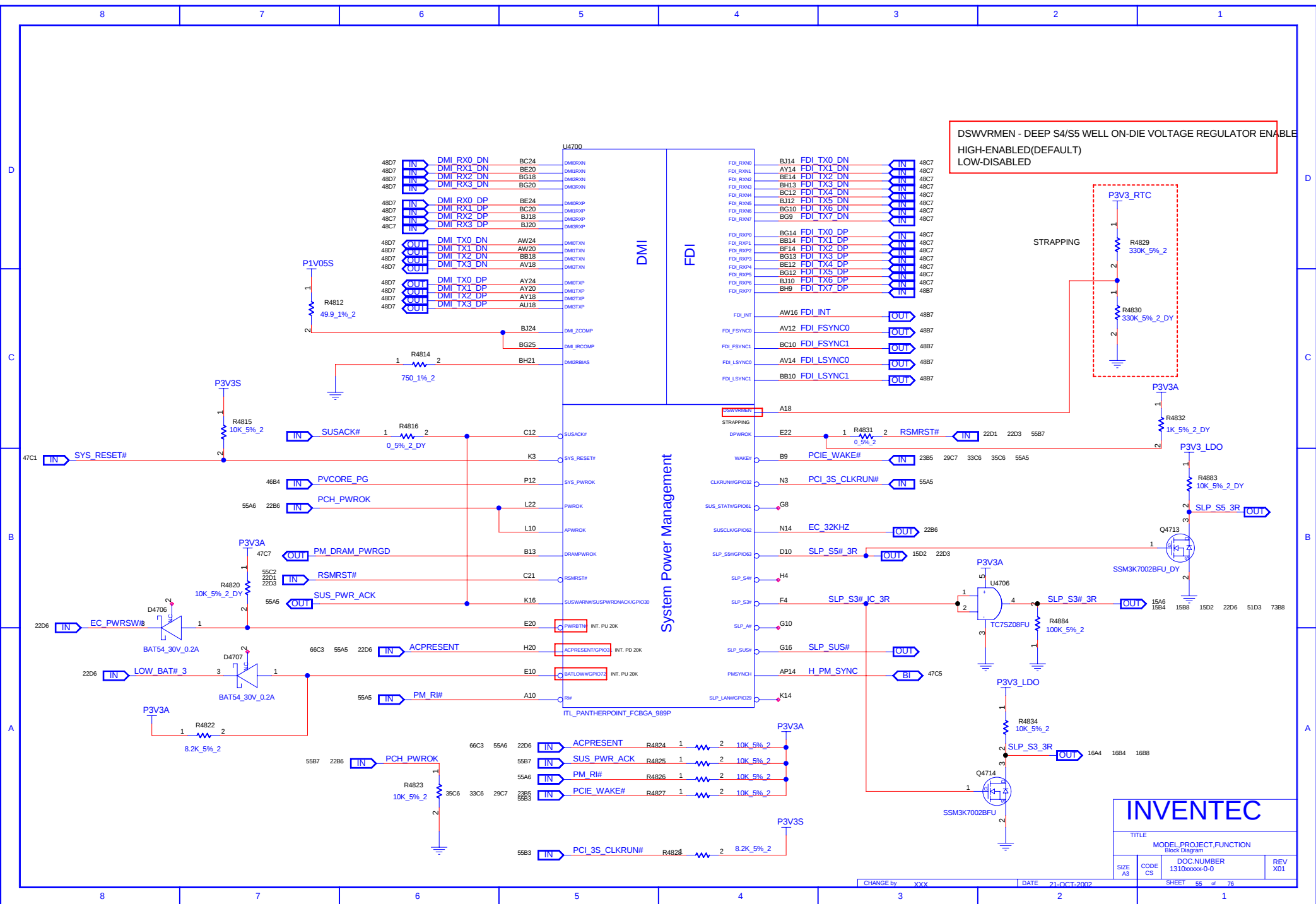
FLEX CLOCKS

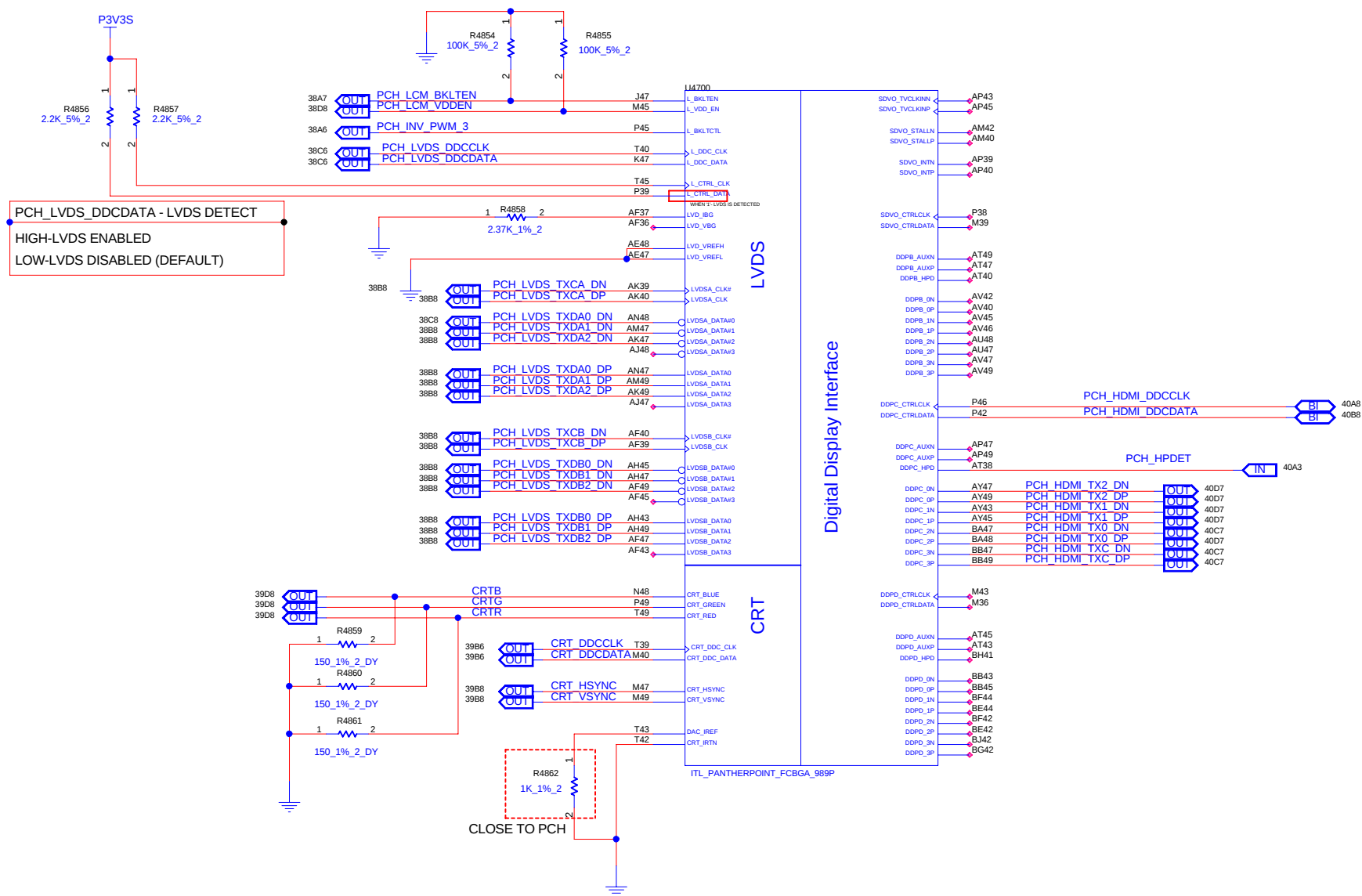
Controller Link

SMBUS



INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
1310xxxx-0-0			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 54 of 76			





INVENTEC

TITLE

MODEL,PROJECT,FUNCTION

DOC NUMBER

1310xxxx-0-0

REV

X01

CHANGE by

XXX

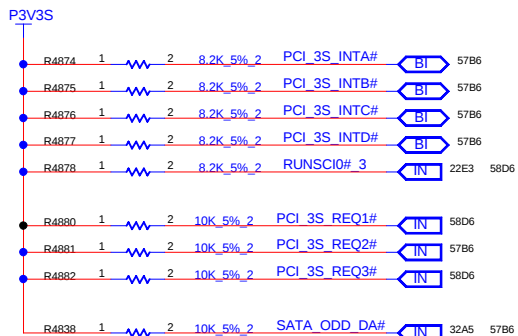
DATE

21-OCT-2002

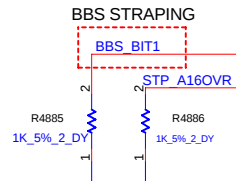
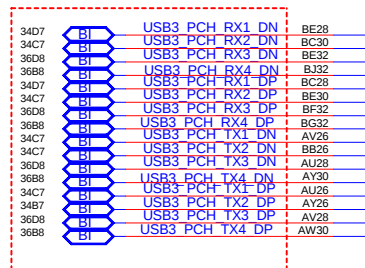
SHEET

56 of 76

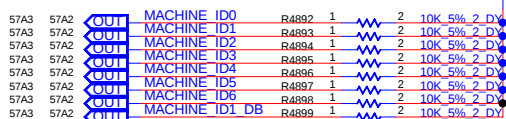
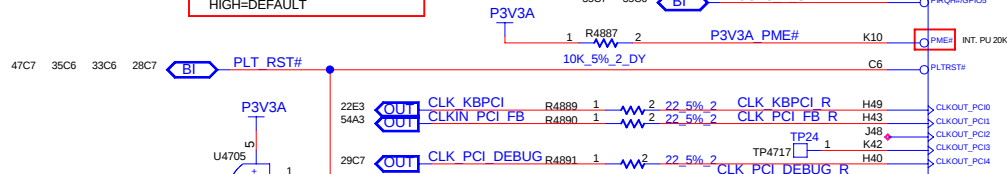
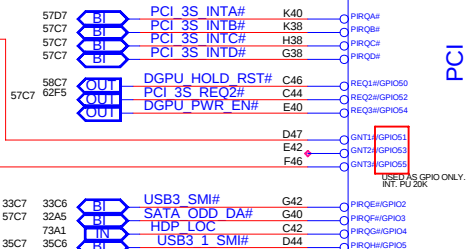
GPIO51 BBS_BT1	GPIO19 BBS_BT0	BOOT BIOS DESTINATION
0	1	RESERVED(NAND)
1	0	*****
1	1	SPI (DEFAULT)
0	0	LPC



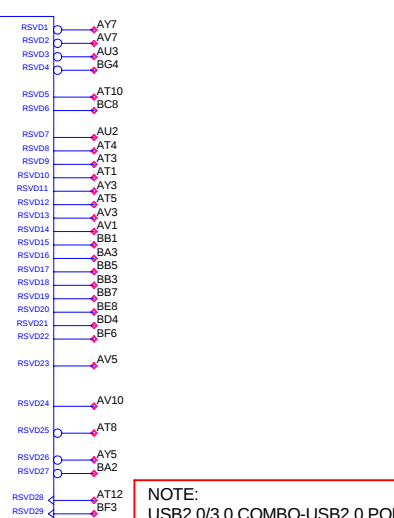
ROUTE WITH 90 OHMS IMPEDANCE
TOTAL LENGTH NO LONGER THAN 11 INCHES



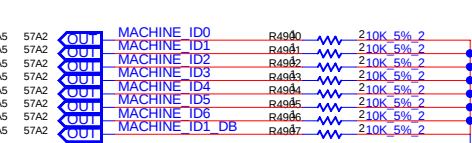
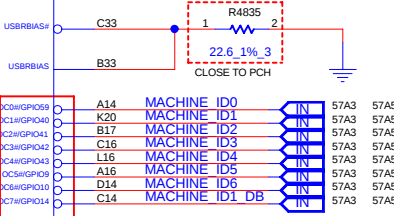
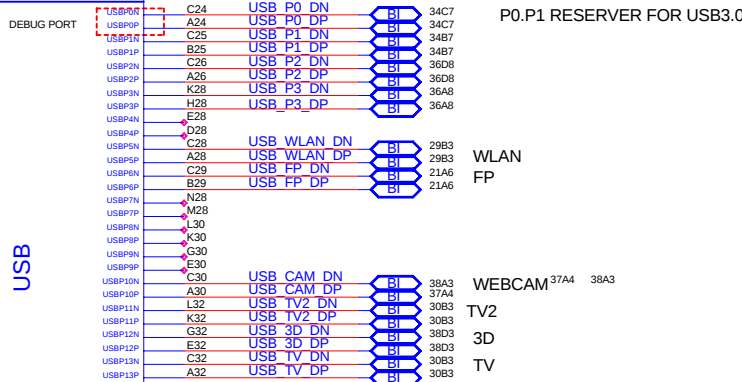
LOW=A16 SWAP OVERRIDE
STP_A16OVR
TOP-BLOCK SWAP OVERRIDE
HIGH=DEFAULT



NOTE:10K 5%(60130B1030ZT)



NOTE:
USB2.0/3.0 COMBO-USB2.0 PORT 0,1 MAPPED USB3.0 PORT 1,2

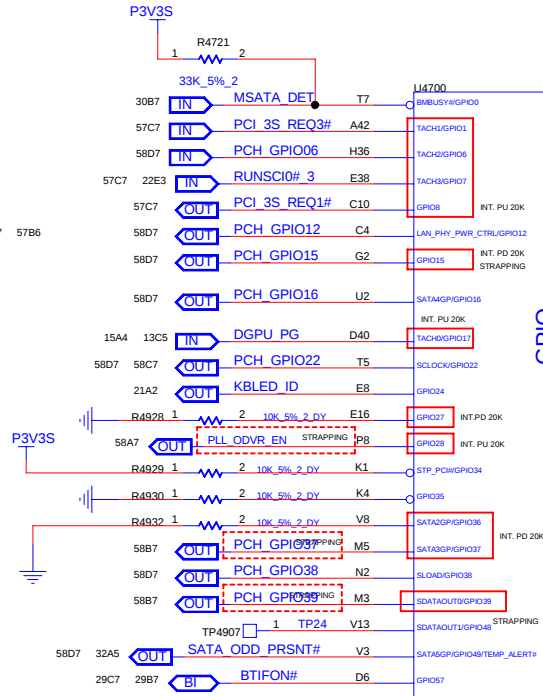
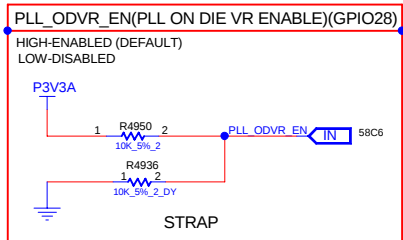
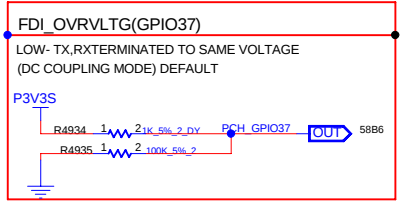
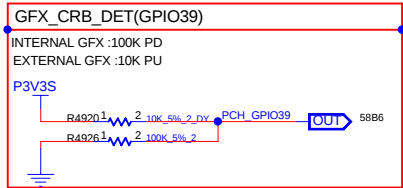
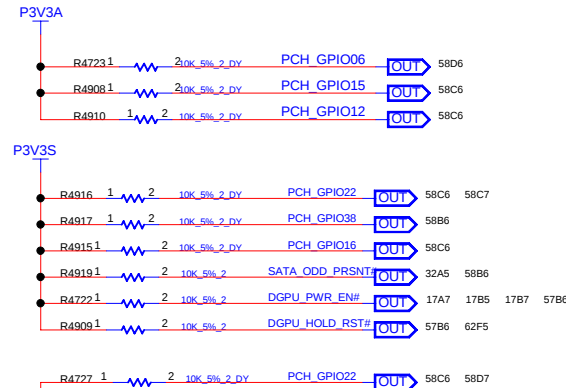


NOTE:10K 5%(60130B1030ZT)



TITLE		
MODEL,PROJECT,FUNCTION Block Diagram		
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0
SHEET 57 of 76		

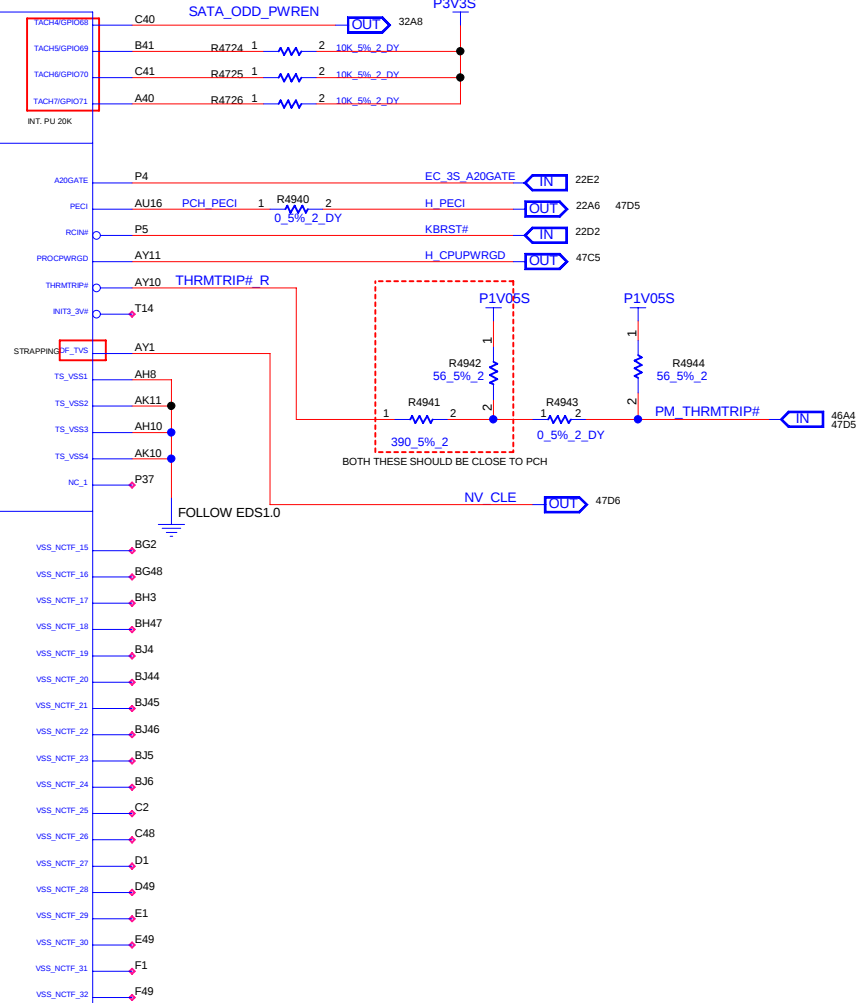
REFERENCE 4700~4949(PCH)



GPIO

NCTF

CPU/MISC



INVENTEC

TITLE MODEL PROJECT FUNCTION

SIZE CODE DOC NUMBER REV
A3 CS 1310xxxx-0-0 X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 58 of 76

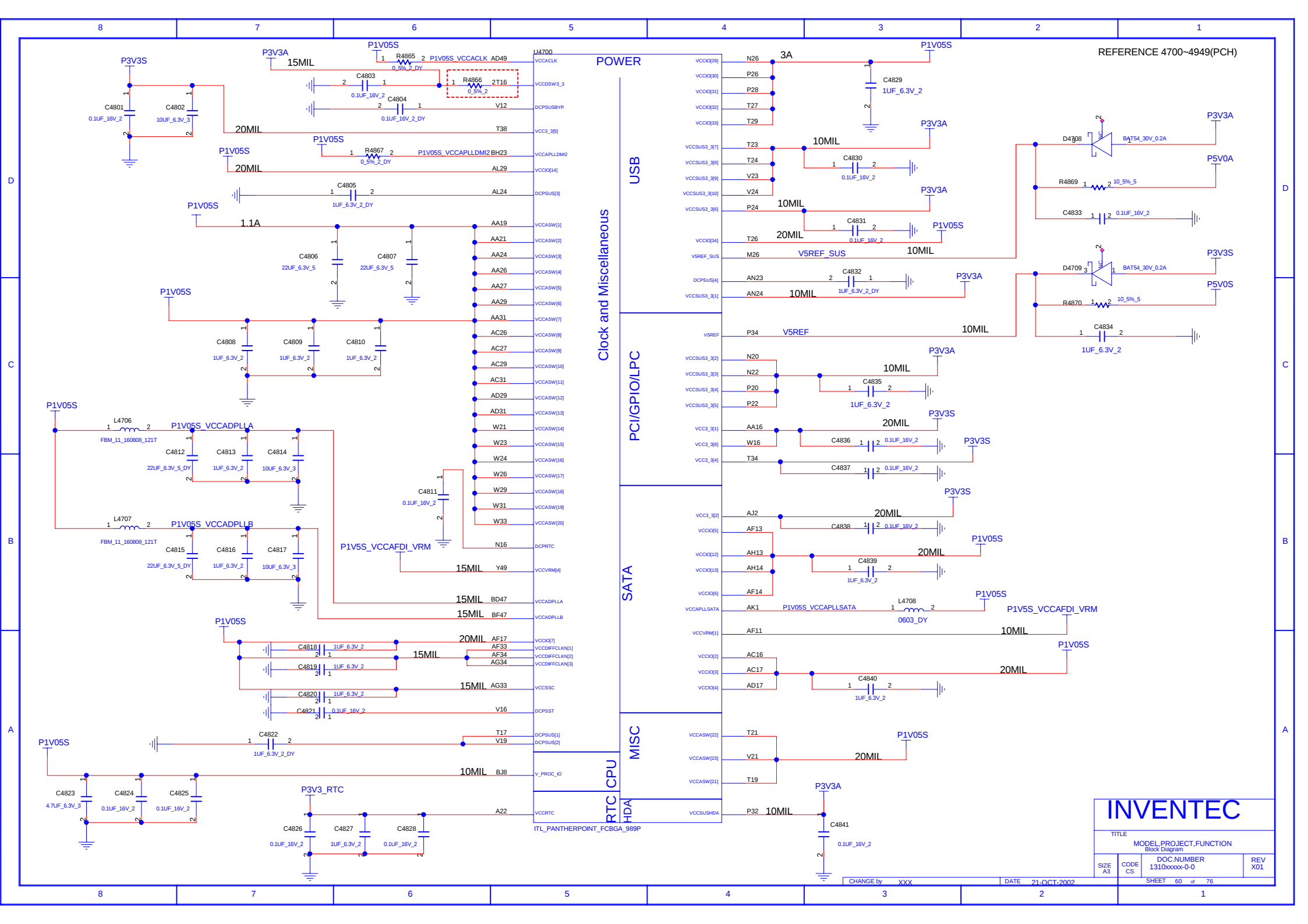
D



VccAFDIPLL
VCCIO[27]

CHANGE by	XXX	DATE	21-OCT-2002
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REFERENCE 4700-4949(PCH)

INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
SIZE A3		DOC NUMBER 1310xxxx-0-0	
CODE CS		REV X01	

8

7

6

5

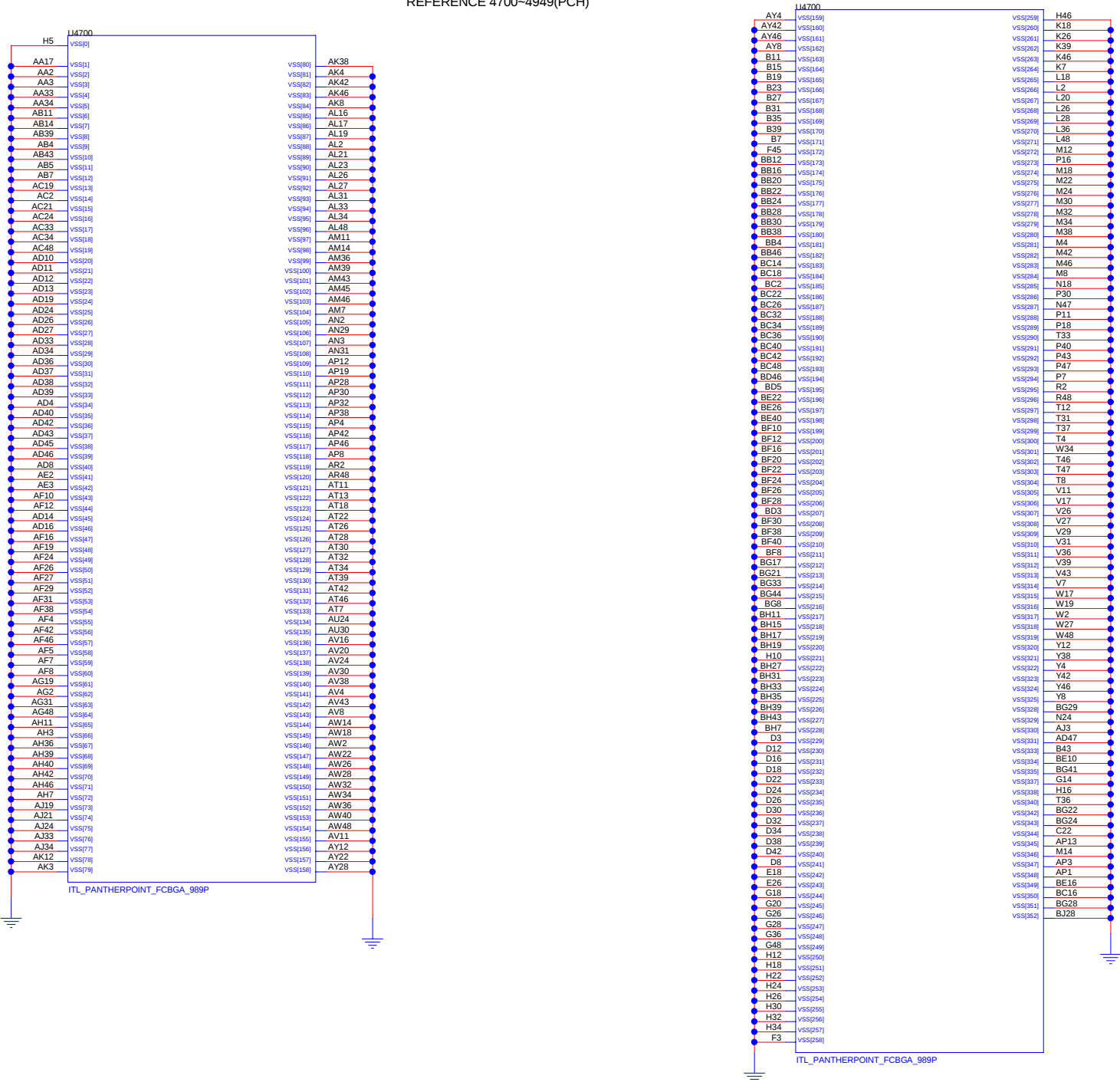
4

3

2

1

REFERENCE 4700~4949(PCH)



INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block DiagramSIZE A3 CODE CS DOC:NUMBER
1310xxxxx-0-0 REV X01

CHANGE by XXX

DATE 21-OCT-2002

SHEET 61 of 76

8

7

6

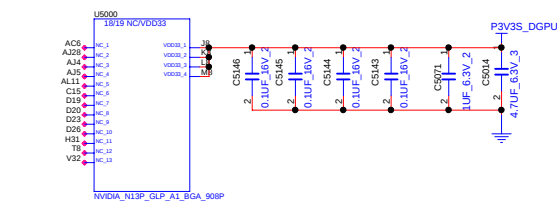
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4

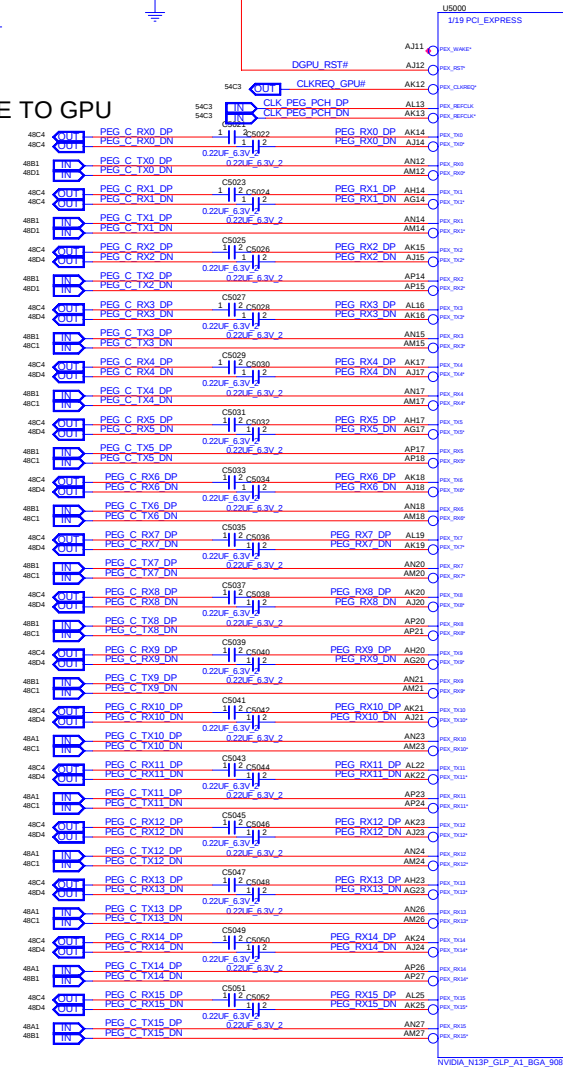
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2

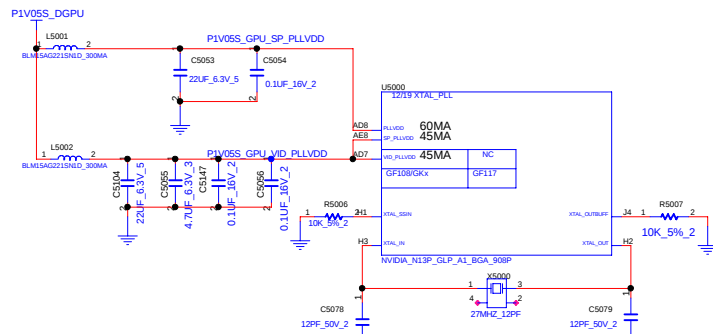
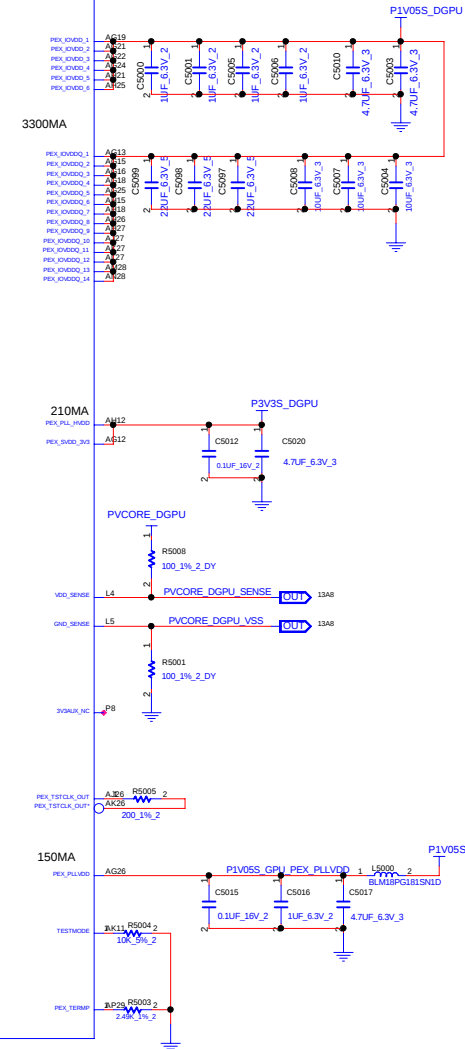
1



CLOSE TO GPU



+1.05V POWER RAIL SHOULD HAVE 3500MA CAPABILITY AT LEAST

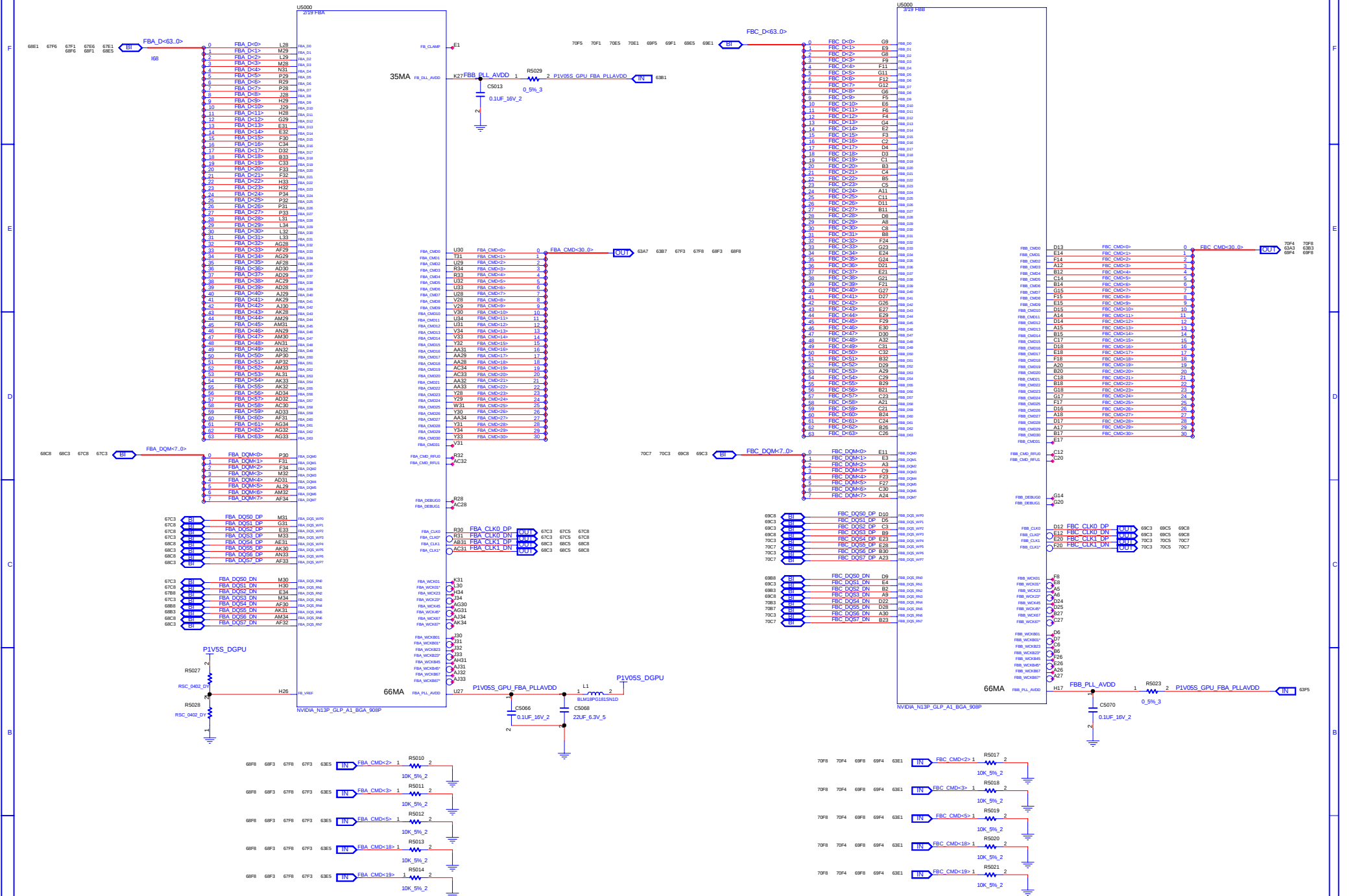


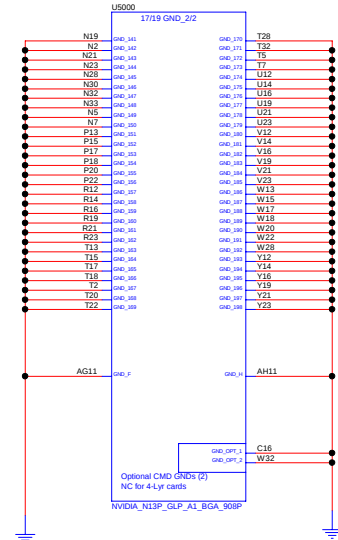
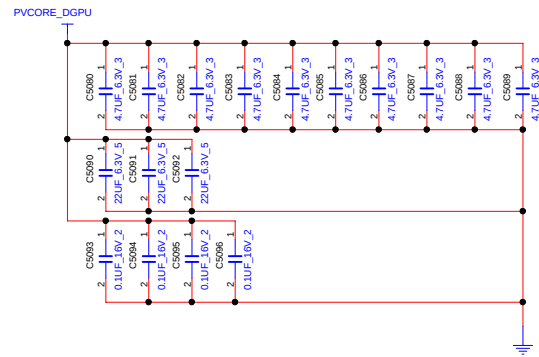
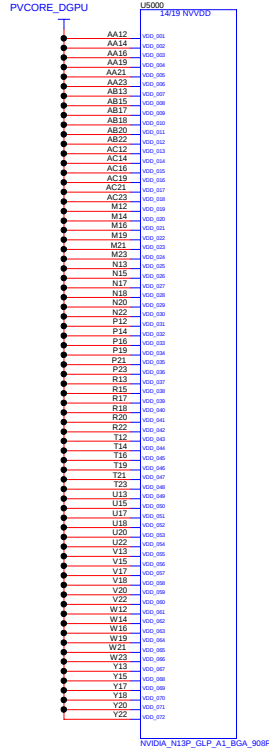
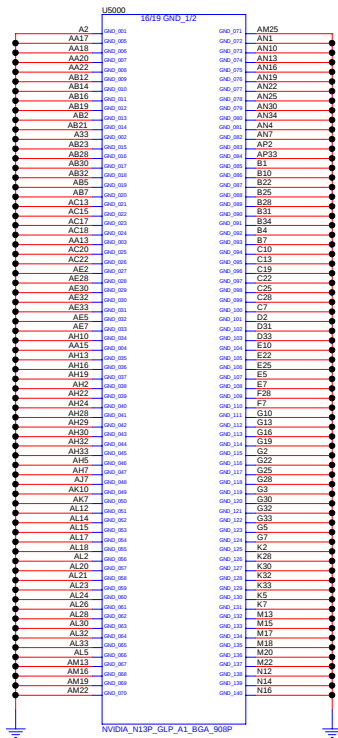
INVENTEC

MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
C	CS	131000000-05	X01
SHEET		PAGE	
62		1	

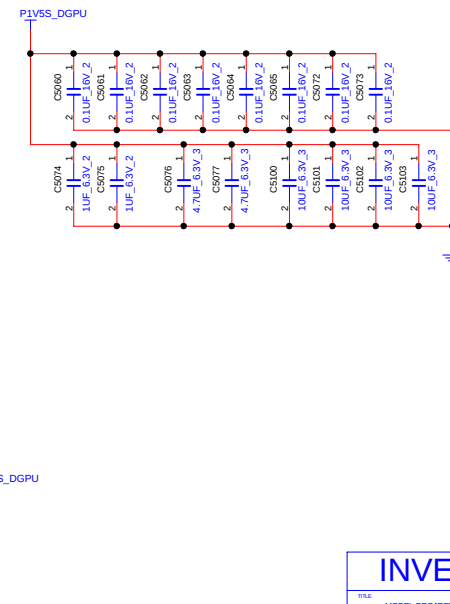
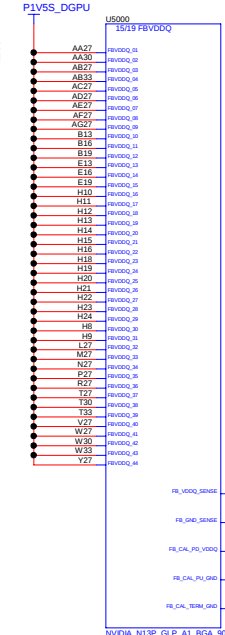
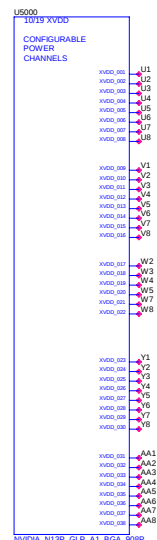
CHANGE BY XXXX DATE 21-OCT-2006

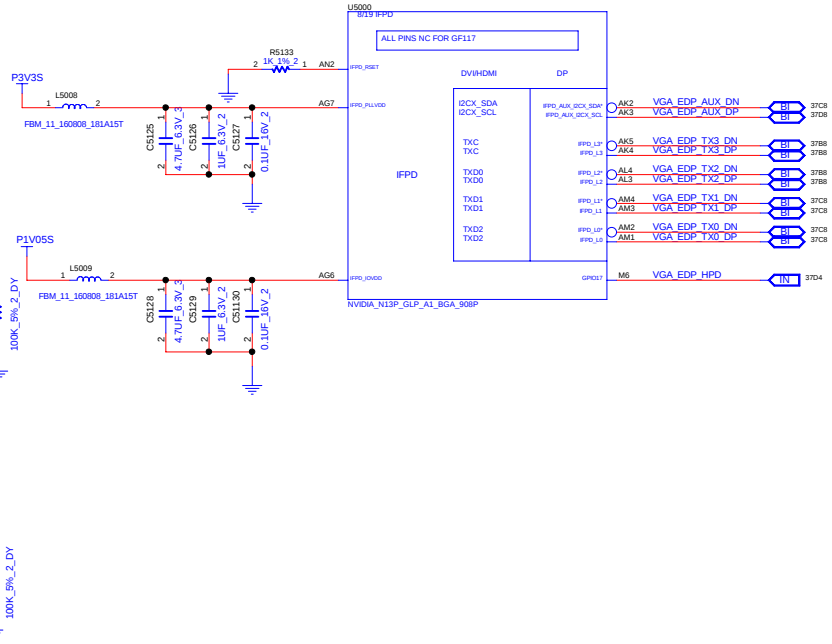
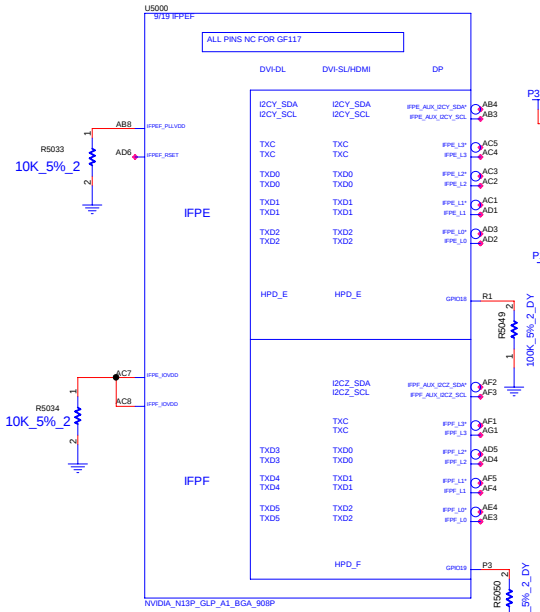
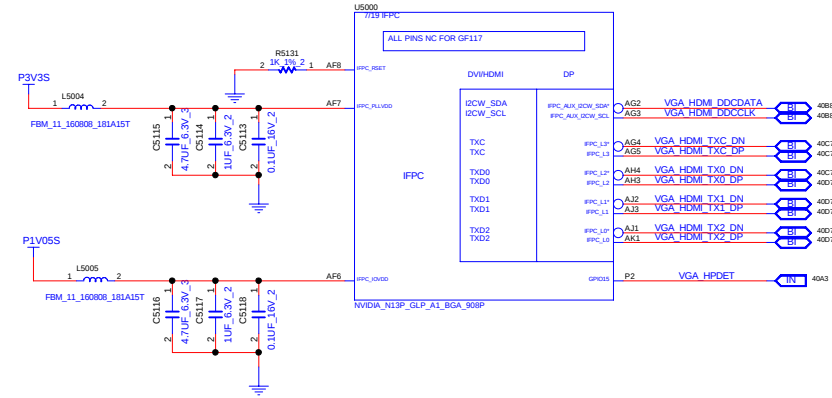
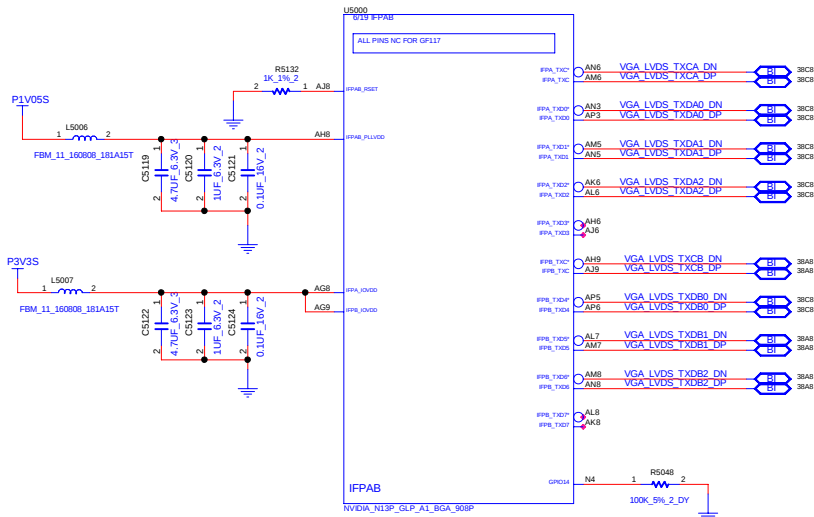
FB_CLAMP
N13P-FS-GU/GLP-NC
N13P-GV-N13M-GS/N13P-GT-GS-LP: PL10K





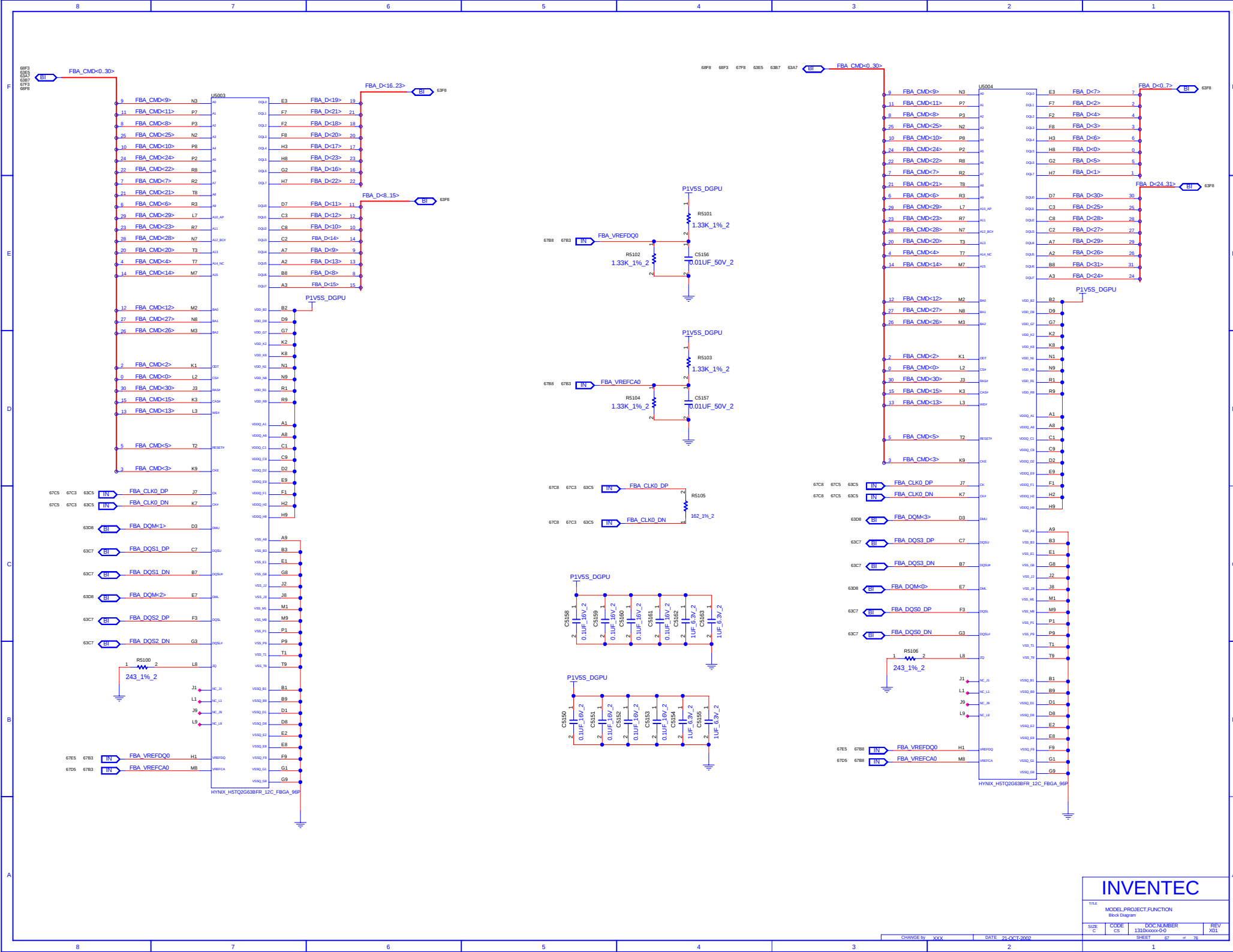
To be configured as needed on the PCB

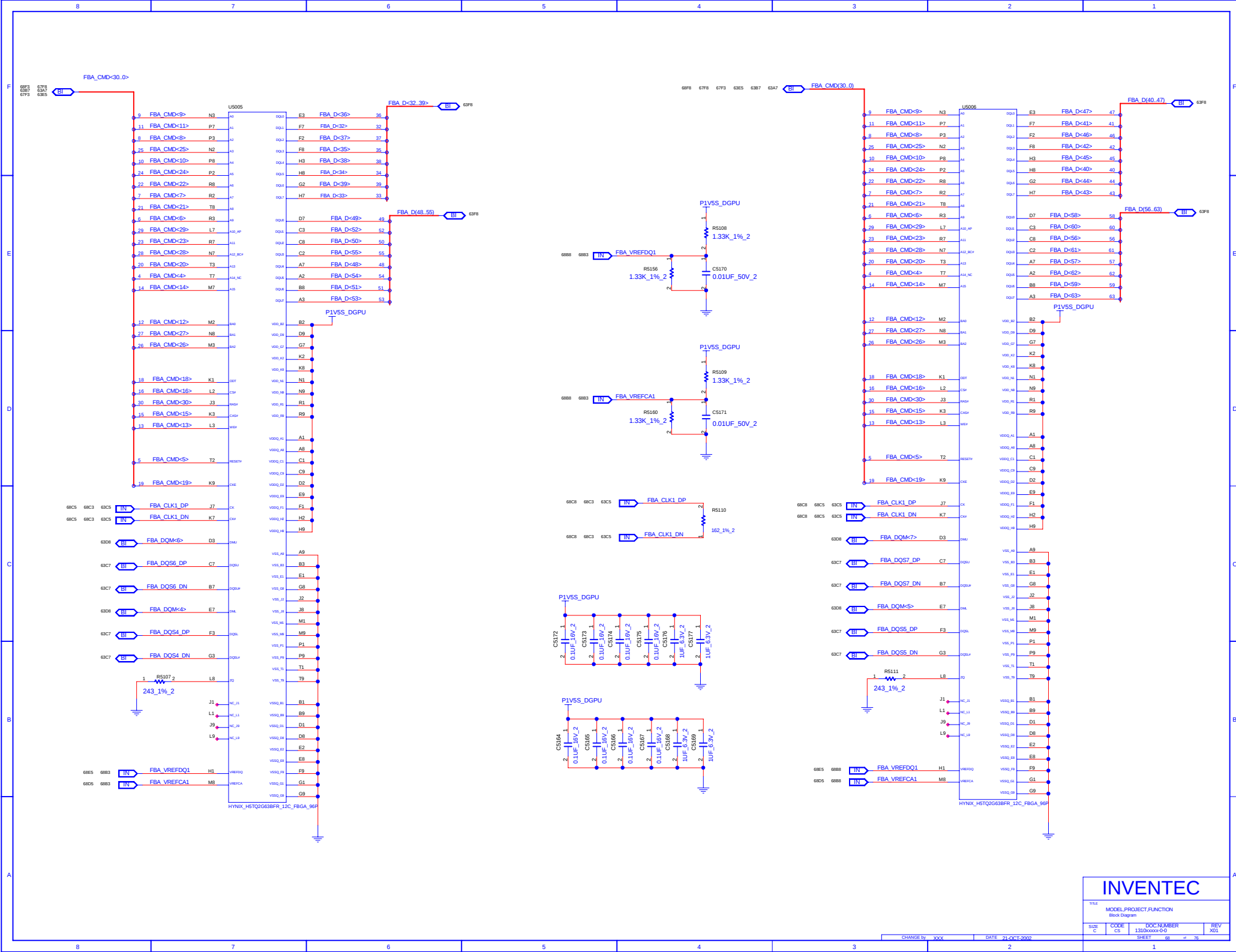


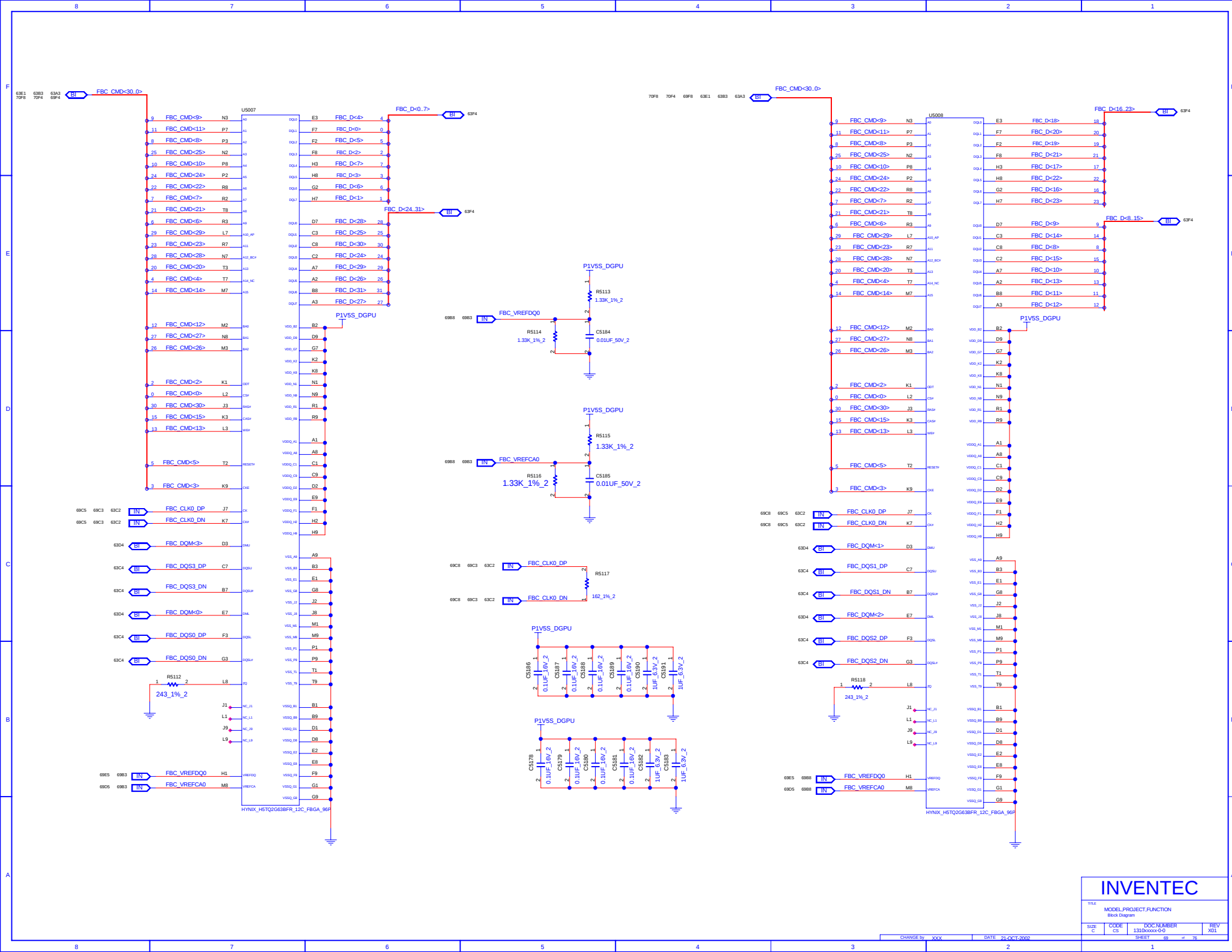


INVENTEC				
TITLE				
MODEL PROJECT FUNCTION				
Block Diagram				
SIZE	CODE	DOC NUMBER	REV	
C	CS	131000000-00	X01	
SHEET 65 of 76				
1				



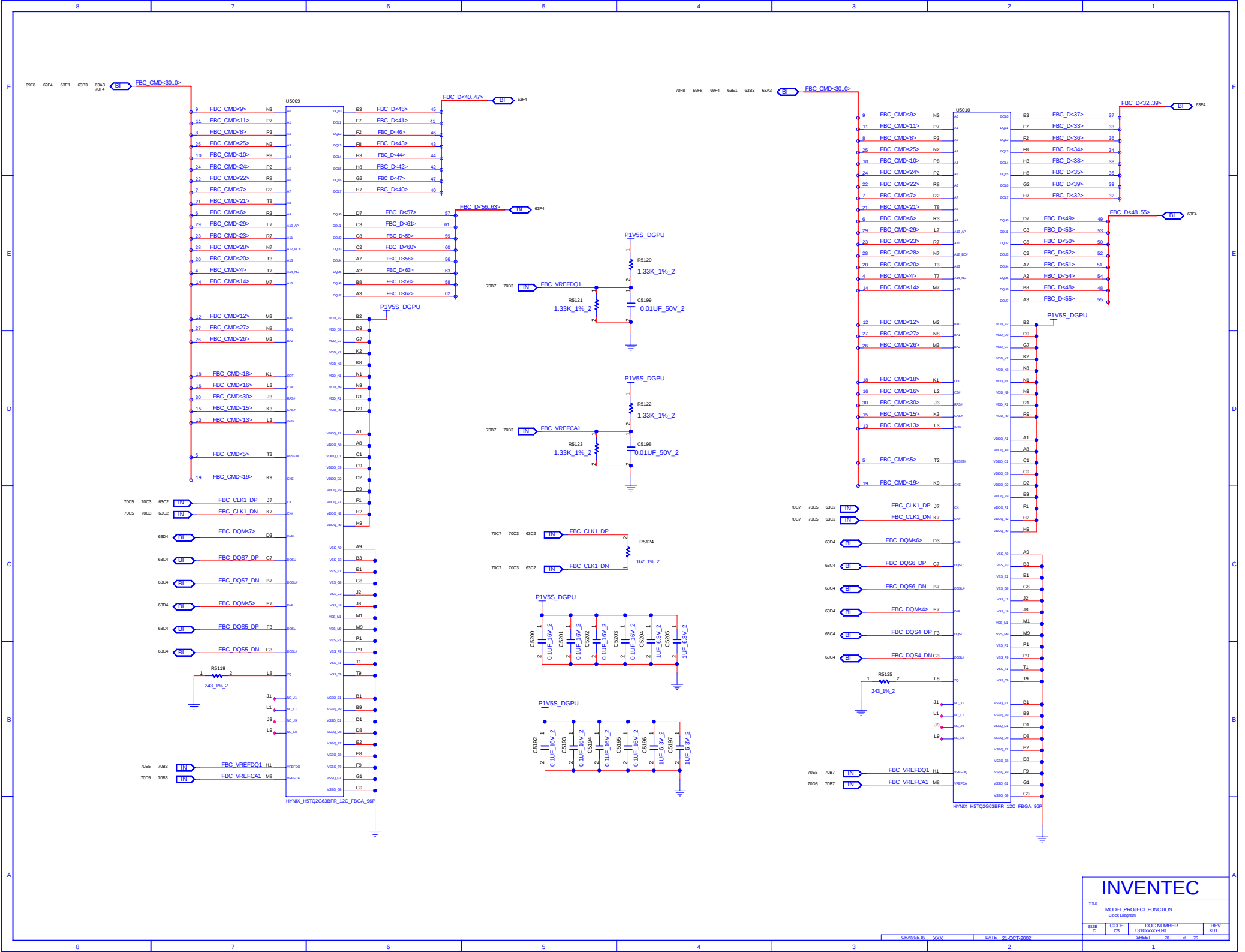






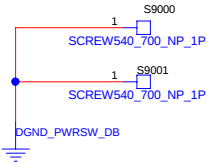
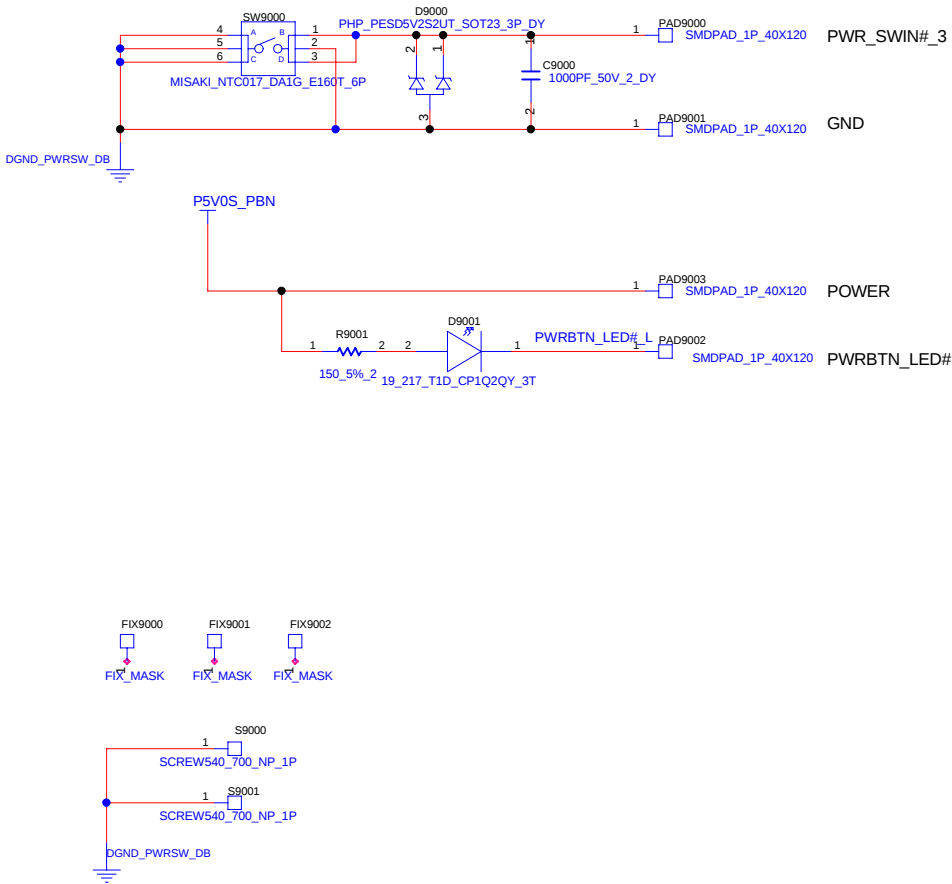
INVENTEC			
TITLE			
MODEL_PROJECT_FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
C	CS	131000000-02	X01
SHEET 89 of 95			

CHANGE by XXXX DATE 21-01-2000



REFERENCE 9000~9999(SMALL BOARD)

POWER BUTTON



INVENTEC

TITLE MODEL PROJECT FUNCTION

Block Diagram

SIZE CODE DOC NUMBER REV

A3 CS 1310xxxxx-0-0 X01

CHANGE by XXX

DATE 21-OCT-2002

SHEET 71 of 76

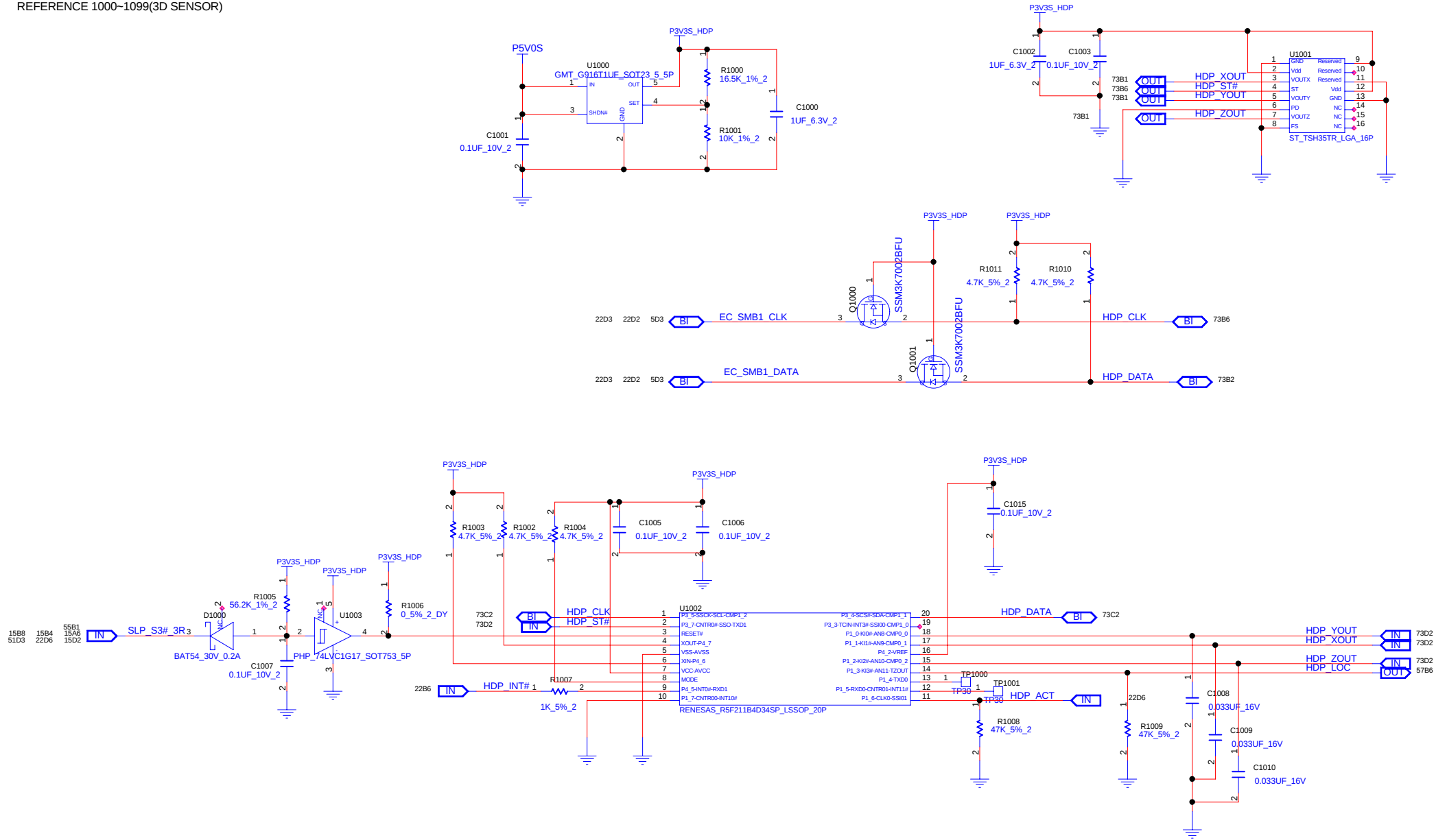
1

8	7	6	5	4	3	2	1
D							D
C							C
B							B
A							A
8	7	6	5	4	3	2	1

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
SHEET 72 of 76			

REFERENCE 1000-1099(3D SENSOR)



INVENTEC

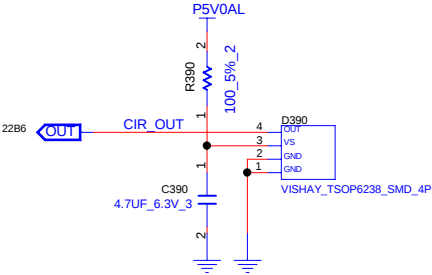
TITLE
MODEL,PROJECT,FUNCTION
Block Diagram
DOC NUMBER
1310xxxx-0-0
REV
X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 73 of 76

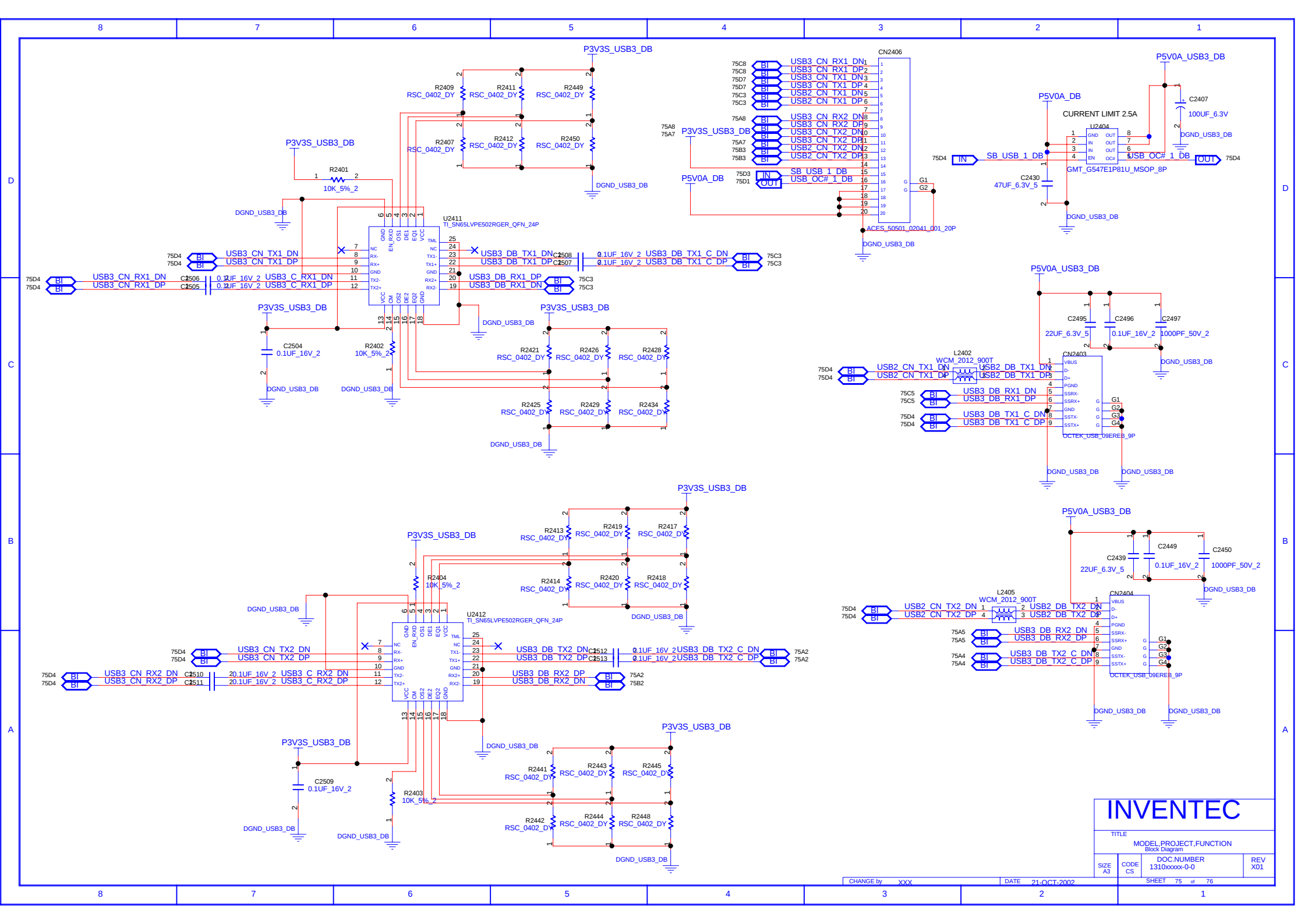
REFERENCE 390~399

CIR



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01



INVENTEC

TITLE

MODEL PROJECT FUNCTION

DOC NUMBER

REV

SIZE

CODE

CS

DATE

21-OCT-2002

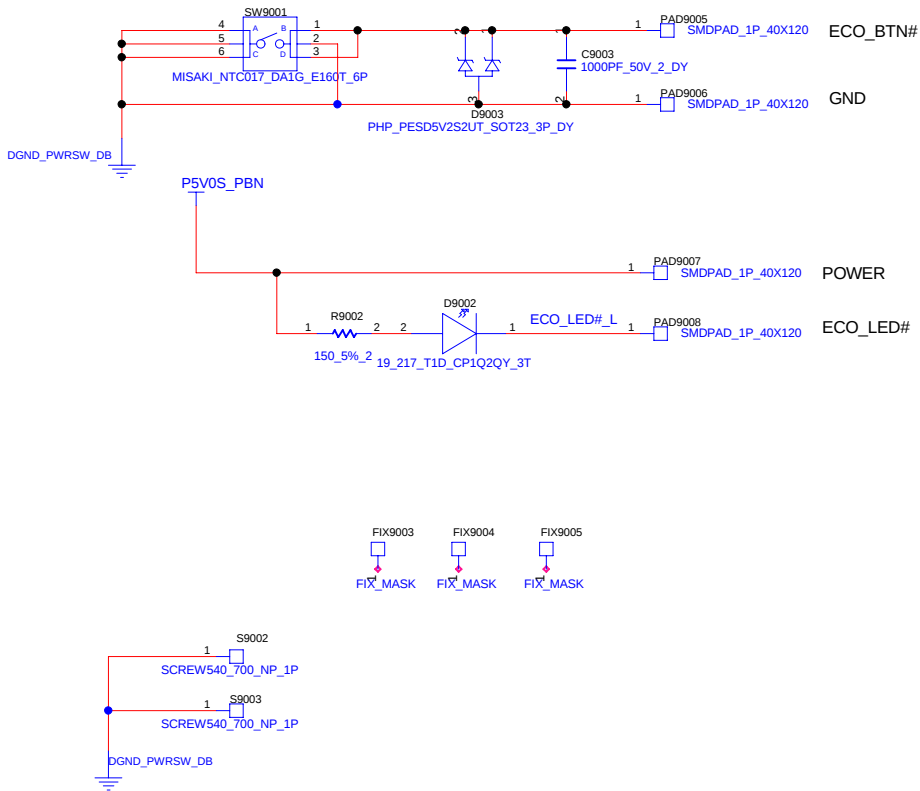
SHEET

75

of

76

3D/ECO VISION



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01