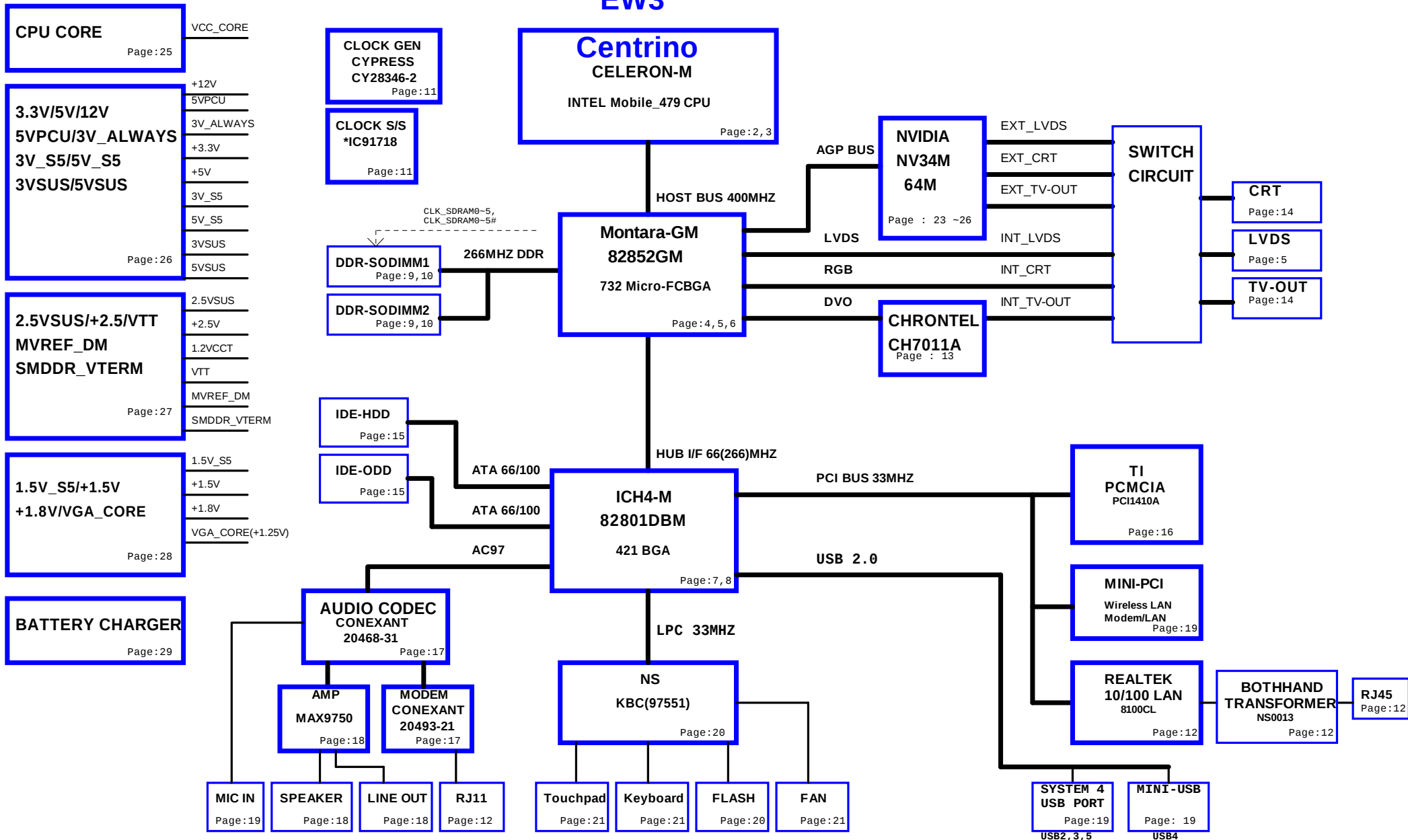




PCI ROUTING TABLE IDSEL INTERRUPT DEVICE

REQ0# / GNT0#	AD18	INTD#	
REQ1# / GNT1#	AD20	INTB# , INTC#	
REQ3# / GNT3#	AD17	INTE#	

REALTEK LAN
MINI-PCI
TI 1410A



PROJECT : EW3
Quanta Computer Inc.

Size	Document Number	Rev
	BLOCK DIAGRAM	B3

Date: Wednesday, October 27, 2004 Sheet 1 of 30

Banias

1 OF 3

REQUEST
PHASE
SIGNALS

DATA
PHASE
SIGNALS

ERROR
SIGNALS

ARBITRATION
PHASE
SIGNALS

SNOOP PHASE
SIGNALS

RESPONSE
PHASE
SIGNALS

PC COMPATIBILITY
SIGNALS

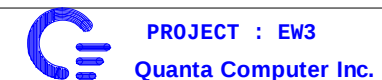
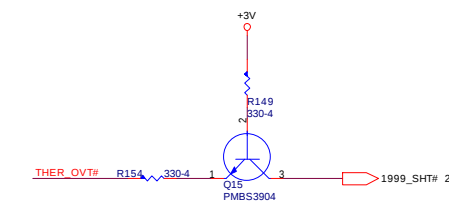
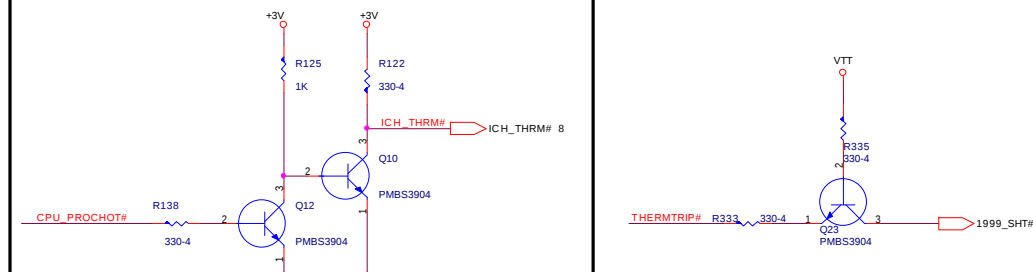
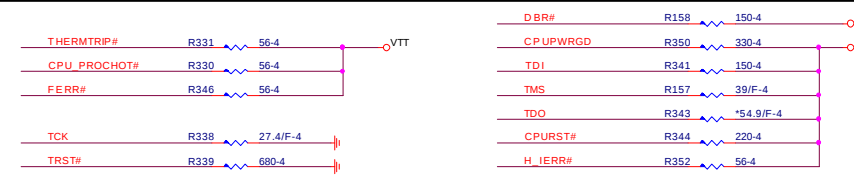
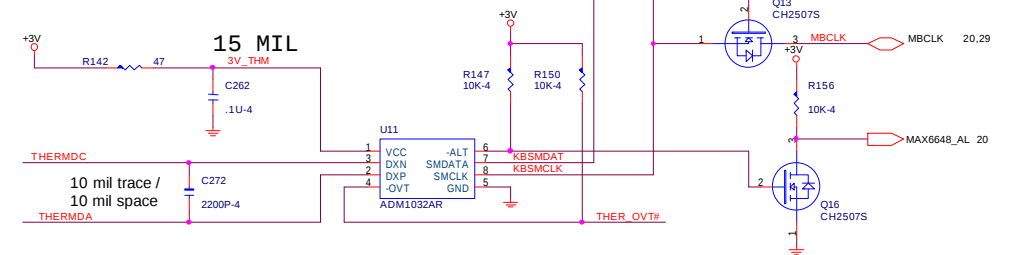
DIAGNOSTIC
& TEST
SIGNALS

EXECUTION
CONTROL
SIGNALS

THERMAL DIODE

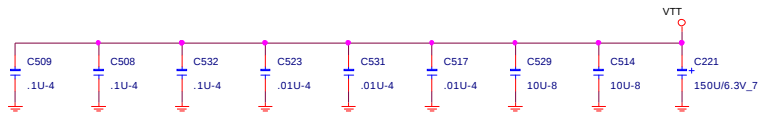
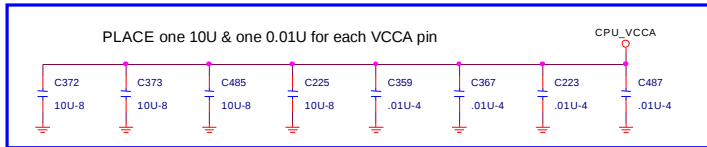
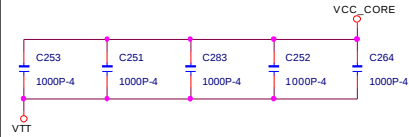
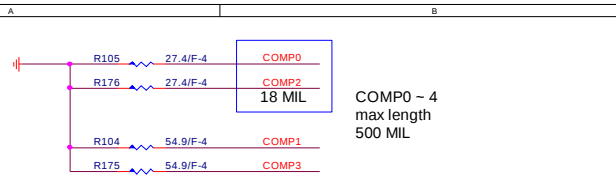
Banias_Processor

H/W MONITOR

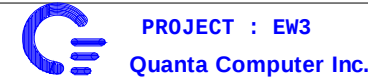
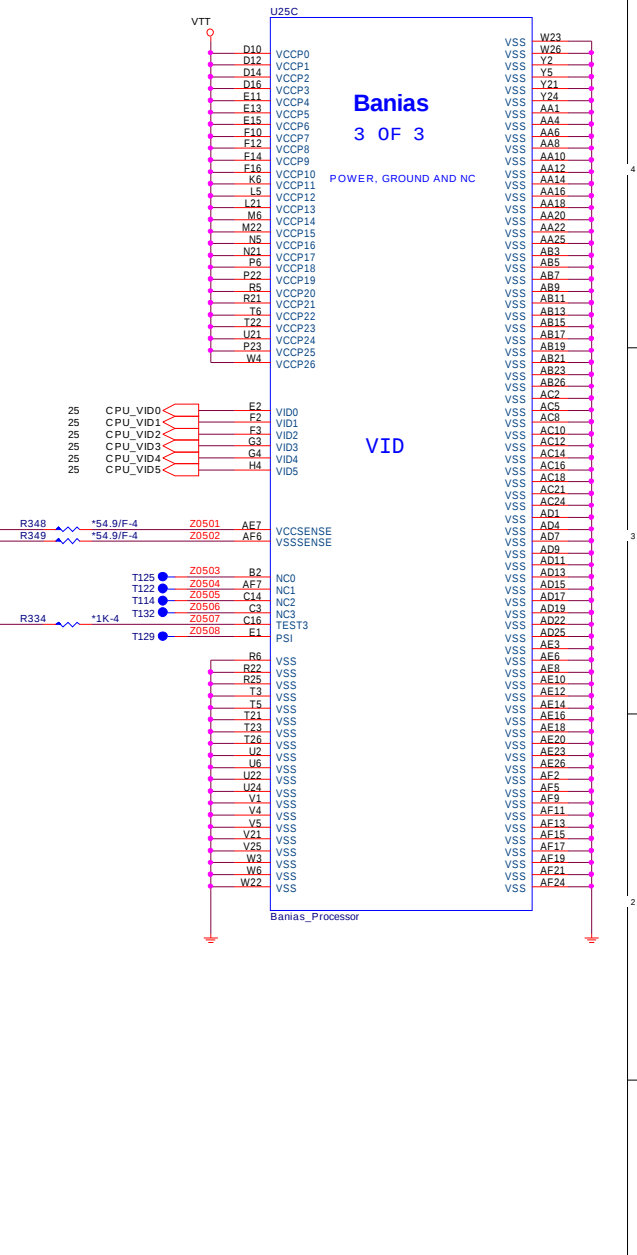
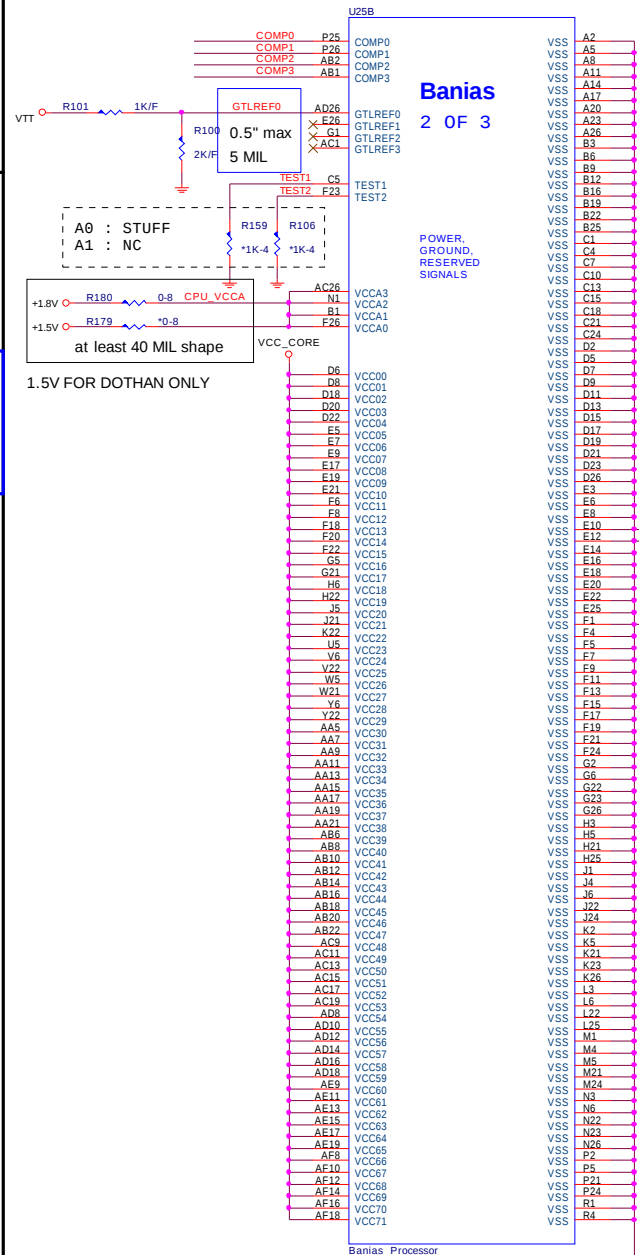
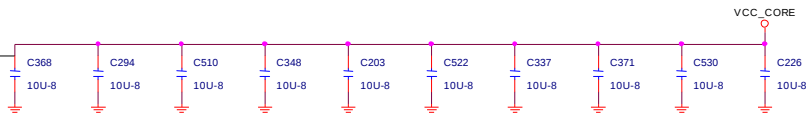
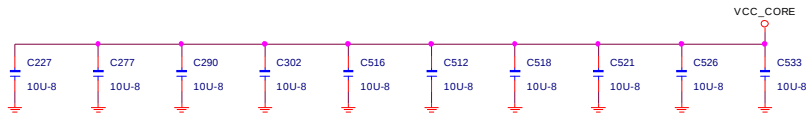
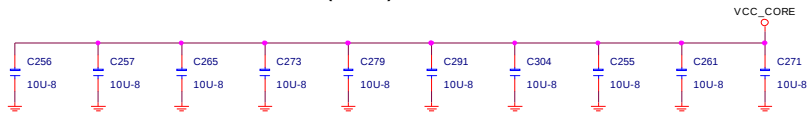


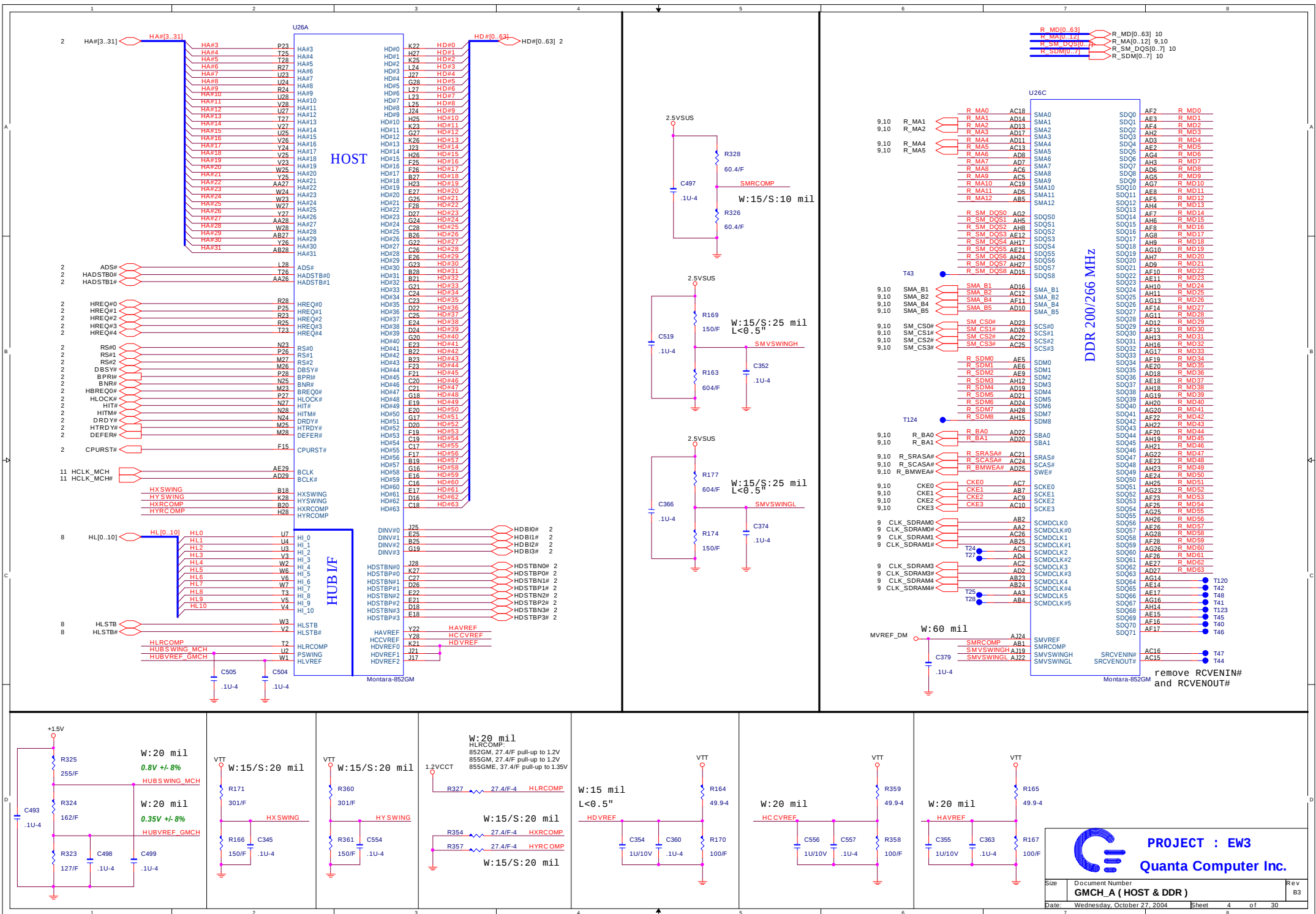
PROJECT : EW3
Quanta Computer Inc.

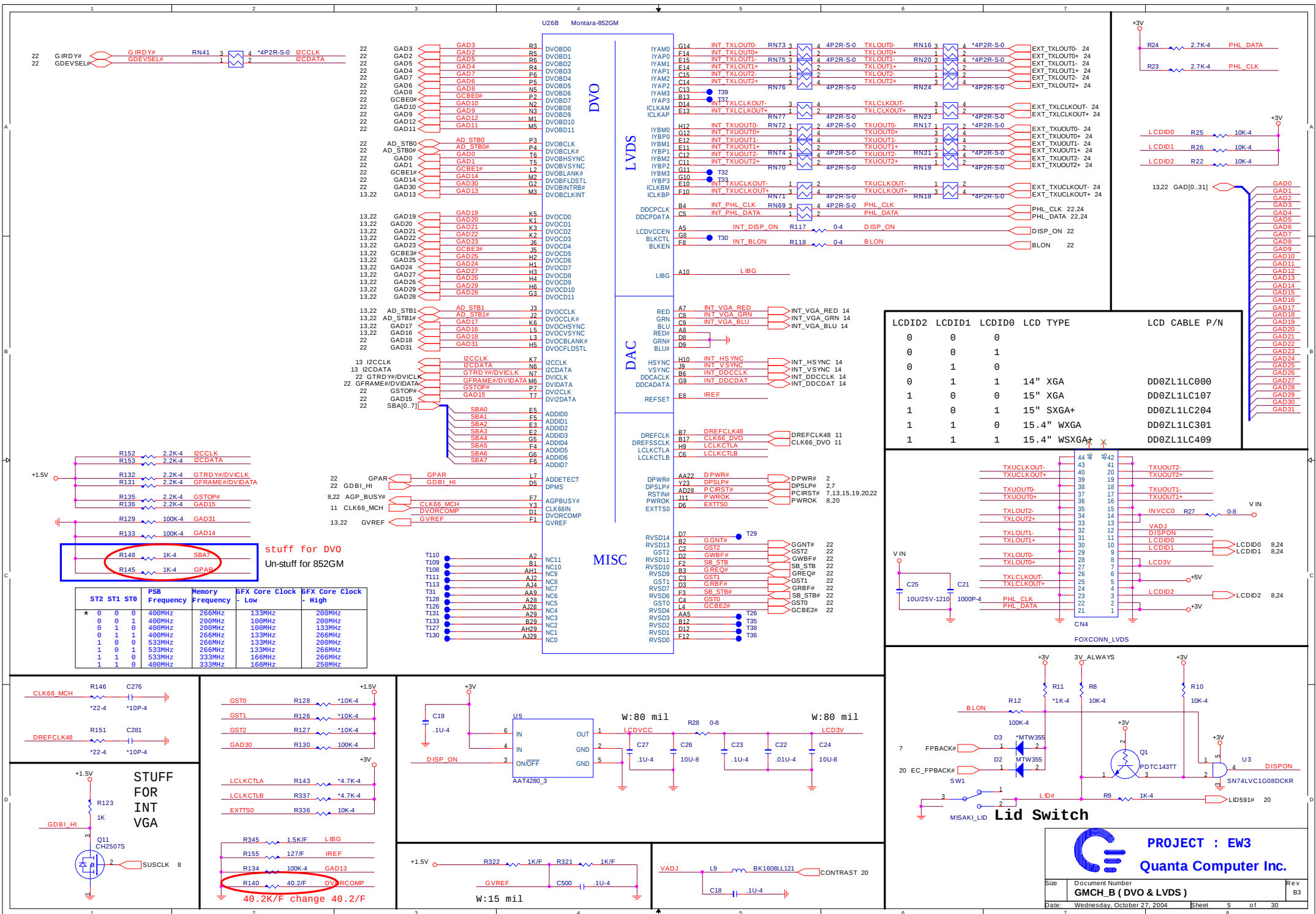
Size	Document Number	Rev
	CPU (HOST BUS)-1	B3
Date:	Wednesday, October 27, 2004	Sheet 2 of 30

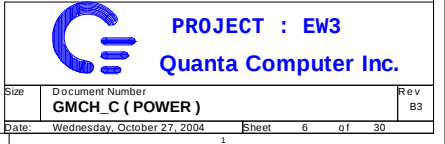


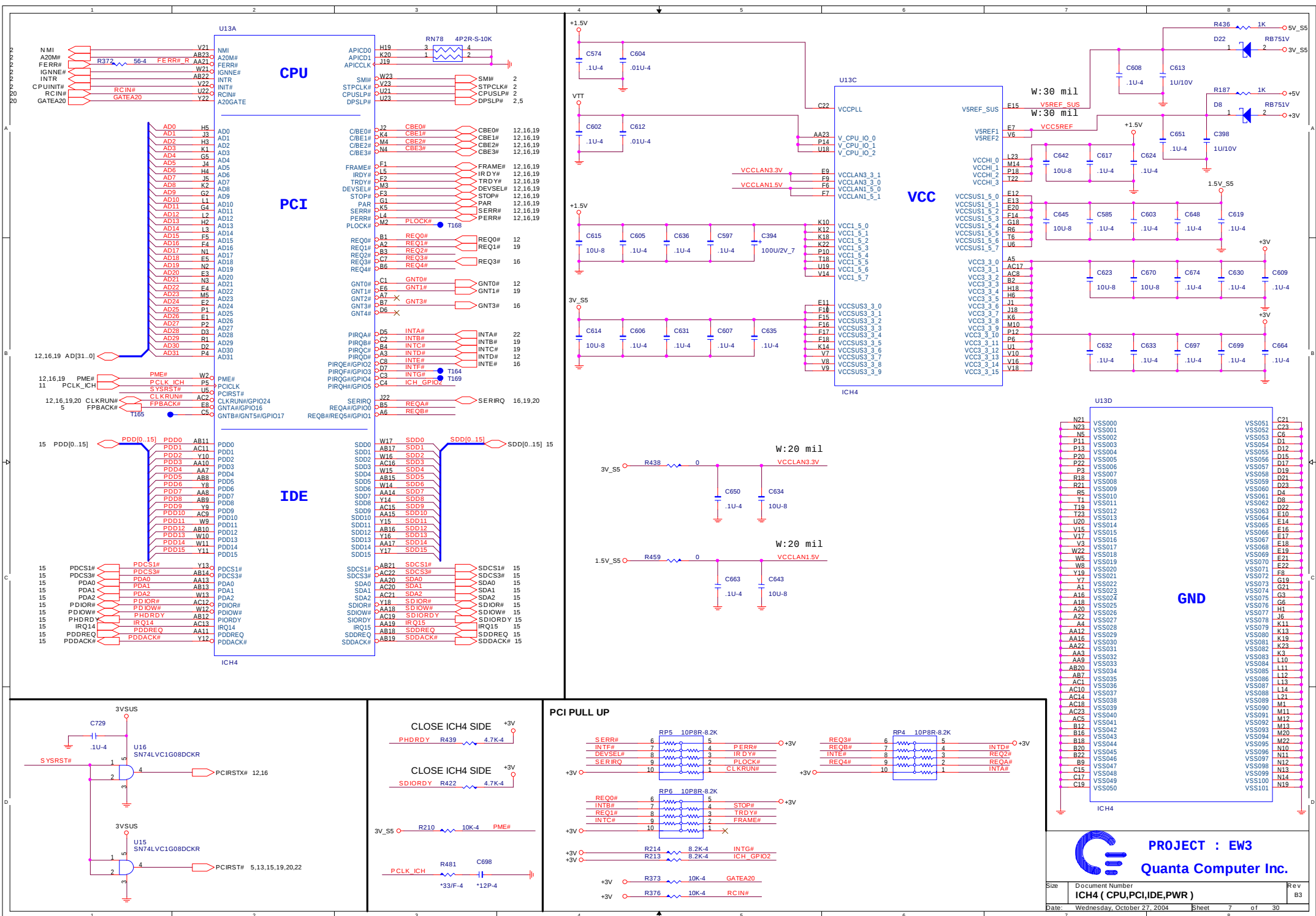
10U/6.3V/X5R(CC0805) *30

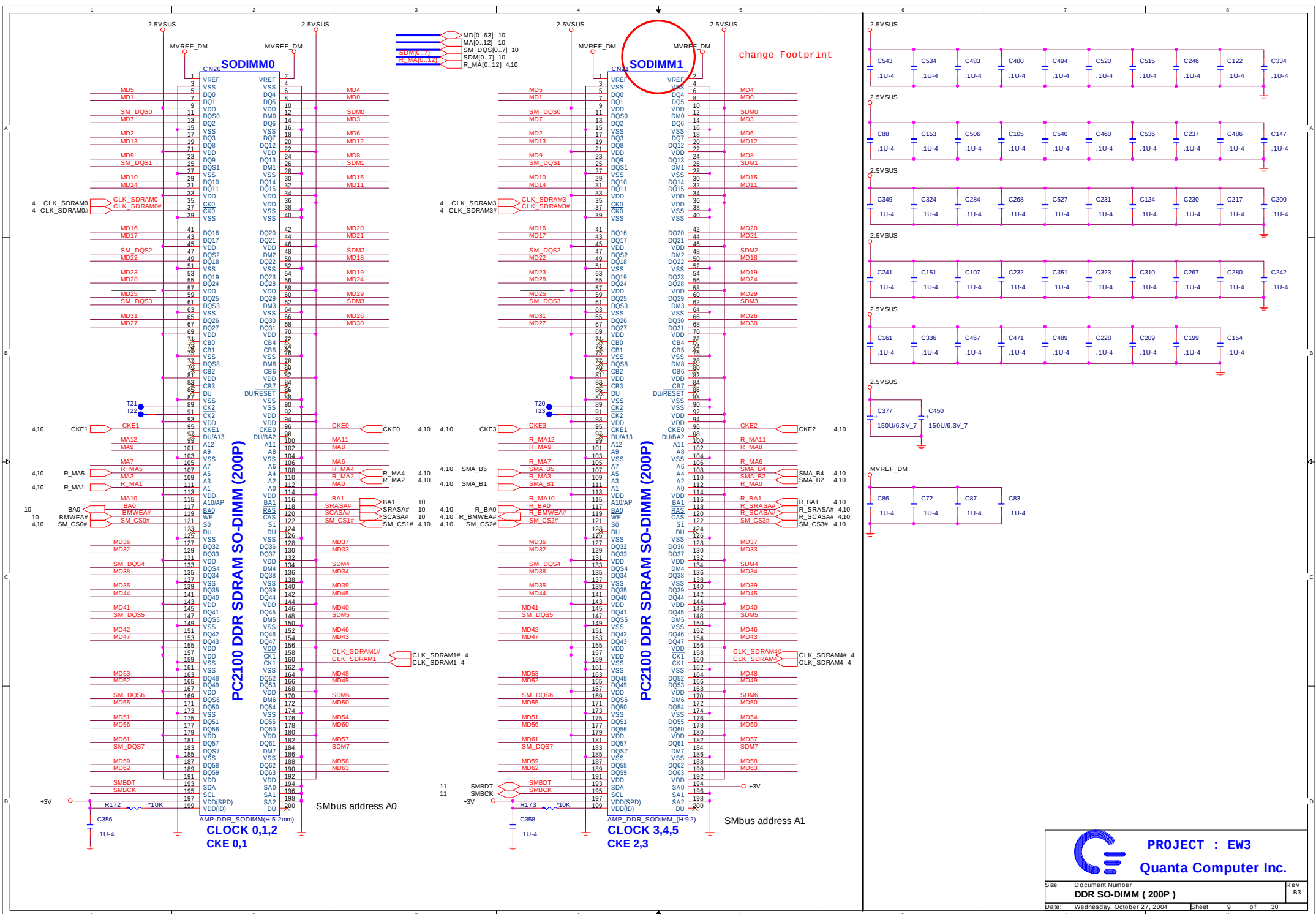




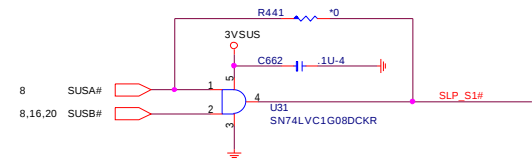








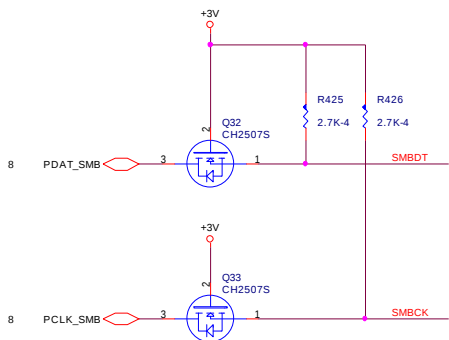
S2	S1	S0	CPU	3V66[0..4]	3V66_5/66IN
1	0	0	66	66IN	66 Input
1	0	1	100	66IN	66 Input
1	1	0	200	66IN	66 Input
1	1	1	133	66IN	66 Input
0	0	0	66	66	66
0	0	1	100	66	66
0	1	0	200	66	66
0	1	1	133	66	66



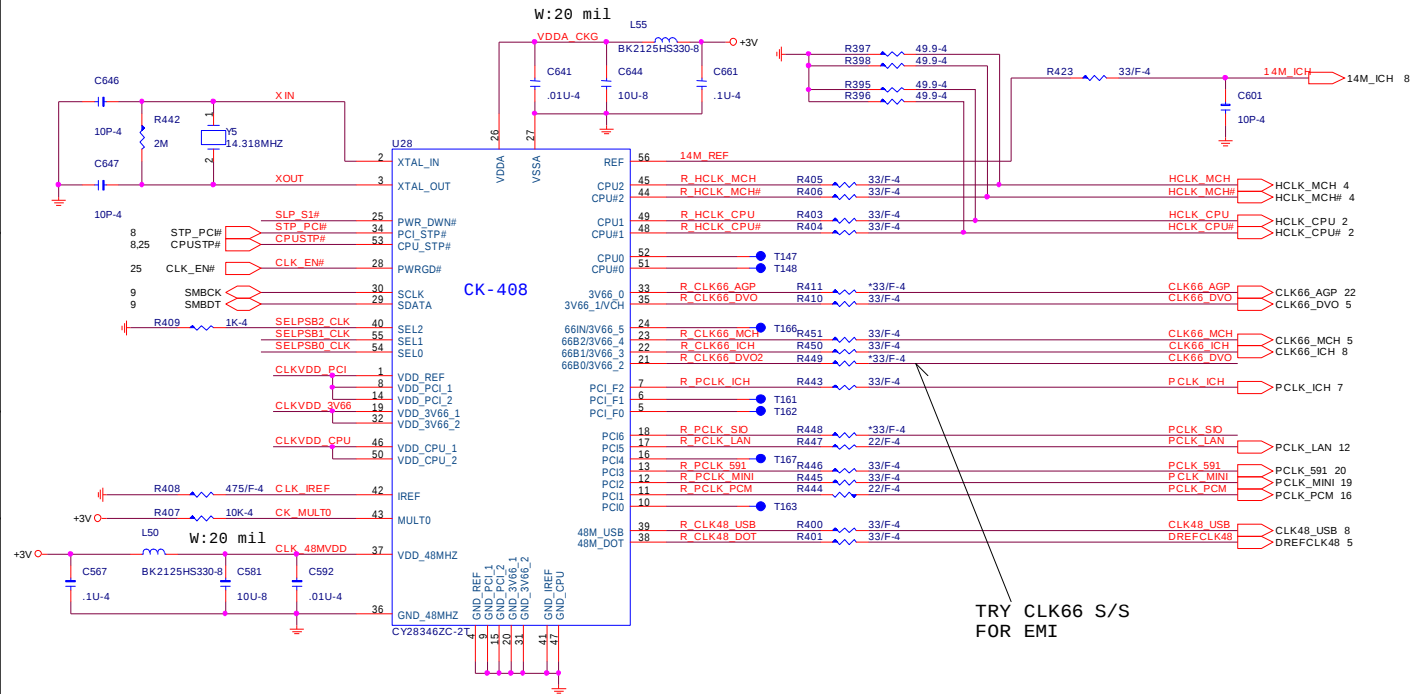
3V66_1/VCH
SMBus Byte 0 , Bit 5 = 0 66 MHz W S/S
SMBus Byte 0 , Bit 5 = 1 48 MHz W/O S/S

Byte 0 , Bit 7 = 0 Disable Spread Spectrum
Byte 0 , Bit 7 = 1 Enable Spread Spectrum

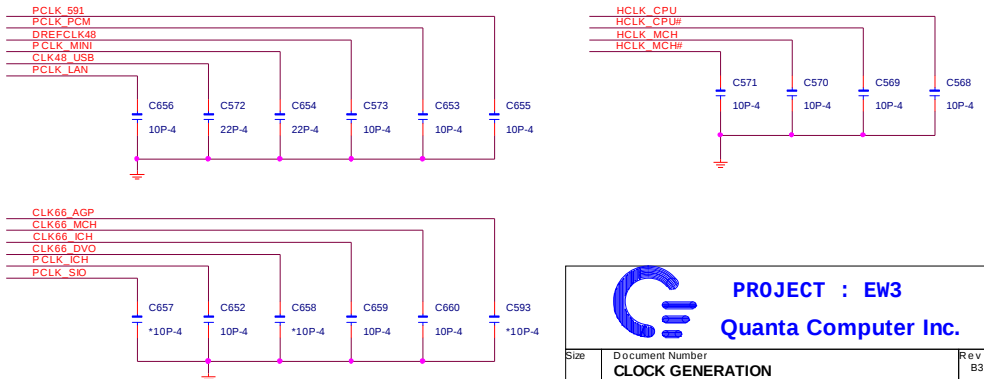
Byte 4 Bit 7	Byte 5 Bit 7	Byte 5 Bit 6	Spread Mode	Spread %
SS2	SS1	SS0		
0	0	0	DOWN	+0.00 , -0.25
0	0	1	DOWN	+0.00 , -0.50
0	1	0	DOWN	+0.00 , -0.75
0	1	1	DOWN	+0.00 , -1.00
1	0	0	CENTER	+0.13 , -0.13
1	0	1	CENTER	+0.25 , -0.25
1	1	0	CENTER	+0.37 , -0.37
1	1	1	CENTER	+0.50 , -1.50



These are for backdrive issue

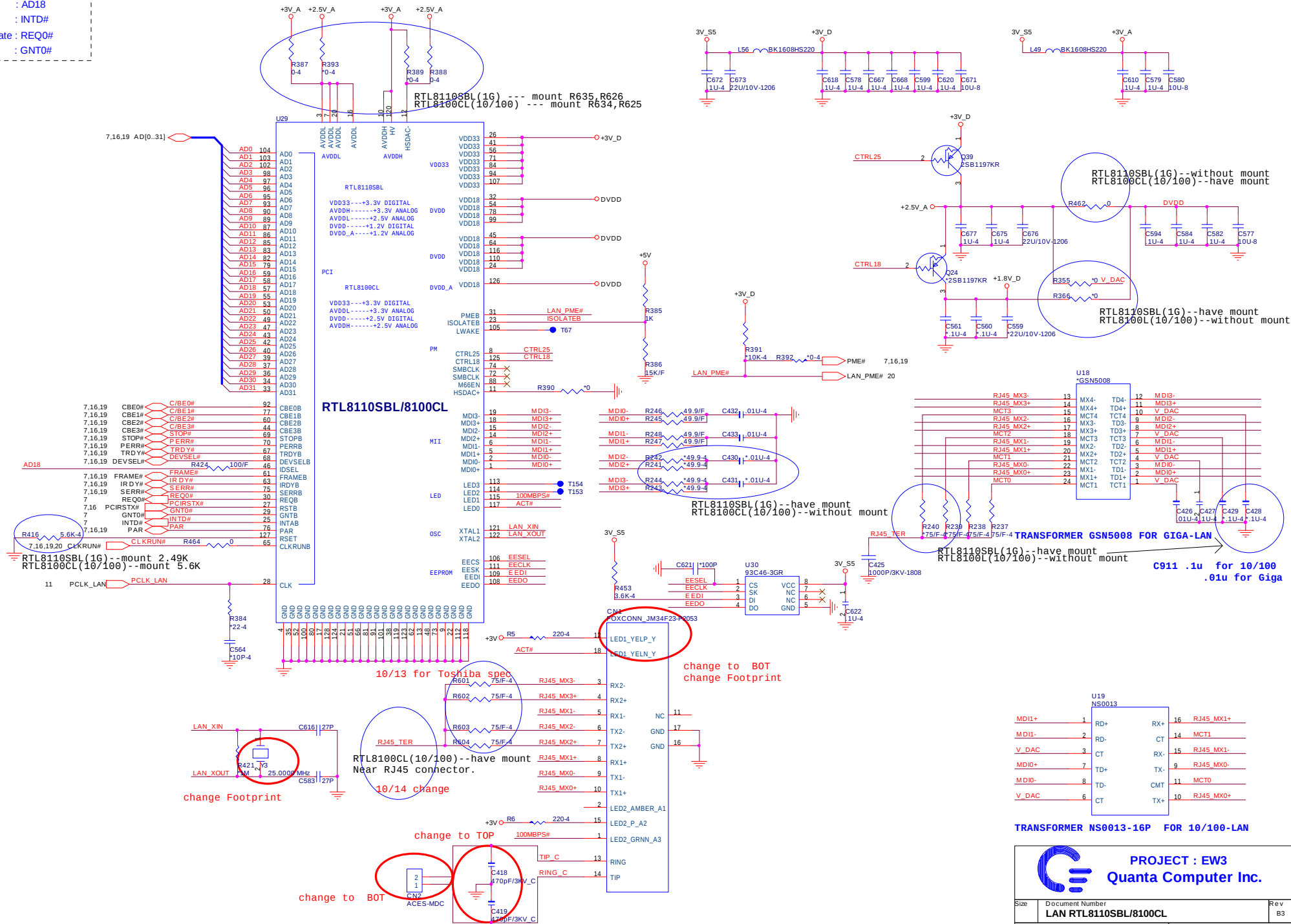


TRY CLK66 S/S
FOR EMI

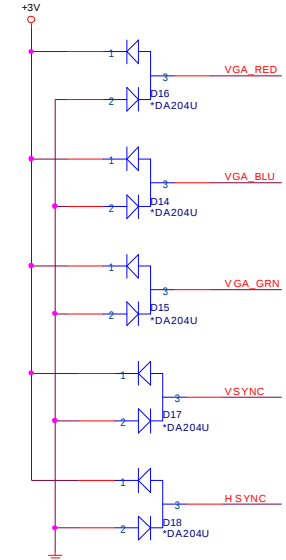
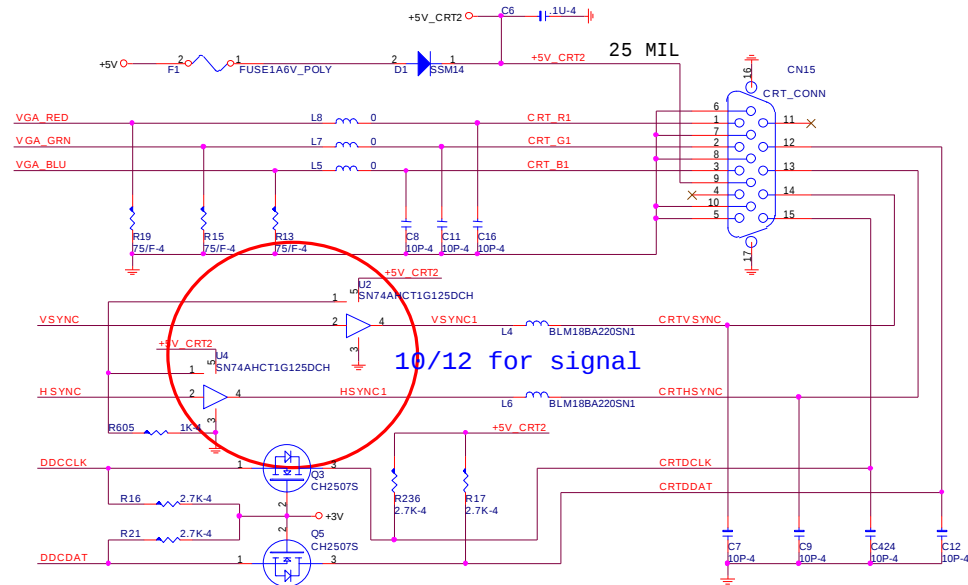
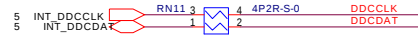
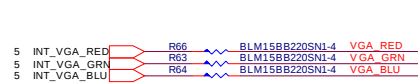


PROJECT : EW3
Quanta Computer Inc.

ID Select : AD18
Interrupt Pin : INTD#
Request Indicate : REQ0#
Grant Indicate : GNT0#



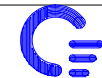
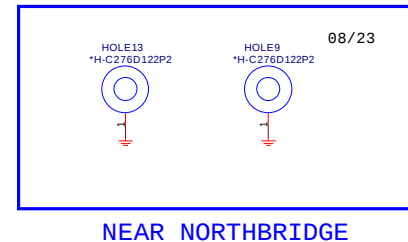
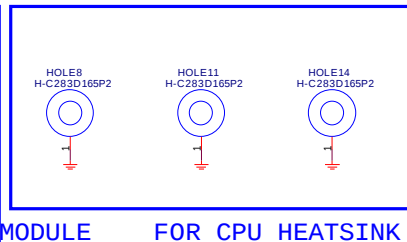
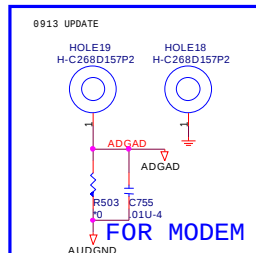
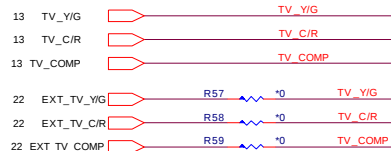
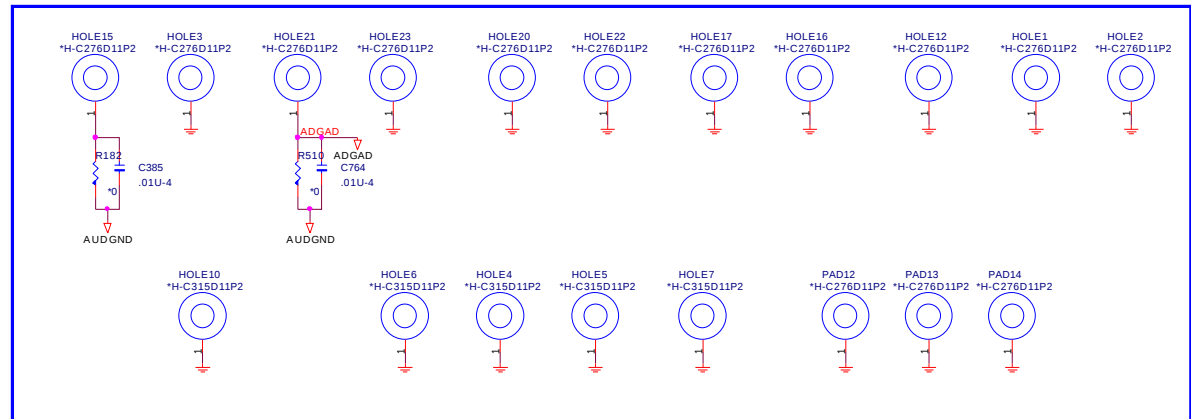
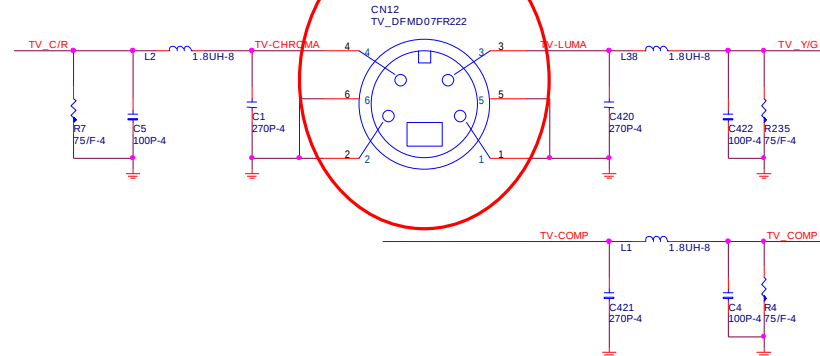
CRT PORT



FOR M/B HOLE

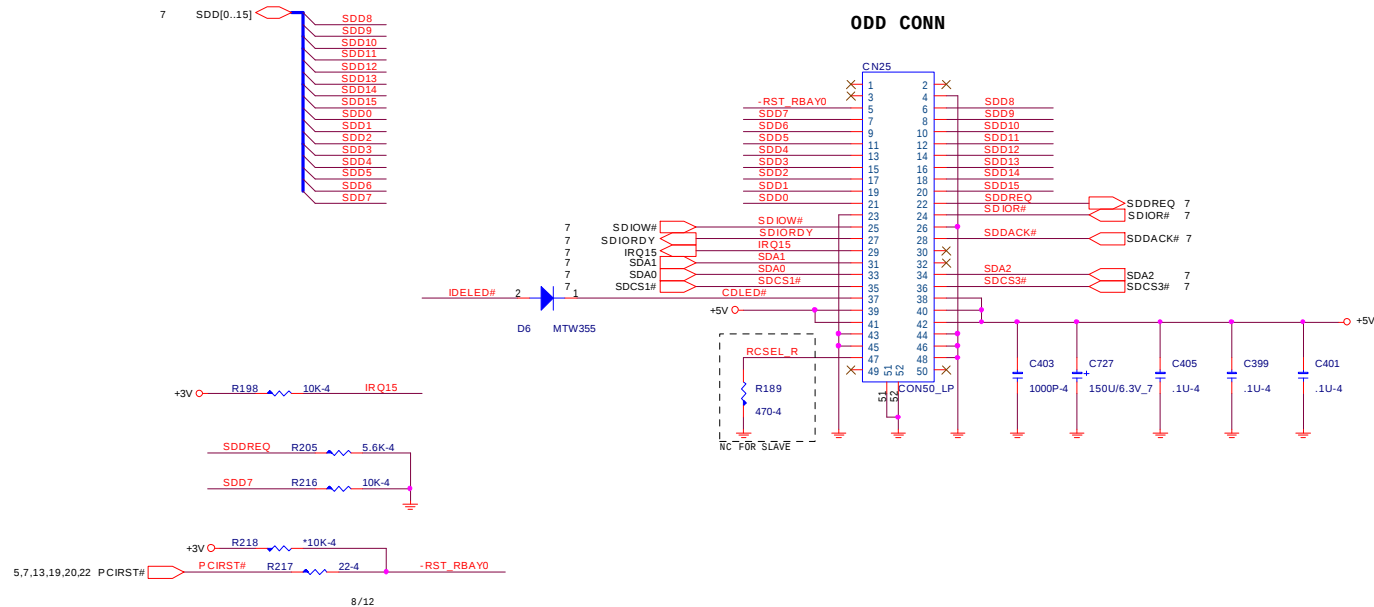
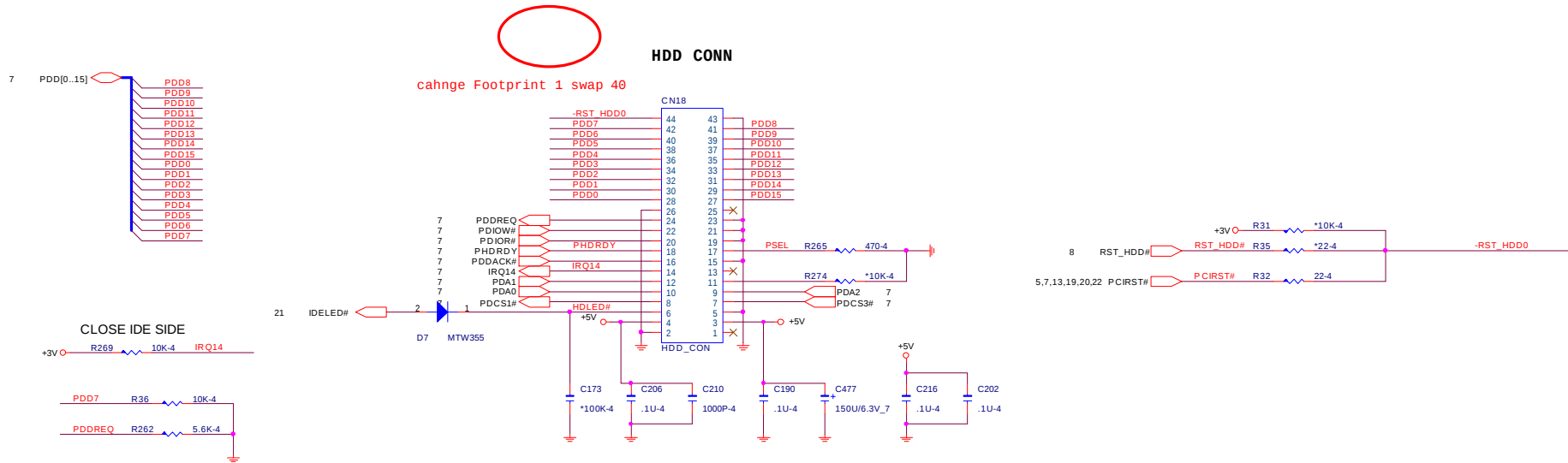
10/8 Spec change

S-VIDEO

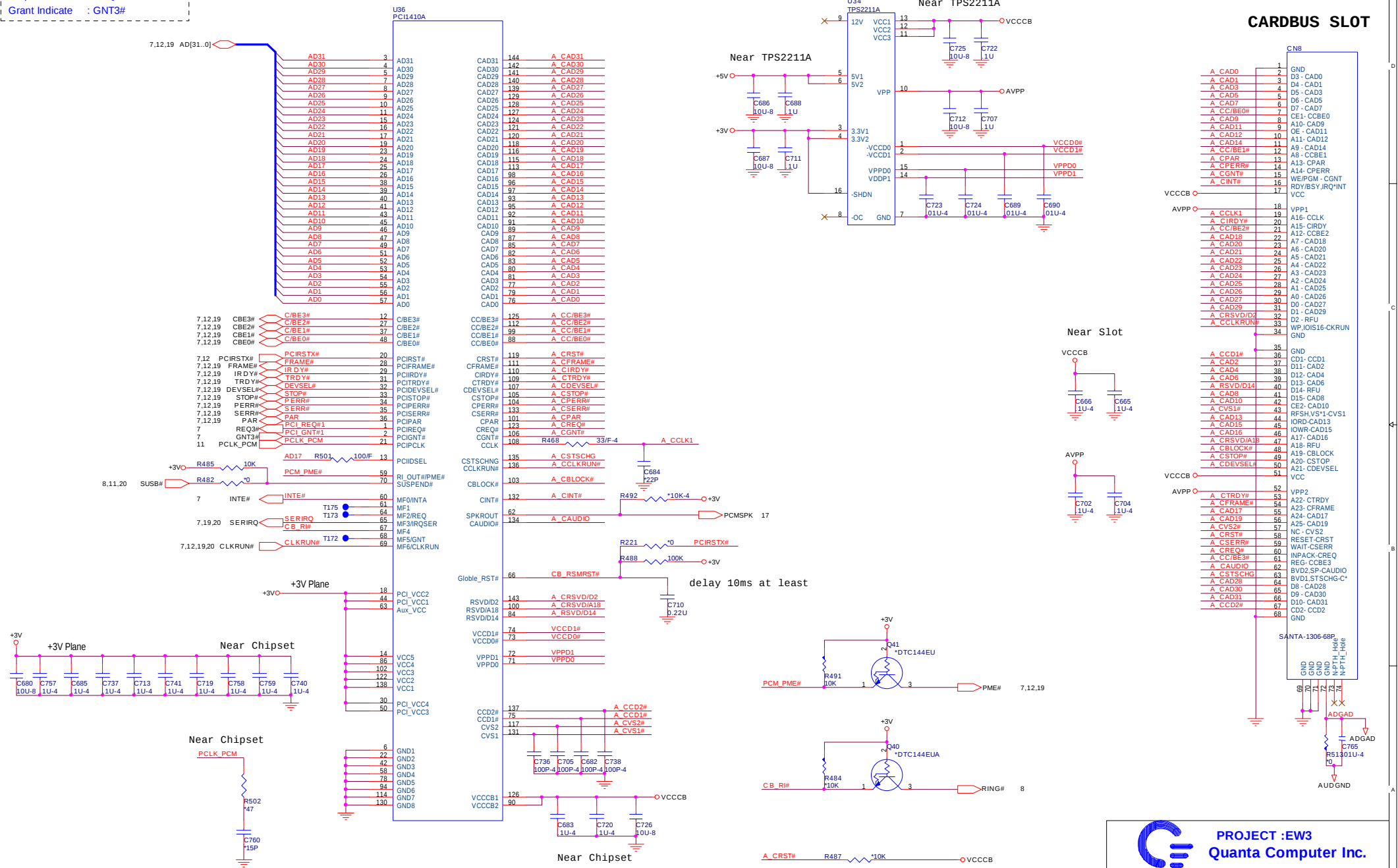


PROJECT : EW3
Quanta Computer Inc.

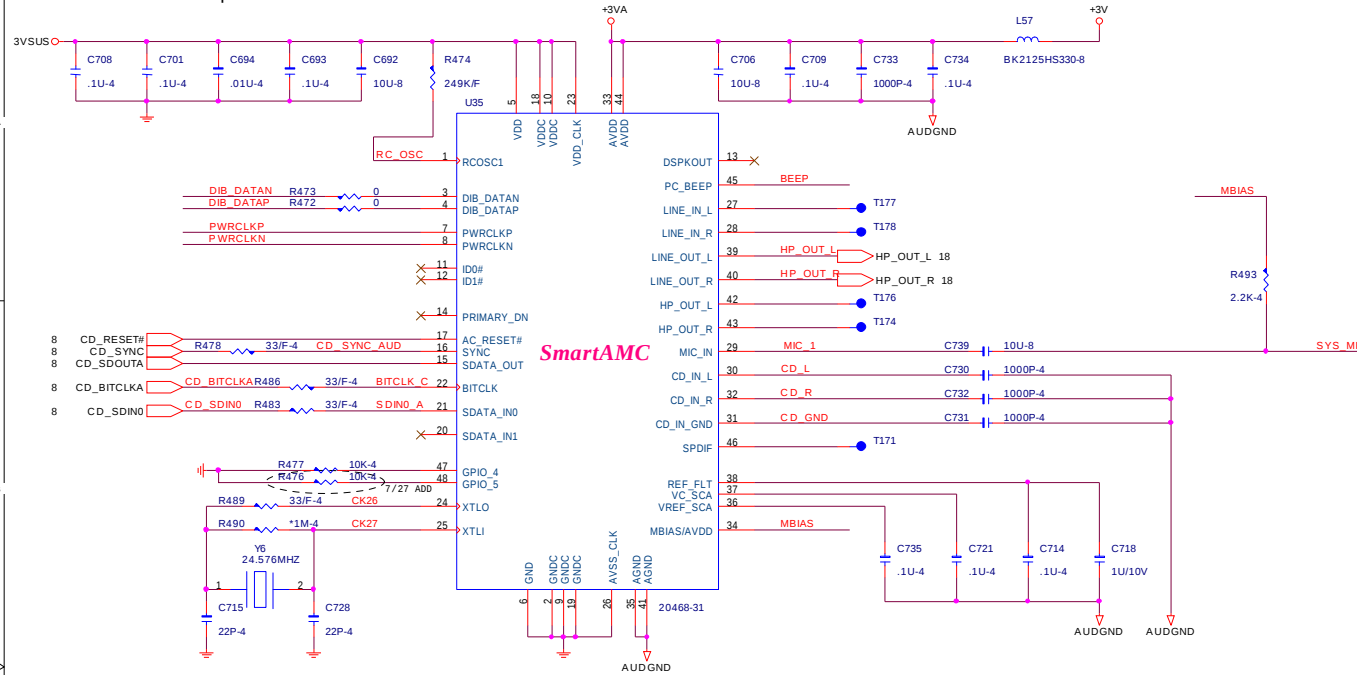
Size	Document Number	Rev
	CRT & HOLES	B3
Date: Wednesday, October 27, 2004	Sheet 14 of 30	



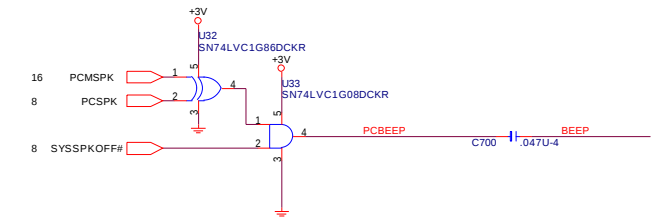
ID Select : AD17
Interrupt Pin : INTE#
Request Indicate : REQ3#
Grant Indicate : GNT3#



Update 0903

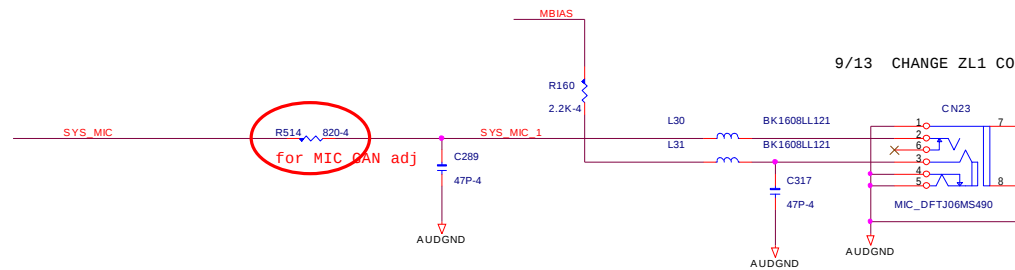


BEEP

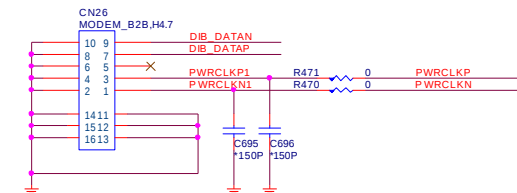


MIC

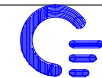
9/13 CHANGE ZL1 CONN



MODEM



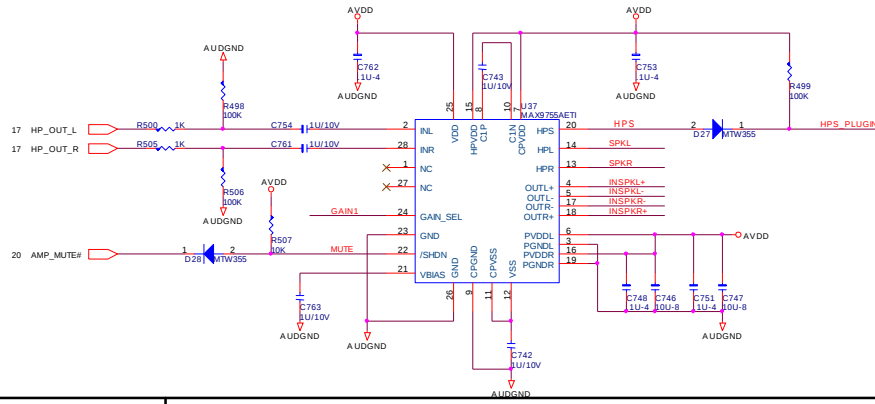
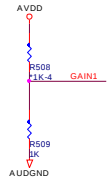
9/3 ADD MODEM Connect



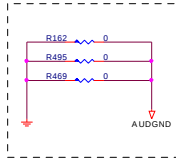
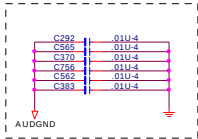
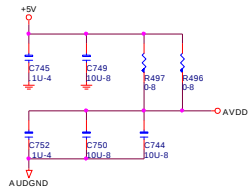
PROJECT : EW3
Quanta Computer Inc.

Size	Document Number	Rev
	CODEC & LINE IN & MIC	B3
Date:	Wednesday, October 27, 2004	Sheet 17 of 30

GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0



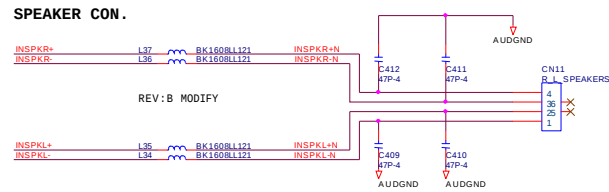
AMP POWER



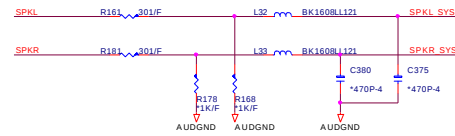
SPEAKER CON.



REV: B MODIFY

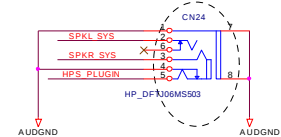


LINE-OUT



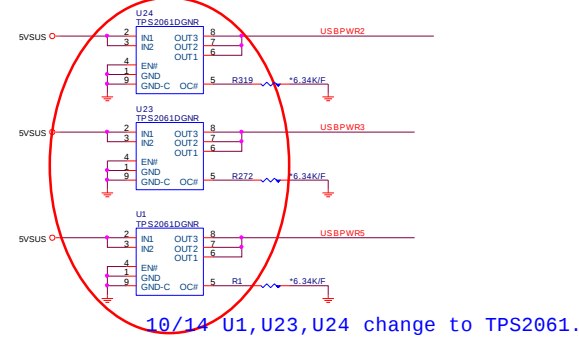
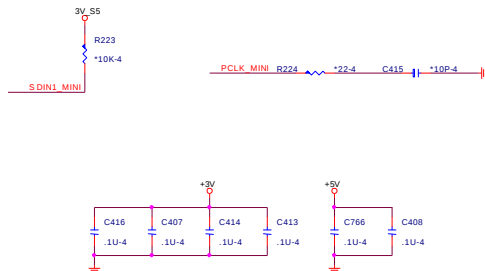
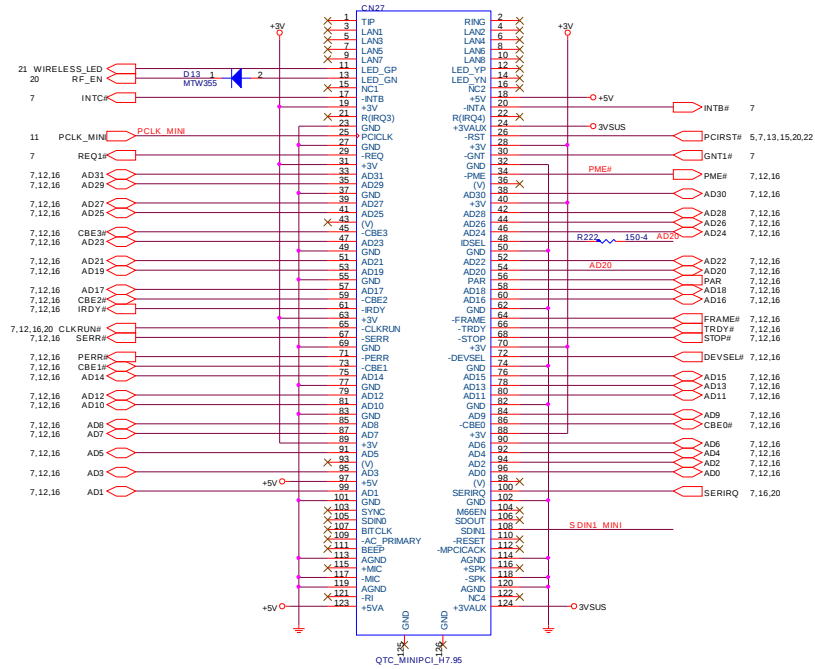
9/13 CHANGE ZL1 CONN

LINE OUT

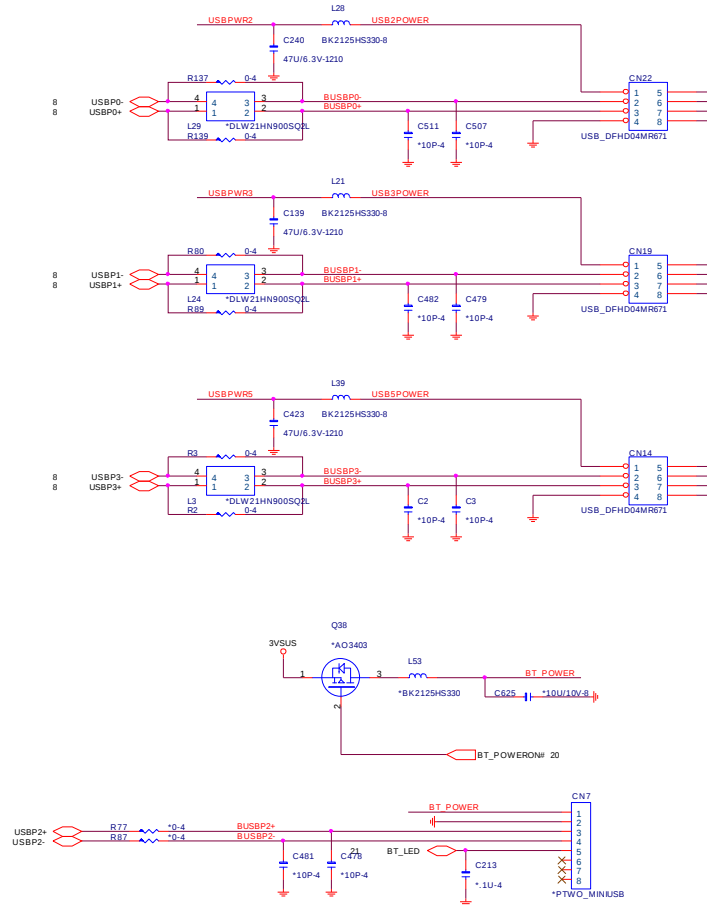


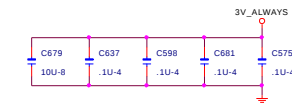
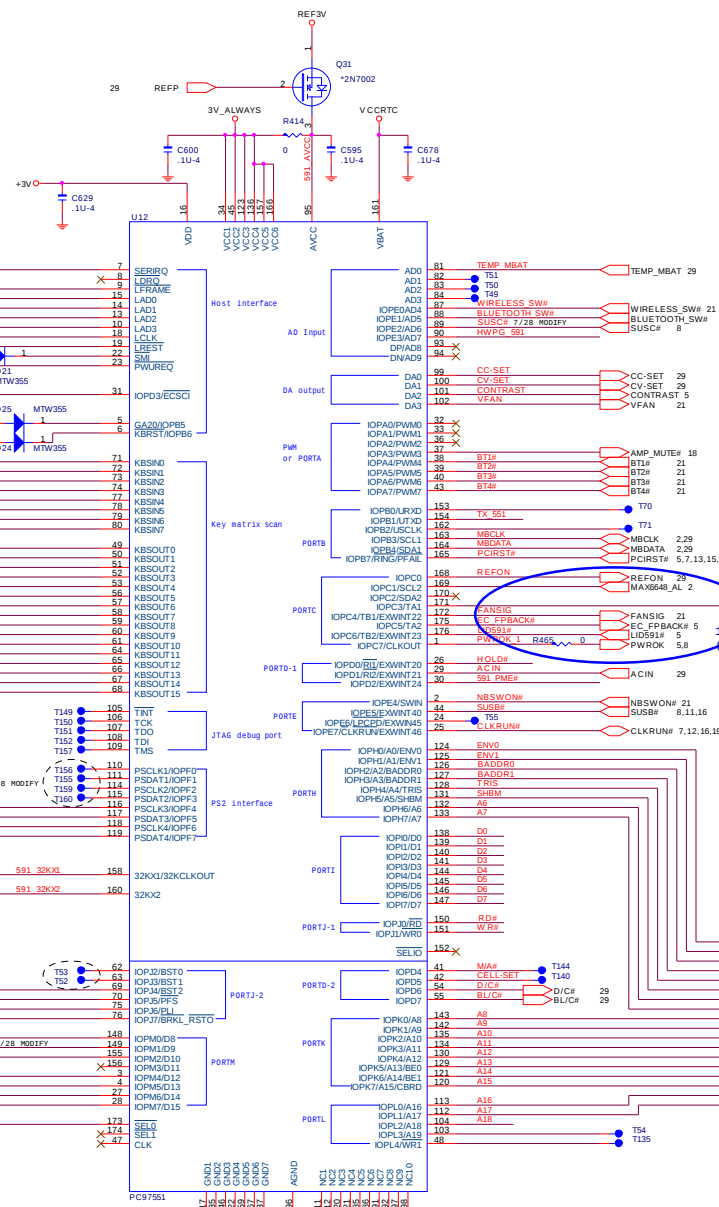
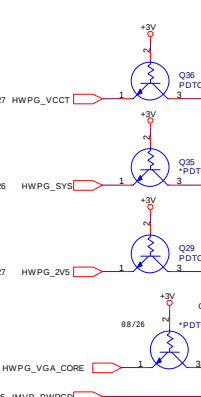
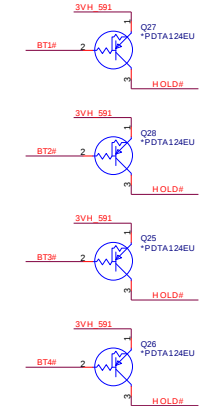
ID Select : AD20
 Interrupt Pin : INTB# , INTCH#
 Request Indicate : REQ1#
 Grant Indicate : GNT1#

MINI-PCI



10/14 U1,U23,U24 change to TPS2061.





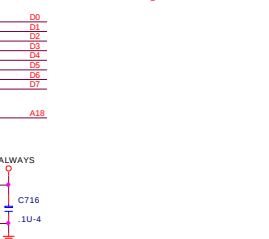
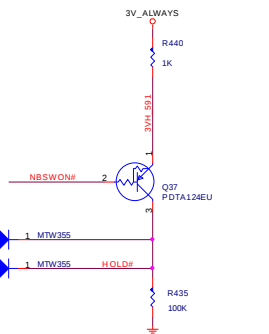
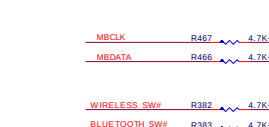
ENV1 R452 *10K-4 3V_ALU

BADDR0 R455 *10K-4

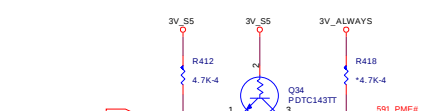
BADDR1 R461 *10K-4

SHRM R463 10K-4

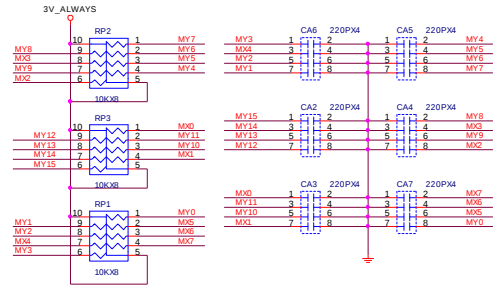
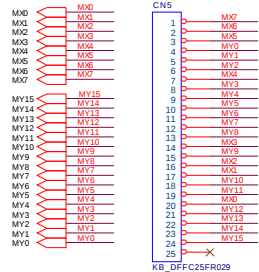
I/O Address	
BADDR1-0	Data
0 0	2E
0 1	4E
1 0	(HCFGBAH, HCFGBAL)(HCFGBAH, HCFGBAL)
1 1	Reserved



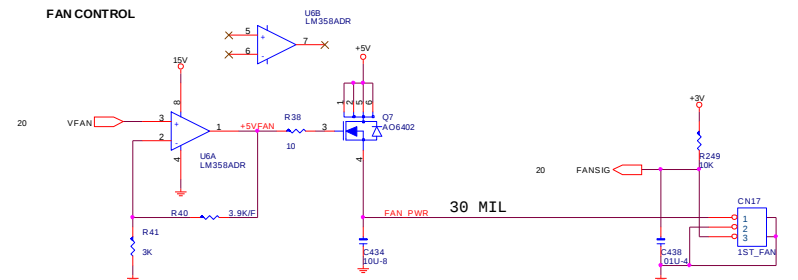
BIU configuration should match flash speed used



INT K/B

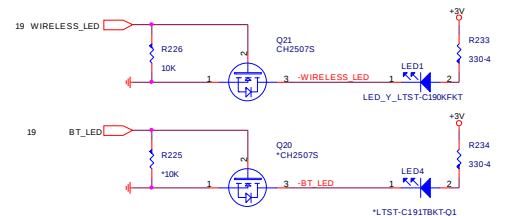
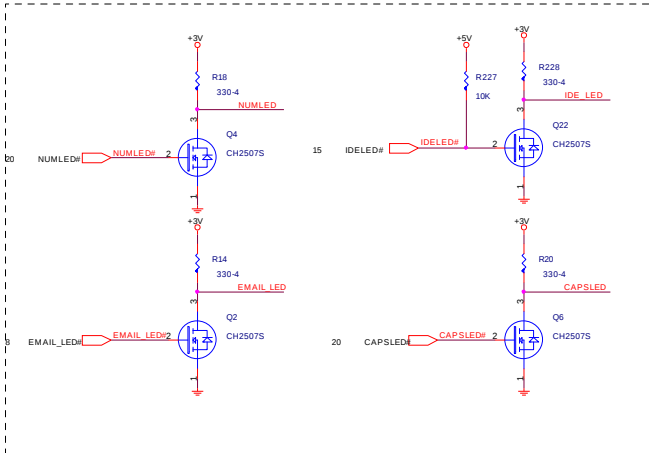
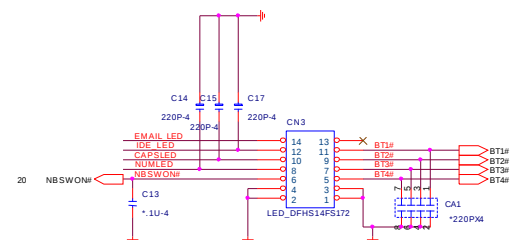
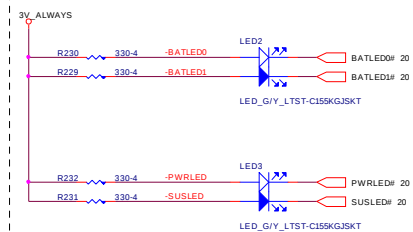
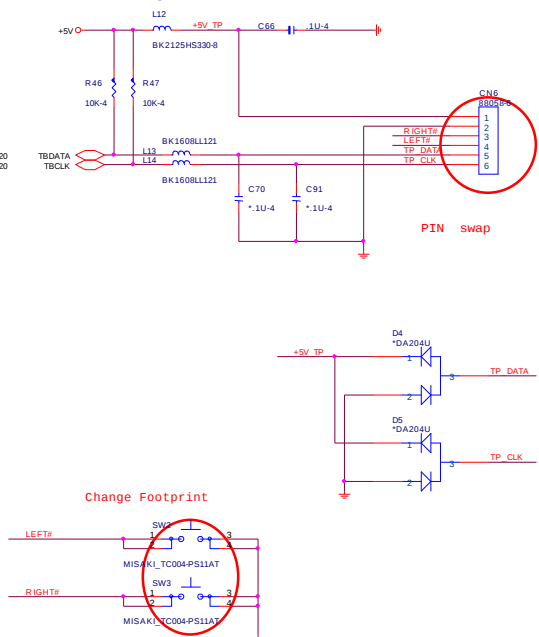


FAN CONTROL

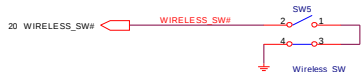


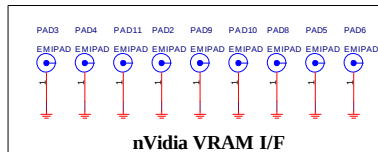
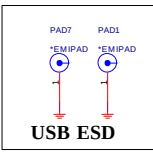
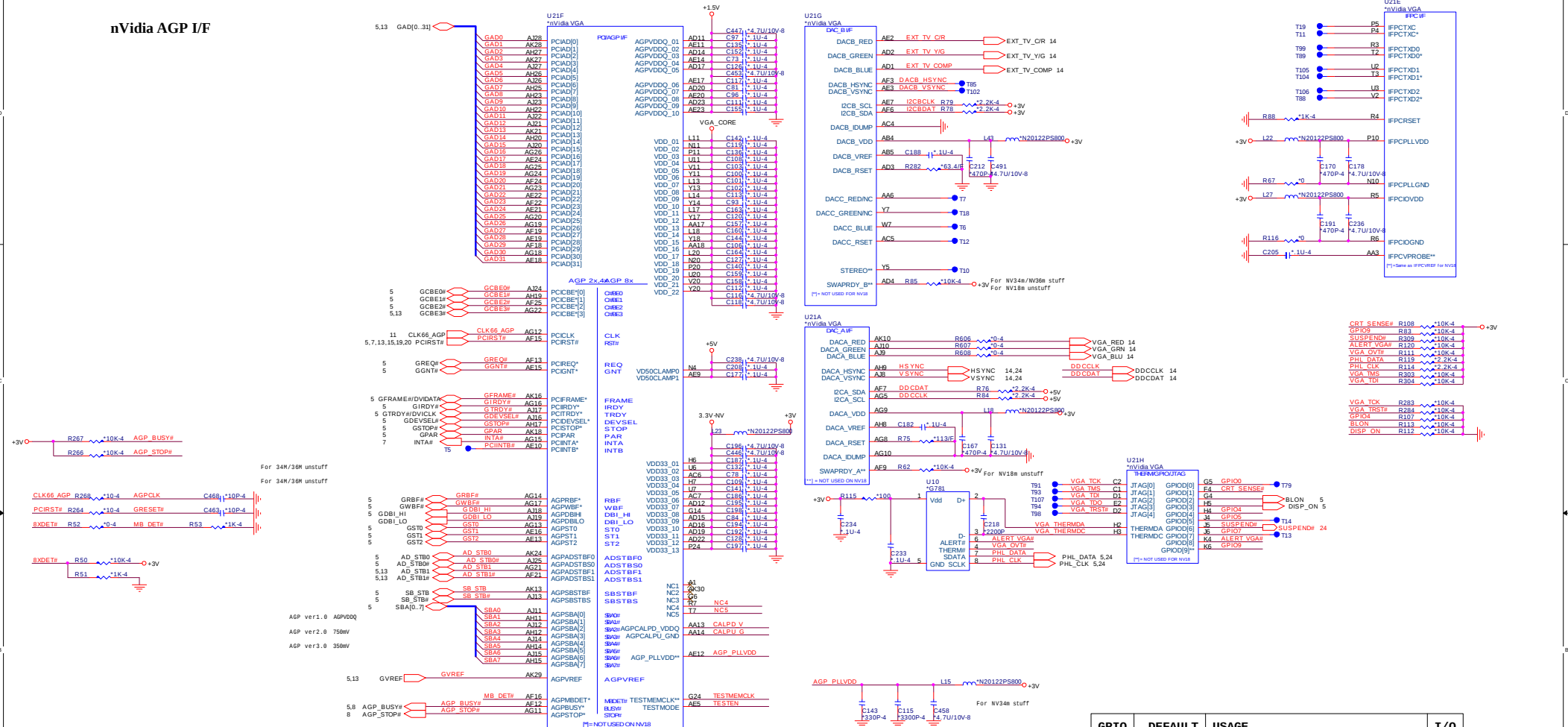
TOUCH PAD

20 MIL



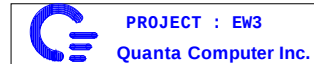
SWITCH



nVidia AGP I/F

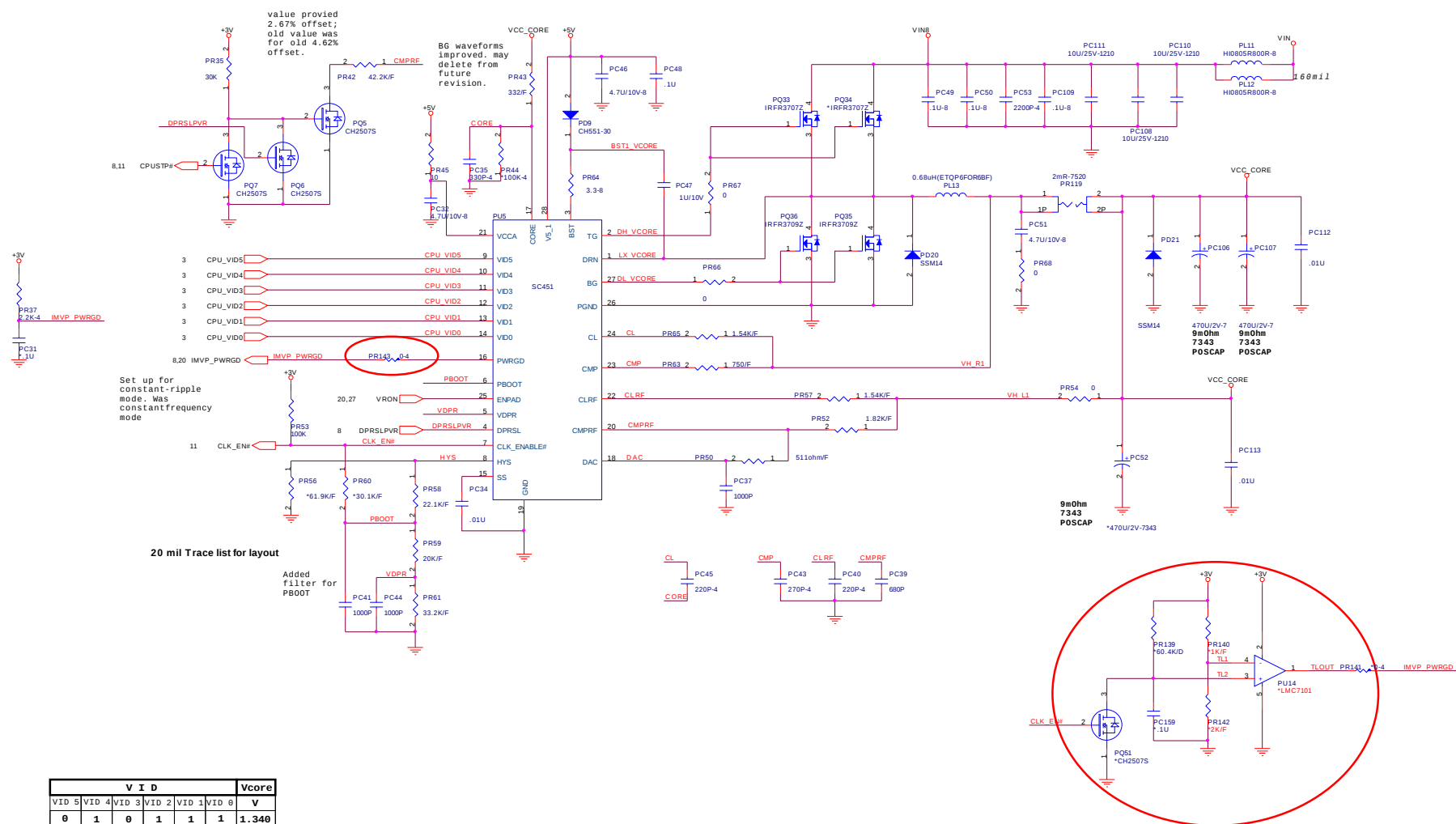
GPIO	DEFAULT STATE	USAGE	I/O
0	PD	Spread spectrum control	0
1	PD	Hot plug/unplug	I/O
2	PD	Panel backlight enable	0
3	PD	Panel power enable	0
4	PD	TBD	I/O
5	PD	Spread spectrum control	0
6	PU	HW suspend	I
7	PU	Dynamic NVVDD voltage control	0
8	PU	Thermal monitor	I
9	PU	Fan Control	I/O

nVidia MEMORY A



Q



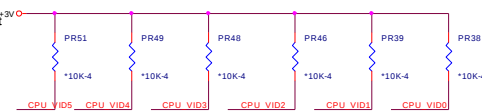


V I D								Vcore
VID 5	VID 4	VID 3	VID 2	VID 1	VID 0			V
0	1	0	1	1	1	1	1	1.340
0	1	1	0	0	0	0	0	1.324
0	1	1	0	1	0	0	0	1.292
0	1	1	1	0	0	0	0	1.260
0	1	1	1	0	1	1	1	1.244
0	1	1	1	1	1	1	1	1.212
1	0	0	0	0	0	1	1	1.180
1	0	0	0	1	1	1	1	1.148
1	0	0	1	1	0	1	0	1.100
1	0	1	0	0	1	1	0	1.052
1	0	1	0	1	1	1	1	1.020
1	0	1	1	1	1	0	0	0.972
1	1	0	0	0	0	0	0	0.940

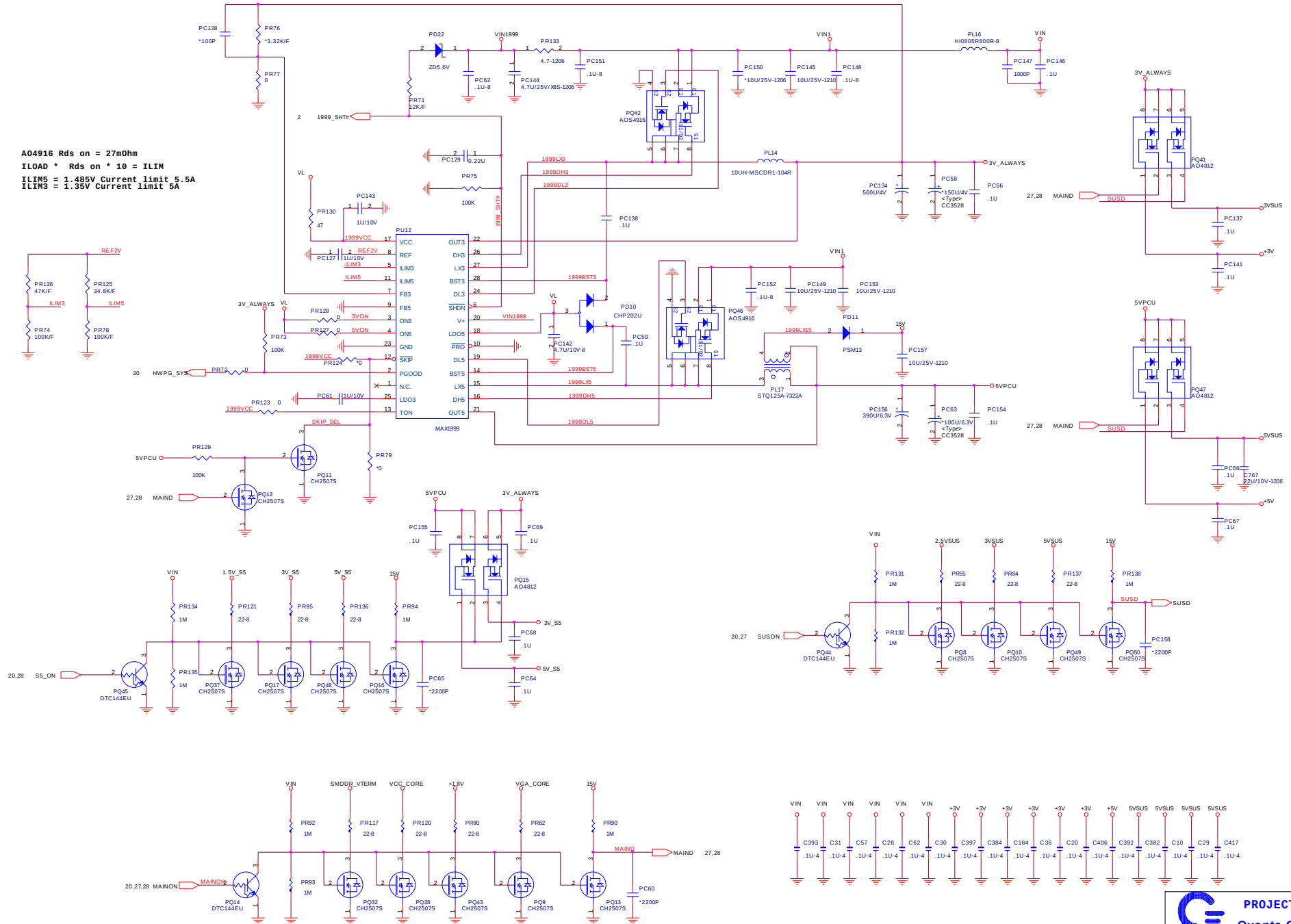
100 mil Trace list for layout

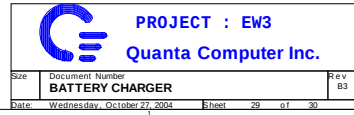
DH_VCORE
LX_VCORE
DL_VCORE
DH_VCORE2
LX_VCORE2
DL_VCORE2

10 mil Trace list for layout



A04916 Rds on = 27mOhm
 ILOAD * Rds on * 10 = ILIM
 ILIM5 = 1.485V Current limit 5.5A
 ILIM3 = 1.38V Current limit 5A





DATE_VER.	MODIFIED BY	CHANGE LIST
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09/08_REVA1		First Version
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09/14_REVA2	for Toshiba spec change for Toshiba spec change for Toshiba spec change	(1) Page 21 del buleetooth function switch. (2) Page 21 update led circuit. (3) Add RTC clear jump.
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09/17_REVA3	for Toshiba spec change for Toshiba spec change for Toshiba spec change	(1) G781 change to ADM1032AR. (2) THER_OVT# net change to 1999_SHT#. (3) USB Port current change to 1A.
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09/21_REVA4	for Toshiba change for Toshiba add	(1) GND_LAN_CHASIS change to GND. (2) MIC JACK add net to MIBIAS.
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10/06_REVB1	EE design issue ME issue EE design issue EE design issue EE design issue EE design issue EE design issue MIC gain adj ME issue Power design issue	(1) Page5 & Page13 change CH7011A DVO source to DVOC from DVOB, R148 & R145 mount 1K, R140 change to 40.2 Ohm from 40.2K Ohm. (2) Page5 CM4 change footprint & swap pin. (3) Page8 change USB port singal to USB0-3 from USB2-5. (4) page8 SW4 pin1 & pin3 swap. (5) Page12 RJ45&RJ11 connector footprint change. (6) Page12 Y3 footprint change. (7) Page15 CN18 change footprint & swap pin. (8) Page17 add R514 for MIC GAIN adj. (9) Page21 CN6 pin change to 6-1 from 1-6. (10) Page25-29 modify for Power.
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###Changer function:
1. PR17 CHANGE TO 60.4K/D.....CS36043D905
2. PR99 CHANGE TO 7.5K/F.....CS27503F909
3. PC76 CHANGE TO 1000P.....CH21000K917
4. PC9 CHANGE TO 4700P.....CH24700K911
5. PR11 CHANGE TO NC
6. PL4 CHANGE TO 10UH.....CV01044MZ18
###CPU OCP function:
1. PR65 , PR57 CHANGE TO 1.2K/F.....CS21203F901
2. PR64 CHANGE TO 0.....CS00004JA07
###2.5VSUS OCP function:
1. PR57 CHANGE TO 1.2K/F.....CS21203F901
2. PC36 CHANGE TO 100P.....CH11006J901
###1.2VCC VOLTAGE REF:
1. PR83 CHANGE TO 10K/F.....CS31003F906
###5VPCU RIPPLe
1. PC63 CHANGE TO 10U/25V.....CH61004M3E5
###Shortage item:
1. PU13 CHANGE TO SC1565.....AL001565003
2. PU8 CHANGE TO LM339AD.....AL0M339M003
3. PD11 CHANGE TO 05FA20.....BC05FA20Z01
4. PD22 , PD17 , PD18 , PD19 CHANGE TO Z05.6V.....BDRLZ56BZ05
5. PD4 CHANGE TO ZD12V.....BDRLZ12BZ05

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10/08_REVB2	for Toshiba Spec change	(1) Page14 S-Video change to 4Pin from 7Pin. (CN12)
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10/13_REVB3	for Toshiba change for Toshiba spec add for Toshiba spec add for ME request	(1) Page8 clear switch change to jump pad.(del SW4 add JP1) (2) Page12 add 75ohm termination for RJ45 not use pin. (add R601-R604) (3) Page29 add Q10,R606 for lid S4 wackup. (4) Page14 add HOLE24 for ME
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10/14_REVB4	for 5VSUS drop.	(1) Page19 Del R1,R272,R319 and U1,U23,U24 change to I TPS2061.
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PROJECT : EW3
Quanta Computer Inc.

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	CHANGE LIST	83
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