TOSHIBA CONFIDENTIAL

SCHEMATIC DIAGRAMS

PRODUCT NAME Lumos10 (VP)

UNIT NAME FLM1M2

UNIT NO. DPL3X072G01

PWA NAME

PWA NO.

PWB NAME

PWB NO.

PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV
1	001	COVER SHEET	06	26	135	Montara-GT strap	00	51	211	PC-CARD I/F	04	76	461	USB POWER	02
2	002	COVER & Rev. SHEET	06	27	140	DDR I/F Rs 1	04	52	212	SD LED	00	77	520	TV I/F	04
3	003	BLOCK DIAGRAM	02	28	141	DDR I/F Rs 2	04	53	213	SD CONN	00	78	530	LCD PS I/F	00
4	004	NOTES	02	29	142	SO-DIMM0 1	04	54	220	MiniPCI I/F	00	79	531	LCD CONN.	00
5	005	Grouping Direction	05	30	143	SO-DIMM0 2	04	55	301	MDC I/F	04	80	532	CRT I/F	00
6	100	CK408	04	31	144	SO-DIMM1 1	04	56	320	SMSC EC/KBC 1	00	81	534	TV CONN.	04
7	105	mPGA479M 1	06	32	145	SO-DIMM1 2	04	57	321	SMSC EC/KBC 2	00	82	535	Level Shift	04
8	106	mPGA479M 2	06	33	146	DDR I/F RT	04	58	322	SMSC EC/KBC 3	02	83	600	AD1981B	00
9	107	mPGA479M 3	06	34	147	DDR DECOUPLING	02	59	323	SMSC EC/KBC 4	05	84	604	SPEAKER CON	00
10	108	mPGA479M 4	06	35	160	ICH4-M 1	00	60	324	SMSC EC/KBC 5	00	85	605	AMP MM1517A	04
11	109	mPGA479M 5	06	36	161	ICH4-M 2	00	61	325	KB I/F	04	86	608	ANALOG POWER	04
12	110	Timing Control	00	37	162	ICH4-M 3	00	62	326	PAD I/F	00	87	750	EMI CAPS	03
13	115	Thermal Sensor	02	38	163	ICH4-M 4	04	63	328	KB LED	00	88	800	[PS] DC-IN	04
14	116	ITP	00	39	164	ICH4-M 5	00	64	331	POWER SWITCH	04	89	801	[PS] PVT SW	00
15	120	Montara-GT 1	00	40	165	ICH4-M 6	00	65	351	PARALLEL I/F	00	90	802	[PS] 1st Battery	04
16	121	Montara-GT 2	00	41	166	ICH4-M 7	02	66	354	BIOS for SMSC	04	91	804	[PS] E5V/E3V	02
17	122	Montara-GT 3	00	42	167	ICH4-M 8	02	67	400	KINNERETH	04	92	805	[PS] PW SW	00
18	123	Montara-GT 4	04	43	180	HDD I/F	00	68	403	NETWORK I/F	03	93	806	[PS] E10V	00
19	124	Montara-GT 5	00	44	181	CD ROM I/F	00	69	420	1394 PHY/LINK (1)	00	94	807	[PS] CURRENT AMP	00
20	125	Montara-GT 6	00	45	200	YEBISUSS (1)	00	70	421	1394 PHY/LINK (2)	04	95	808	[PS] CHARGE	00
21	126	Montara-GT 7	00	46	201	YEBISUSS (2)	00	71	422	1394 EEPROM	00	96	809	[PS] MCV/S*V	00
22	127	Montara-GT 8	00	47	202	YEBISUSS (3)	00	72	423	1394 I/F	02	97	810	[PS] Analog Input	00
23	128	Montara-GT 9	00	48	203	YEBISUSS (4)	00	73	424	1394 POWER/Etc	00	98	811	[PS] PSC(1)	04
24	129	Montara-GT 10	00	49	205	YEBISUSS (5)	00	74	425	1394 COMMENT	00	99	812	[PS] PSC(2)	00
25	130	Montara-GT 11	00	50	210	PC-CARD POWER	00	75	460	USB I/F	00	100	813	[PS] CPUVCC(1)	04

APPROVED BY	CHECKED BY	DESIGNED BY	TITLE	TOTAL	PAGE NO.	REV. MARK	DRAWING. NO.
[PC設1](1P設7) A. Nagae	[PC設1](1P設7) A. Sakai	[PC設1](1P設7) Y. Shimazaki	FLM1M2	113	1	06	360048117

2003. 11. 17 14:10

TOSHIBA CORPORATION

SCHEMATIC DIAGRAMS

PRODUCT NAME

UNIT NAME

UNIT NO.

PWA NAME

PWA NO.

PWB NAME

PWB NO.

PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV
101	814	[PS] CPUVCC(2)	00	126			00
102	815	[PS] CPUVCC(3)	00	127			
103	816	[PS] 2R5-B2V	02	128			
104	817	[PS] 1R25-P1V	02	129			
105	818	[PS] PW-SW	00	130			
106	819	[PS] VID1R2-P1V	02	131			
107	820	[PS] 1R5-C1V	02	132			
108	822	[PS] RTCVCC	00	133			
109	823	[PS] FANVCC	02	134			
110	824	[PS] FANVCC(1)	06	135			
111	950	FLMGL* I/F	00	136			
112	955	FLMGS* I/F	00	137			
113	960	FLMCD* I/F	00	138			
114			00	139			
115			00	140			
116			00	141			
117			00	142			
118			00	143			
119			00	144			
120			00	145			
121			00	146			
122			00	147			
123			00	148			
124			00	149			
125			00	150			

REV. MARK	CONTENTS	APPROVED BY	REVISED BY	REGISTERED
00	ISSUE			
01	1PH8-CR-03A050 実施 変更頁：98			
02	ISSUE for CS2			
	変更頁：1-5. 13. 29-32. 34. 41. 42. 58. 64. 66. 68. 70. 72. 76. 86-88. 90. 91. 103. 104. 106. 107. 109			
03	CS2 DR中の変更			
	PHY-CR-03A_C-083. CR-LUM10-002 適用 変更頁：1. 2. 7-11. 67. 68. 87			
04	ISSUE for VP	A. Nagae (1P設7) 2003/10/22	Y. Shimazaki (1P設7) 2003/10/22	
	変更頁：1. 2. 5-11. 18. 27-33. 38. 51. 55. 61. 64. 66. 67. 70. 77. 81. 82. 85. 86. 88. 90. 98. 100			
05	TV-Out機能有りモデル対応 変更頁：1. 2. 5. 59	A. Nagae (1P設7) 2003/11/4	Y. Shimazaki (1P設7) 2003/11/4	
06	CN22P20031107030適用 (CPU Socket変更、2ndFAN電圧定数変更) 変更頁：1. 2. 7-11. 110	A. Nagae (1P設7) 2003/11/17	Y. Shimazaki (1P設7) 2003/11/17	

APPROVED BY	CHECKED BY	DESIGNED BY	TITLE	TOTAL	PAGE NO.	REV. MARK	DRAWING. NO.
			FLM1M2		2	06	360048117

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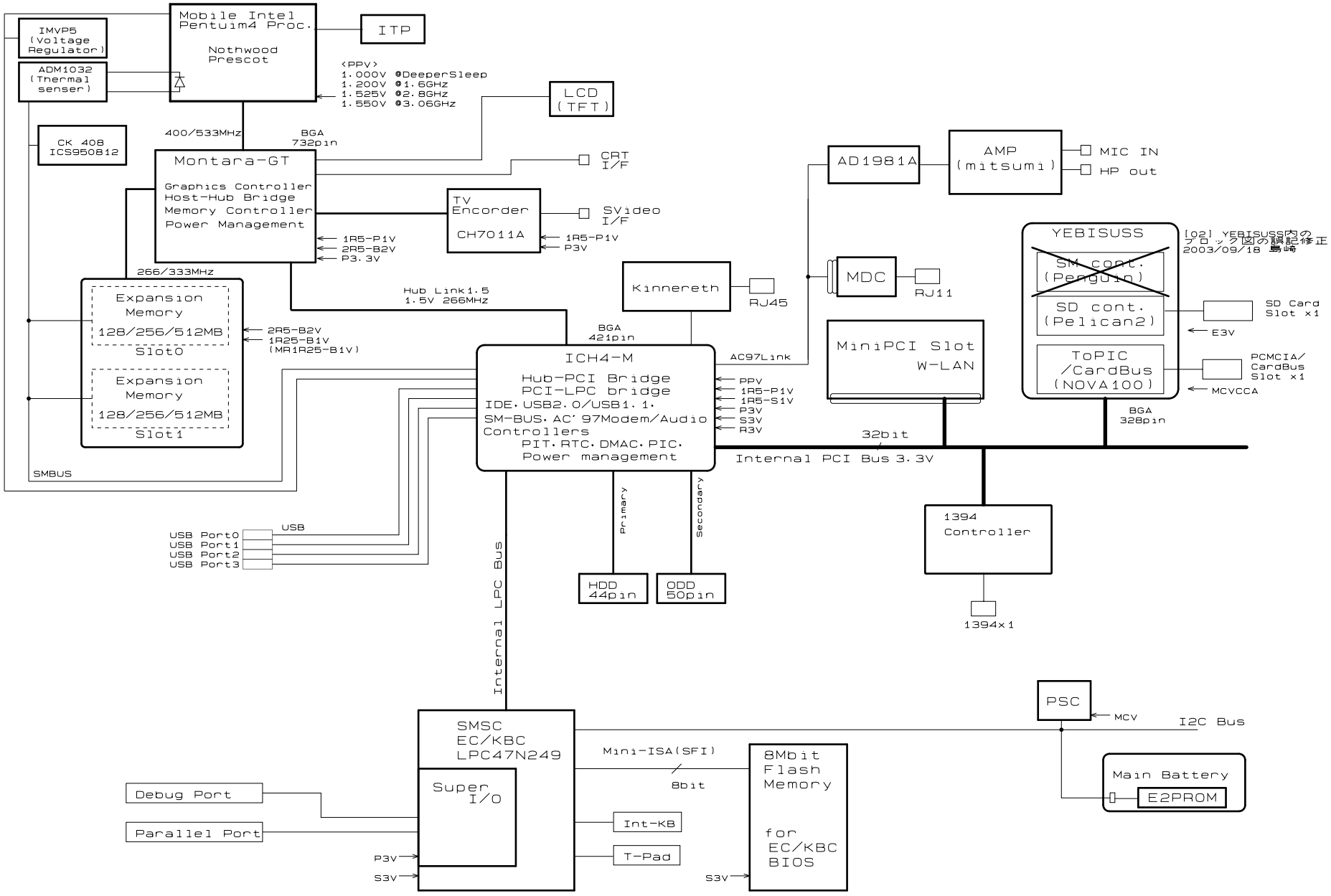
5T03655D



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Y. Shimazaki	FLM1M2	BLOCK DIAGRAM	3	3	02	360048117

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関連資料

BGA BALL MAP ： 360047440 [02]
回路設計仕様書： 360047355 [02]

基板構成

ペットネーム：FLM***

SYSTEM PCB	：FLM1M*	異種多面構成
LED PCB	：FLM1L*	
SOUND PCB	：FLM1S*	
CD BUTTON PCB	：FLMCD*	

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Y. Shimazaki	FLM1M2	NOTES	4	4	00	360048117

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4 [05]

システム基板には以下の２種類のグループがある。[02]

FLM1M* G01 : SD/1394 有. TV-Out 無
FLM1M* G02 : SD/1394 無. TV-Out 無
FLM1M* G03 : SD/1394 有. TV-Out 有 [05]
FLM1M* G04 : SD/1394 無. TV-Out 有 [05]

SD/1394有無の違い [05]

Sht.No.	Ref.No.	G01 / G03 (SD/1394有) [05]	G02 / G04 (SD/1394無) [05]
SH.200	R2000	MOUNT	NOT MOUNT
	W2001	NOT MOUNT	MOUNT
SH.212	DS2120	MOUNT	NOT MOUNT
	Q2121		
	R2122		
SH.213	C2130	MOUNT	NOT MOUNT
	PJ2130		
SH.323	R3246	NOT MOUNT	MOUNT
	R3248	MOUNT	NOT MOUNT
SH.420	IC4200	MOUNT	NOT MOUNT
	R4200-R4205		
	R4214		
	RM4201		
SH.421	C4200-C4228	MOUNT	NOT MOUNT
	C4230		
	C4236		
	C4240		
	IC4200		
	L4200		
	R4206-R4207		
	~~W4200~~ [04] -W4201		
	X4200		
SH.422	C4243	MOUNT	NOT MOUNT
	IC4201		
SH.423	C4244-C4245	MOUNT	NOT MOUNT
	L4202-L4203		
	PJ4200		
	R4209-R4213		

TV-Out有無の違い [05]

[05]

Sht.No.	Ref.No.	G01 / G02 (TV-Out無)	G03 / G04 (TV-Out有)
SH.123	R1201	MOUNT	NOT MOUNT
	R1237	NOT MOUNT	MOUNT
SH.323	R3244	NOT MOUNT	MOUNT
	R3245	MOUNT	NOT MOUNT
SH.520	C5400-C5412	NOT MOUNT	MOUNT
	FL5400-FL5402		
	IC5400		
	L5400		
	R5403-R5407		
	RM5400		
	X5400		
SH.534	C5641-C5646	NOT MOUNT	MOUNT
	L5641-L5642		
	PJ5640		
SH.535	IC5640	NOT MOUNT	MOUNT
	R5641-5643		

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Y.Shimazaki	FLM1M2	Grouping Direction	5	5	05	360048117

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FS[210]	CPUCLK
000	66
001	100
010	200
011	133

[04] Lumos専用番号に変更
GDM460000317->GDM460000681
2003/10/20 島崎

GDM460000681

RDDP Montara-GT Rev0.7より

CPUCLK	2.0-8.5inch
CLK66	2.0-9.0inch

上記の配線長制限の中で
CPUCLKをCLK66より短く
配線お願いします

BSEL[10]	CPUCLK
00	(100MHz)
01	133MHz
10	(200MHz)
11	RESERVED

R1000削除
2003/07/14 島崎

W1000削除
2003/07/14 島崎

ICS950812 CK 408
[U950812]
IC1000

950812CGT

Lumos10:
X66GPU-P3P, R1015削除
2003/08/21 島崎

長さをそろえて下さい。

Lumos:1PH8-CR-03A042より
C1004, C1005, C1006 Not Mount
C1020, C1021 追加
2003/08/21 島崎

REF:Red(ES)ver.6/30

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DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki	FLM1M2	CK408	100	6	04	360048117

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ROC.1050

システム課指定のソケットを使用すること
Lumos:システム課指定品であること確認済

[04] CPUソケット変更
GDM250000070 -> GDM250000075
2003/10/21 島崎

[06] CPUソケット変更
GDM250000075 -> GDM250000081
2003/11/17 島崎

GDM250000081

1/6
[SOCKET478B]
IS1050

120	HD00-PPN	B21	D0#	DSTBP0#	F21	DSTBP0-PPN	120	
120	HD01-PPN	B22	D1#	DSTBN0#	E22	DSTBN0-PPN	120	
120	HD02-PPN	A23	D2#					
120	HD03-PPN	A25	D3#					
120	HD04-PPN	C21	D4#					
120	HD05-PPN	D22	D5#					
120	HD06-PPN	B24	D6#					
120	HD07-PPN	C23	D7#					
120	HD08-PPN	C24	D8#					
120	HD09-PPN	B25	D9#					
120	HD10-PPN	G22	D10#					
120	HD11-PPN	H21	D11#					
120	HD12-PPN	C26	D12#					
120	HD13-PPN	D23	D13#					
120	HD14-PPN	J21	D14#					
120	HD15-PPN	D25	D15#	DBI0#	E21	DBI0-PPN	120	
120	HD16-PPN	H22	D16#	DSTBP1#	J23	DSTBP1-PPN	120	
120	HD17-PPN	E24	D17#	DSTBN1#	K22	DSTBN1-PPN	120	
120	HD18-PPN	G23	D18#					
120	HD19-PPN	F23	D19#					
120	HD20-PPN	F24	D20#					
120	HD21-PPN	E25	D21#					
120	HD22-PPN	F26	D22#					
120	HD23-PPN	D26	D23#					
120	HD24-PPN	L21	D24#					
120	HD25-PPN	G26	D25#					
120	HD26-PPN	H24	D26#					
120	HD27-PPN	M21	D27#					
120	HD28-PPN	L22	D28#					
120	HD29-PPN	J24	D29#					
120	HD30-PPN	K23	D30#					
120	HD31-PPN	H25	D31#	DBI1#	G25	DBI1-PPN	120	
120	HD32-PPN	M23	D32#	DSTBP2#	P23	DSTBP2-PPN	120	
120	HD33-PPN	N22	D33#	DSTBN2#	R22	DSTBN2-PPN	120	
120	HD34-PPN	P21	D34#					
120	HD35-PPN	M24	D35#					
120	HD36-PPN	N23	D36#					
120	HD37-PPN	M26	D37#					
120	HD38-PPN	N26	D38#					
120	HD39-PPN	N25	D39#					
120	HD40-PPN	R21	D40#					
120	HD41-PPN	P24	D41#					
120	HD42-PPN	R25	D42#					
120	HD43-PPN	R24	D43#					
120	HD44-PPN	T26	D44#					
120	HD45-PPN	T25	D45#					
120	HD46-PPN	T22	D46#					
120	HD47-PPN	T23	D47#	DBI2#	P26	DBI2-PPN	120	
120	HD48-PPN	U26	D48#	DSTBP3#	W23	DSTBP3-PPN	120	
120	HD49-PPN	U24	D49#	DSTBN3#	W22	DSTBN3-PPN	120	
120	HD50-PPN	U23	D50#					
120	HD51-PPN	V25	D51#					
120	HD52-PPN	U21	D52#					
120	HD53-PPN	V22	D53#					
120	HD54-PPN	V24	D54#					
120	HD55-PPN	W26	D55#					
120	HD56-PPN	Y26	D56#					
120	HD57-PPN	W25	D57#					
120	HD58-PPN	Y23	D58#					
120	HD59-PPN	Y24	D59#					
120	HD60-PPN	Y21	D60#					
120	HD61-PPN	AA25	D61#					
120	HD62-PPN	AA22	D62#					
120	HD63-PPN	AA24	D63#	DBI3#	V21	DBI3-PPN	120	

[PSB Data 4x]
[Source Synchronous]

REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Y.Shimazaki	FLM1M2	mPGA479M 1	105	7	06	360048117

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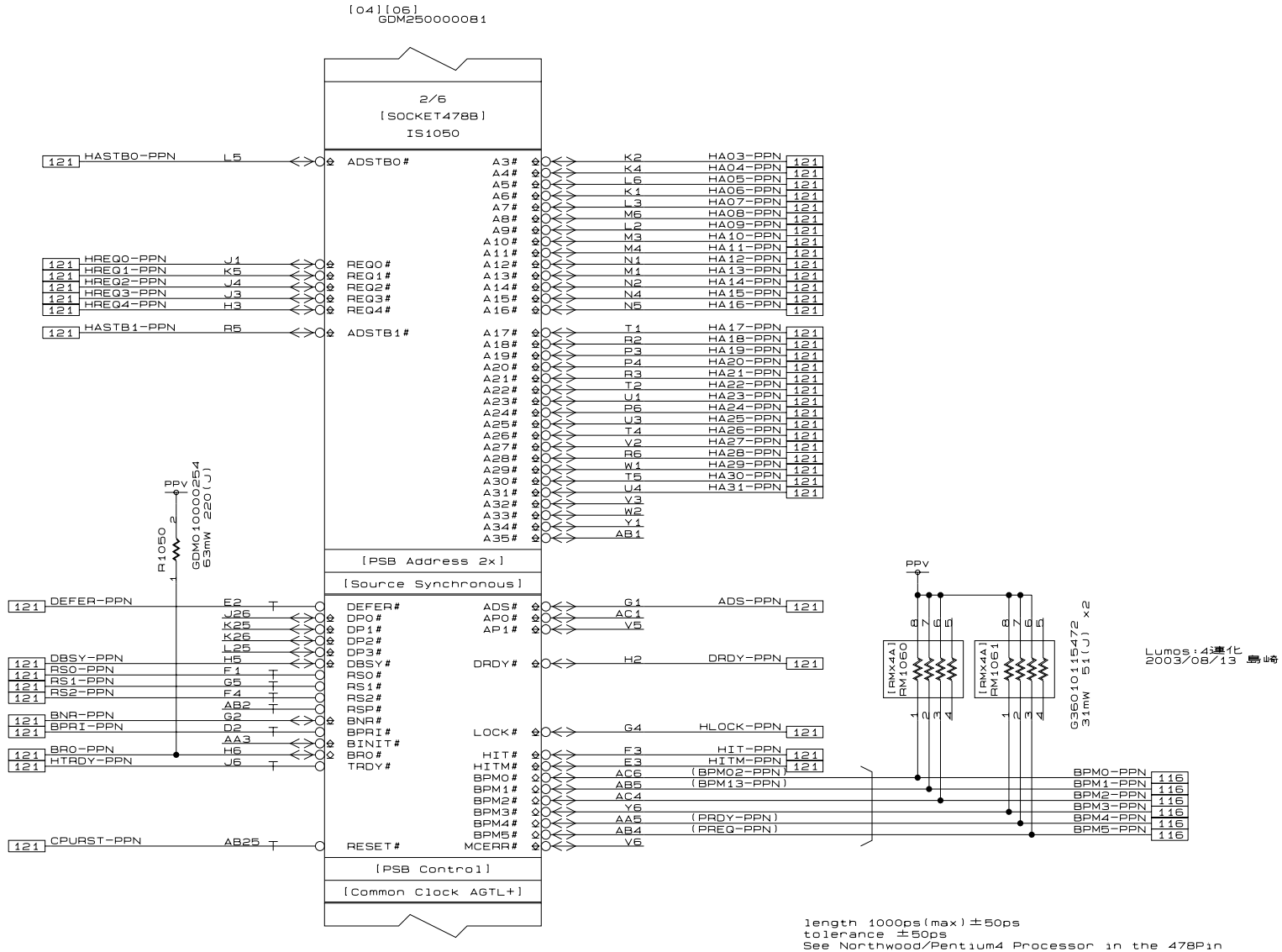

REF:Red(ES)ver.6/30

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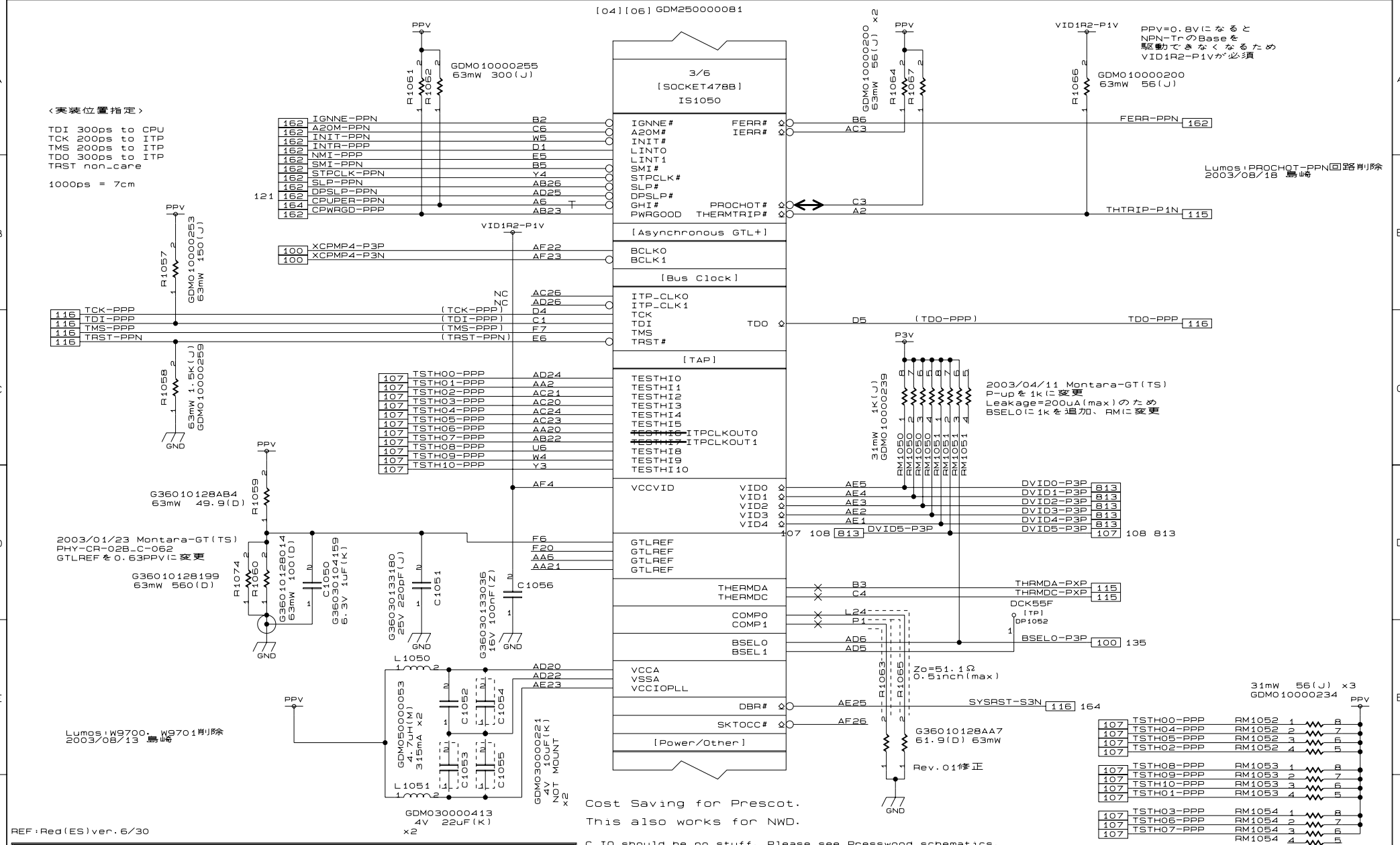
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Y. Shimazaki	FLM1M2	mPGA479M 2	106	8	06	360048117

2003.11.17 14:10



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Y. Shimazaki	FLM1M2	mPGA479M 3	107	9	06	360048117

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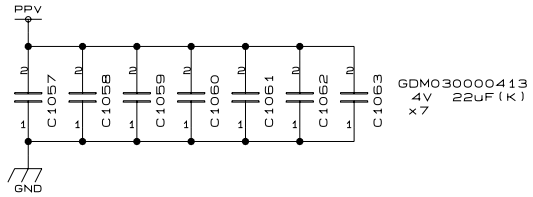
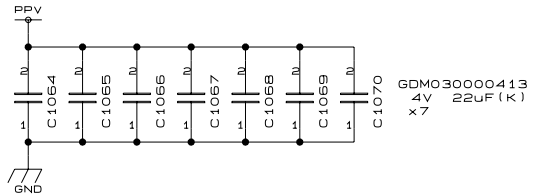
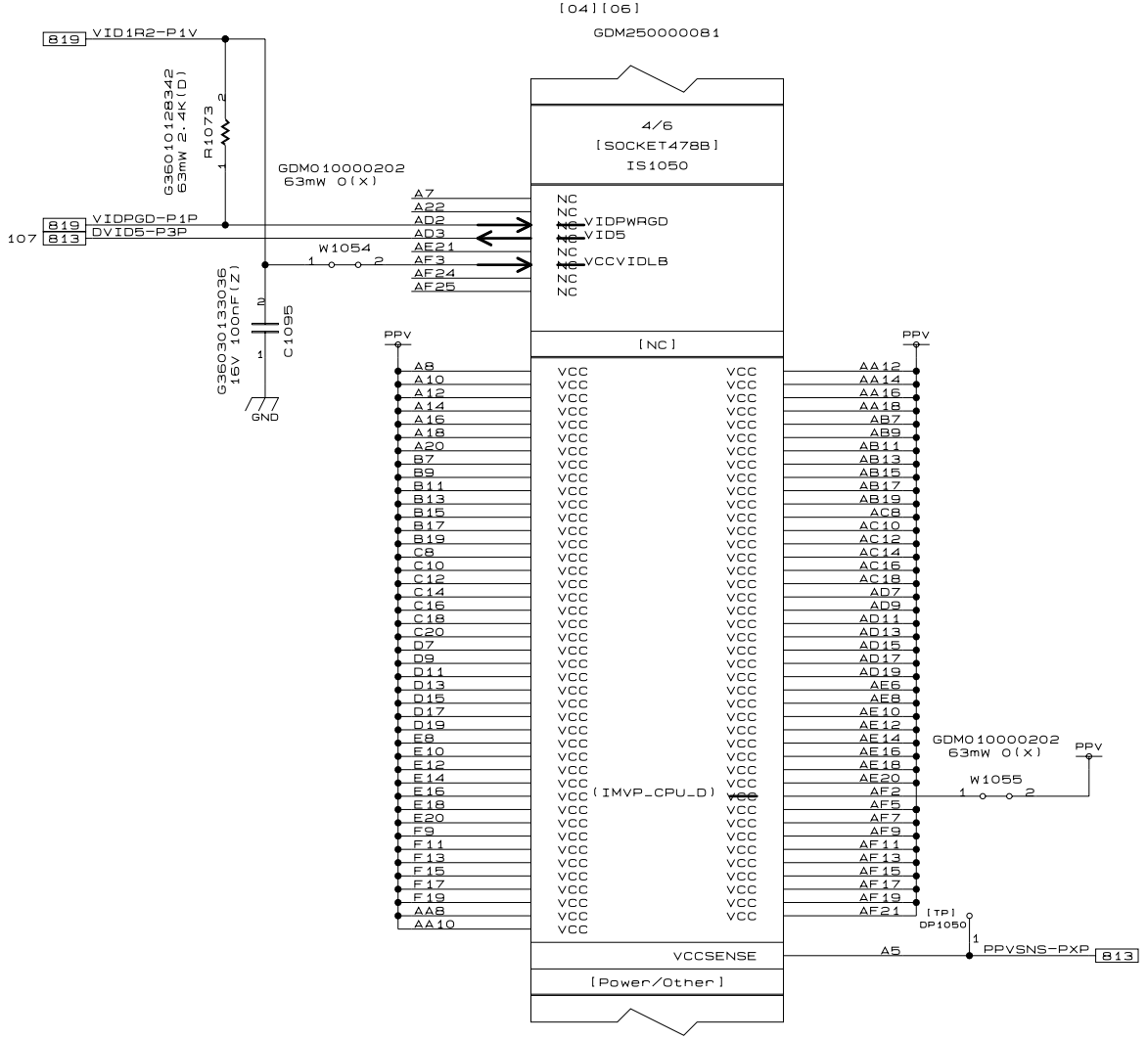
		最新	EMP
10uF(3216)	G36030110099	11.0	4.5
10uF(2125)	GDM030000221	4.35	4.95
22uF(3225)	G36030119054	18.0	12.0
22uF(3216)	GDM03*******		







REF:Red(ES)ver.6/30

TOSHIBA CONFIDENTIAL

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Y. Shimazaki	FLM1M2	mPGA479M 4	108	10	06	360048117

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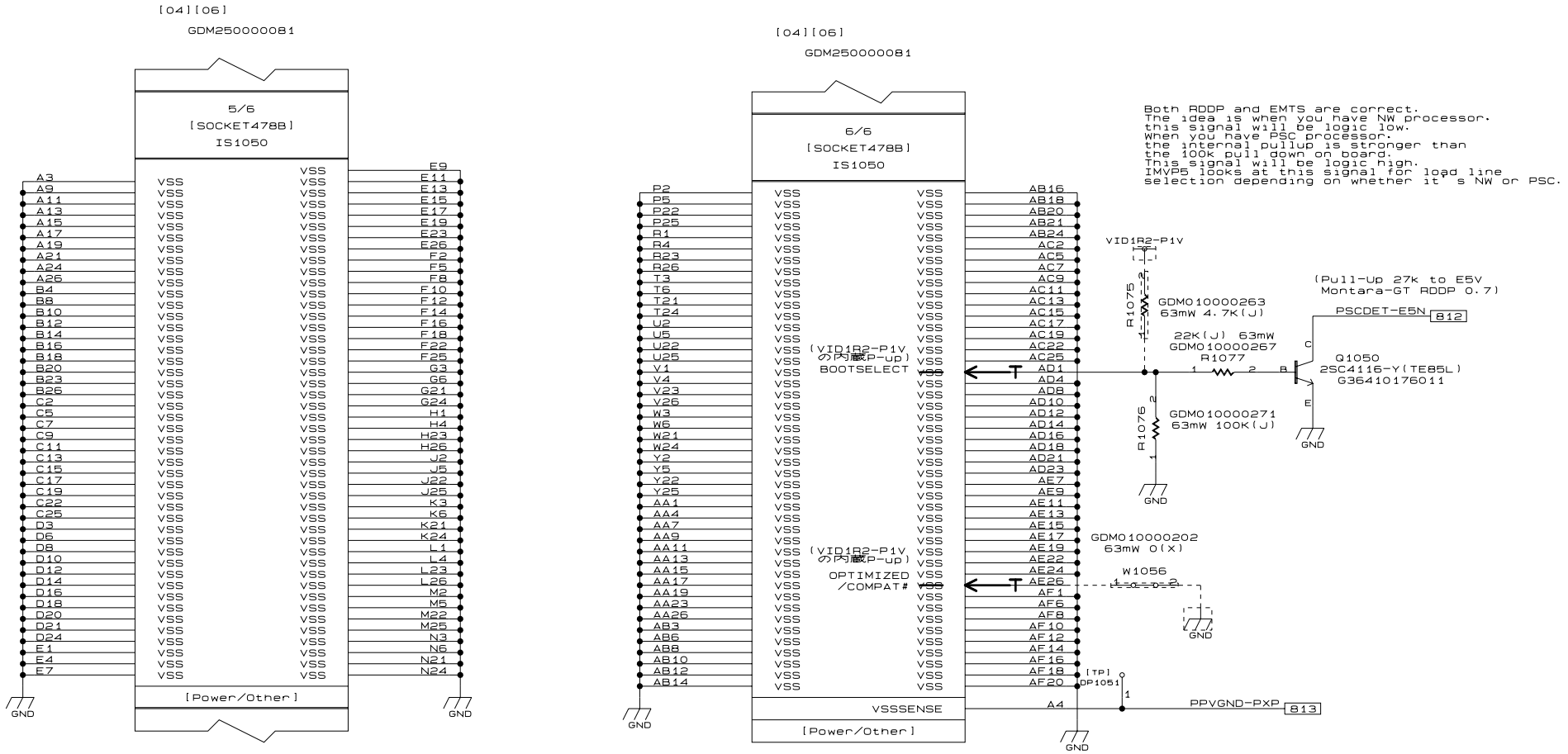

REF:Red(ES)ver.6/30

TOSHIBA CONFIDENTIAL

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Y.Shimazaki	FLM1M2	mPGA479M 5	109	11	06	360048117

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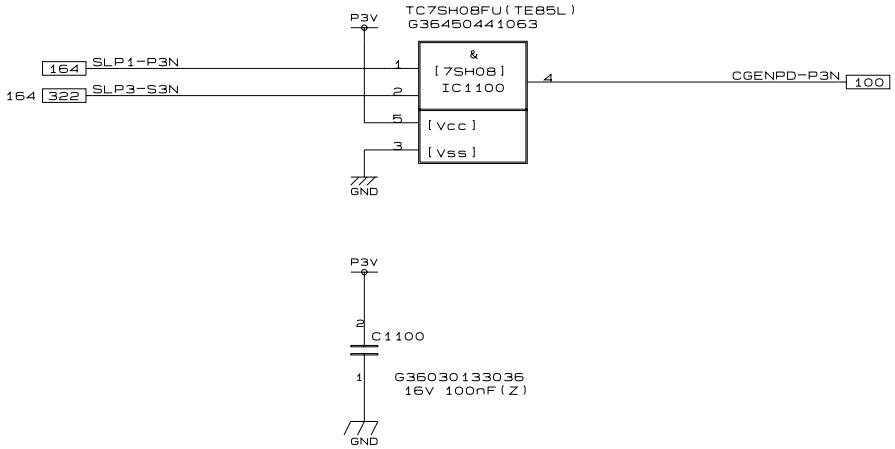
TOSHIBA CORPORATION

ROC. 1100



REF:Red(ES)ver.6/30

TOSHIBA CONFIDENTIAL

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Y. Shimazaki	FLM1M2	Timing Control	110	12	00	360048117

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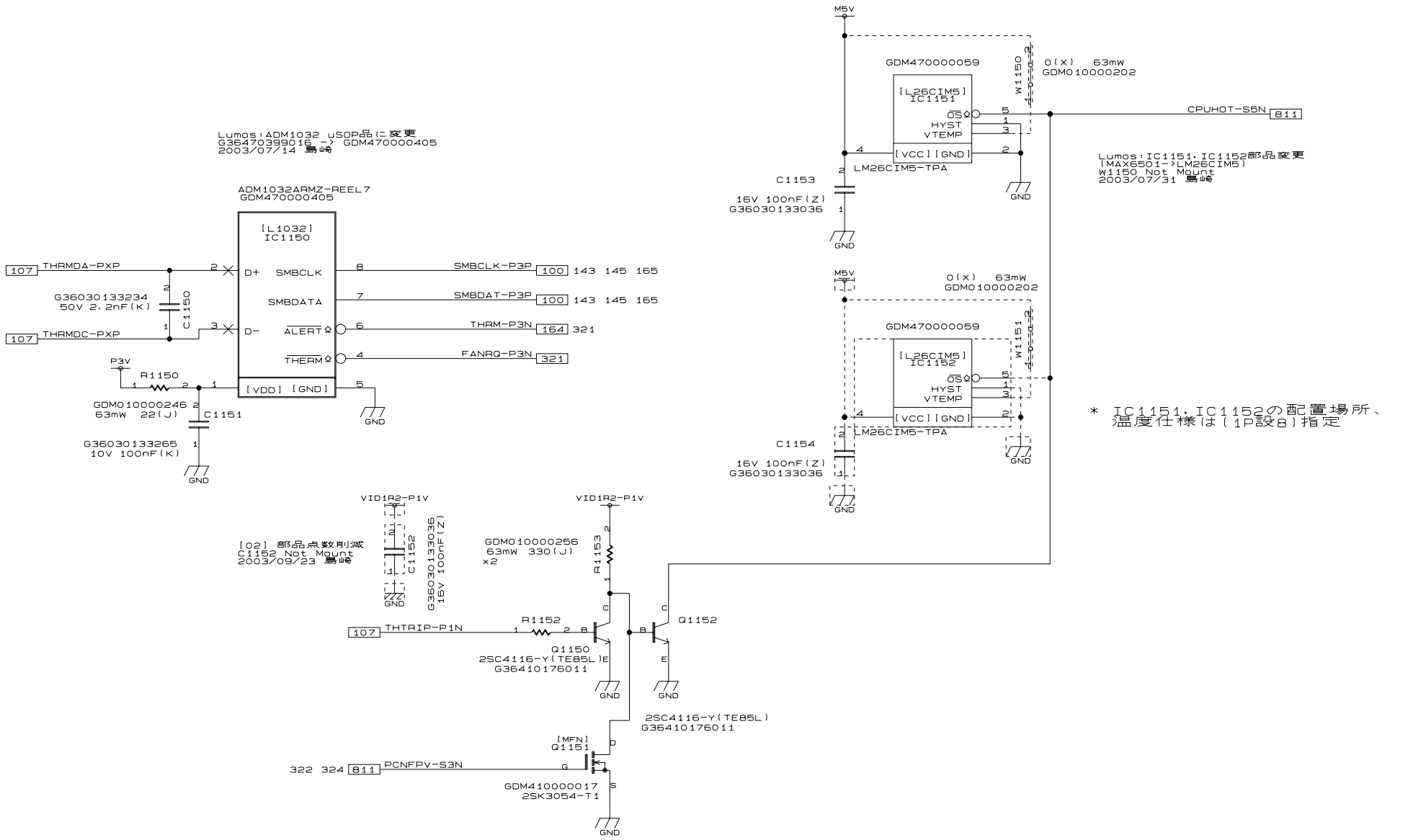
ROC.1150



REF:Red(ES)ver.6/30

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DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki	FLM1M2	Thermal Sensor	115	13	02	360048117

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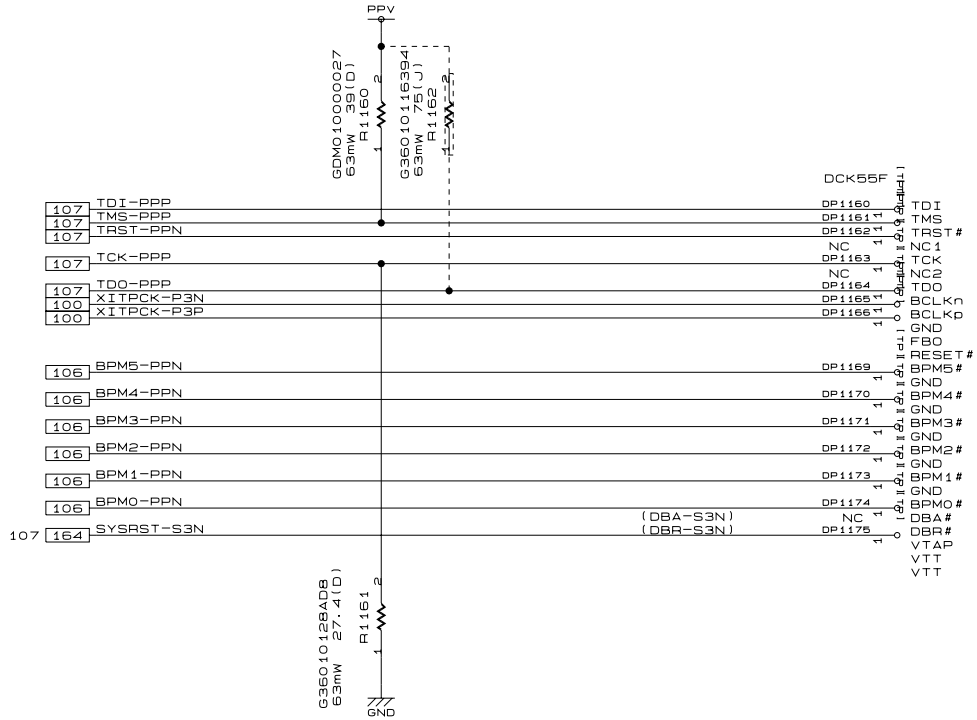
ROC.1160



REF:Red(ES)ver.6/30

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DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki	FLM1M2	ITP	116	14	00	360048117

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TOSHIBA CORPORATION

ROC. 1200

Lumos10 CS1:Montara-GTを
Kコードから Gコードに変更
2003/08/28 島崎

GDM460000635

Montara-GT GMCH 1/10
[U82852GME]
IC1200

(VTT)

105	DSTBP0-PPN	K27	HDSTBP0#	HD0#	K22	HD00-PPN	105
105	DSTBN0-PPN	J28	HDSTBN0#	HD1#	H27	HD01-PPN	105
				HD2#	K25	HD02-PPN	105
				HD3#	L24	HD03-PPN	105
				HD4#	J27	HD04-PPN	105
				HD5#	G28	HD05-PPN	105
				HD6#	L27	HD06-PPN	105
				HD7#	L23	HD07-PPN	105
				HD8#	L25	HD08-PPN	105
				HD9#	J24	HD09-PPN	105
				HD10#	H25	HD10-PPN	105
				HD11#	K23	HD11-PPN	105
				HD12#	G27	HD12-PPN	105
				HD13#	K26	HD13-PPN	105
				HD14#	J23	HD14-PPN	105
105	DBI0-PPN	J25	DINV0#	HD15#	H26	HD15-PPN	105
105	DSTBP1-PPN	D26	HDSTBP1#	HD16#	F25	HD16-PPN	105
105	DSTBN1-PPN	C27	HDSTBN1#	HD17#	F26	HD17-PPN	105
				HD18#	B27	HD18-PPN	105
				HD19#	H23	HD19-PPN	105
				HD20#	E27	HD20-PPN	105
				HD21#	G25	HD21-PPN	105
				HD22#	F28	HD22-PPN	105
				HD23#	D27	HD23-PPN	105
				HD24#	G24	HD24-PPN	105
				HD25#	C28	HD25-PPN	105
				HD26#	B26	HD26-PPN	105
				HD27#	G22	HD27-PPN	105
				HD28#	C26	HD28-PPN	105
				HD29#	E26	HD29-PPN	105
				HD30#	G23	HD30-PPN	105
105	DBI1-PPN	E25	DINV1#	HD31#	B28	HD31-PPN	105
105	DSTBP2-PPN	E21	HDSTBP2#	HD32#	B21	HD32-PPN	105
105	DSTBN2-PPN	E22	HDSTBN2#	HD33#	G21	HD33-PPN	105
				HD34#	C24	HD34-PPN	105
				HD35#	C23	HD35-PPN	105
				HD36#	D22	HD36-PPN	105
				HD37#	C25	HD37-PPN	105
				HD38#	E24	HD38-PPN	105
				HD39#	D24	HD39-PPN	105
				HD40#	G20	HD40-PPN	105
				HD41#	E23	HD41-PPN	105
				HD42#	B22	HD42-PPN	105
				HD43#	B23	HD43-PPN	105
				HD44#	F23	HD44-PPN	105
				HD45#	F21	HD45-PPN	105
				HD46#	C20	HD46-PPN	105
105	DBI2-PPN	B25	DINV2#	HD47#	C21	HD47-PPN	105
105	DSTBP3-PPN	E18	HDSTBP3#	HD48#	G18	HD48-PPN	105
105	DSTBN3-PPN	D18	HDSTBN3#	HD49#	E19	HD49-PPN	105
				HD50#	E20	HD50-PPN	105
				HD51#	G17	HD51-PPN	105
				HD52#	D20	HD52-PPN	105
				HD53#	F19	HD53-PPN	105
				HD54#	C19	HD54-PPN	105
				HD55#	C17	HD55-PPN	105
				HD56#	F17	HD56-PPN	105
				HD57#	B19	HD57-PPN	105
				HD58#	G16	HD58-PPN	105
				HD59#	E16	HD59-PPN	105
				HD60#	C16	HD60-PPN	105
				HD61#	E17	HD61-PPN	105
				HD62#	D16	HD62-PPN	105
105	DBI3-PPN	G19	DINV3#	HD63#	C18	HD63-PPN	105

[AGTL+ 4x]

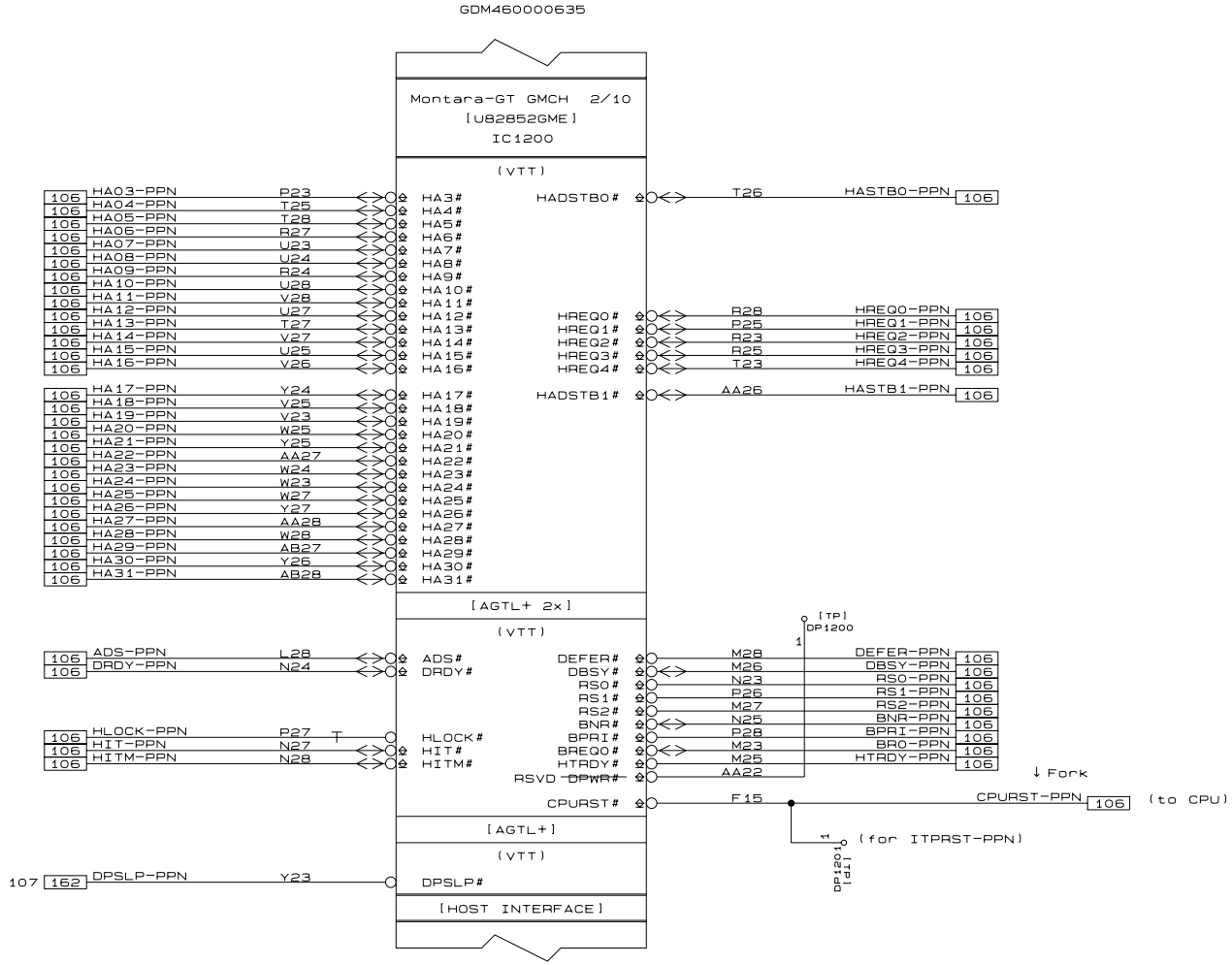
[HOST INTERFACE]

TOSHIBA CONFIDENTIAL

REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y. Shimazaki	FLM1M2	Montara-GT 1	120	15	00	360048117

2003.11.17 14:10



TOSHIBA CONFIDENTIAL

REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki	FLM1M2	Montara-GT 2	121	16	00	360048117

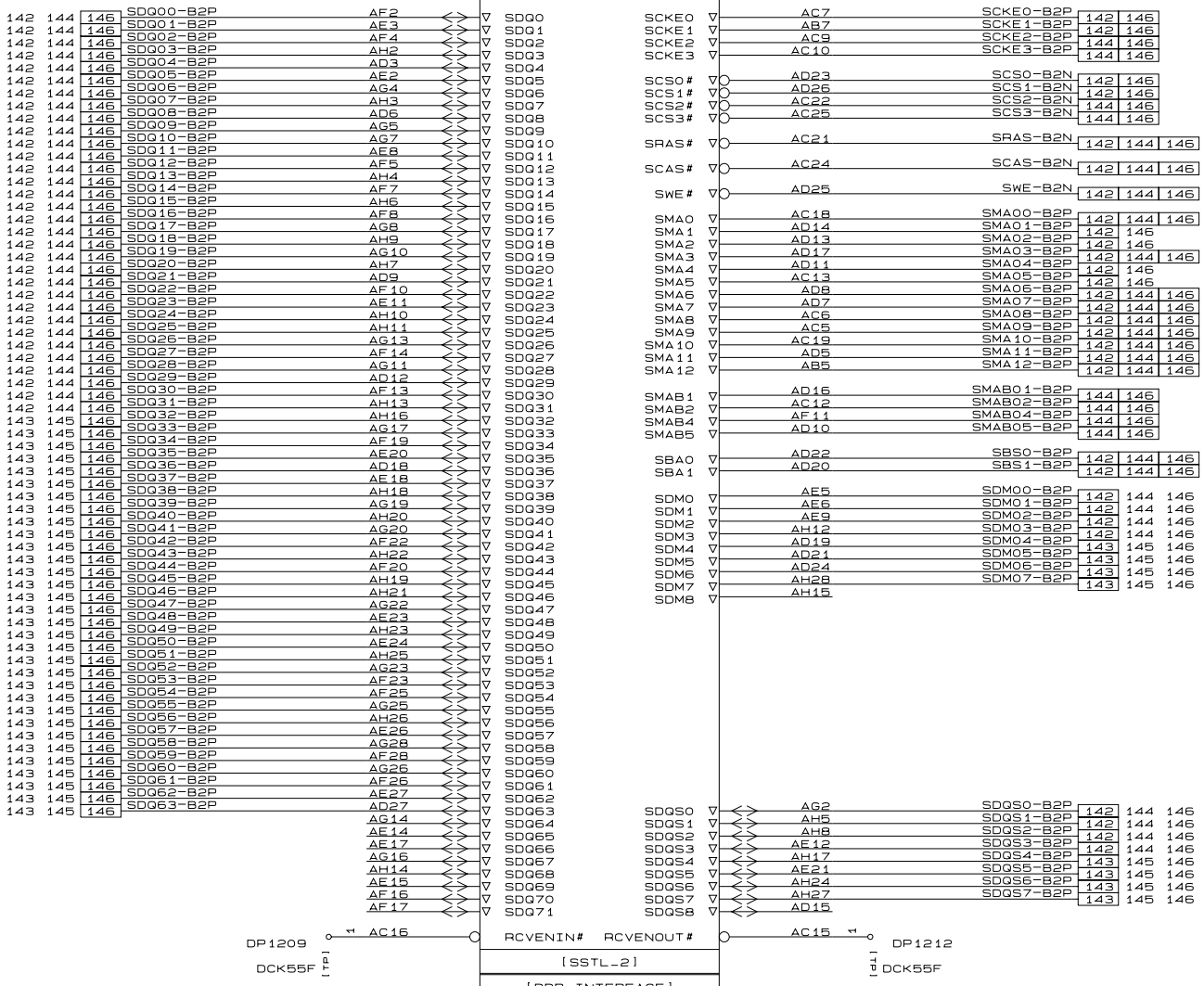
2003.11.17 14:10

TOSHIBA CORPORATION

GDM460000635

Montana-GT GMCH 3/10
[U82852GME]
IC1200

(VCCSM)



REF:Red(ES)ver.6/30

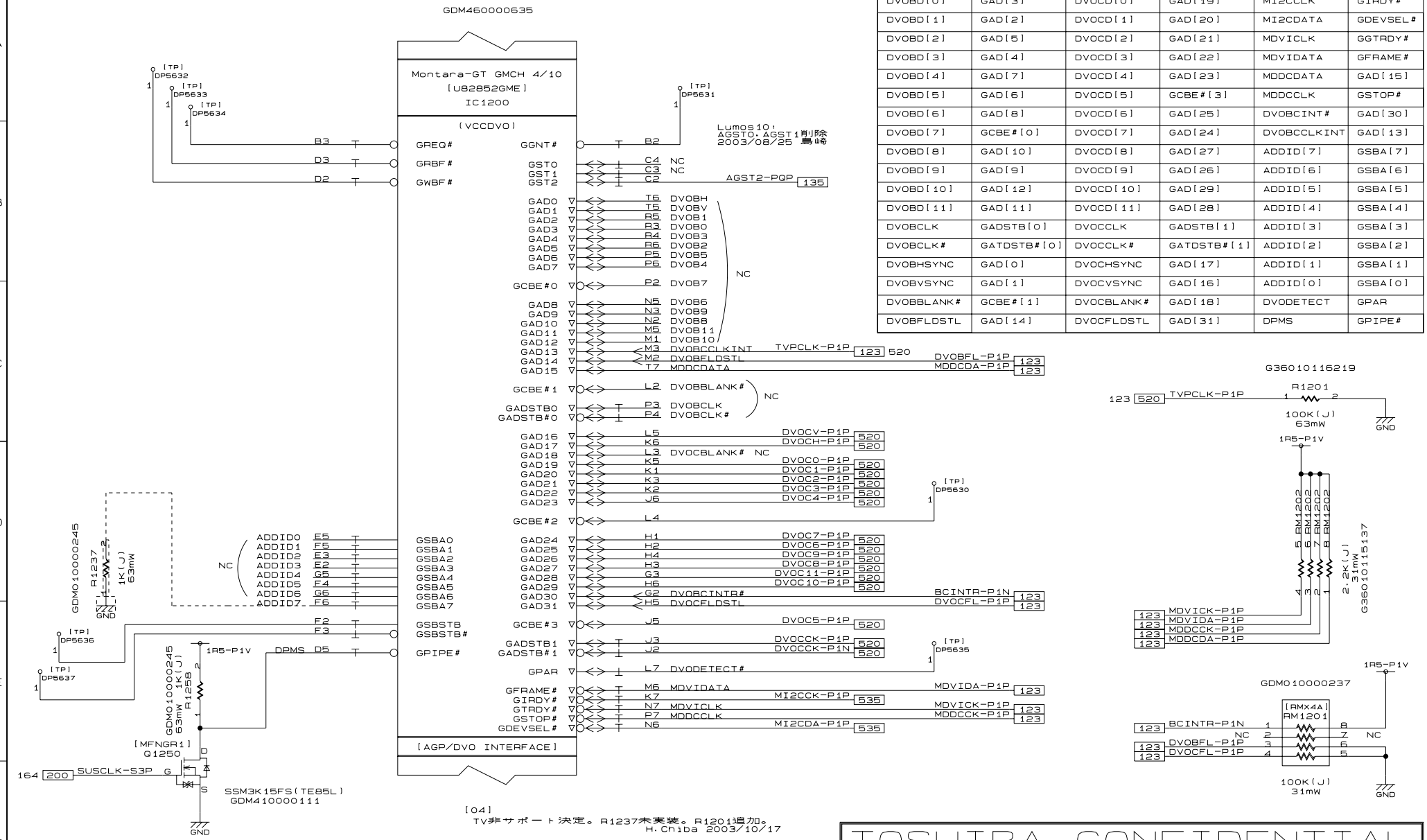
TOSHIBA CONFIDENTIAL

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki	FLM1M2	Montara-GT 3	122	17	00	360048117

2003.11.17 14:10

TOSHIBA CORPORATION

DVO MODE	AGP MODE	DVO MODE	AGP MODE	DVO MODE	AGP MODE
DVOBD[0]	GAD[3]	DVOCD[0]	GAD[19]	MI2CCLK	GIRDY#
DVOBD[1]	GAD[2]	DVOCD[1]	GAD[20]	MI2CDATA	GDEVSEL#
DVOBD[2]	GAD[5]	DVOCD[2]	GAD[21]	MDVICLK	GGTRDY#
DVOBD[3]	GAD[4]	DVOCD[3]	GAD[22]	MDVIDATA	GFRAME#
DVOBD[4]	GAD[7]	DVOCD[4]	GAD[23]	MDDCDATA	GAD[15]
DVOBD[5]	GAD[6]	DVOCD[5]	GCBE#[3]	MDDCCLK	GSTOP#
DVOBD[6]	GAD[8]	DVOCD[6]	GAD[25]	DVOBCINT#	GAD[30]
DVOBD[7]	GCBE#[0]	DVOCD[7]	GAD[24]	DVOBCCLKINT	GAD[13]
DVOBD[8]	GAD[10]	DVOCD[8]	GAD[27]	ADDID[7]	GSBA[7]
DVOBD[9]	GAD[9]	DVOCD[9]	GAD[26]	ADDID[6]	GSBA[6]
DVOBD[10]	GAD[12]	DVOCD[10]	GAD[29]	ADDID[5]	GSBA[5]
DVOBD[11]	GAD[11]	DVOCD[11]	GAD[28]	ADDID[4]	GSBA[4]
DVOBCLK	GADSTB[0]	DVOCCLK	GADSTB[1]	ADDID[3]	GSBA[3]
DVOBCLK#	GATDSTB#[0]	DVOCCLK#	GATDSTB#[1]	ADDID[2]	GSBA[2]
DVOBHSYNC	GAD[0]	DVOCHSYNC	GAD[17]	ADDID[1]	GSBA[1]
DVOBVSYNC	GAD[1]	DVOCVSYNC	GAD[16]	ADDID[0]	GSBA[0]
DVOBBLANK#	GCBE#[1]	DVOCBLANK#	GAD[18]	DVODETECT	GPAR
DVOBFLDSTL	GAD[14]	DVOCFLDSTL	GAD[31]	DPMS	GPIPE#



[04]
TV非サポート決定。R1237未実装。R1201追加。
H.Chiba 2003/10/17

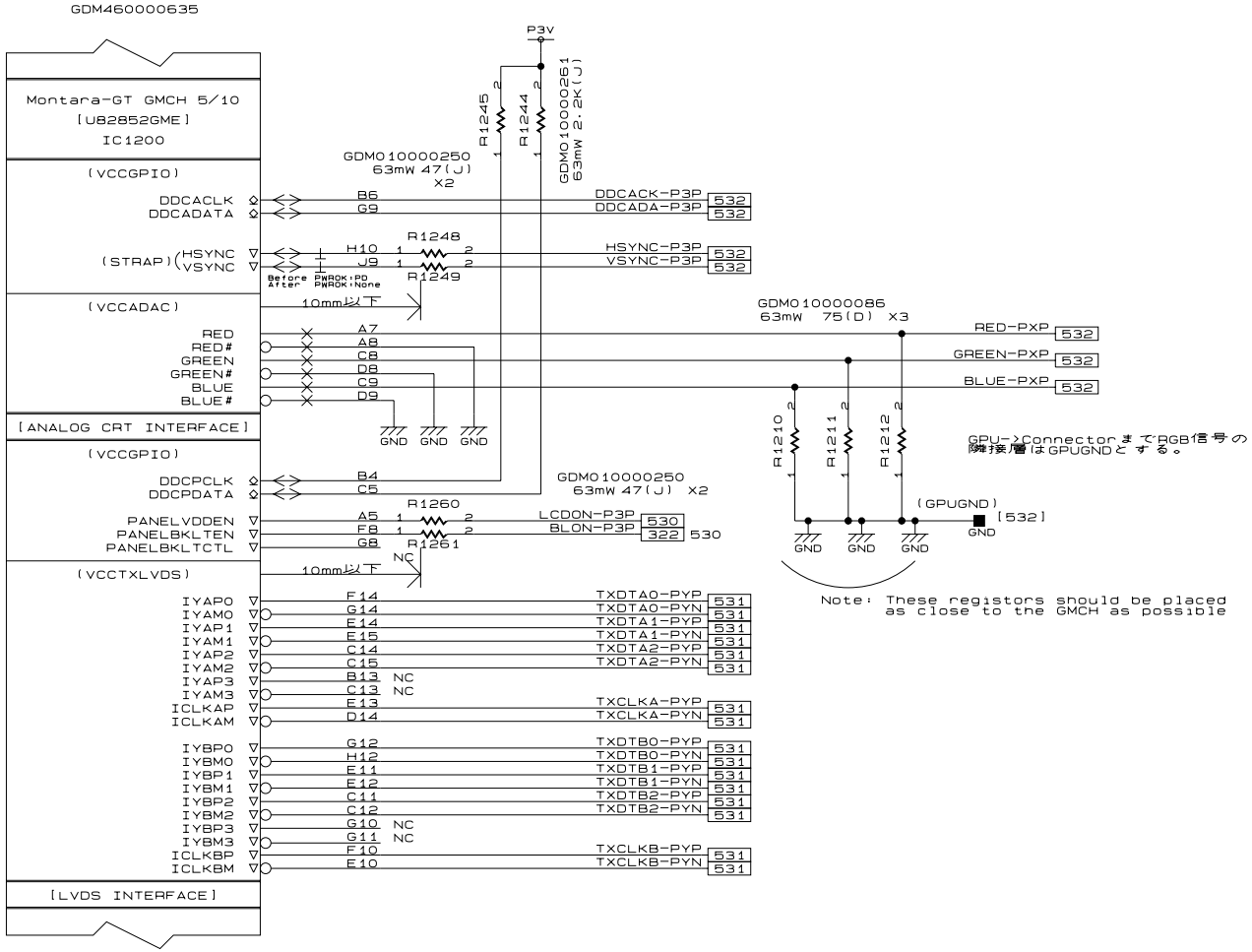
GSBSTB/GSBSTB#にTP追加。
AGP-DVO対応表追加。 2003/8/11 H.Chiba

REF:Red(ES)ver.6/30
2002/5/14

TOSHIBA CONFIDENTIAL

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
H. Chiba 2003/8/11	FLM1M2	Montara-GT 4	123	18	04	360048117

2003.11.17 14:10



ロケーションをLumos10Gに合わせる。
2003/8/14 Y.Shimazaki

IC1200をM-GTに変更。 2003/8/11 H.Chiba

REF:ASAHI VP(FASSM1) SH.124

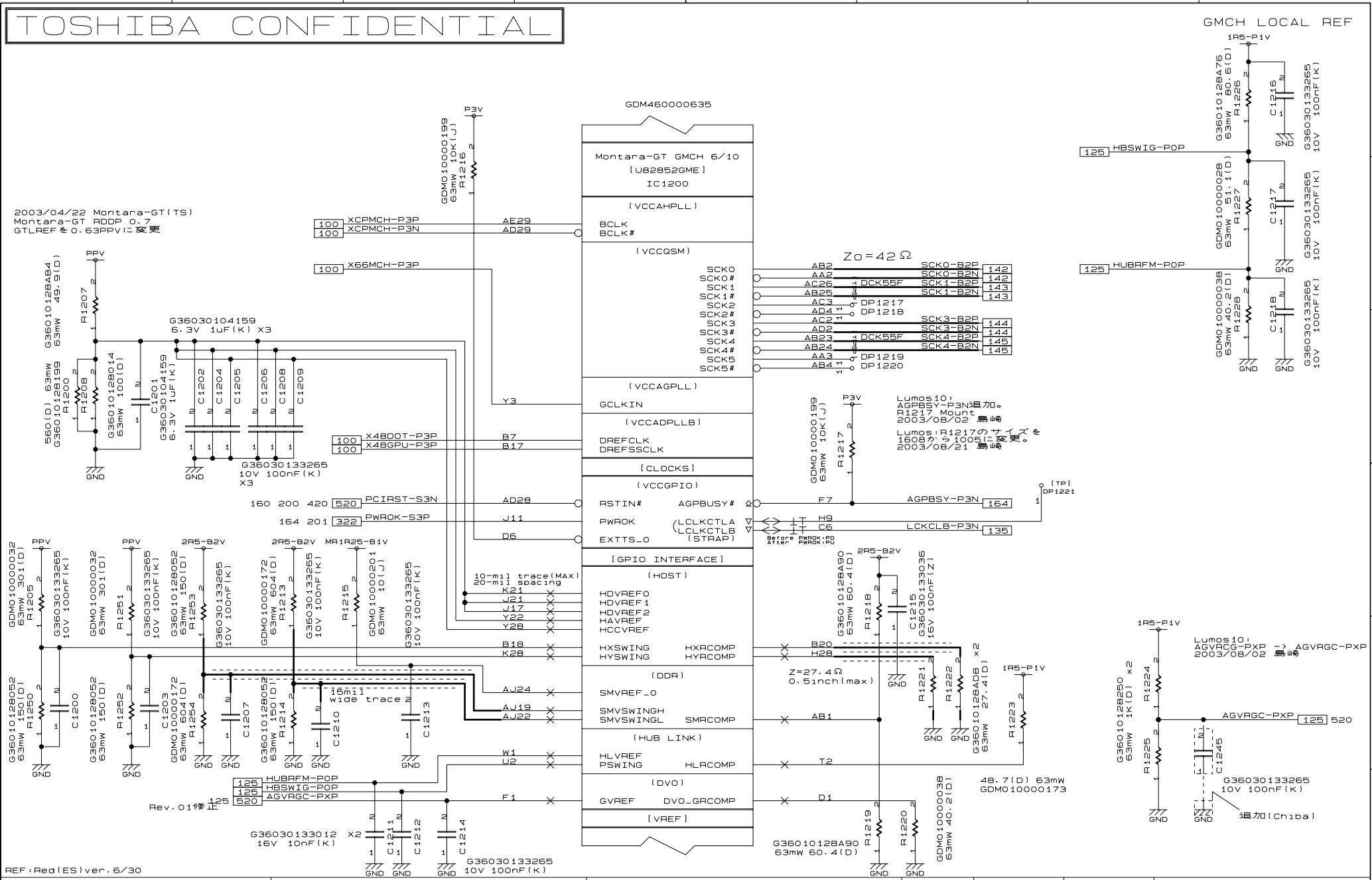
(DAC&LVDS I/F)

TOSHIBA CONFIDENTIAL

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/8/11 H.Chiba Y.Shimazaki	FLM1M2	Montara-GT 5	124	19	00	360048117

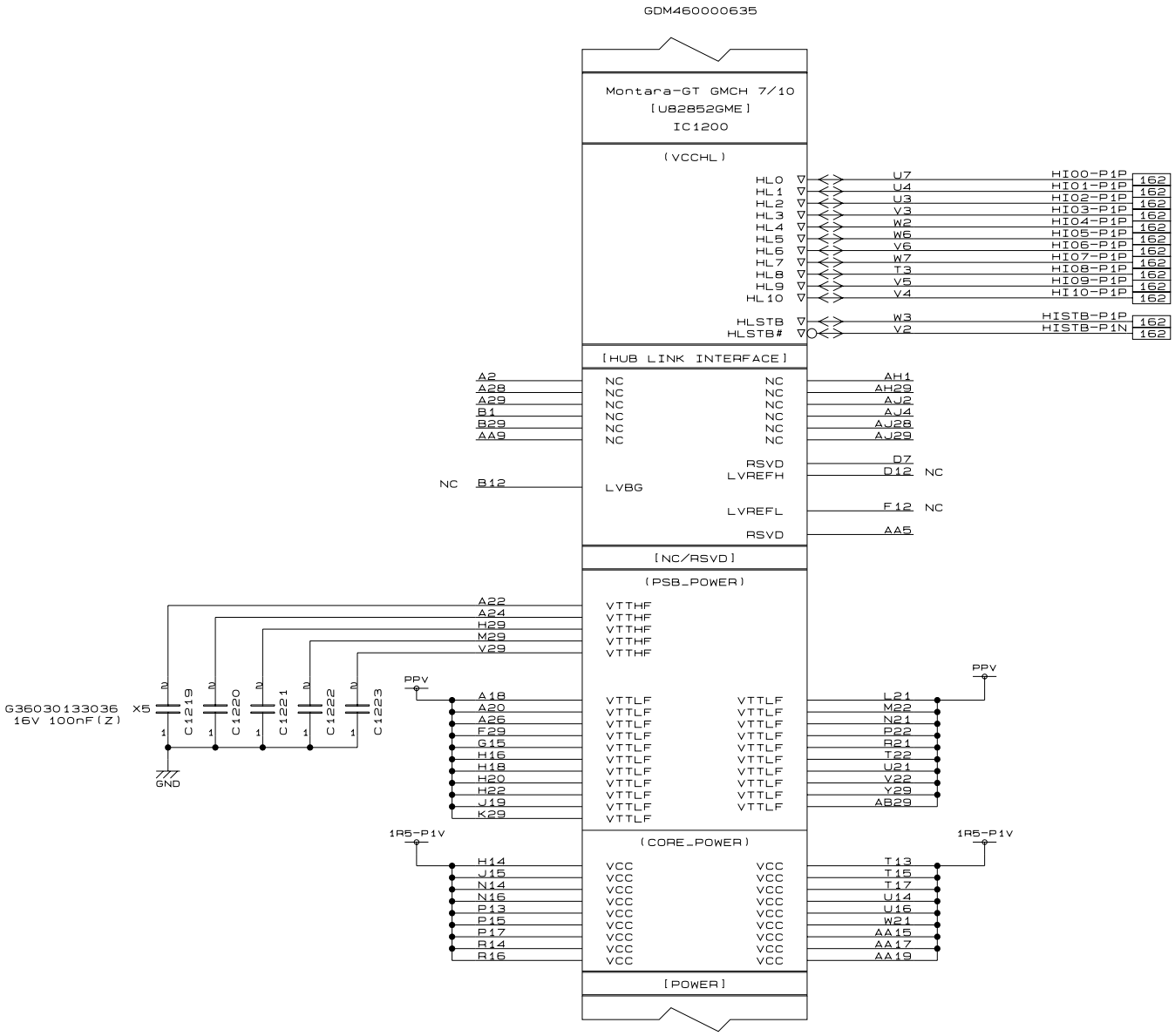
2003.11.17 14:10

TOSHIBA CORPORATION



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki 2002/11/26 H.Chiba	FLM1M2	Montara-GT 6	125	20	00	360048117

2003.11.17 14:10



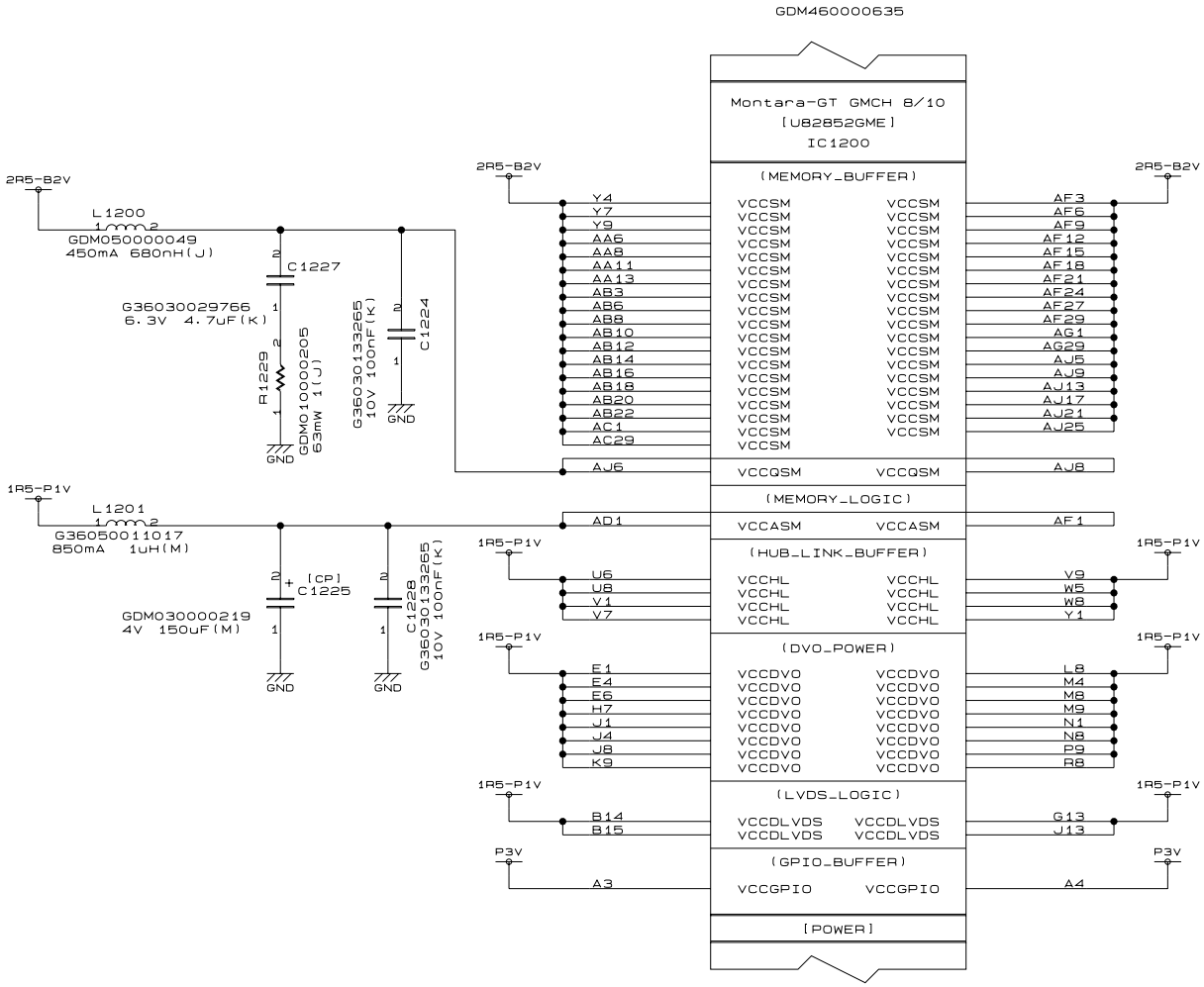
LVDS Test Pad削除。2003/07/30 H.Chiba

REF:Red(ES)ver.6/30

TOSHIBA CONFIDENTIAL

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki 2002/8/8 H.Chiba	FLM1M2	Montara-GT 7	126	21	00	360048117

2003.11.17 14:10



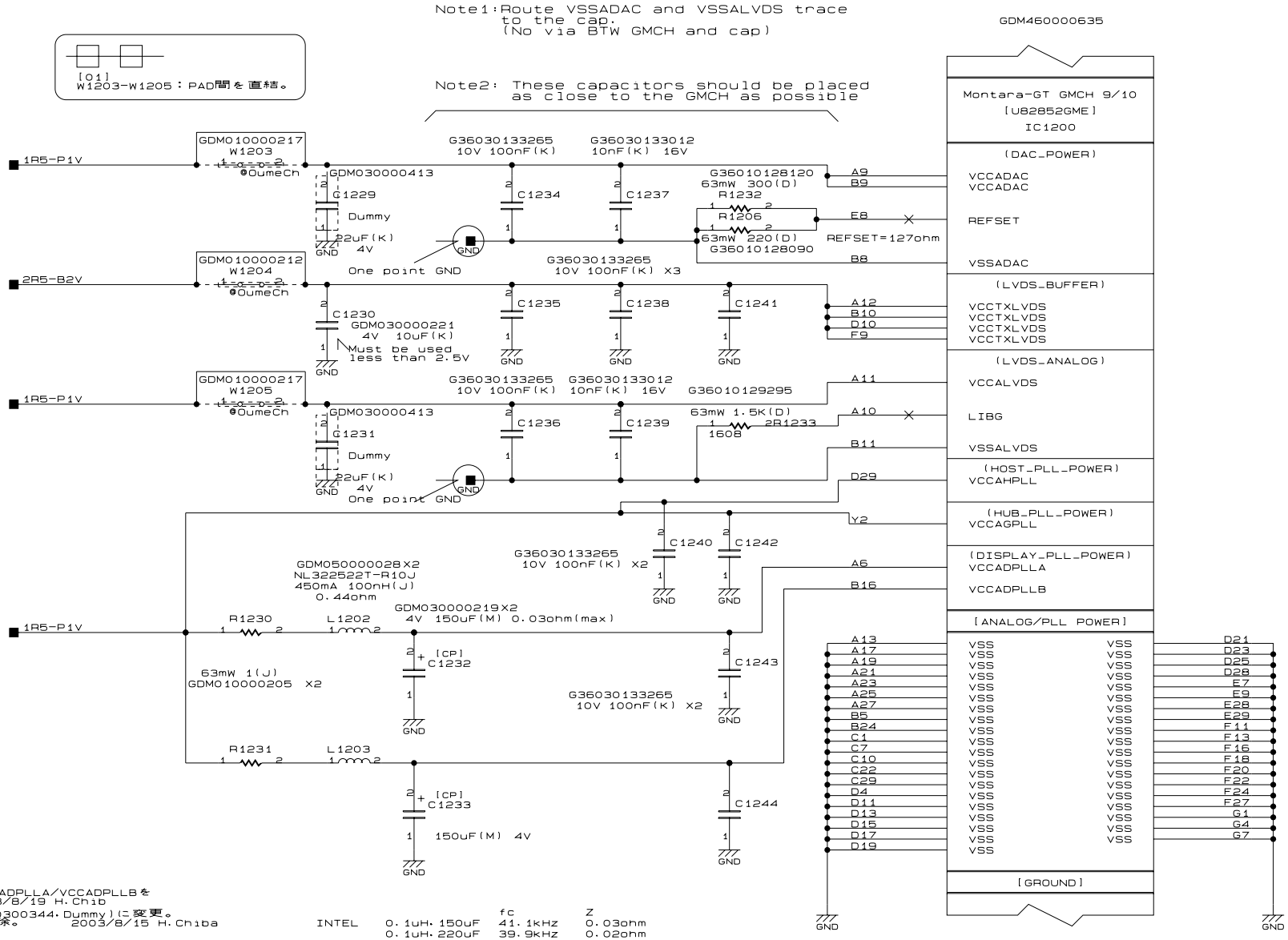
REF:Red(ES)ver.6/30

TOSHIBA CONFIDENTIAL

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki 2002/5/20 H.Chiba	FLM1M2	Montara-GT 8	127	22	00	360048117

2003.11.17 14:10

TOSHIBA CORPORATION



VCCAHPLL/VCCAGPLL/VCCADPLLA/VCCADPLLBを 1.5Vに修正。 2003/8/19 H.Chib

C1229,C1231を小型版(K0300344-Dummy)に変更。
「要評価」のコメント削除。 2003/8/15 H.Chiba

ロケーション番号をLumos10Gに合わせる。
2003/8/14 Y.Shimazaki

~~C1245,C1232を小型版(K0300344-Dummy)に変更。~~
~~「要評価」のコメント削除。~~ 2003/8/11 H.Chiba

	INTEL	fc	Z
0.1uH.150uF		41.1kHz	0.03ohm
0.1uH.220uF		39.9kHz	0.02ohm

REF:HAWAII10 CS(HWISY1) SH.128

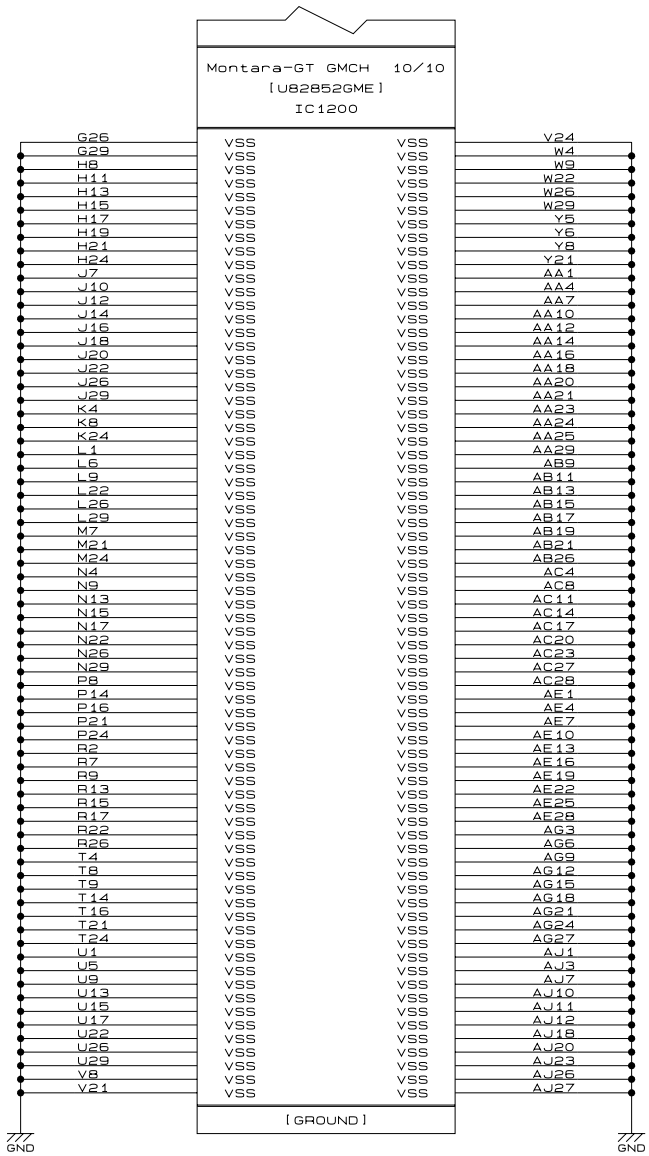
(ANALOG/PLL POWER)

TOSHIBA CONFIDENTIAL

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
2003/8/19 H.Chiba Y.Shimazaki	FLM1M2	Montara-GT 9	128	23	00	360048117

2003.11.17 14:10

GDM460000635



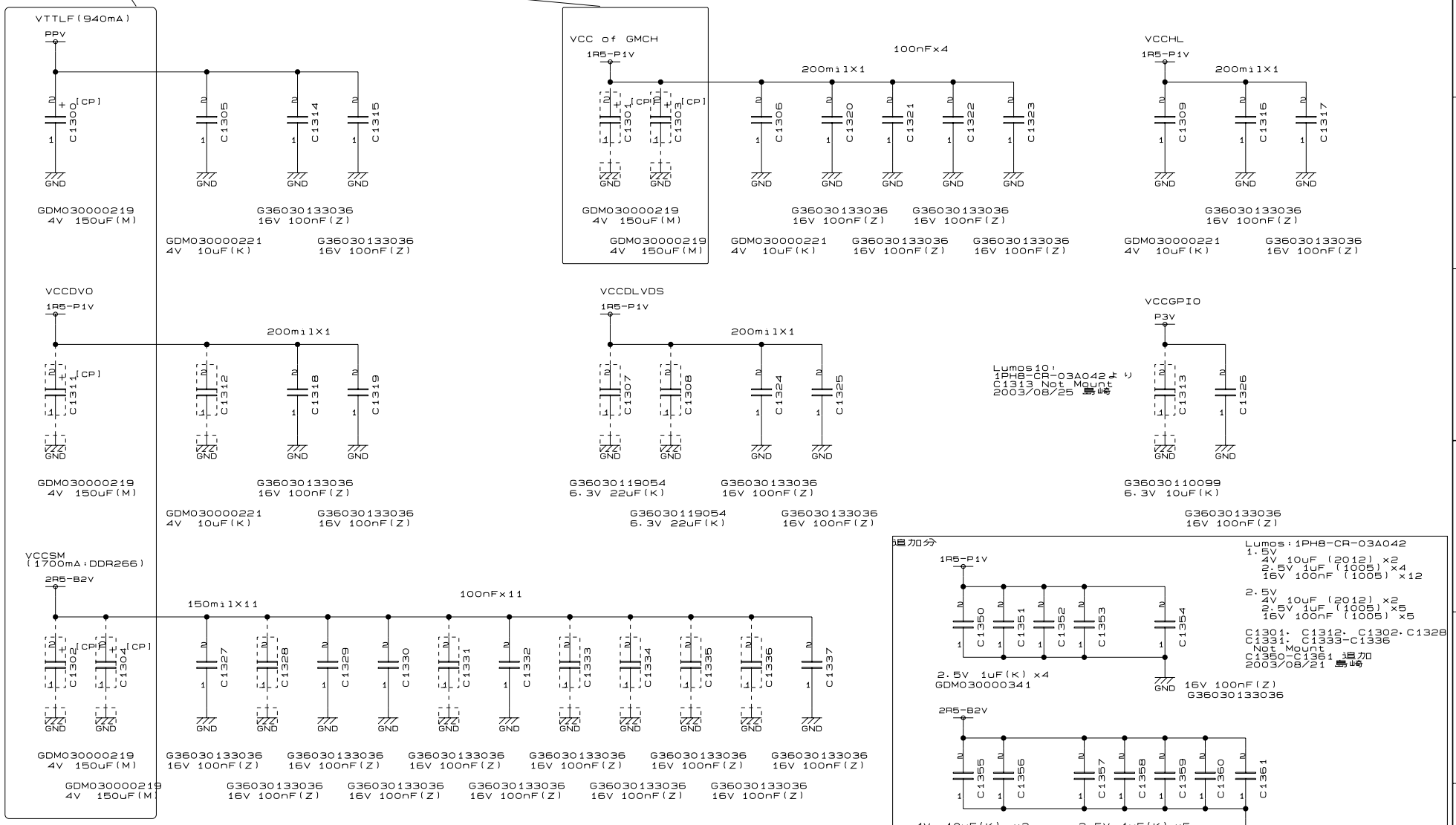
REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki	FLM1M2	Montara-GT 10	129	24	00	360048117

2003.11.17 14:10

ROC. 1300

BGAのフットプリントから10mm以内に配置すること（10uF，100nFはピン直傍）



TOSHIBA CONFIDENTIAL

REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y. Shimazaki	FLM1M2	Montara-GT 11	130	25	00	360048117

ROC.1350

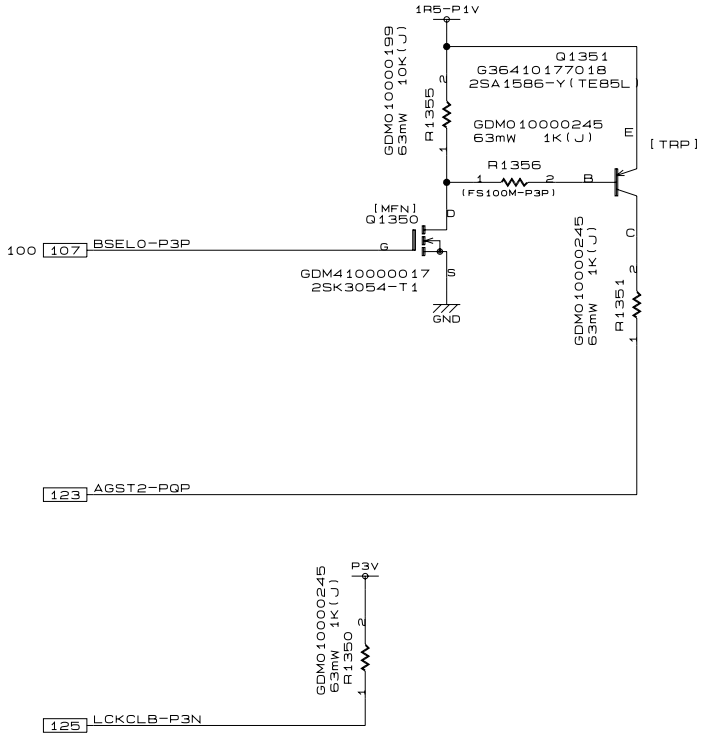
	L	H
LCLKCTRLB (def=L)	?	Mobile_P4 Mobile_Prescott Celeron
HSYNC (def=L)	Normal_Op.	XOR_test
VSYNC (def=L)	Normal_Op.	AllZ_test
DVODETECT =GPAR (def=L)	DVO	AGP
SBA[7:0]	ADDID=no_default	
GST[2:0]	durling CPURST# internal pulled-down after CPURST# and C3/D1/D3 Hi-Z	

GST[210]	PSB	MEM	GFX(L)	GFX(H)
000	400	266	133	200
001	400	200	100	200
010	400	200	100	133
011	400	266	133	266
100	533	266	133	200
101	533	266	133	266
110	533	333	166	266
111	400	333	166	250

FS[210]	CPUCLK
000	66
001	100
010	200
011	133

BSEL[10]	CPUCLK
00	(100MHz)
01	133MHz
10	(200MHz)
11	RESERVED

		LFM	HFM
Northwood	Volt	1.200V	1.550V
	VID	111010	101100
Prescott	Volt	1.150V	1.325V
	VID	111100	110101



Lumos10:内蔵Gfxなので、
AGPAB-PQP (DVODETECT)の
プルアップ抵抗削除。
2003/08/02 島崎

Lumos10:内蔵Gfxのため、
AGST0・AGST1の行き先がない
ので、削除。
AGST2についてもCleanup。
2003/08/25 島崎

TOSHIBA CONFIDENTIAL

REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
2003/5/24 Y.Shimazaki	FLM1M2	Montara-GT strap	135	26	00	360048117

2003.11.17 14:10

TOSHIBA CORPORATION

ROC. 1400

[04] 1PH8-CRE-03A040より
RM1400-1415を削除。
(メモリI/Fはシリーズ抵抗無し)
2003/10/20 鳥崎

TOSHIBA CONFIDENTIAL

REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki	FLM1M2	DDR I/F Rs 1	140	27	04	360048117

2003.11.17 14:10

TOSHIBA CORPORATION

5T03655D

[04] 1PH8-CRE-03A040より
W1400-1418, RM1416-1419を削除。
〈メモリI/Fはシリーズ抵抗無し〉
2003/10/20 島崎

TOSHIBA CONFIDENTIAL

REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki	FLM1M2	DDR I/F Rs 2	141	28	04	360048117

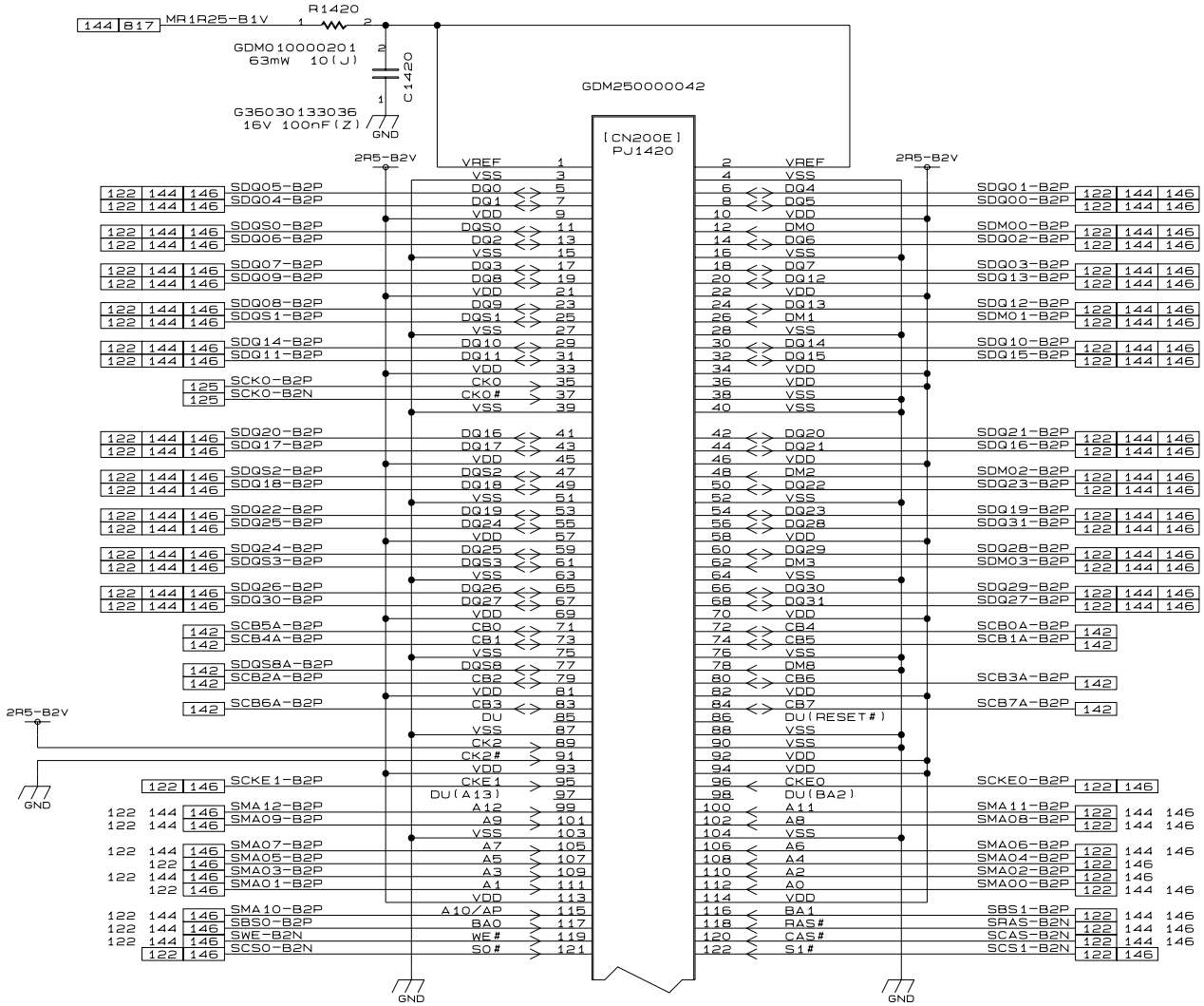
2003.11.17 14:10

TOSHIBA CORPORATION

ROC. 1420

[02] レイアウトに従い、
PJ1420とPJ1440でコネクタ
の入れ替え
2003/09/18 島崎

[04] 1PH8-CRE-03B005
メモリのシリーズ抵抗削除に
より、信号名変更。
2003/10/20 島崎



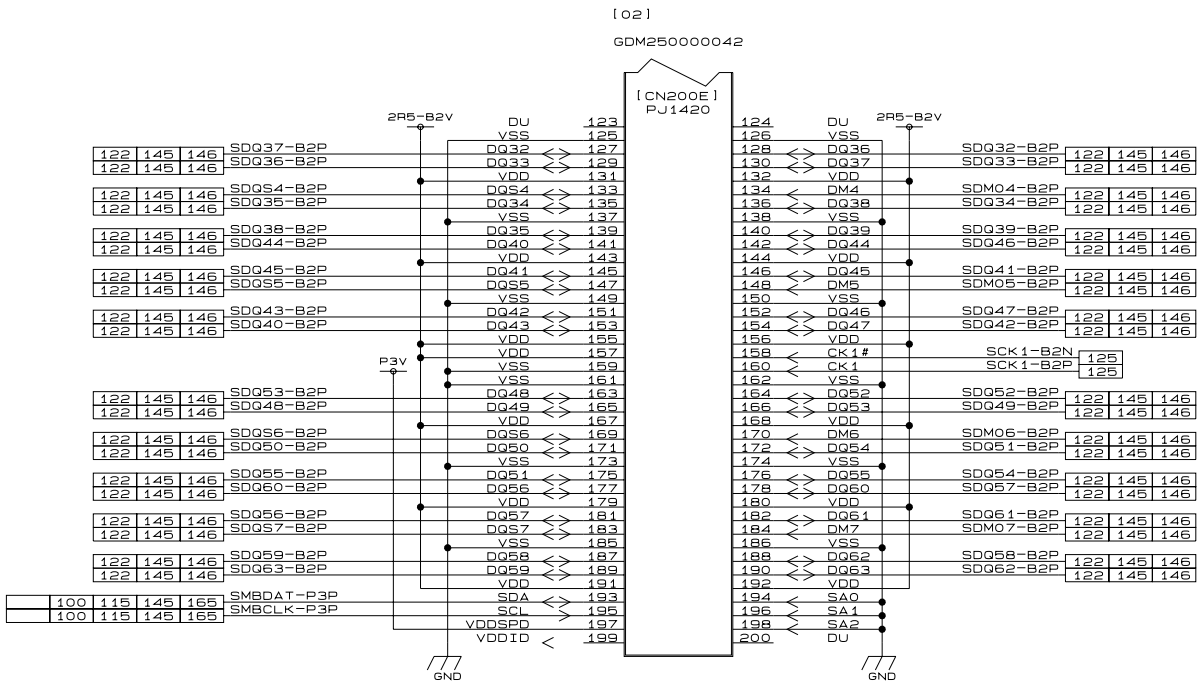
TOSHIBA CONFIDENTIAL

REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y. Shimazaki	FLM1M2	SO-DIMM0 1	142	29	04	360048117

2003.11.17 14:10

TOSHIBA CORPORATION



[04] 1PHB-CRE-03B005
メモリのシリーズ抵抗削除により、信号名変更。
2003/10/20 島崎

TOSHIBA CONFIDENTIAL

REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki	FLM1M2	SO-DIMM0 2	143	30	04	360048117

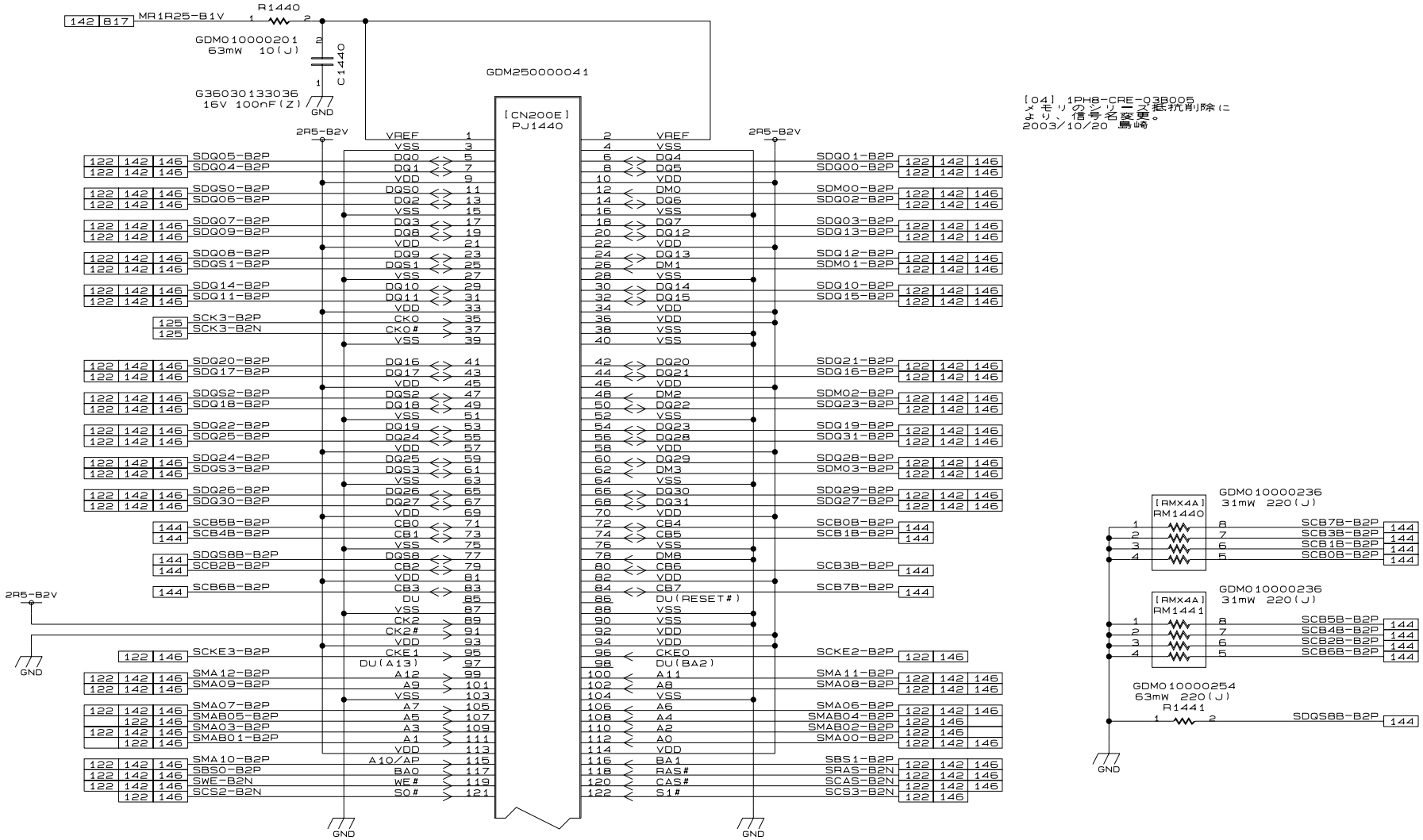
2003.11.17 14:10

TOSHIBA CORPORATION

ROC. 1440

[02] レイアウトに従い、
PJ1420とPJ1440でコネクタ
の入れ替え
2003/09/18 島崎

[04] 1PH8-CRE-03B005
メモリのシリーズ抵抗削除に
より、信号名変更。
2003/10/20 島崎



TOSHIBA CONFIDENTIAL

REF:Red(ES)ver.6/30

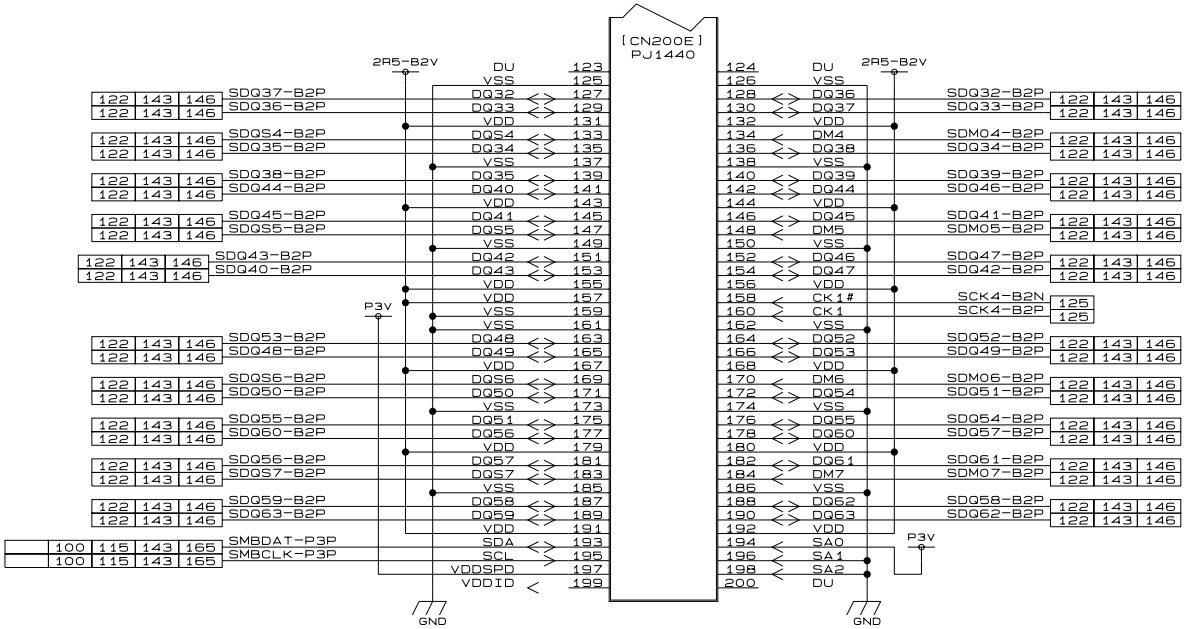
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y. Shimazaki	FLM1M2	SO-DIMM1 1	144	31	04	360048117

2003.11.17 14:10

TOSHIBA CORPORATION

[02]

GDM250000041



[04] 1PH8-CRE-03B005
メモリのシリーズ抵抗削除により、信号名変更。
2003/10/20 島崎

REF:Red(ES)ver.6/30

TOSHIBA CONFIDENTIAL

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y. Shimazaki	FLM1M2	SO-DIMM1 2	145	32	04	360048117

2003.11.17 14:10

TOSHIBA CORPORATION

ROC. 1460

1R25-B1V

GDM0100002340 31mW 56(J) [RMX4A] RM1460
122 142 144 SDQ07-B2P 1 8
122 142 144 SDQ06-B2P 2 7
122 142 144 SDQ04-B2P 3 6
122 142 144 SDQ05-B2P 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1461
122 142 144 SDQ01-B2P 1 8
122 142 144 SDQ00-B2P 2 7
122 142 144 SDQ02-B2P 3 6
122 142 144 SDQ03-B2P 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1462
122 142 144 SDQ11-B2P 1 8
122 142 144 SDQ14-B2P 2 7
122 142 144 SDQ08-B2P 3 6
122 142 144 SDQ09-B2P 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1463
122 142 144 SDQ13-B2P 1 8
122 142 144 SDQ12-B2P 2 7
122 142 144 SDQ10-B2P 3 6
122 142 144 SDQ15-B2P 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1464
122 142 144 SDQ22-B2P 1 8
122 142 144 SDQ18-B2P 2 7
122 142 144 SDQ17-B2P 3 6
122 142 144 SDQ20-B2P 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1465
122 142 144 SDQ21-B2P 1 8
122 142 144 SDQ16-B2P 2 7
122 142 144 SDQ23-B2P 3 6
122 142 144 SDQ19-B2P 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1466
122 142 144 SDQ30-B2P 1 8
122 142 144 SDQ26-B2P 2 7
122 142 144 SDQ24-B2P 3 6
122 142 144 SDQ25-B2P 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1467
122 142 144 SDQ31-B2P 1 8
122 142 144 SDQ28-B2P 2 7
122 142 144 SDQ29-B2P 3 6
122 142 144 SDQ27-B2P 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1468
122 143 145 SDQ38-B2P 1 8
122 143 145 SDQ35-B2P 2 7
122 143 145 SDQ36-B2P 3 6
122 143 145 SDQ37-B2P 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1469
122 143 145 SDQ32-B2P 1 8
122 143 145 SDQ33-B2P 2 7
122 143 145 SDQ34-B2P 3 6
122 143 145 SDQ39-B2P 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1470
122 143 145 SDQ40-B2P 1 8
122 143 145 SDQ43-B2P 2 7
122 143 145 SDQ45-B2P 3 6
122 143 145 SDQ44-B2P 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1471
122 143 145 SDQ46-B2P 1 8
122 143 145 SDQ41-B2P 2 7
122 143 145 SDQ47-B2P 3 6
122 143 145 SDQ42-B2P 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1472
122 143 145 SDQ53-B2P 1 8
122 143 145 SDQ48-B2P 2 7
122 143 145 SDQ50-B2P 3 6
122 143 145 SDQ55-B2P 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1473
122 143 145 SDQ52-B2P 1 8
122 143 145 SDQ49-B2P 2 7
122 143 145 SDQ51-B2P 3 6
122 143 145 SDQ54-B2P 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1474
122 143 145 SDQ60-B2P 1 8
122 143 145 SDQ56-B2P 2 7
122 143 145 SDQ59-B2P 3 6
122 143 145 SDQ63-B2P 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1475
122 143 145 SDQ57-B2P 1 8
122 143 145 SDQ61-B2P 2 7
122 143 145 SDQ58-B2P 3 6
122 143 145 SDQ62-B2P 4 5

[04] 1PH8-CRE-03B005
メモリのシリーズ抵抗削除に
より、信号名変更。
2003/10/20 島崎

1R25-B1V

122 142 144 SDQS0-B2P — GDM0100002000 63mW 56(J) R1460
122 142 144 SDQS1-B2P — GDM0100002000 63mW 56(J) R1461
122 142 144 SDQS2-B2P — GDM0100002000 63mW 56(J) R1462
122 142 144 SDQS3-B2P — GDM0100002000 63mW 56(J) R1463
122 143 145 SDQS4-B2P — GDM0100002000 63mW 56(J) R1464
122 143 145 SDQS5-B2P — GDM0100002000 63mW 56(J) R1465
122 143 145 SDQS6-B2P — GDM0100002000 63mW 56(J) R1466
122 143 145 SDQS7-B2P — GDM0100002000 63mW 56(J) R1467
122 142 144 SDM00-B2P — GDM0100002000 63mW 56(J) R1468
122 142 144 SDM01-B2P — GDM0100002000 63mW 56(J) R1469
122 142 144 SDM02-B2P — GDM0100002000 63mW 56(J) R1470
122 142 144 SDM03-B2P — GDM0100002000 63mW 56(J) R1471
122 143 145 SDM04-B2P — GDM0100002000 63mW 56(J) R1472
122 143 145 SDM05-B2P — GDM0100002000 63mW 56(J) R1473
122 143 145 SDM06-B2P — GDM0100002000 63mW 56(J) R1474
122 143 145 SDM07-B2P — GDM0100002000 63mW 56(J) R1475

GDM0100002340 31mW 56(J) [RMX4A] RM1476
122 144 SCKE3-B2P 1 8
122 144 SCKE0-B2P 2 7
122 142 SCKE1-B2P 3 6
122 144 SCKE2-B2P 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1477
122 144 SCS3-B2N 1 8
122 142 SCS1-B2N 2 7
122 142 SCS0-B2N 3 6
122 144 SCS2-B2N 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1478
122 142 144 SMA11-B2P 1 8
122 142 144 SMA08-B2P 2 7
122 142 144 SMA06-B2P 3 6
4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1479
122 142 SMA05-B2P 1 8
122 142 SMA01-B2P 2 7
122 142 SMA04-B2P 3 6
122 142 SMA02-B2P 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1480
1 8
122 142 144 SMA07-B2P 2 7
122 142 144 SMA09-B2P 3 6
122 142 144 SMA12-B2P 4 5

GDM0100002340 31mW 56(J) [RMX4A] RM1481
122 142 144 SMA03-B2P 1 8
122 142 144 SMA10-B2P 2 7
3 6
122 142 144 SBS0-B2P 4 5

122 142 144 SRAS-B2N — GDM0100002000 63mW 56(J) R1476
122 142 144 SCAS-B2N — GDM0100002000 63mW 56(J) R1477
122 142 144 SWE-B2N — GDM0100002000 63mW 56(J) R1478

GDM0100002340 31mW 56(J) [RMX4A] RM1482
122 144 SMAB04-B2P 1 8
122 144 SMAB02-B2P 2 7
122 144 SMAB05-B2P 3 6
122 144 SMAB01-B2P 4 5

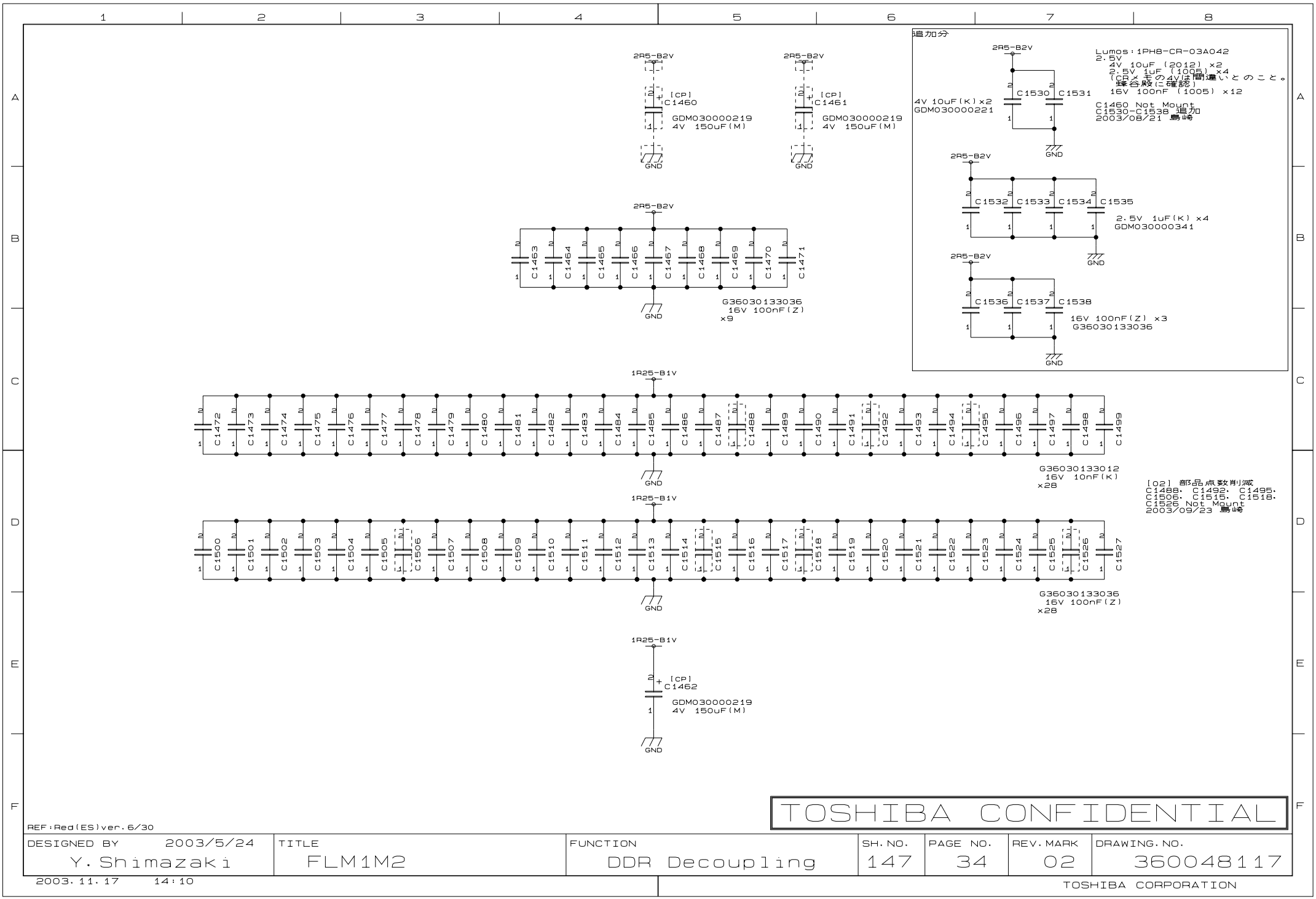
122 142 144 SMA00-B2P — GDM0100002000 63mW 56(J) R1479
122 142 144 SBS1-B2P — GDM0100002000 63mW 56(J) R1480

REF:Red(ES)ver.6/30

TOSHIBA CONFIDENTIAL

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y. Shimazaki	FLM1M2	DDR I/F RT	146	33	04	360048117

2003.11.17 14:10



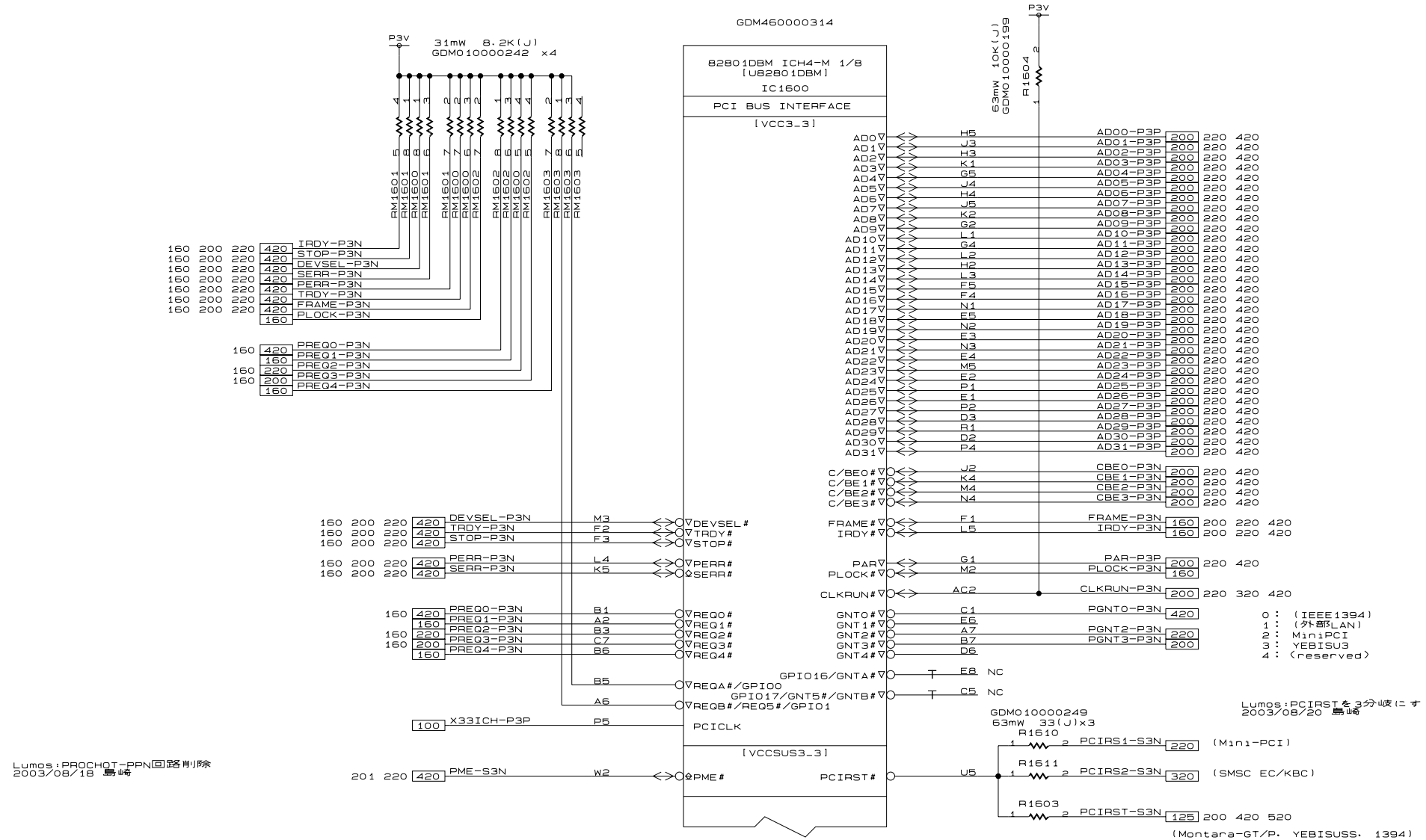
TOSHIBA CONFIDENTIAL

REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y. Shimazaki	FLM1M2	DDR Decoupling	147	34	02	360048117

2003.11.17 14:10

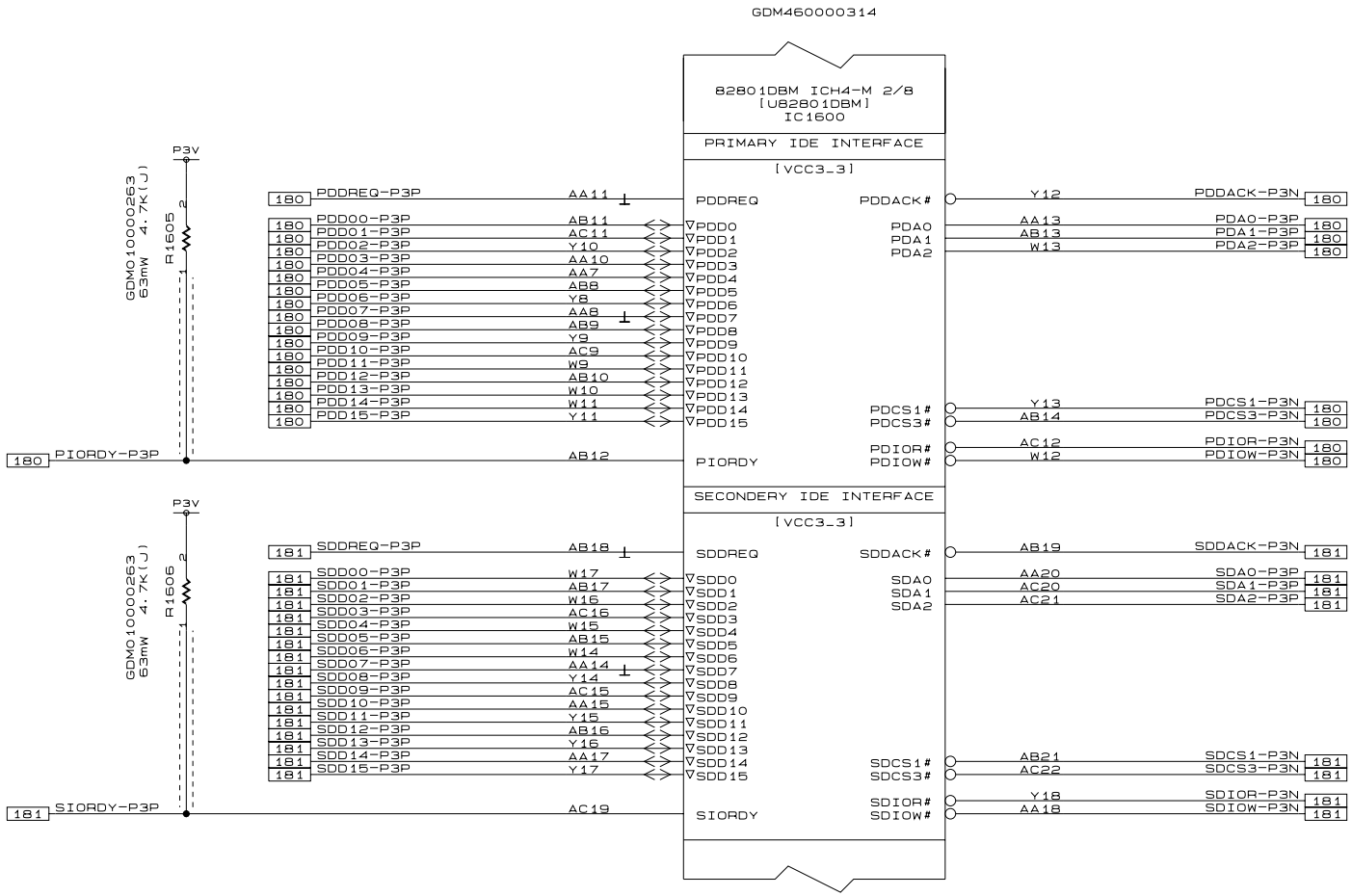
TOSHIBA CORPORATION



REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y. Shimazaki	FLM1M2	ICH4-M 1	160	35	00	360048117

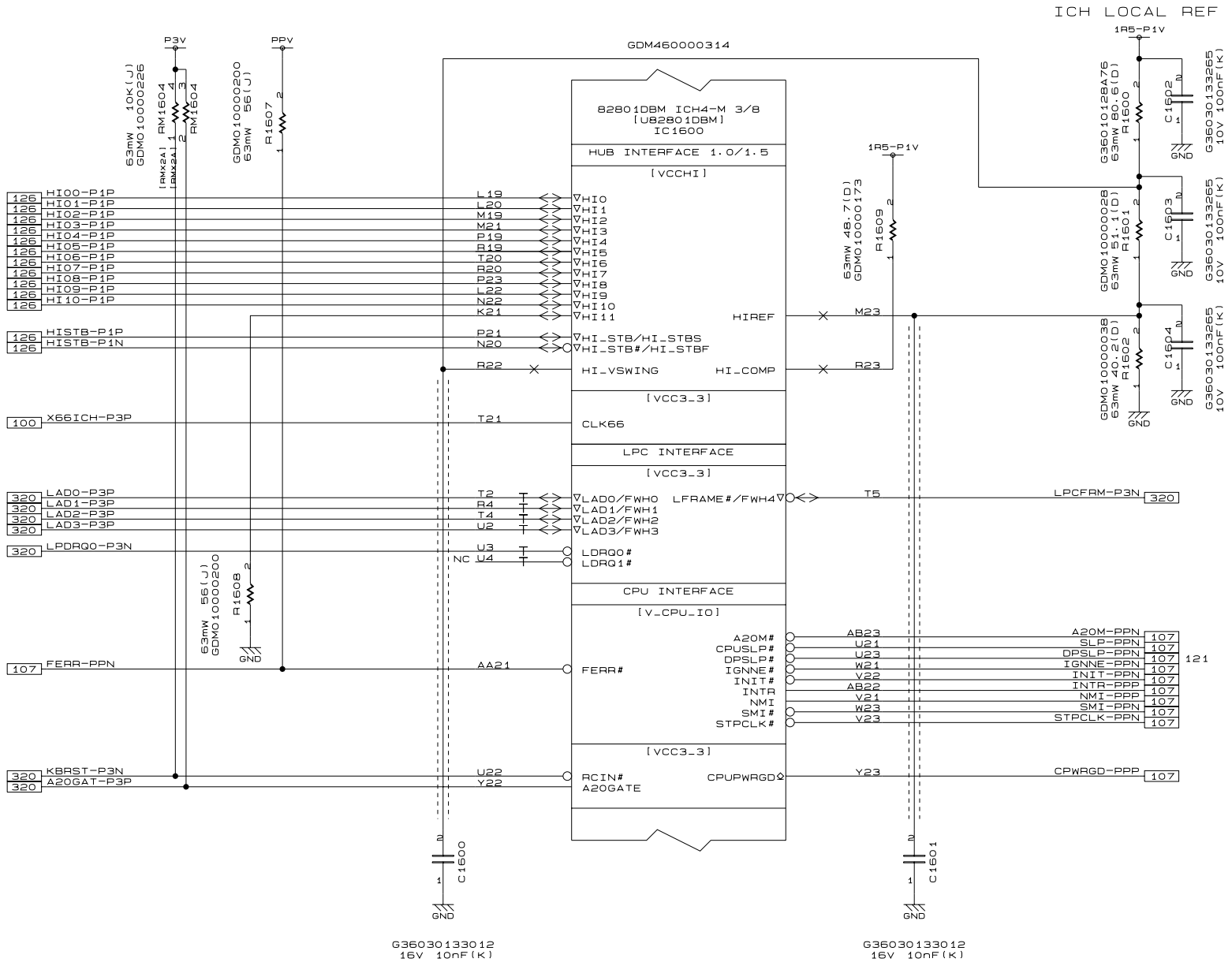
2003.11.17 14:10



REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki	FLM1M2	ICH4-M 2	161	36	00	360048117

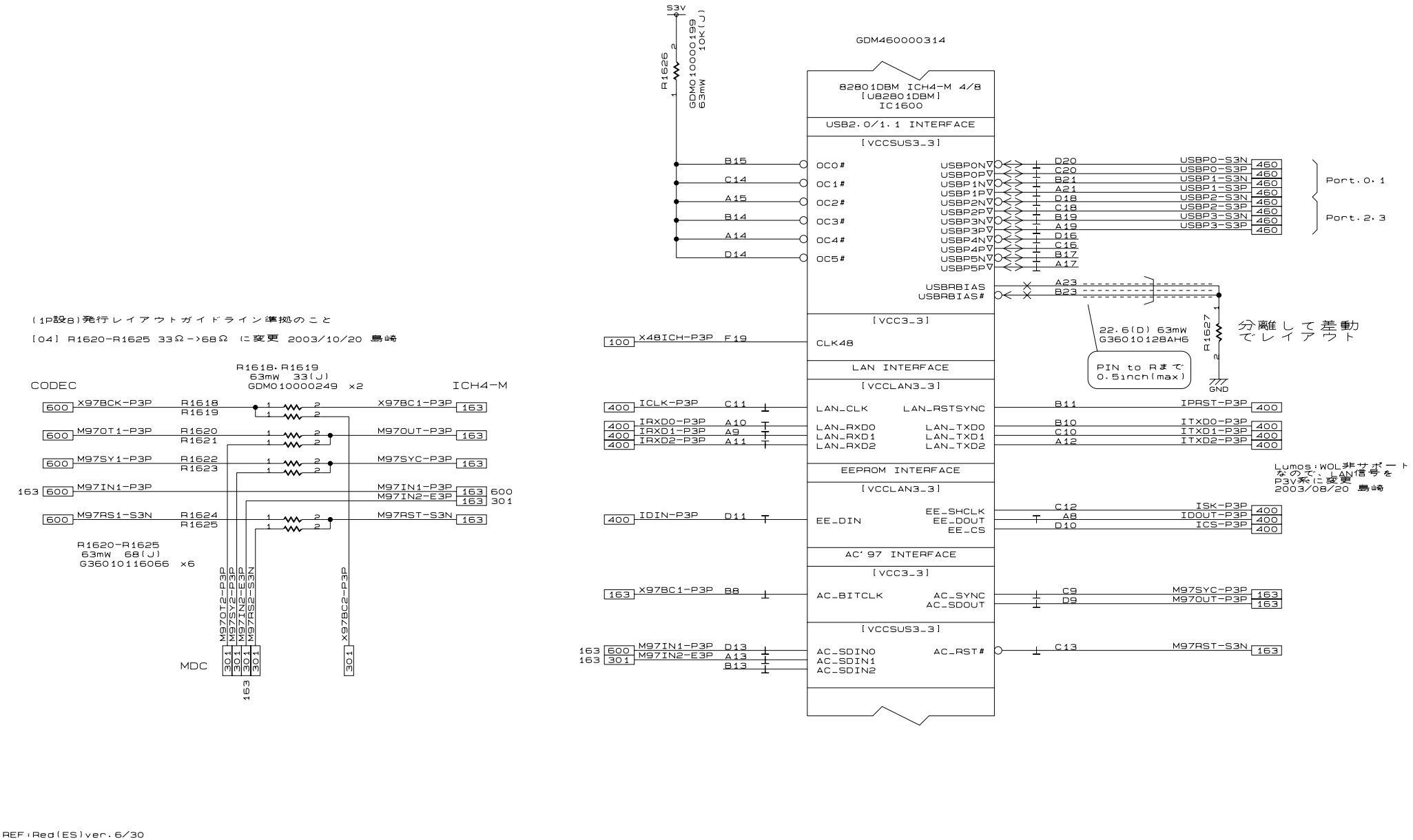
2003.11.17 14:10



REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki	FLM1M2	ICH4-M 3	162	37	00	360048117

2003.11.17 14:10

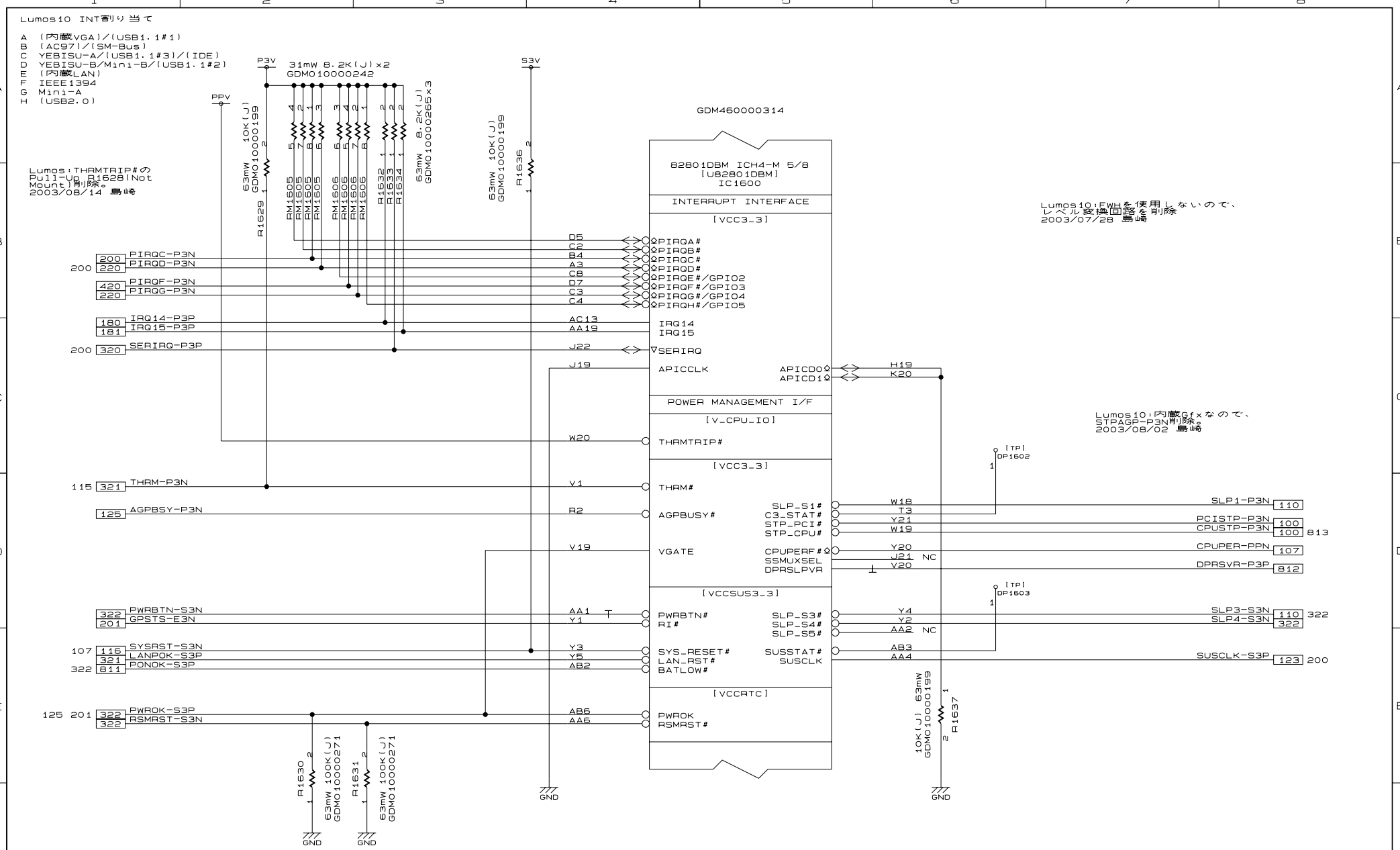


REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki	FLM1M2	ICH4-M 4	163	38	04	360048117

2003.11.17 14:10

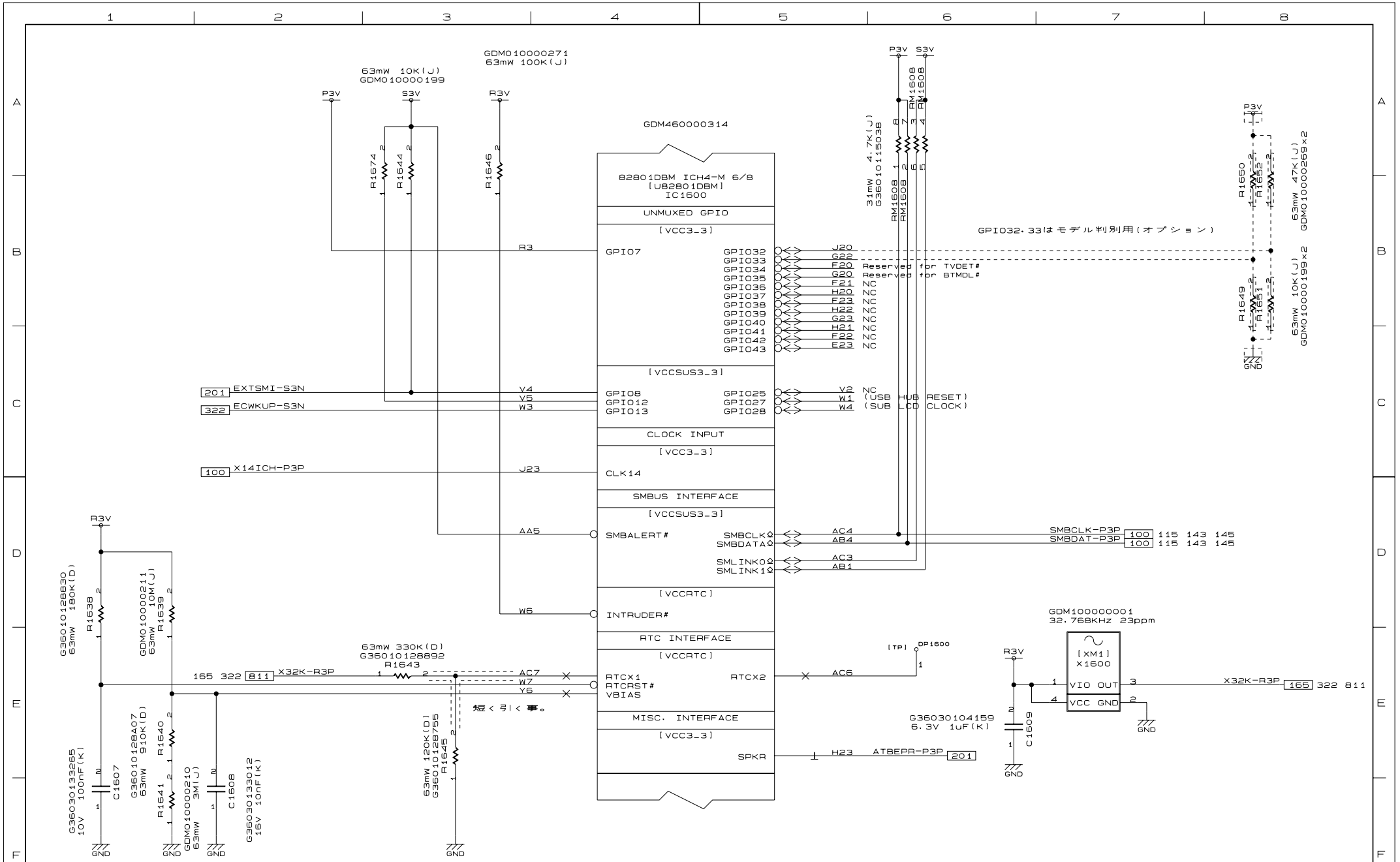
TOSHIBA CORPORATION



REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y. Shimazaki	FLM1M2	ICH4-M 5	164	39	00	360048117

2003.11.17 14:10

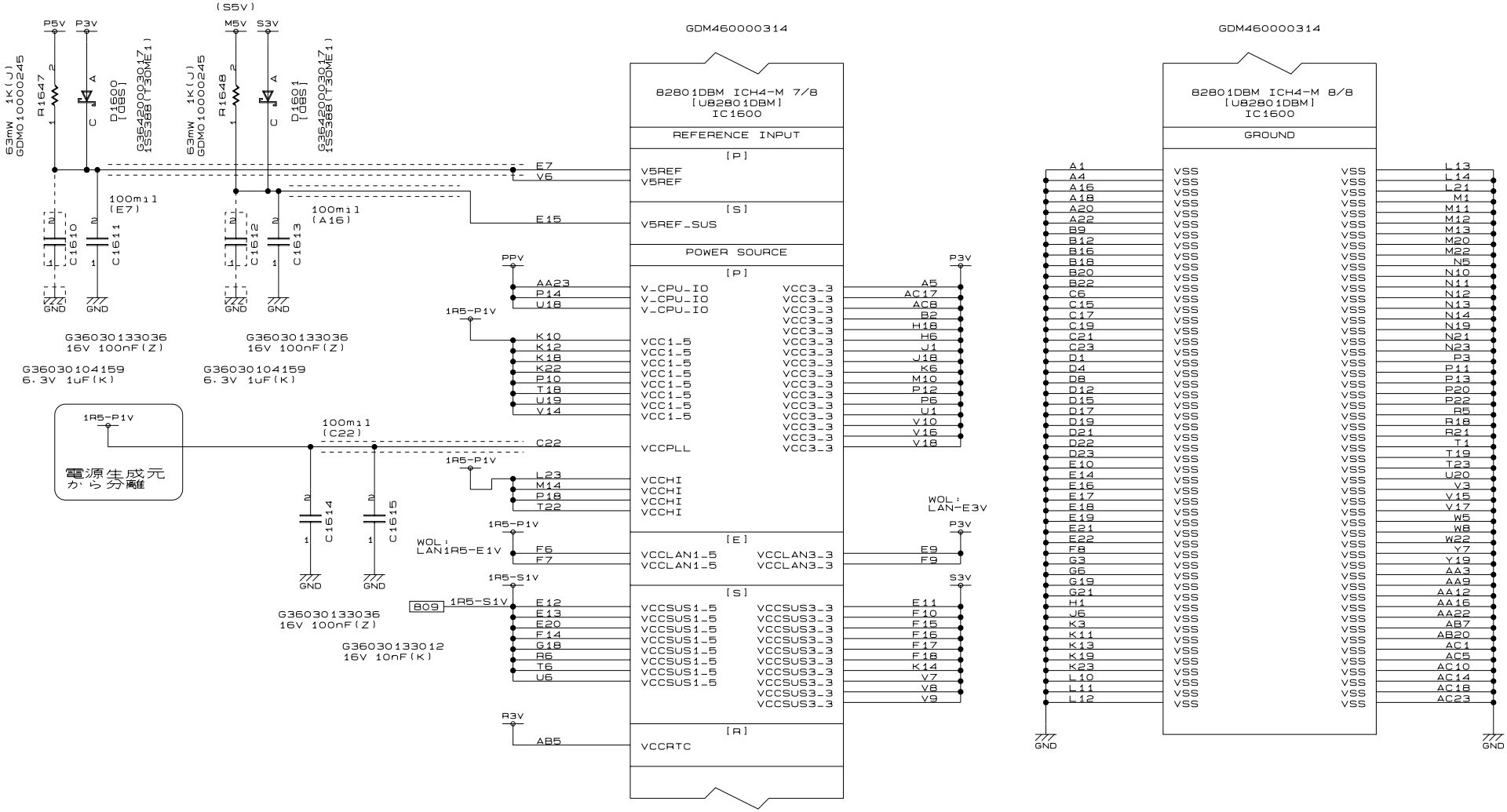


REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y. Shimazaki	FLM1M2	ICH4-M 6	165	40	00	360048117

2003.11.17 14:10

[02] 部品点数削減
C1610, C1612 Not Mount
2003/09/23 島崎



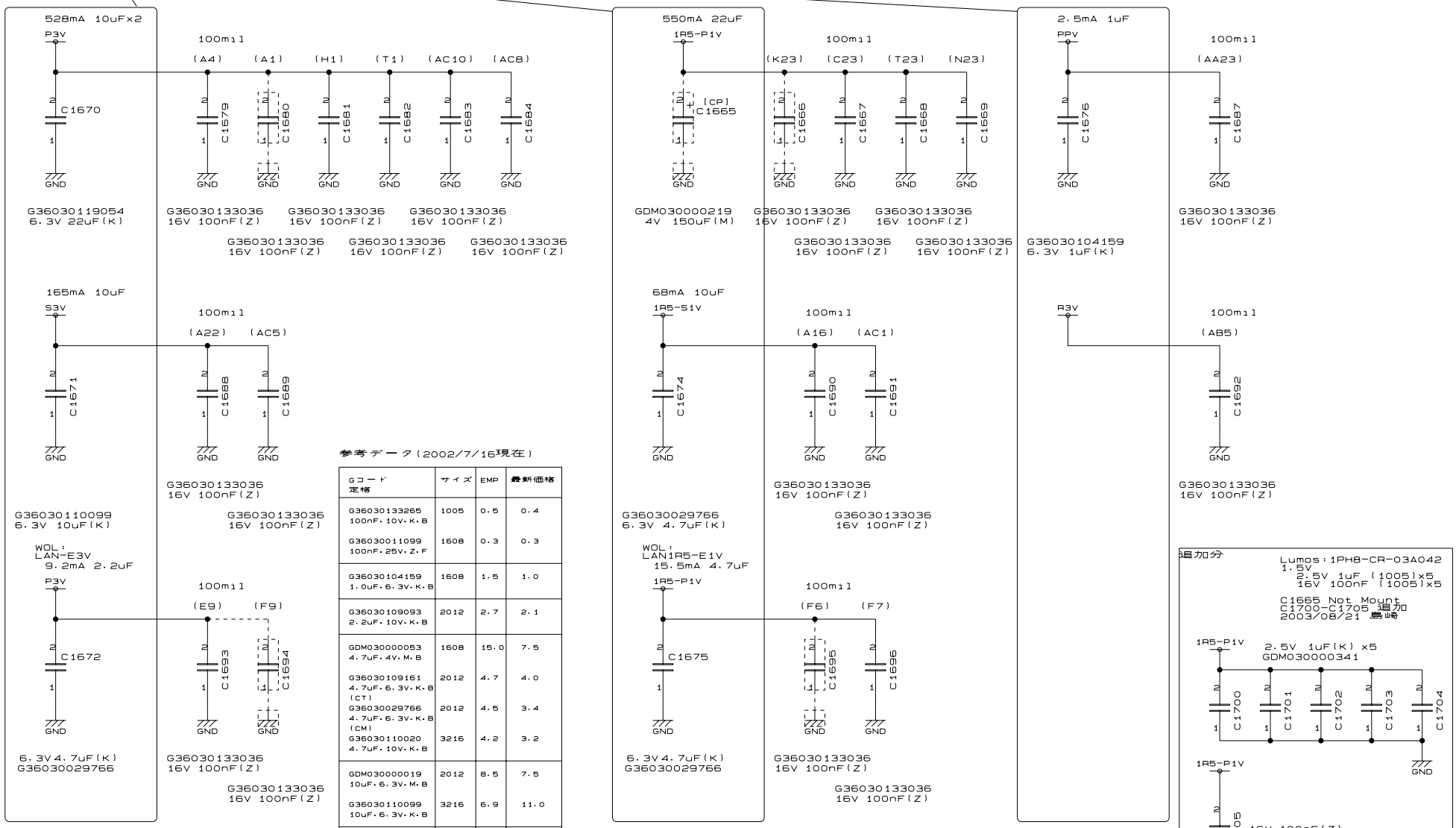
REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y. Shimazaki	FLM1M2	ICH4-M 7	166	41	02	360048117

2003.11.17 14:10

TOSHIBA CORPORATION

BGAのフットプリント以内に配置すること（100nFはピン直傍）



参考データ（2002/7/16現在）

Gコード 定格	サイズ	EMP	最新価格
G36030133265 100nF.10V.K.B	1005	0.5	0.4
G36030011099 100nF.25V.Z.F	1608	0.3	0.3
G36030104159 1.0uF.6.3V.K.B	1608	1.5	1.0
G36030109093 2.2uF.10V.K.B	2012	2.7	2.1
GDM030000053 4.7uF.4V.M.B	1608	15.0	7.5
G36030109161 4.7uF.6.3V.K.B (CT)	2012	4.7	4.0
G36030029766 4.7uF.6.3V.K.B (CM)	2012	4.5	3.4
G36030110020 4.7uF.10V.K.B	3216	4.2	3.2
GDM030000019 10uF.6.3V.M.B	2012	8.5	7.5
G36030110099 10uF.6.3V.K.B	3216	6.9	11.0
G36030119054 22uF.6.3V.K.B	3225	18.8	18.0
G36030046022 22uF.10V.K.B	4532	17.1	29.0

[02] 部品点数削減
C1666, C1680, C1694,
C1695 Not Mount
2003/09/23 島崎

パスコン容量変更（2002/9/19）[変更要求メモPHY-CR-02A_C-073]
C1514/C1575/C1530定数変更 C1582-3削除 C1581マウント

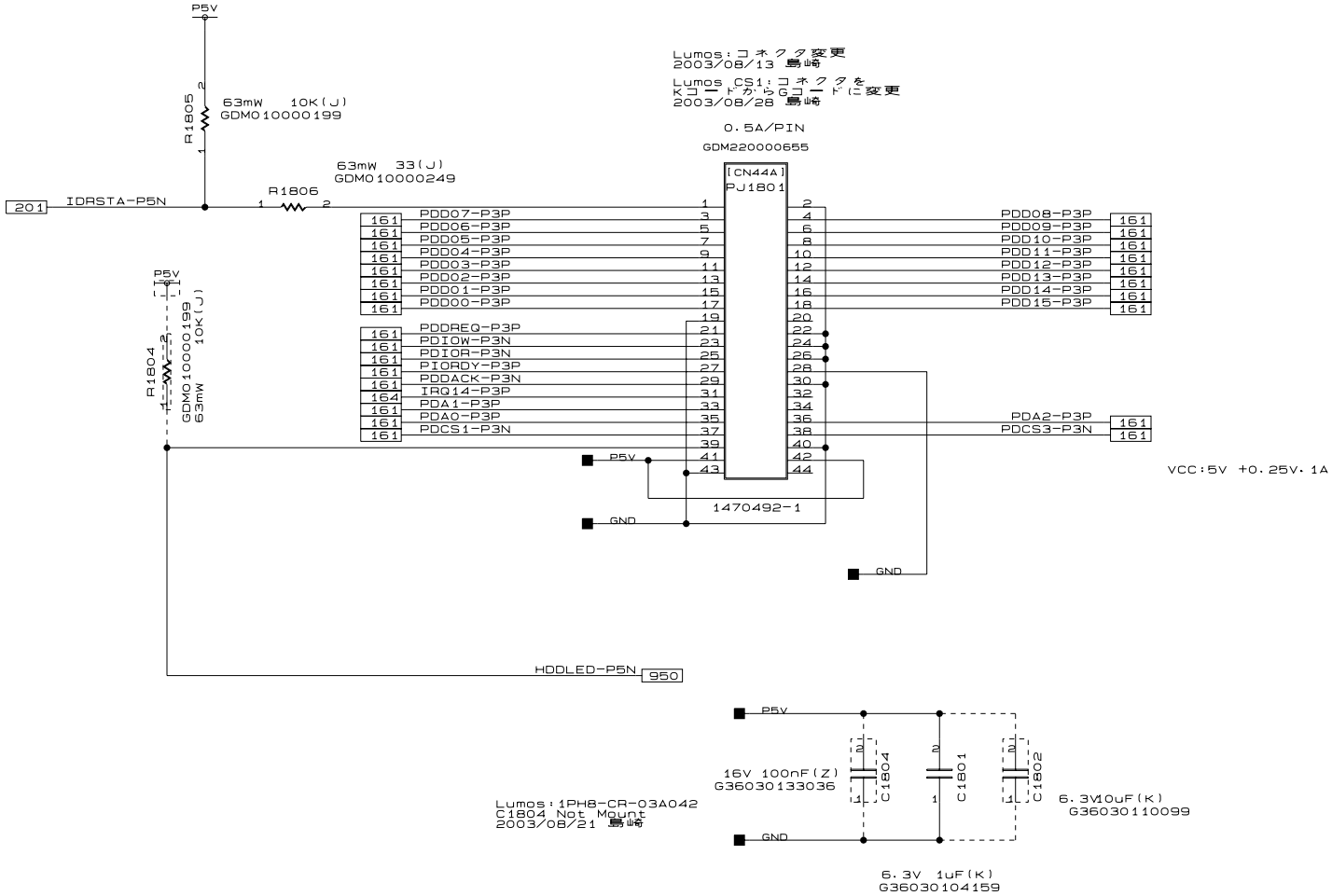
パスコン容量変更（2002/7/16）
P3.3V=10uFx2. S3.3V=10uF. LAN3.3V=2.2uF
P1.5V=10uFx2. S1.5V=10uF. LAN1.5V=4.7uF
PTV =1uF. P1.8V=10uF

REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y. Shimazaki	FLM1M2	ICH4-M 8	167	42	02	360048117

2003.11.17 14:10

5T03655D

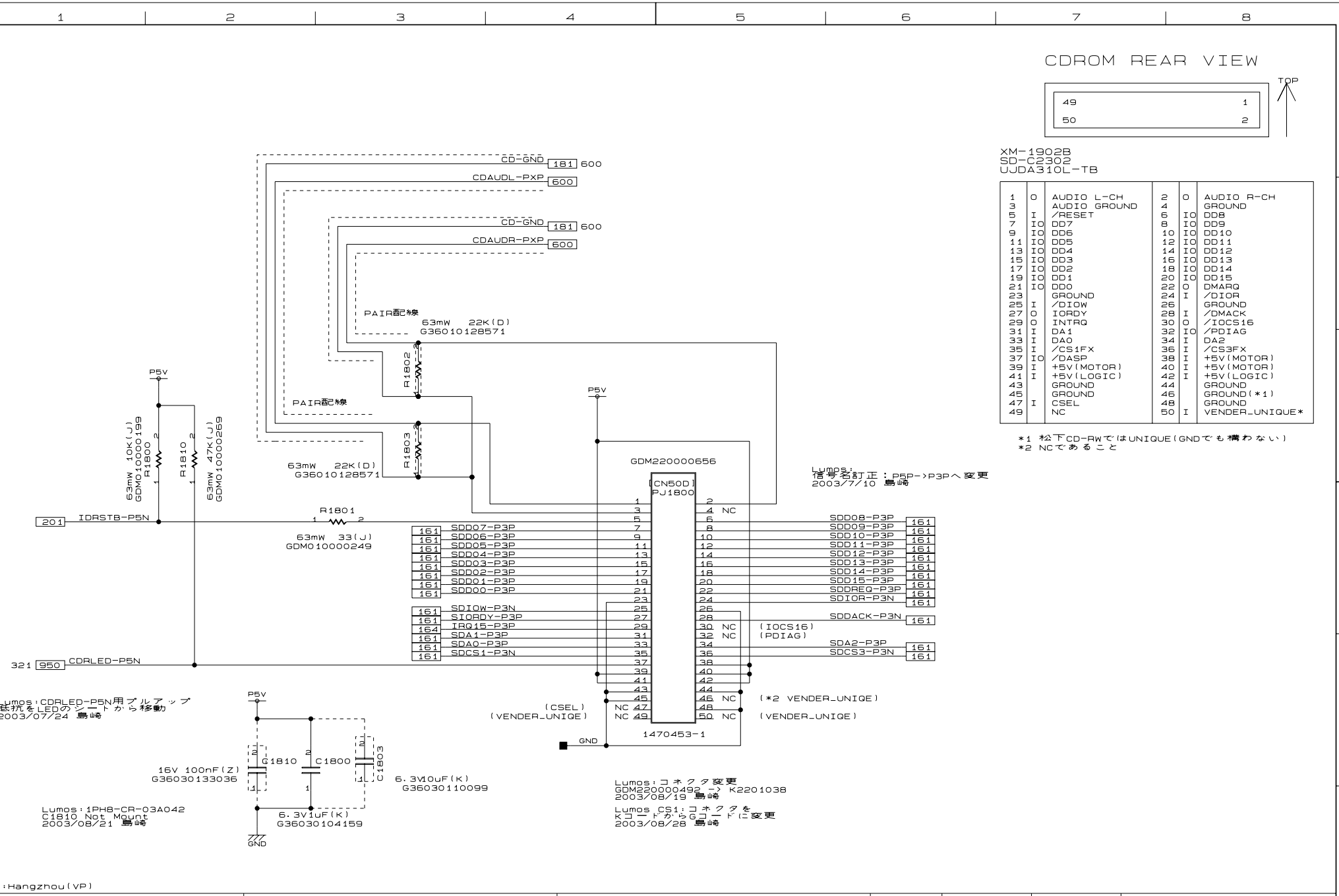


REF:Hangzhou(VP)

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2002/7/22 Y. Shimazaki	FLM1M2	HDD I/F	180	43	00	360048117

2003.11.17 14:10

TOSHIBA CORPORATION



REF:Hangzhou(VP)

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2002/8/14 Y. Shimazaki	FLM1M2	CD ROM I/F	181	44	00	360048117

2003.11.17 14:10

		Lumos10	Lumos10G
YEBISU	INTA	PIRQC	PIRQD
	INTB	PIRQD	PIRQA
	INTC	NC	NC
	INTD	NC	NC
Mini PCI	INTA	PIRQG	PIRQC
	INTB	PIRQD	PIRQA

GDM450000221

[GYEBISUSS]
IC2000

[PCI BUS Interface]

PGNT3-P3N → GNTZ (K12)
PCIRST-S3N → PCIRSTZ (H13)

63mW 100(J) G36010134084
[RMx2A] RM2000 ×2 (R15, T14)
IDSLSD-P3P → IDSELSD (T15)

AD00-P3P … AD31-P3P → AD00 … AD31

REQZ ← PREQ3-P3N
IRQDTZ ← SERIRQ-P3P
CLKRUNZ ← CLKRUN-P3N
FRAMEZ ← FRAME-P3N
IRDYZ ← IRDY-P3N
DEVSELZ ← DEVSEL-P3N
TRDYZ ← TRDY-P3N
STOPZ ← STOP-P3N
PAR ← PAR-P3P
CBE0Z ← CBE0-P3N
CBE1Z ← CBE1-P3N
CBE2Z ← CBE2-P3N
CBE3Z ← CBE3-P3N
INTAZ ← PIRQC-P3N
INTBZ ← PIRQD-P3N
INTCZ ← NC
INTDZ ← NC
PERRZ ← PERR-P3N
SERRZ ← SERR-P3N

Lumos10用割り当て

[CLOCK INPUT]

X33MCC-P3P → PCICLK (K1)
SUSCLK-S3P → CK48M (B2), CK14M (B1), CK32K (R2)

63mW 100(J) GDM010000252
AD29-P3P — R2000 — IDSLSD-P3P

W2001 (1-2)
63mW 0(X) GDM010000202
GND

	SD Card I/F	
	有	無
R2000	Mount	Not Mount
W2001	Not Mount	Mount

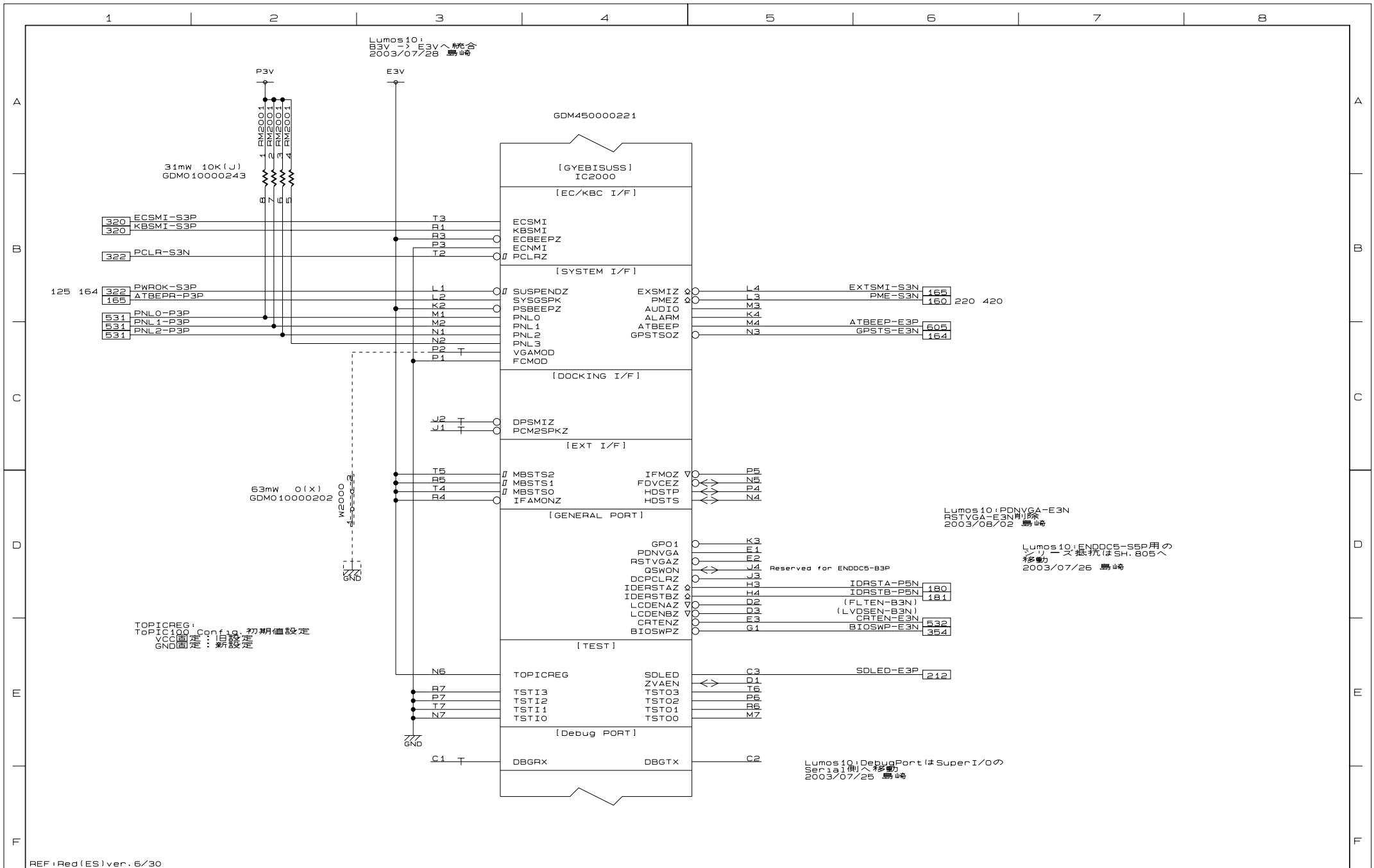
Lumos:YEBISUのDebugPortは
使用しないので、CK48MをGND直結
(DebugPortはSuperI/OのSerial)
2003/07/25 島崎

Lumos:SD Card 有り/無しモデルの
切り替えをIDSELで切り替えるように変更。
2003/08/14 島崎

REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki	FLM1M2	YEBISUSS(1)	200	45	00	360048117

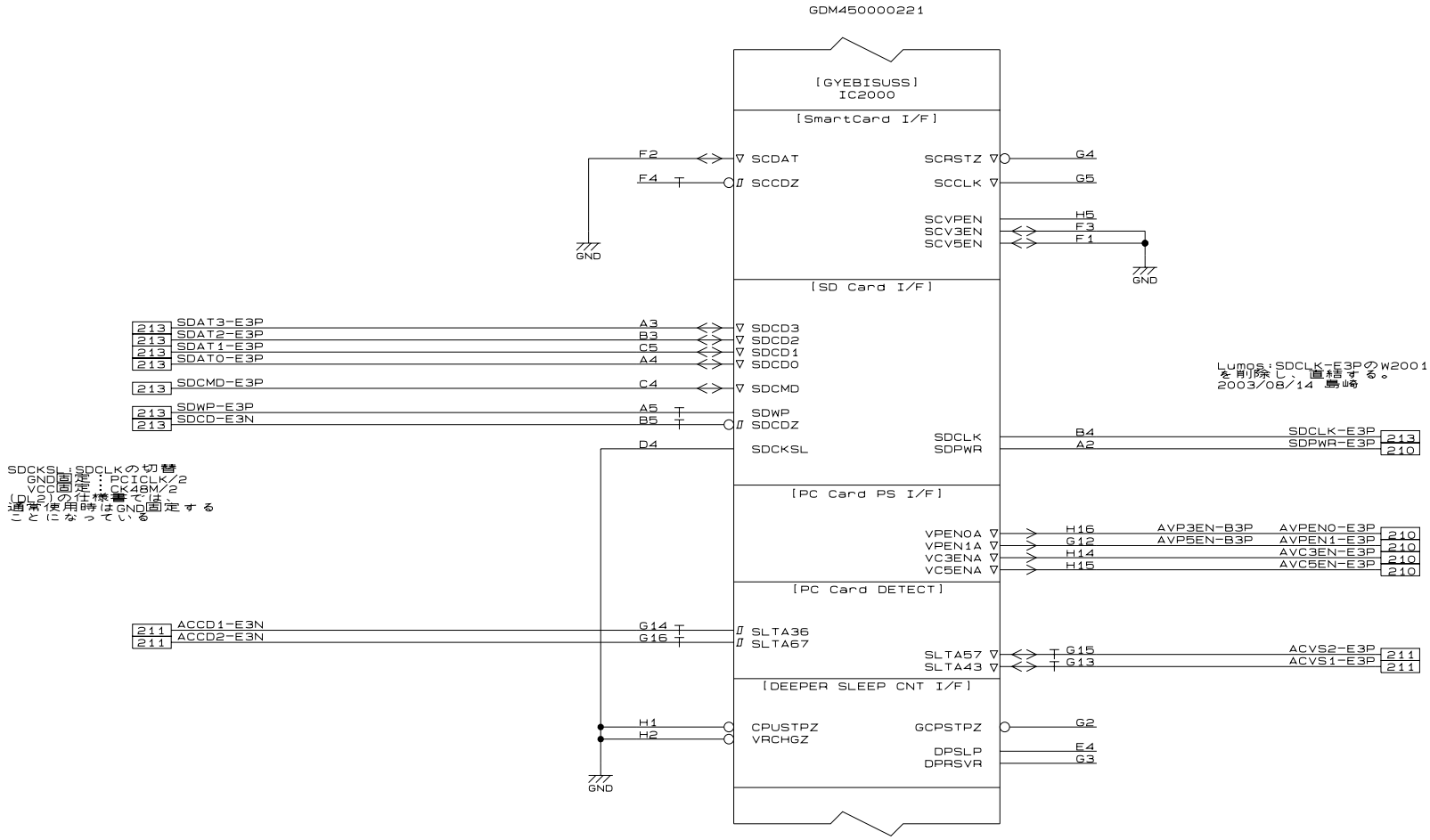
2003.11.17 14:10



REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki	FLM1M2	YEBISUSS(2)	201	46	00	360048117

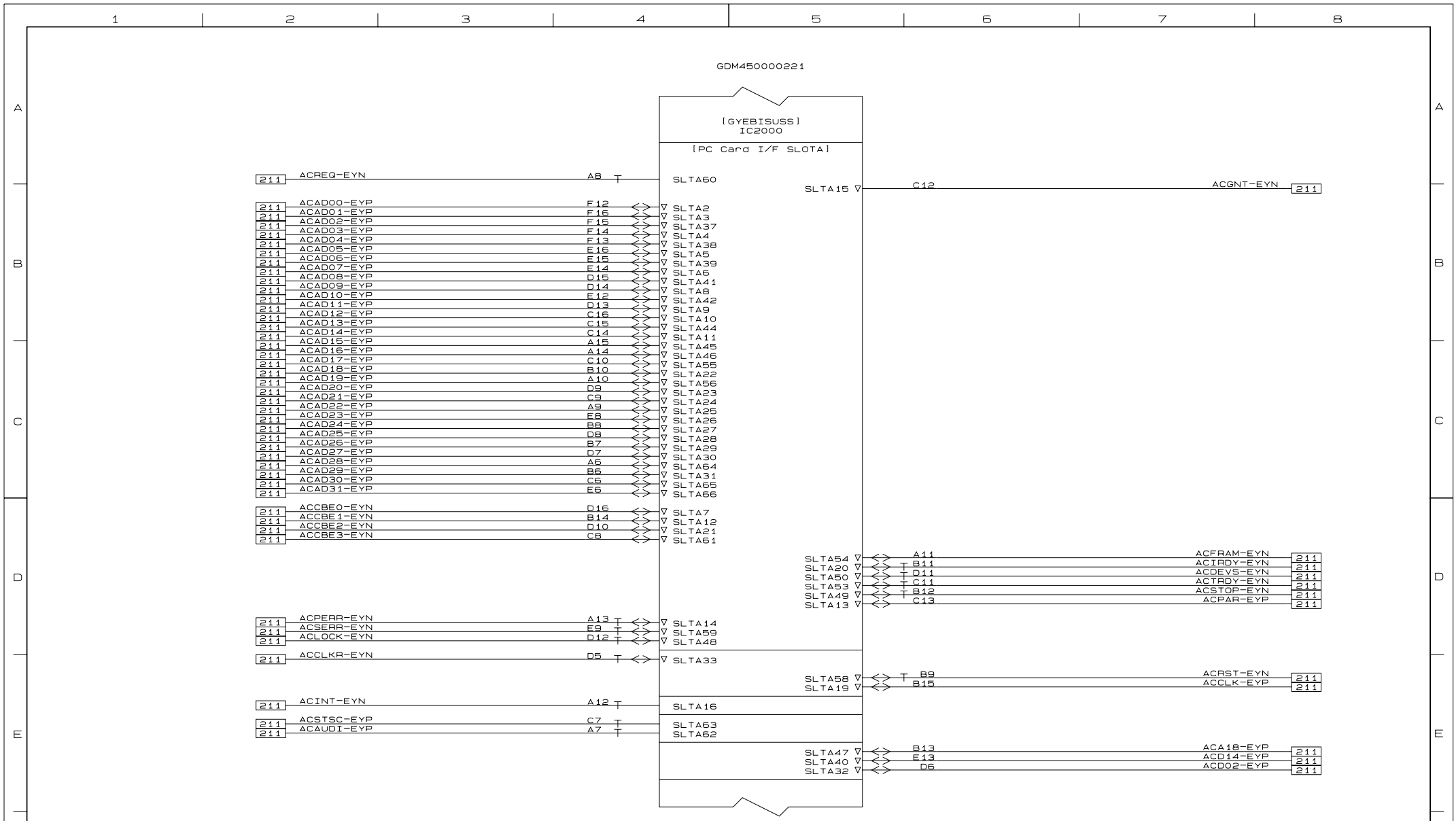
2003.11.17 14:10



REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y. Shimazaki	FLM1M2	YEBISUSS(3)	202	47	00	360048117

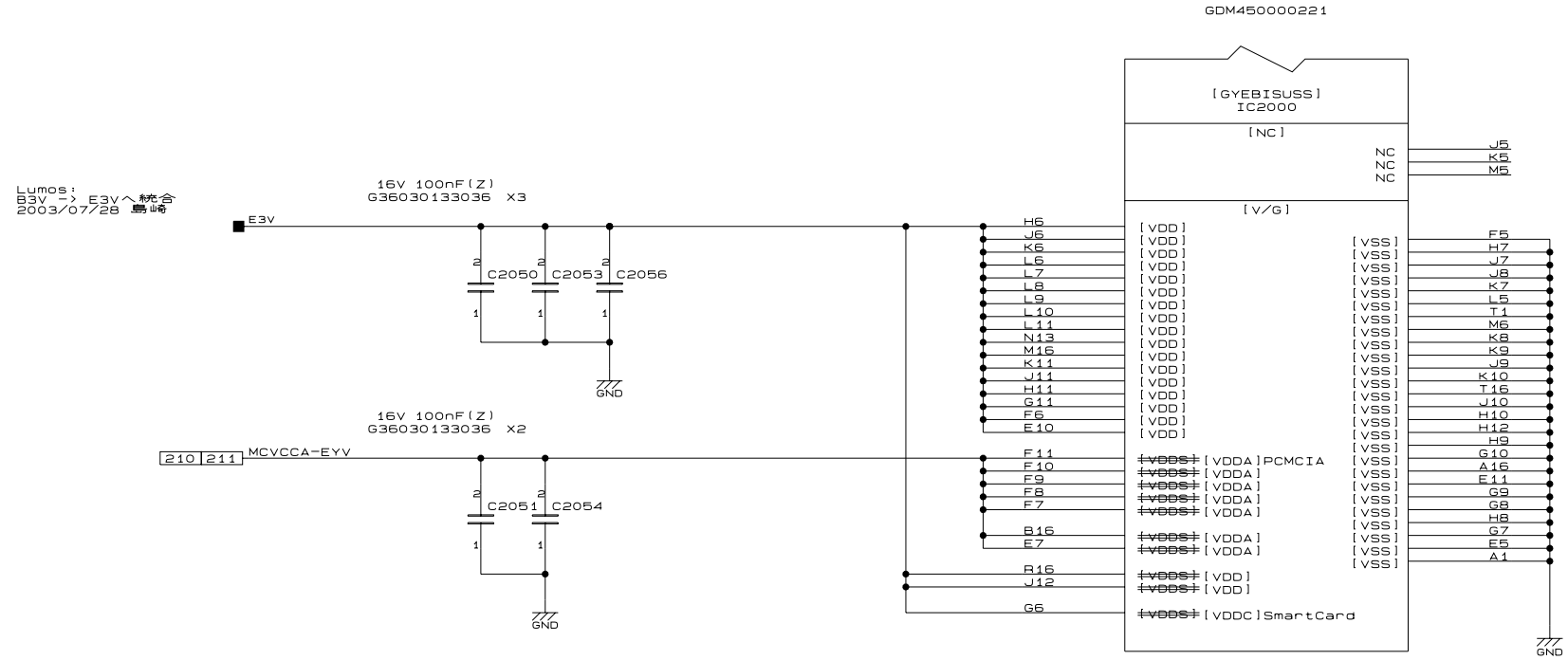
2003. 11. 17 14:10



REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y. Shimazaki	FLM1M2	YEBISUSS(4)	203	48	00	360048117

2003.11.17 14:10



REF:Red(ES)ver.6/30

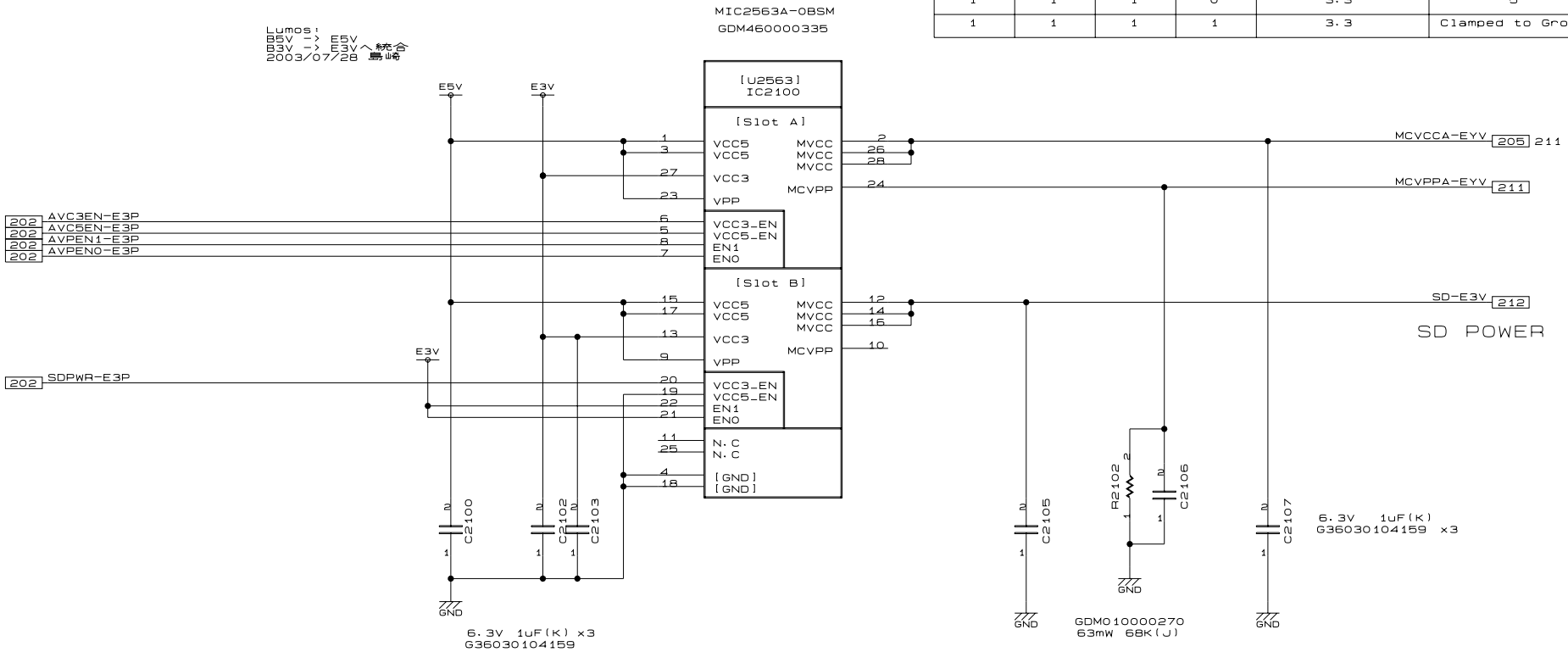
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki	FLM1M2	YEBISUSS(5)	205	49	00	360048117

2003.11.17 14:10

5T03655D

MIC2563A-0 Control Logic Table

VCC5_EN	VCC3_EN	EN1	EN0	VCC OUT	VPP OUT
0	0	0	0	Clamped to Ground	High Z
0	0	0	1	Clamped to Ground	High Z
0	0	1	0	Clamped to Ground	High Z
0	0	1	1	Clamped to Ground	Clamped to Ground
0	1	0	0	3.3	High Z
0	1	0	1	3.3	3.3
0	1	1	0	3.3	12
0	1	1	1	3.3	Clamped to Ground
1	0	0	0	5	High Z
1	0	0	1	5	5
1	0	1	0	5	12
1	0	1	1	5	Clamped to Ground
1	1	0	0	3.3	High Z
1	1	0	1	3.3	3.3
1	1	1	0	3.3	5
1	1	1	1	3.3	Clamped to Ground



REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y. Shimazaki	FLM1M2	PC-Card POWER	210	50	00	360048117

2003.11.17 14:10

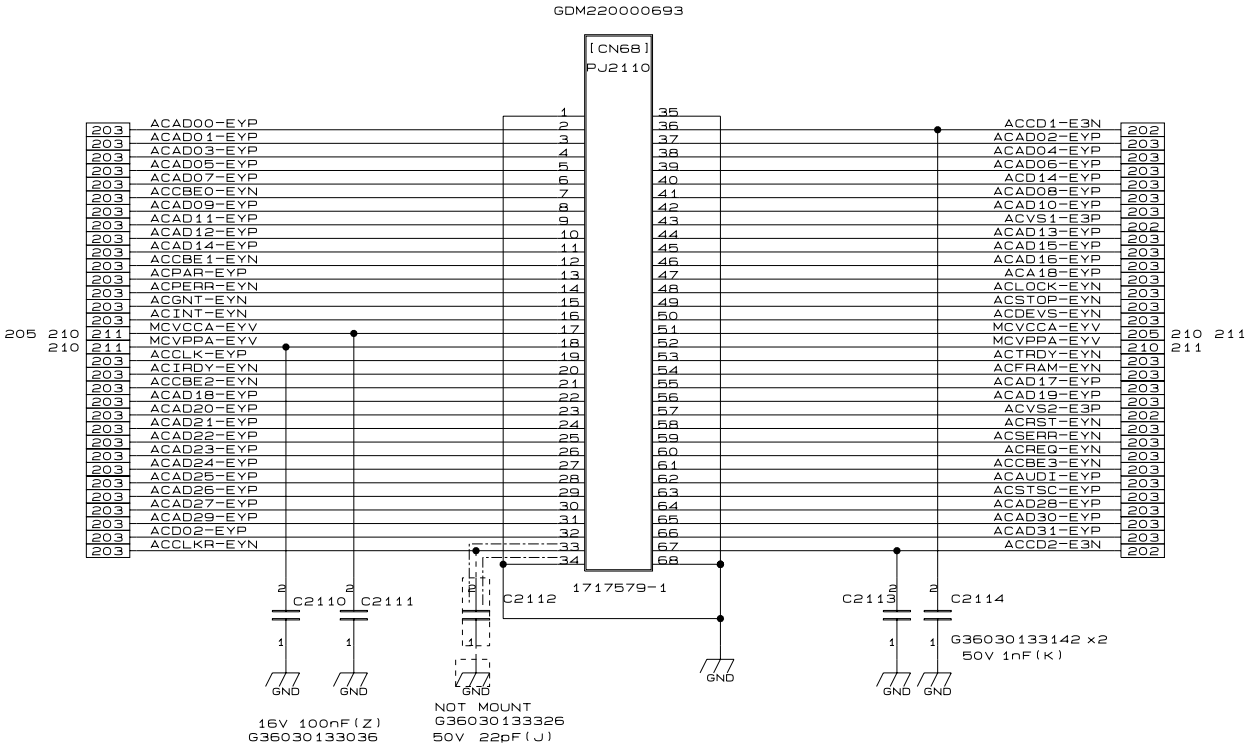
TOSHIBA CORPORATION

ROC.2110

PJ2110 68PIN GDM220000693 [04]
EJECTOR GDM220000653

ピンアサインを確認すること
Lumos:確認済み。2003/08/16

[04] コネクタ変更
GDM220000658->GDM220000693
2003/10/21 島崎

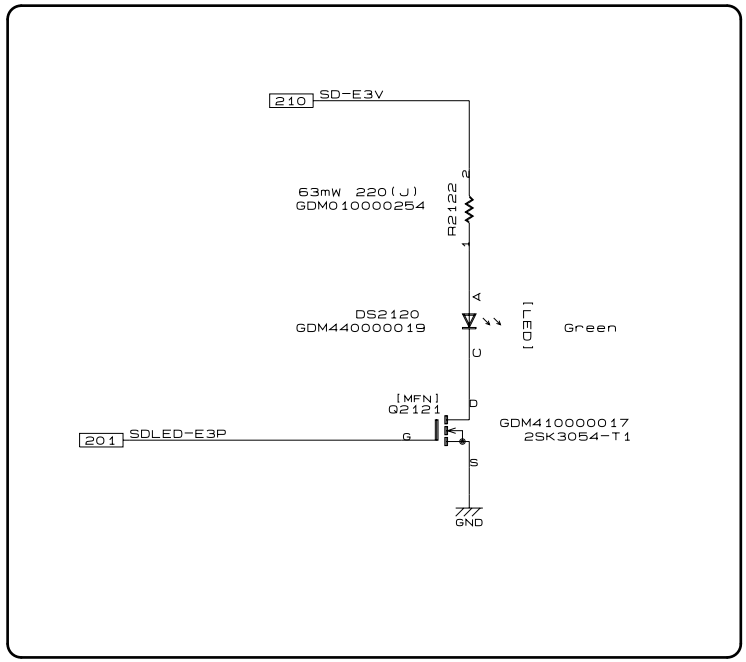


REF:Red(ES)ver.6/30

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/5/24 Y.Shimazaki	FLM1M2	PC-Card I/F	211	51	04	360048117

2003.11.17 14:10

SD無しモデルの場合はこの部分をNot Mount



LED発光輝度（抵抗値）はES筐体に入れて再度システム課と共同チェックを行う。

新LEDは逆耐圧（Vr）=4V

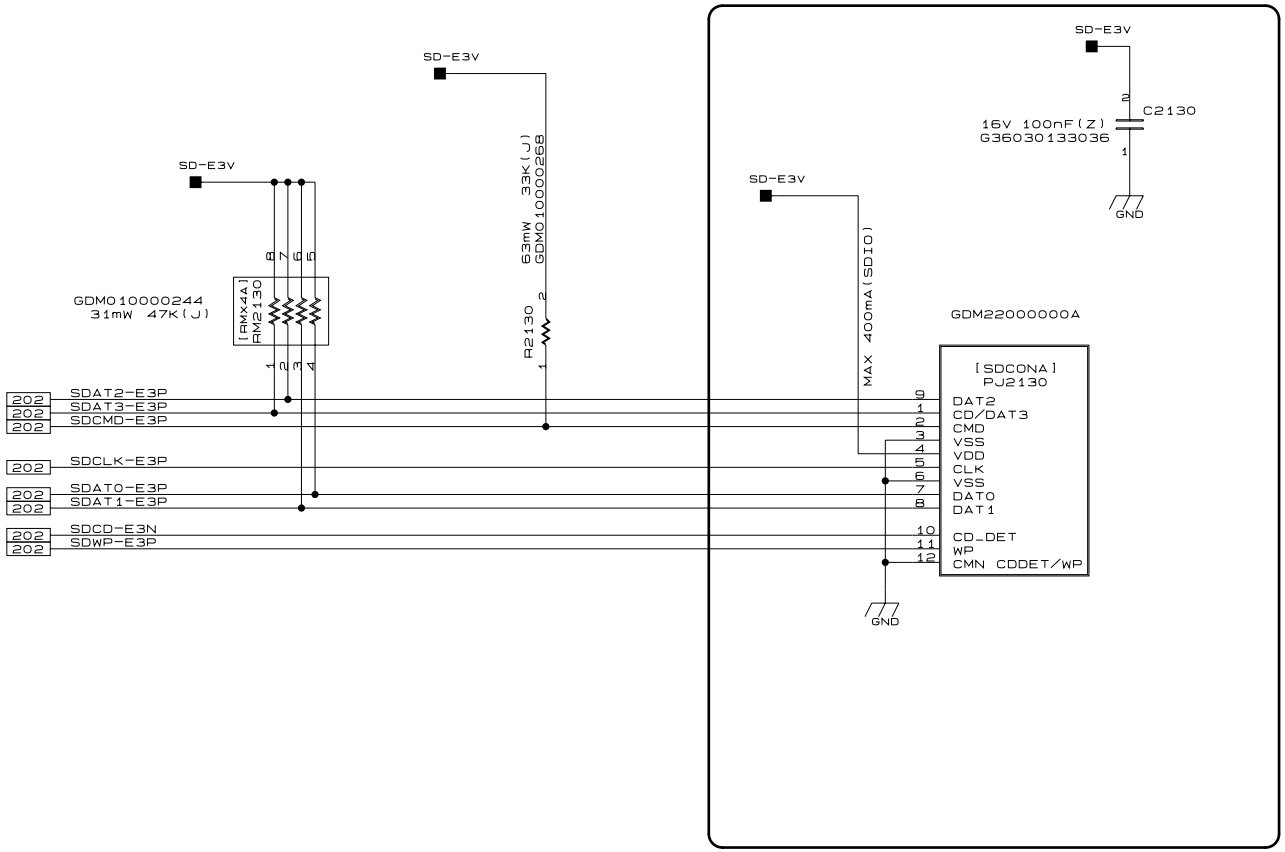
	色	波長	100%光度	設計値 (DC-IN...)	設計値 (MAIL/SD/BT)
従来	黄緑(G)	565nm	32mcd at 20mA	6.4mA(20%)	
	黄(Y)	585nm	16mcd at 20mA	4.4mA(15%)	
新規	TLGV	571nm	40mcd at 20mA	2.0mA → 1.5kohm	6.0mA → 220ohm (at 3.3V)
	TLYV	590nm	90mcd at 20mA	1.1mA → 2.7kohm	3.3mA → 1kohm

REF:Red(TS)

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/7/14 Y.Shimazaki	FLM1M2	SD LED	212	52	00	360048117

YEBISUSS側のSD I/Fの未使用処理が
Pull-Upではなく、Pull-Downとなる。

SD無しモデルの場合はこの部分をNot Mount



REF:Imola100(VP)

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/4/2 Y.Shimazaki	FLM1M2	SD CARD I/F	213	53	00	360048117

2003.11.17 14:10

Mini-PCI 有りモデル：このシートの全ての部品をMount
Mini-PCI 無しモデル：このシートの全ての部品をNot Mount

Lumos：コネクタ変更
GDM220000510 → G36220358010
2003/08/09 島崎

Lumos：コネクタ変更（二重登録品のため）
G36220358010 → GDM220000085
2003/08/21 島崎

		Lumos10	Lumos10G
YEBISU	INTA	PIRQC	PIRQD
	INTB	PIRQD	PIRQA
	INTC	NC	NC
	INTD	NC	NC
Mini PCI	INTA	PIRQG	PIRQC
	INTB	PIRQD	PIRQA

0.5A/PIN
GDM220000085

16V 100nF(Z)x2
G36030133036

MINI PCI CONNECTOR TYPE III
[CNMPCIT3]
PJ2200

1	(TIP)	(RING)	2
3	(8MPJ-3)	(8MPJ-1)	4
5	(8MPJ-6)	(8MPJ-2)	6
7	(8MPJ-7)	(8MPJ-4)	8
9	(8MPJ-8)	(8MPJ-5)	10
11	(LED1-GRNP)	(LED2-YELP)	12 NC
13	(LED1-GRNN)	(LED2-YELN)	14 NC
15	(CHSGND)	(RESERVED)	16 NC
17	(INTB#)	(5V)	18
19	(3.3V)	(INTA#)	20
21	(RESERVED)	(RESERVED)	22 NC
23	(GROUND)	(3.3VAUX)	24
25	(CLK)	(RST#)	26
27	(GROUND)	(3.3V)	28
29	(REQ#)	(GNT#)	30
31	(3.3V)	(GROUND)	32
33	(AD31)	(PME#)	34
35	(AD29)	(RESERVED)	36 NC
37	(GROUND)	(AD30)	38
39	(AD27)	(3.3V)	40
41	(AD25)	(AD28)	42
43	(RESERVED)	(AD26)	44
45	(CBE3#)	(AD24)	46
47	(AD23)	(IDSEL)	48
49	(GROUND)	(GROUND)	50
51	(AD21)	(AD22)	52
53	(AD19)	(AD20)	54
55	(GROUND)	(PAR)	56
57	(AD17)	(AD18)	58
59	(CBE2#)	(AD16)	60
61	(IRDY#)	(GROUND)	62
63	(3.3V)	(FRAME#)	64
65	(CLKRUN#)	(TRDY#)	66
67	(SERR#)	(STOP#)	68
69	(GROUND)	(3.3V)	70
71	(PERR#)	(DEVSEL#)	72
73	(CBE1#)	(GROUND)	74
75	(AD14)	(AD15)	76
77	(GROUND)	(AD13)	78
79	(AD12)	(AD11)	80
81	(AD10)	(GROUND)	82
83	(GROUND)	(AD09)	84
85	(AD08)	(CBE0#)	86
87	(AD07)	(3.3V)	88
89	(3.3V)	(AD06)	90
91	(AD05)	(AD04)	92
93	(RESERVED)	(AD02)	94
95	(AD03)	(AD00)	96
97	(5V)	(RESERVED_WIP)	98 NC
99	(AD01)	(RESERVED_WIP)	100 NC
101	(GROUND)	(GROUND)	102
103	(AC_SYNC)	(M66EN)	104
105	(AC_SDATA_IN)	(AC_SDATA_OUT)	106
107	(AC_BIT_CLK)	(AC_CODEC_ID0#)	108
109	(AC_CODEC_ID1#)	(AC_RESET)	110
111	(MOD_AUDIO_MON)	(RESERVED)	112 NC
113	(AUDIO_GND)	(GROUND)	114
115	(SYS_AUDIO_OUT)	(SYS_AUDIO_IN)	116
117	(SYS_AUDIO_OUT_GND)	(SYS_AUDIO_IN_GND)	118
119	(AUDIO_GND)	(AUDIO_GND)	120
121	(RESERVED)	(MPCIACT#)	122
123	(VCC5VA)	(3.3VAUX)	124

P3V
P5V
E3V
GND
GND (FG)
NC

C2200 C2201
C2202
C2203
C2204
16V 100nF(Z)
G36030133036
16V 100nF(Z) X2
G36030133036

2SK3054-T1
GDM410000017
[MFN]
Q2200
Lumos10:
SSM3K15FS->2SK3054
2003/07/26 島崎

WLON-S3N
PIRQD-P3N
Lumos10用割り当て
X33MPC-P3P
PREQ2-P3N
AD31-P3P
AD29-P3P
AD27-P3P
AD25-P3P
CBE3-P3N
AD23-P3P
AD21-P3P
AD19-P3P
AD17-P3P
CBE2-P3N
IRDY-P3N
CLKRUN-P3N
SERR-P3N
PERR-P3N
CBE1-P3N
AD14-P3P
AD12-P3P
AD10-P3P
AD08-P3P
AD07-P3P
AD05-P3P
AD03-P3P
AD01-P3P

PIRQG-P3N
Lumos10用割り当て
PCIRS1-S3N
Lumos：PCIRSIを3分岐するので、信号名変更。
2003/08/20 島崎
PGNT2-P3N
PME-S3N
AD30-P3P
AD28-P3P
AD26-P3P
AD24-P3P
63mW 100(J) x2
GDM010000252
R2200
AD26-P3P
R2201
AD21-P3P
Lumos10：IDSELをAD26からAD21へ変更。
(Dev.ID: 10->5)
2003/07/26 島崎
AD22-P3P
AD20-P3P
PAR-P3P
AD18-P3P
AD16-P3P
FRAME-P3N
TRDY-P3N
STOP-P3N
DEVSEL-P3N
AD15-P3P
AD13-P3P
AD11-P3P
AD09-P3P
CBE0-P3N
AD06-P3P
AD04-P3P
AD02-P3P
AD00-P3P
Lumos10：
B3V -> E3Vへ統合
2003/07/28 島崎

REF：Hangzhou10(VP)

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2002/10/8 Y. Shimazaki	FLM1M2	MINI PCI I/F	220	54	00	360048117

2003.11.17 14:10

Lumos:B3V -> E3Vへ統合
2003/07/28 島崎

804 805 809 811 E3V

170mA 33uH(M)
G36050011024
L3000

16V 100nF(Z)
G36030133036
C3004
C3006

GND GND

6.3V 10uF(K)
G36030110099

[04]コネクタ変更
GDM220000485->G36220409057
2003/10/21 島崎

G36220409057

MDC CONNECTOR
[CNMDC]
PJ3000

1	MONO OUT/PC BEEP	AUDIO PWRDN	2
3	GND	MONO PHONE	4
5	AUXA RIGHT	RESERVED	6
7	AUXA LEFT	GND	8
9	CD GND	5Vmain	10
11	CD RIGHT	RESERVED	12
13	CD LEFT	RESERVED	14
15	GND	PRIMARY DN	16
17	3.3VAUX	5VD	18
19	GND	GND	20
21	3.3Vmain	SYNC	22
23	SDATA OUT	SDATA INB	24
25	RESET#	SDATA INA	26
27	GND	GND	28
29	MSTRCLK	BITCLK	30

3-179397-5

(MONO PHONE)

Lumos:MONO PHONE部分の回路削除
2003/07/23 島崎

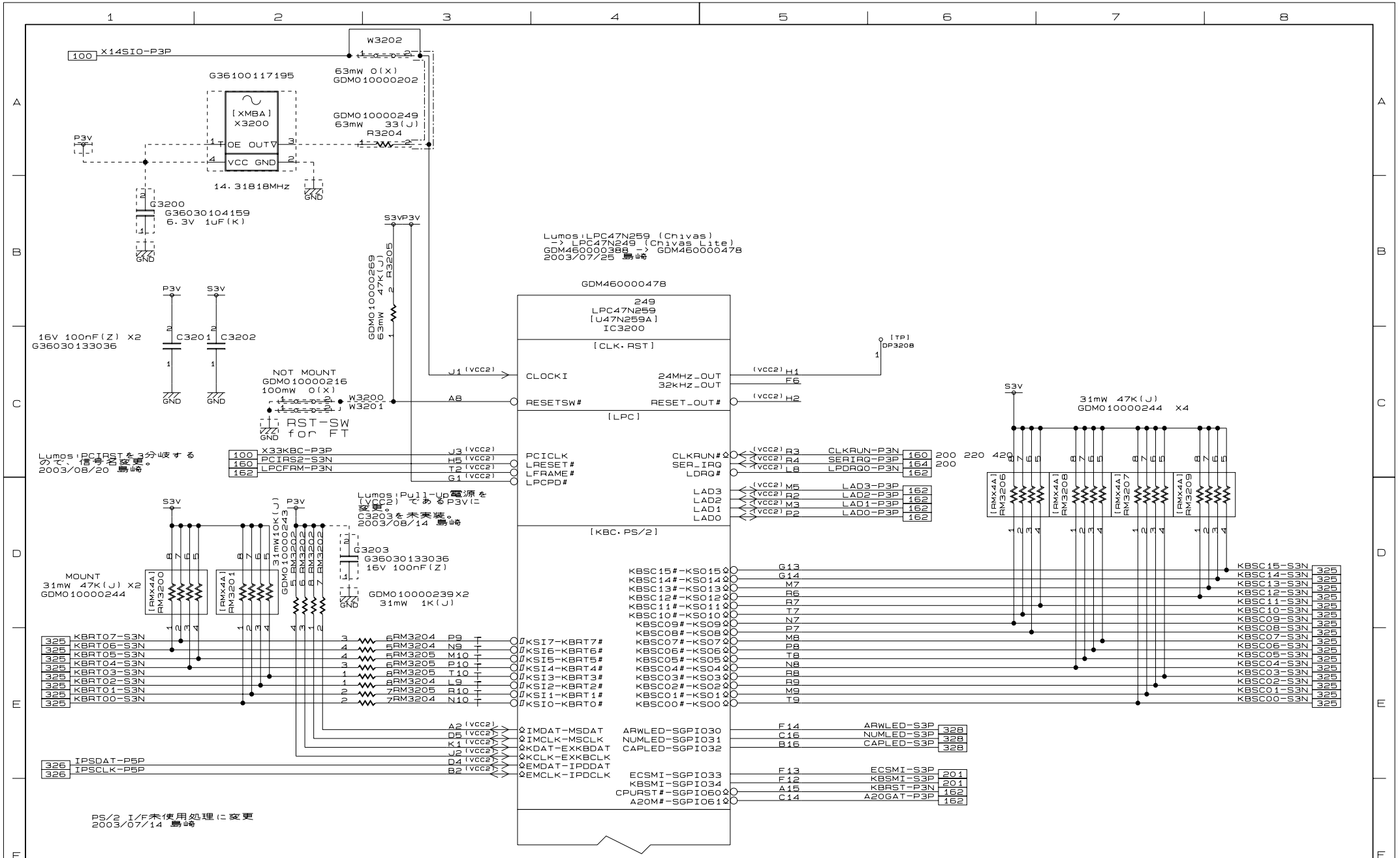
GDM010000249
63mW 33(J)
R3007

M97OT2-P3P 163
M97RS2-S3N 163
M97SY2-P3P 163
M97IN2-E3P 163
X97BC2-P3P 163

ACLinkの信号名のSuffixを
変更(Odem/Montara-GM系に
合わせる)
* M97OT1-P3P -> M97OT2-P3P
* M97RS1-S3N -> M97RS2-S3N
* M97SY1-P3P -> M97SY2-P3P
* M97DI1-B3P -> M97IN2-B3P
* X97BC1-P3P -> X97BC2-P3P
2003/7/10 島崎

REF:Hayate10(VP)

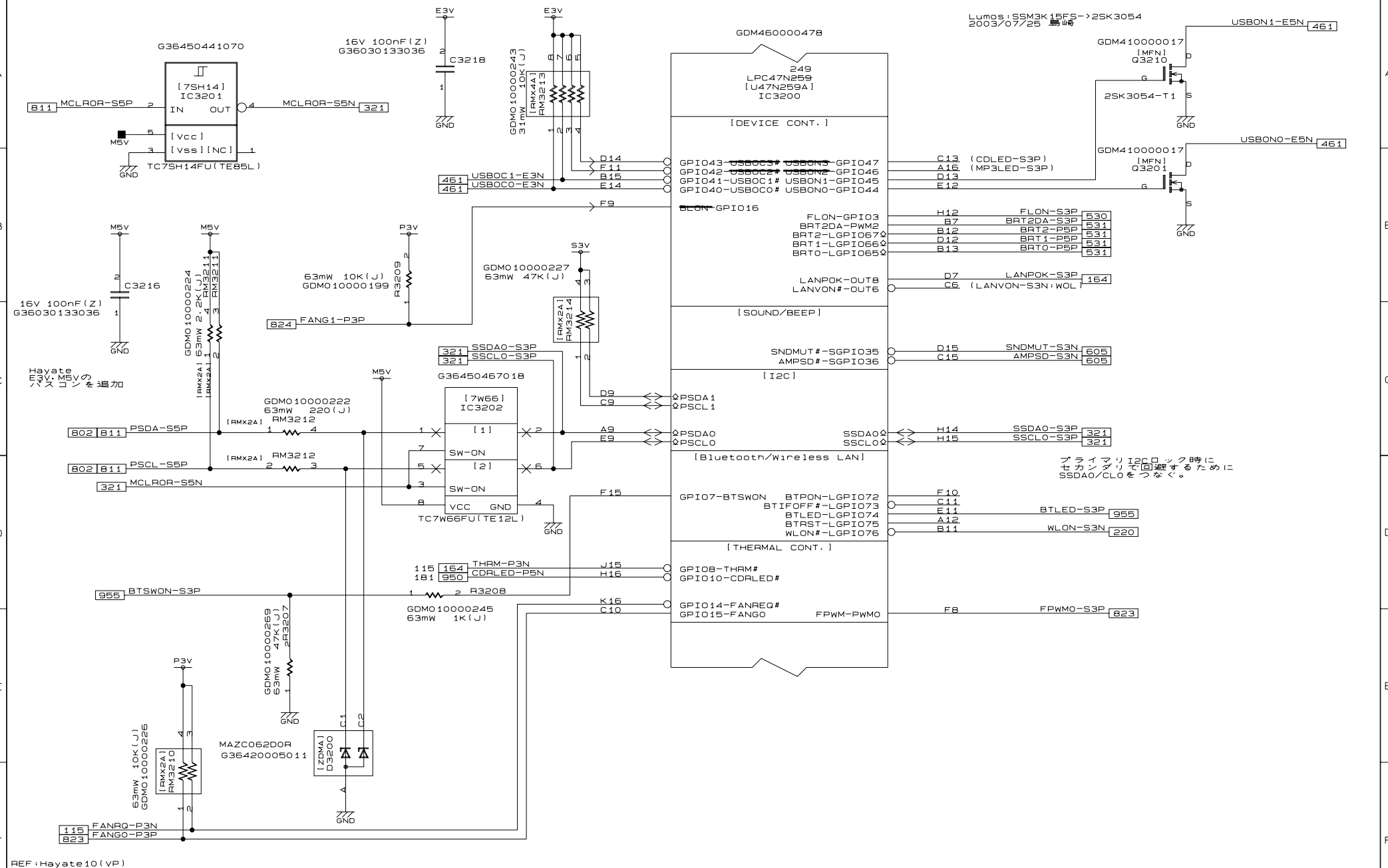
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/3/13 Y.Shimazaki	FLM1M2	MDC I/F	301	55	04	360048117



REF:Hayate10(VP)

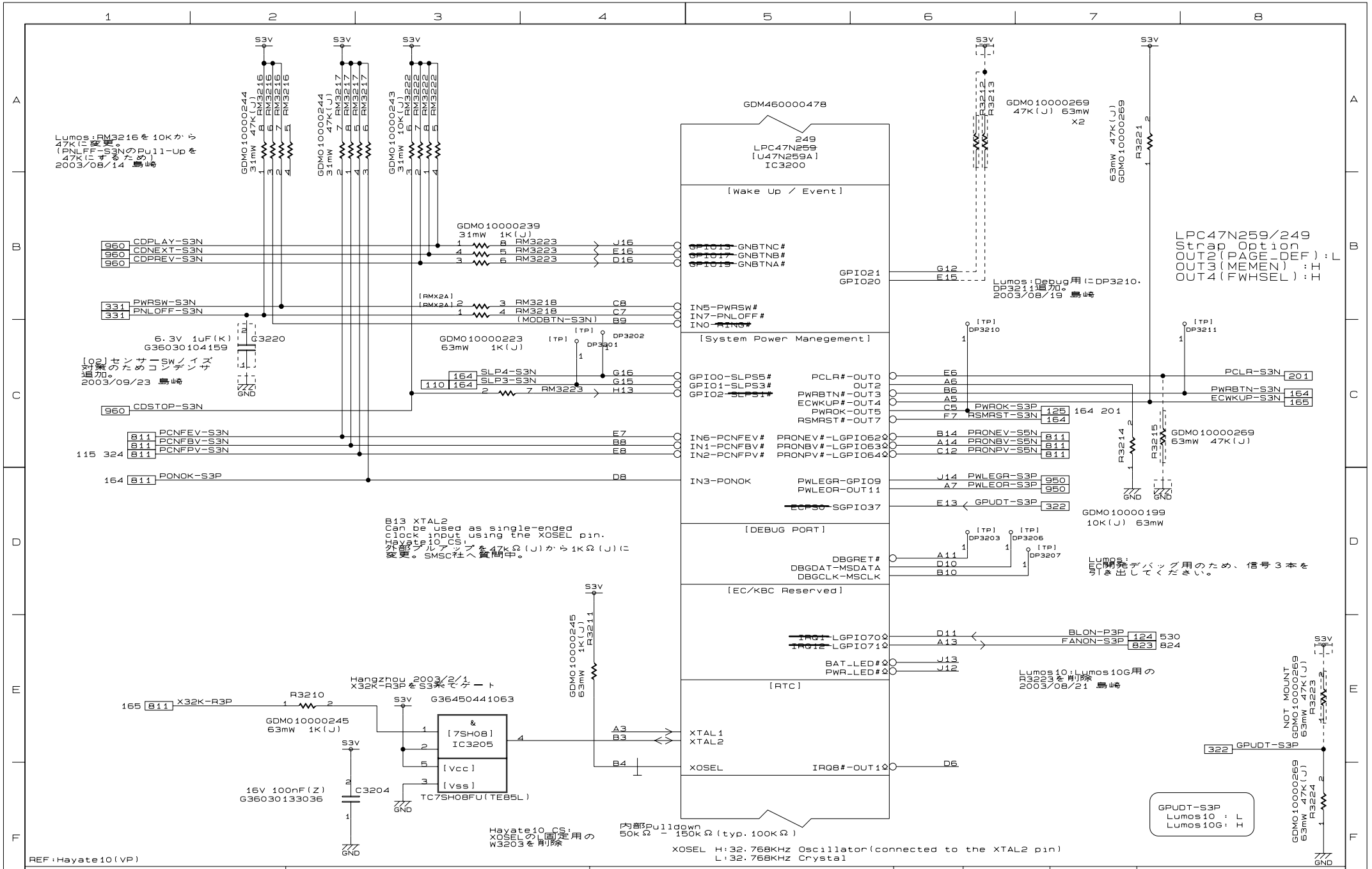
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Y. Shimazaki	FLM1M2	SMSC EC/KBC 1	320	56	00	360048117

2003.11.17 14:10



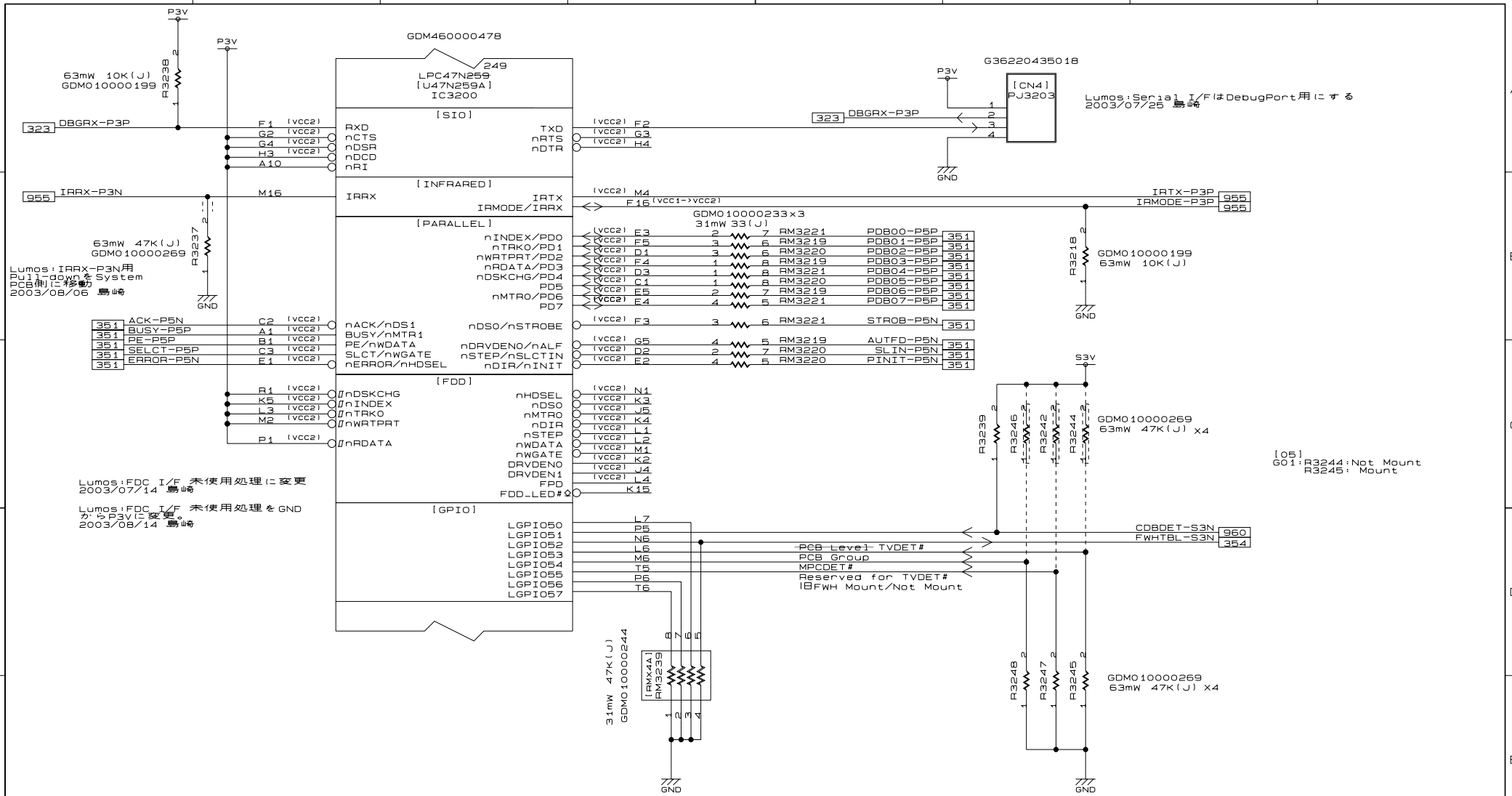
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Y. Shimazaki	FLM1M2	SMSC EC/KBC 2	321	57	00	360048117

2003.11.17 14:10



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Y. Shimazaki	FLM1M2	SMSC EC/KBC 3	322	58	02	360048117

2003.11.17 14:10



Pin	Lumos10	H	L
GPIO50	RESERVED		
GPIO51	(CDBDET#)	(NOT SUPPORT)	(SUPPORT)
GPIO52	FWHTBL		
GPIO53	TVDET#	SUPPORT	NOT SUPPORT
GPIO54	PCB Group	NO SD/1394	YES SD/1394
GPIO55	MPCDET#	NOT SUPPORT	SUPPORT
GPIO56	(TVDET#)		(SUPPORT TV)
GPIO57	旧FWH M/NM	(NOT MOUNT)	(MOUNT)

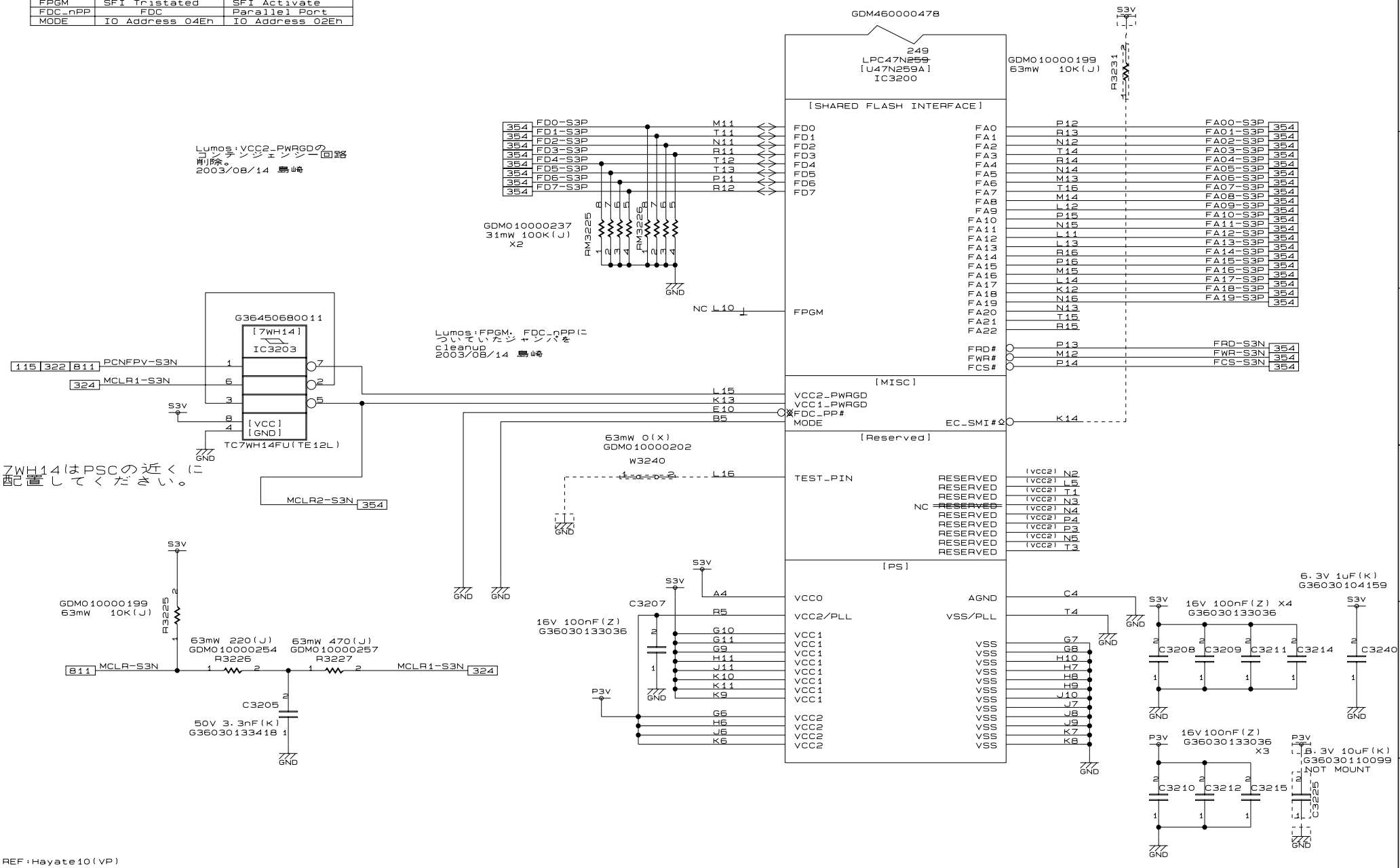
[05]

REF:Hayate10(VP)

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Y. Shimazaki	FLM1M2	SMSC EC/KBC 4	323	59	05	360048117

2003.11.17 14:10

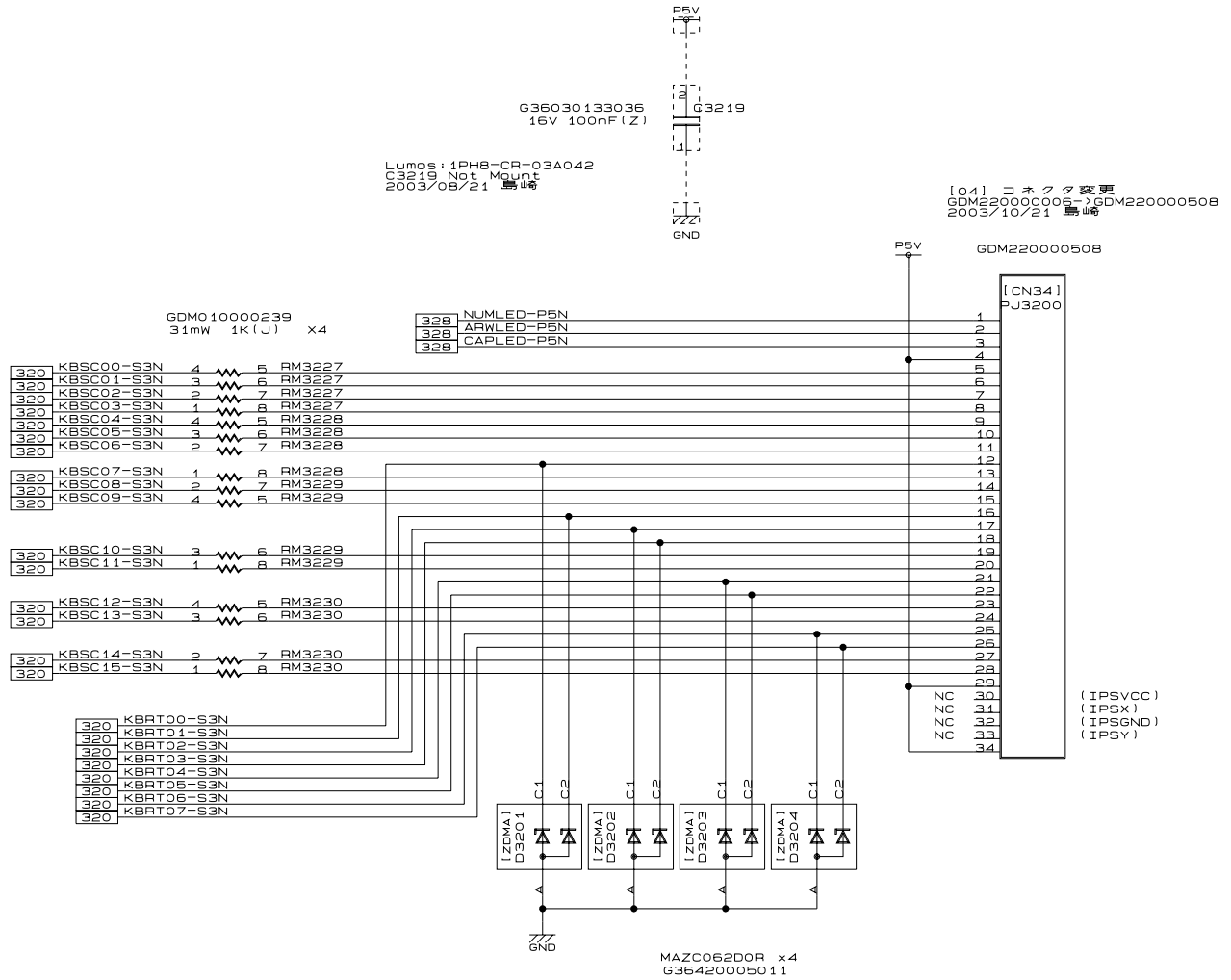
	H	L
FPGM	SFI Tristated	SFI Activate
FDC_nPP	FDC	Parallel Port
MODE	IO Address 04Eh	IO Address 02Eh



REF:Hayate10(VP)

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Y. Shimazaki	FLM1M2	SMSC EC/KBC 5	324	60	00	360048117

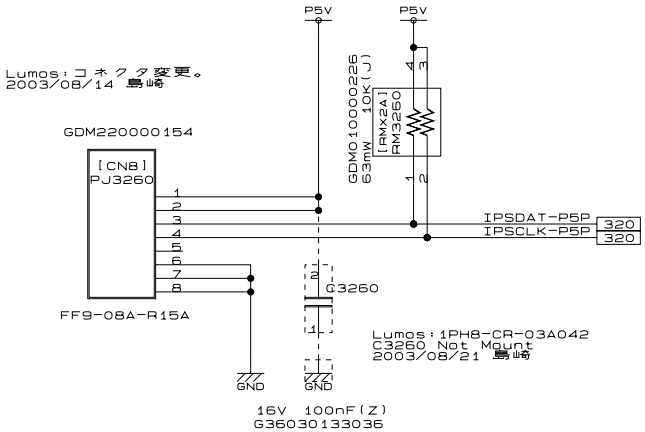
2003.11.17 14:10

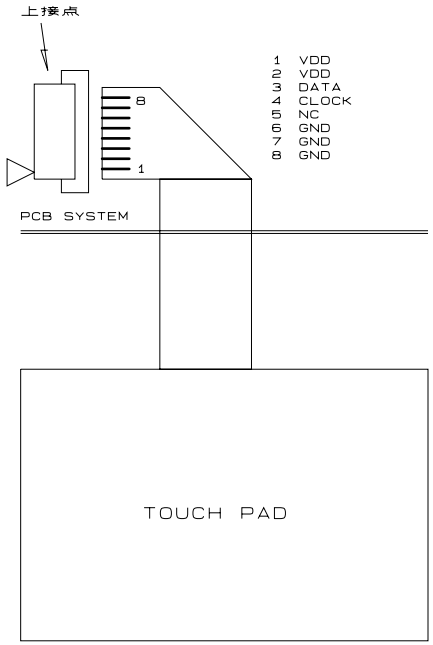


REF:Asahi10(VP)

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/4/28 Y. Shimazaki	FLM1M2	KB I/F	325	61	04	360048117

2003.11.17 14:10

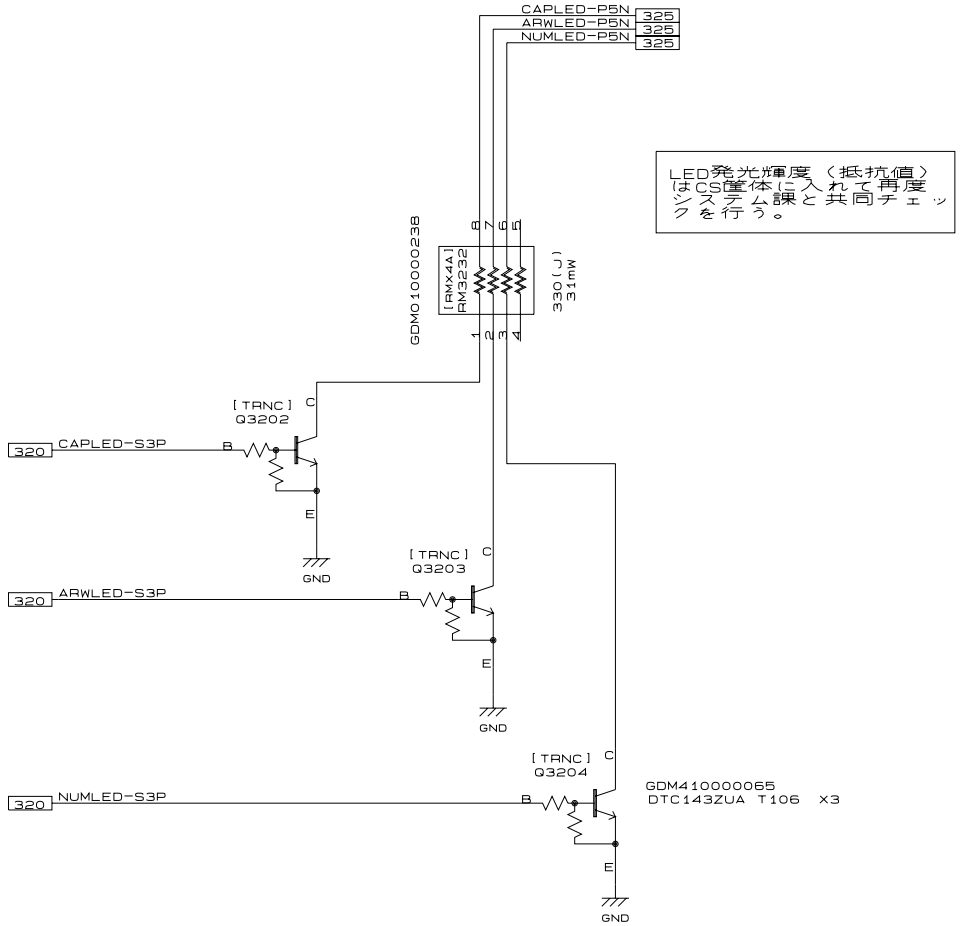




REF:Hangzhou10(VP)

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2002/1/15 Y.Shimazaki	FLM1M2	PAD IF	326	62	00	360048117

2003.11.17 14:10

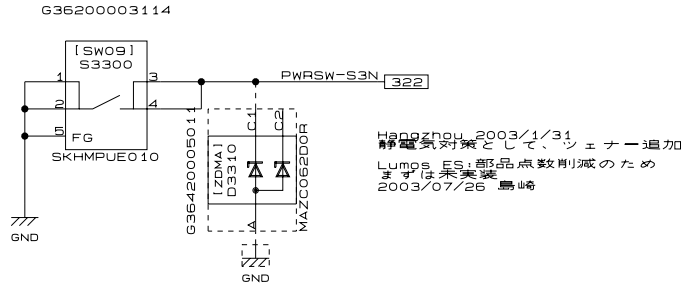


REF:Asahi10(VP)

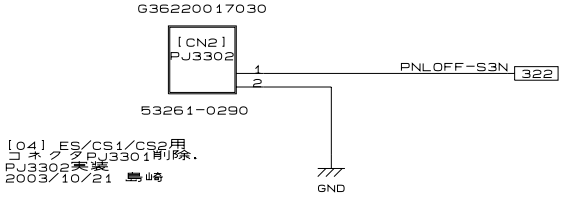
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2002/3/6 Y. Shimazaki	FLM1M2	KB LED	328	63	00	360048117

2003.11.17 14:10

5T03655D



Hangzhou 2003/1/31
静電気対策として、ツェナー追加
Lumos ES:部品点数削減のため
まずは未実装
2003/07/26 島崎



[04] ES/CS1/CS2用
コネクタPJ3301削除.
PJ3302実装
2003/10/21 島崎

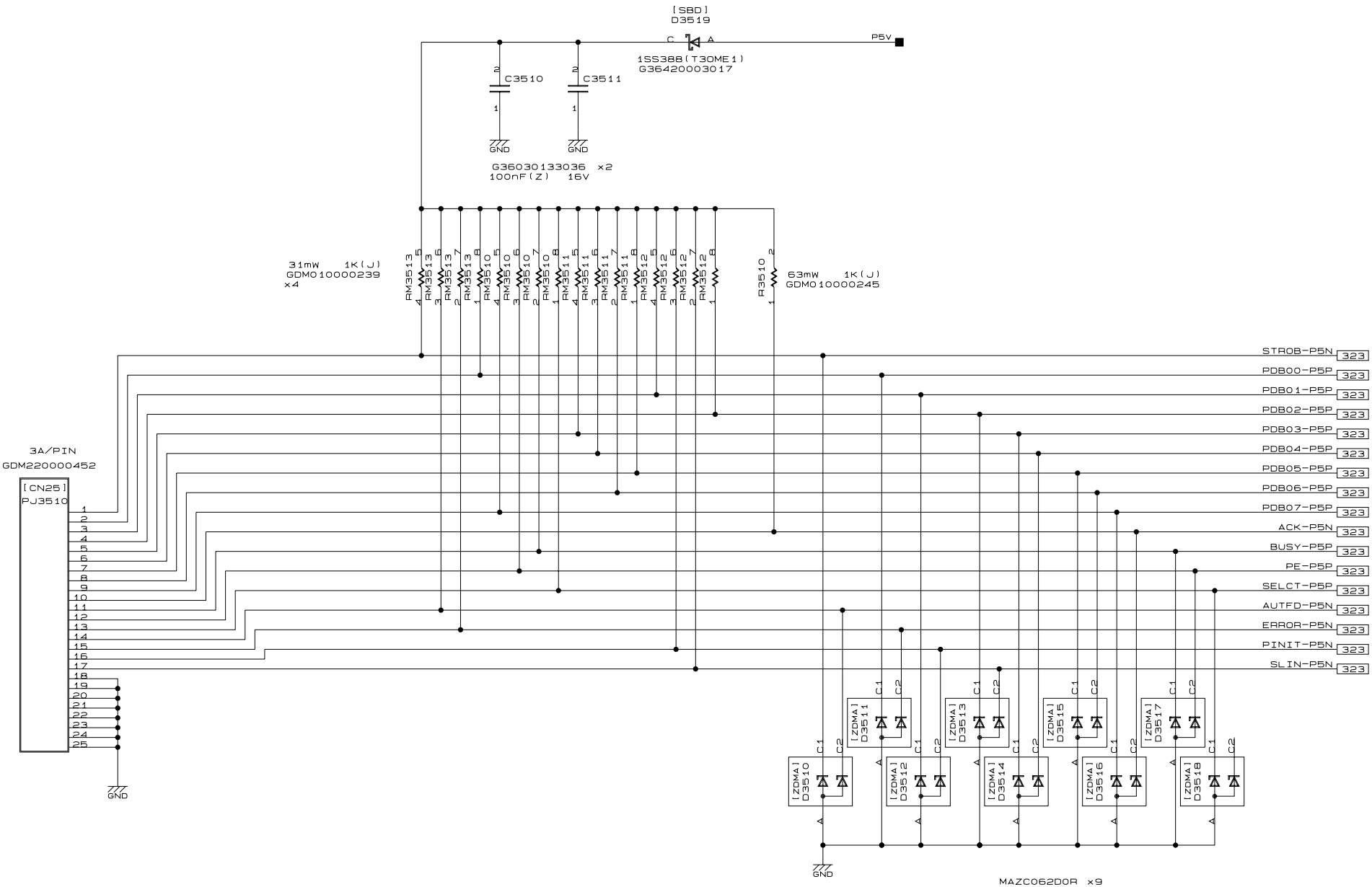
REF:Hangzhou10(VP)

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2002/10/18 Y. Shimazaki	FLM1M2	POWER SWITCH	331	64	04	360048117

2003. 11. 17 14:10

TOSHIBA CORPORATION

5T03655D



REF:Hayate10(VP)

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/2/22 Y. Shimazaki	FLM1M2	PARALLEL I/F	351	65	00	360048117

2003.11.17 14:10

TOSHIBA CORPORATION

Lumos10 BIOS/EC

GDM460000393 (生ROM)

CS1	GDM4A0000411	F0041101
CS2	GDM4A0000412	F0041201
VP	GDM4A0000413	F0041301

Lumos10と10Gではbiosが異なる。

[04] ROMのGコードを VP用に変更 2003/10/20 島崎

G36450441094

[7SH32] IC3402

TC7SH32FU(TE85L)

G36450441049

[7SH04] IC3401

TC7SH04FU(TE85L)

GDM4A0000413

[MFLV800AT] IC3403

GDM410000088

[MFPI] Q3400

TPC6104(TE85L)

AMD/FUJITSU製 8Mb Flashを使用する場合はNot Mount
SHARP製 8Mb Flashを使用する場合はMount

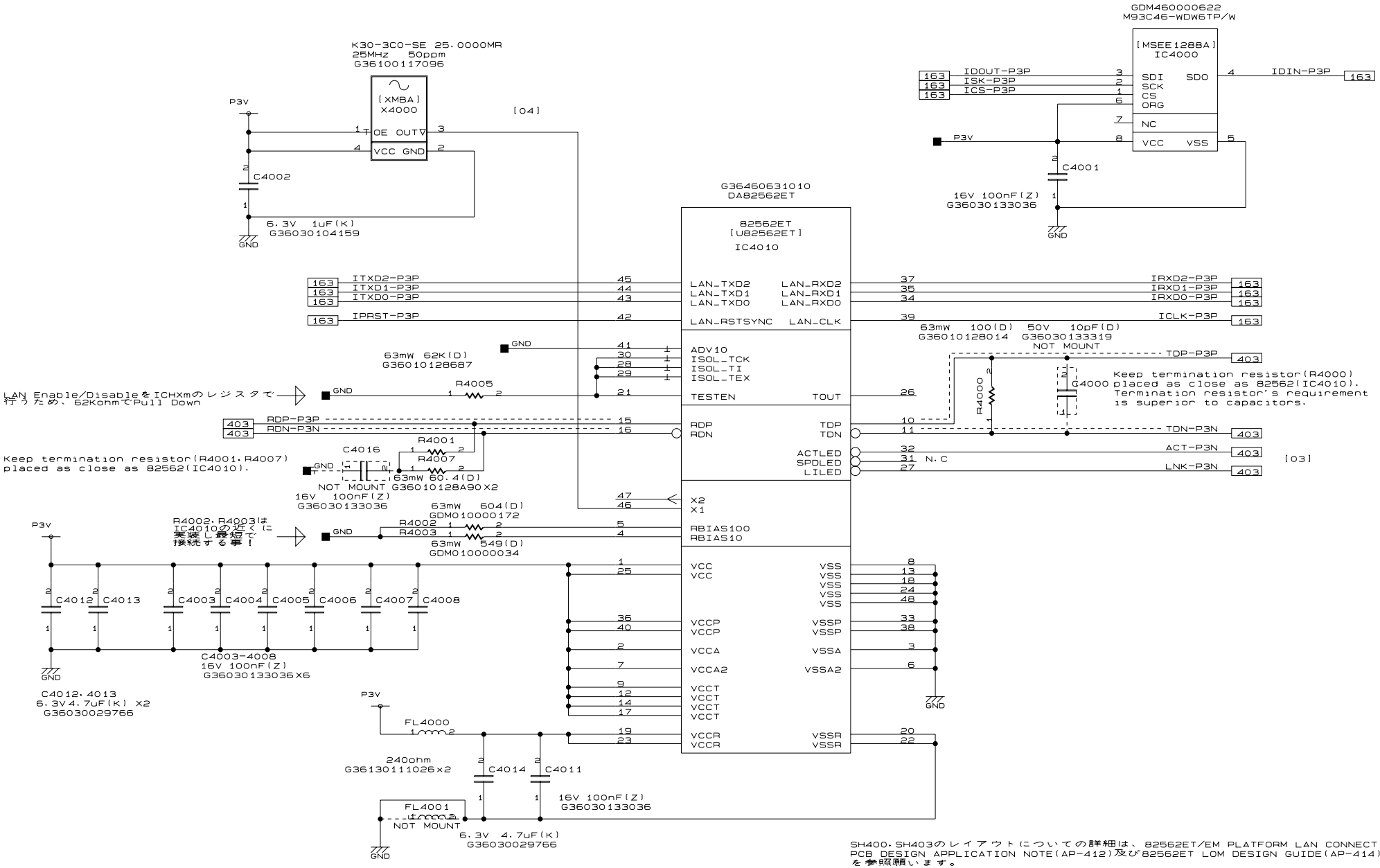
Flash memoryのピンの差：
Sharp : 13: VCCW. 14: WP#
AMD/FUJITSU: 13: NC. 14: NC

[Address Input]

REF:Hayate10(VP)

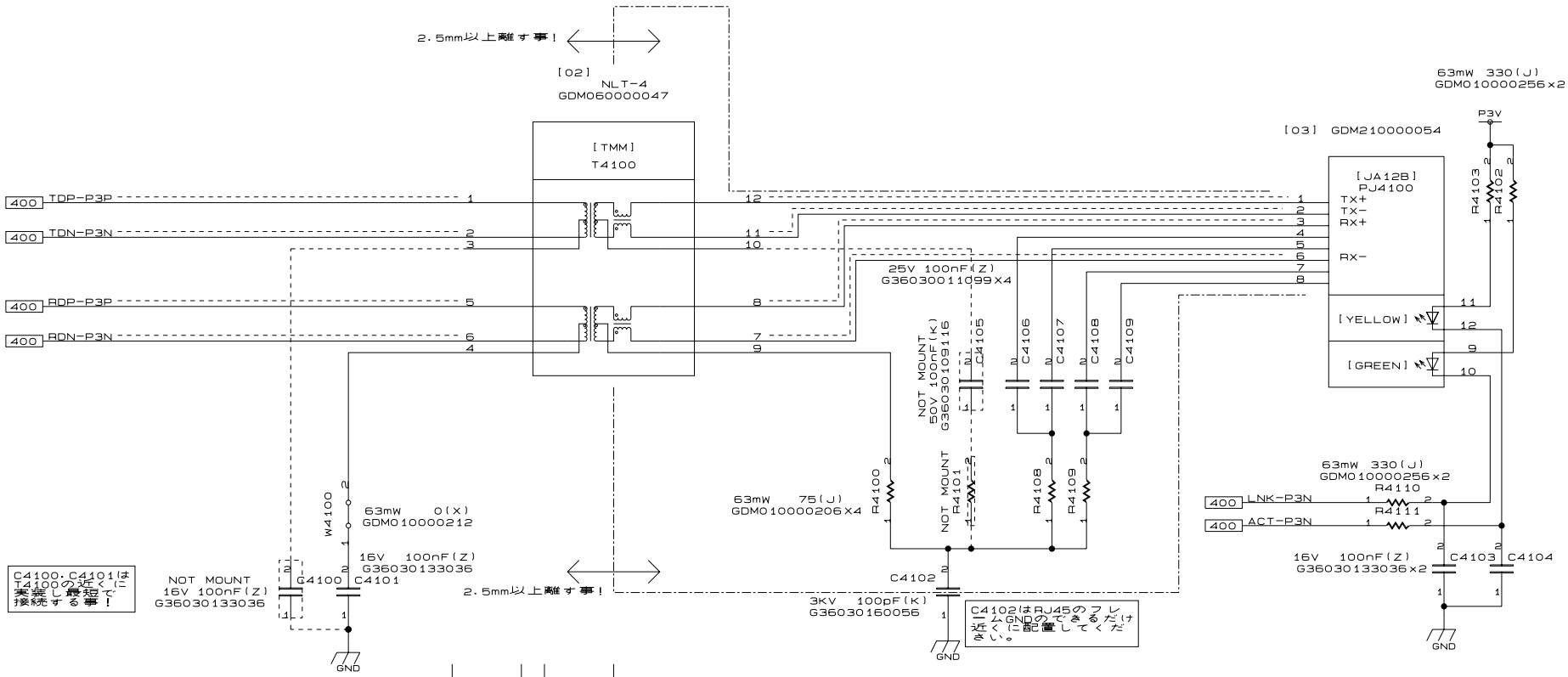
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Y. Shimazaki	FLM1M2	BIOS for SMSC	354	66	04	360048117

2003.11.17 14:10



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/10/21 K. Horibe	FLM1M2	KINNERETH	400	67	04	360048117

2003.11.17 14:10

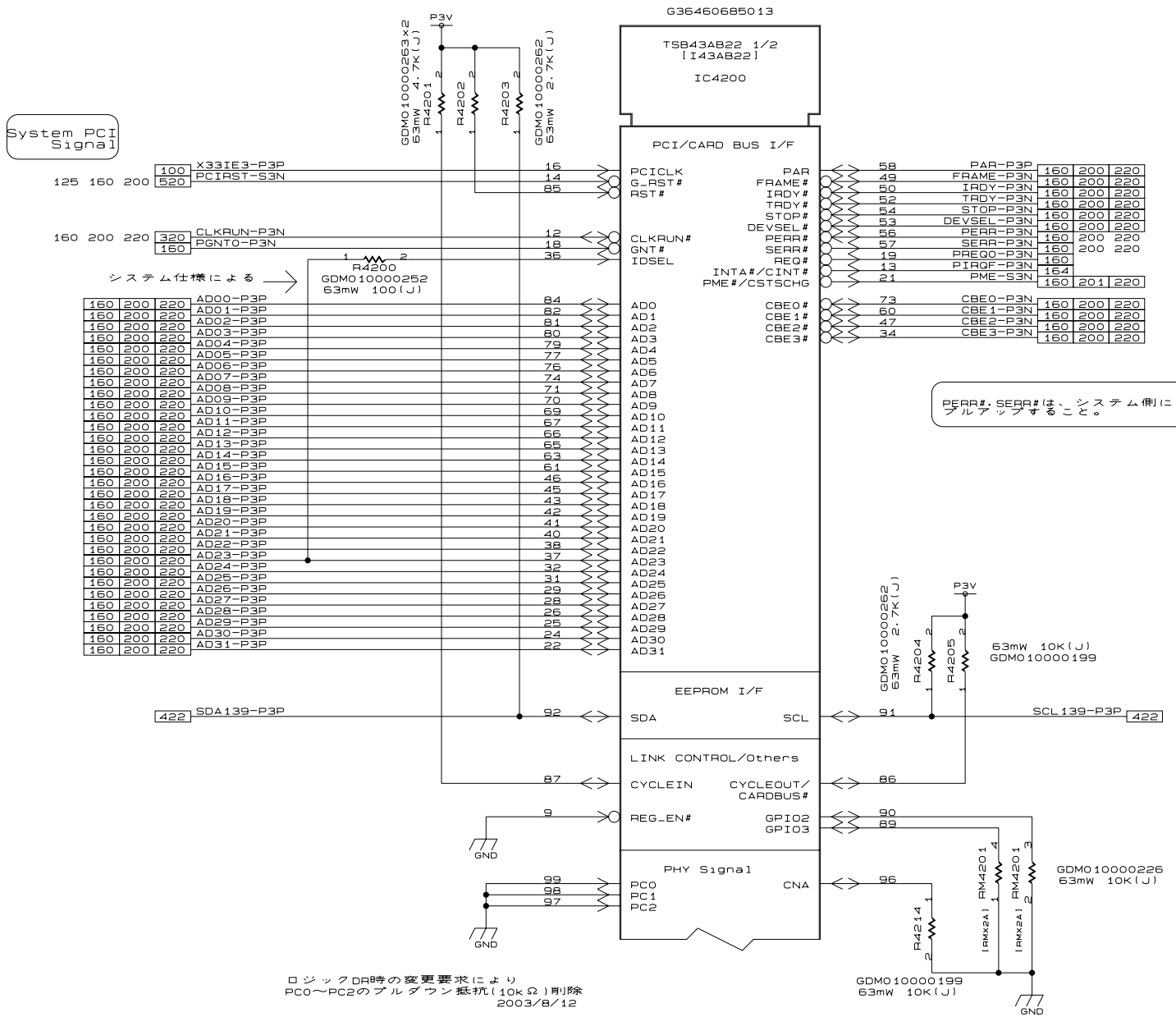


The differential impedance of trace
with --- line should be 100 ohm.
(the single ended traces impedance is 50 ohm)
The differential pairs should be
symmetrical end equal length traces.
Placed in on same side of the board and with no via.

The distance labeled 'A' should be given
the highest priority in board layout.
Distance A should be kept to less than
one inch of separation.

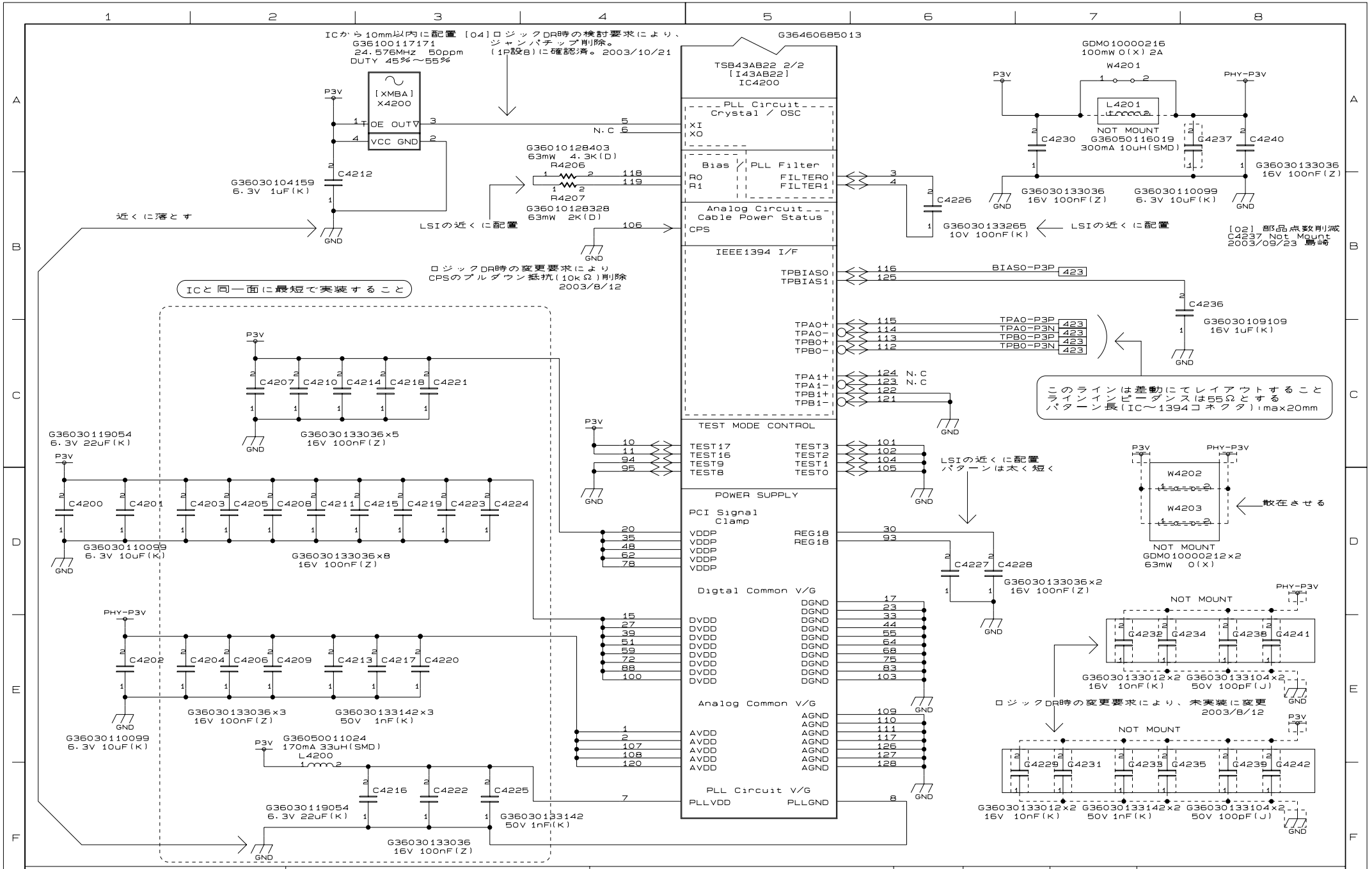
Distance B should also be designed to extend
less than one inch between device.

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
K. HORIBE 2003/9/25	FLM1M2	NETWORK I/F	403	68	03	360048117



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/8/12 K. Uchida	FLM1M2	1394 PHY/LINK(1)	420	69	00	360048117

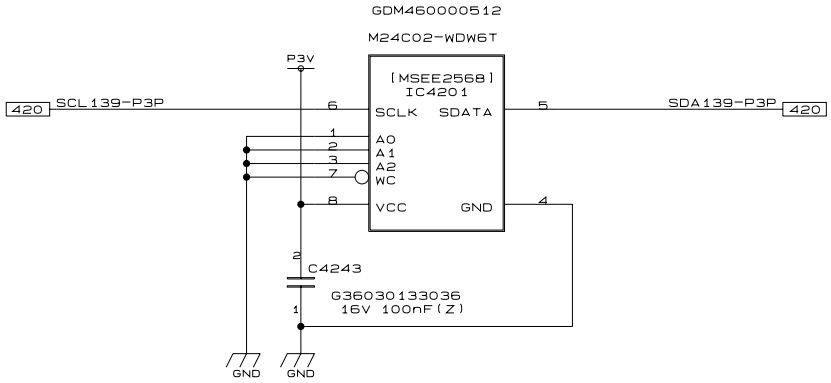
2003.11.17 14:10



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/8/12 K. Uchida	FLM1M2	1394 PHY/LINK(2)	421	70	04	360048117

2003.11.17 14:10

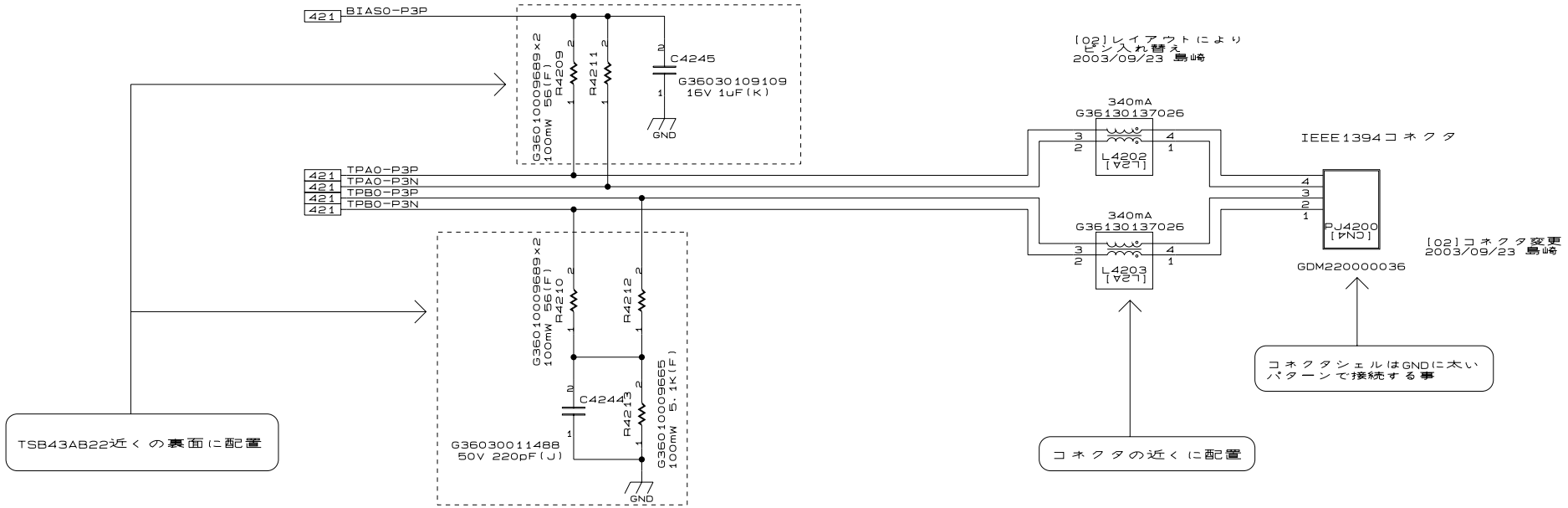
5T03655D



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/7/3 K. Uchida	FLM1M2	1394 EEPROM	422	71	00	360048117

2003. 11. 17 14:10

TOSHIBA CORPORATION



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/7/3 K. Uchida	FLM1M2	1394 I/F	423	72	02	360048117

2003. 11. 17 14:10

TOSHIBA CORPORATION

WakeUp未サポートの為、電源回路／リセット回路削除

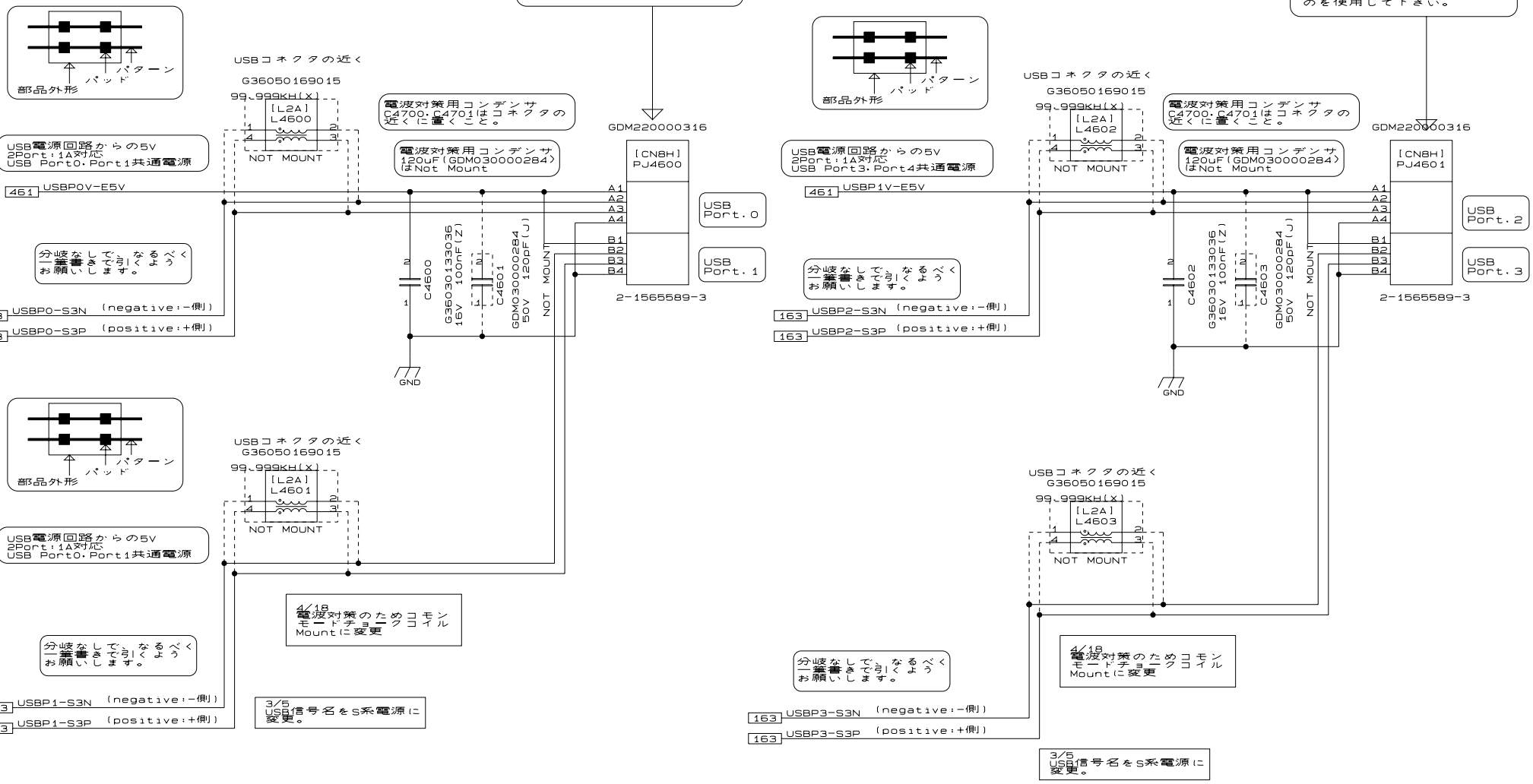
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/7/3 K. Uchida	FLM1M2	1394 POWER/Etc	424	73	00	360048117

下表に、TSB43AB22の各電源ピンごとに配置するパスコンの容量と数量について記述する。

PIN NAME	PIN NO.	パスコンの容量と数量			
		1nF	100nF	10uF	22uF
VDDP	20		×1		
	35		×1		
	48		×1		
	62		×1		
	78		×1		
DVDD	15		×1	×1	×1 (注1)
	27		×1		
	39		×1		
	51		×1		
	59		×1		
	72		×1		
	88		×1		
	100		×1		
AVDD	1	×1	×1	×1	
	2				
	107	×1	×1		
	108				
	120	×1	×1		
PLLVDD	7	×1	×1		×1

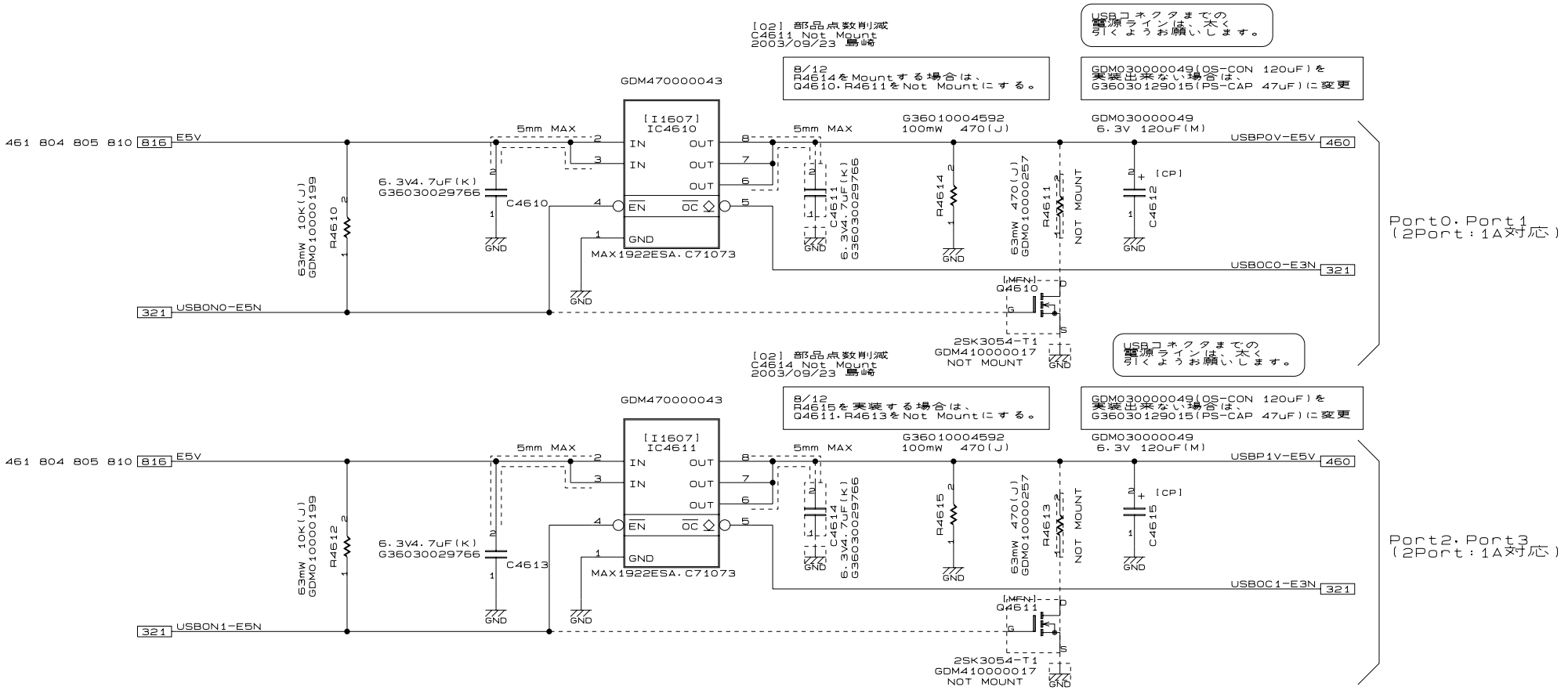
(注1)WakeUpサポート用電源回路実装の場合
電源回路出力部(SI9933の近く)に配置

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/7/3 K. Uchida	FLM1M2	1394 COMMENT	425	74	00	360048117



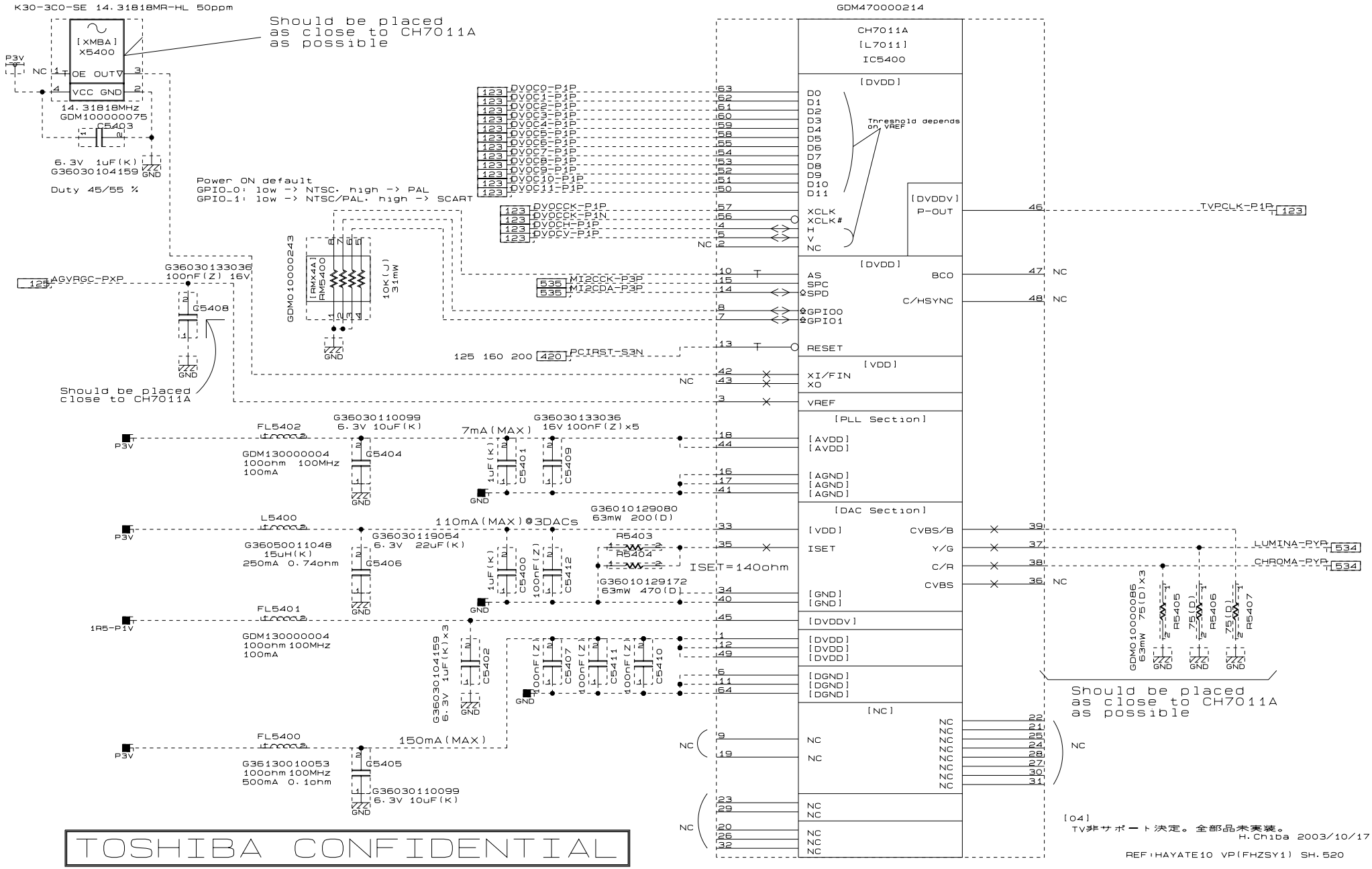
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/07/02 I. Hamano	FLM1M2	USB I/F	460	75	00	360048117

2003.11.17 14:10

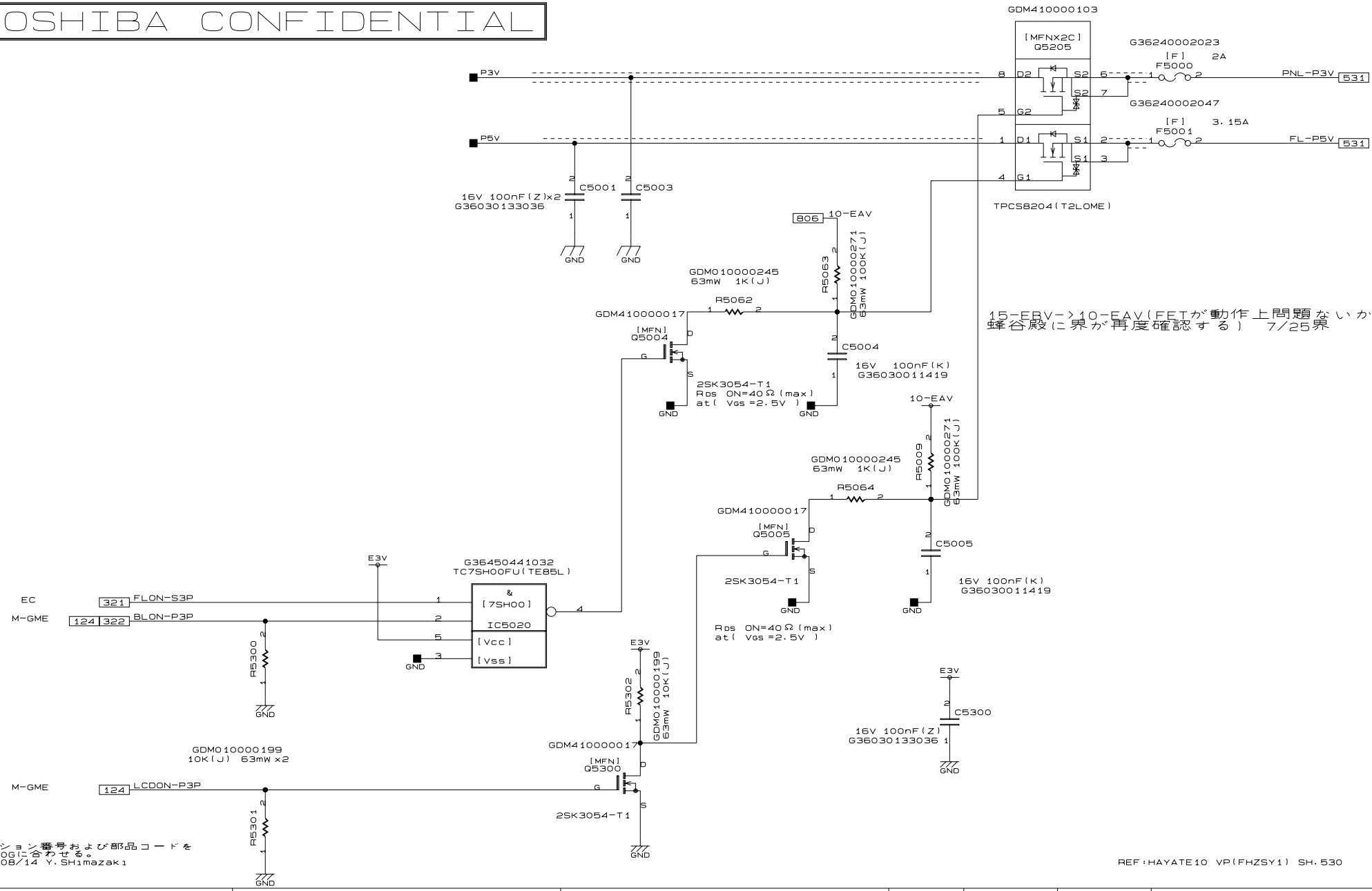


DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/07/02 I. Hamano	FLM1M2	USB POWER	461	76	02	360048117

2003.11.17 14:10



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
H. Chiba 2003/10/17	FLM1M2	TV Encoder	520	77	04	360048117



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
H. Chiba 2003/8/13	FLM1M2	LCD PS I/F	530	78	00	360048117

2003.11.17 14:10

TOSHIBA CORPORATION

TOSHIBA CONFIDENTIAL

PNL2	PNL1	PNL0	LCD TYPE
0	0	0	Reserved
0	0	1	Reserved
0	1	0	Reserved
0	1	1	Reserved
1	0	0	1024x768 (14.1" TFT)
1	0	1	1024x768 (15.0" TFT)
1	1	0	1400x1050 (15.0" TFT)
1	1	1	1024x768 (13.3" TFT)

1)配線の全長は150mm以内。

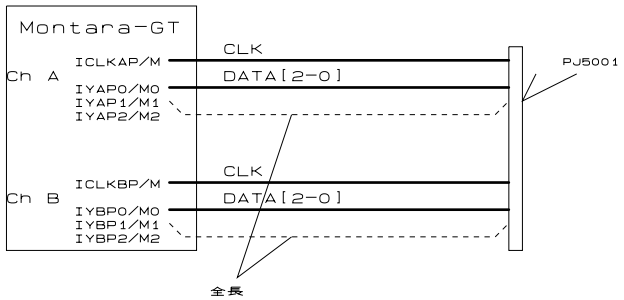
2)ChA、及び、ChBのDATA信号は、ChAのCLK信号(POS)に対して +/- 0.5mm以内に配線。

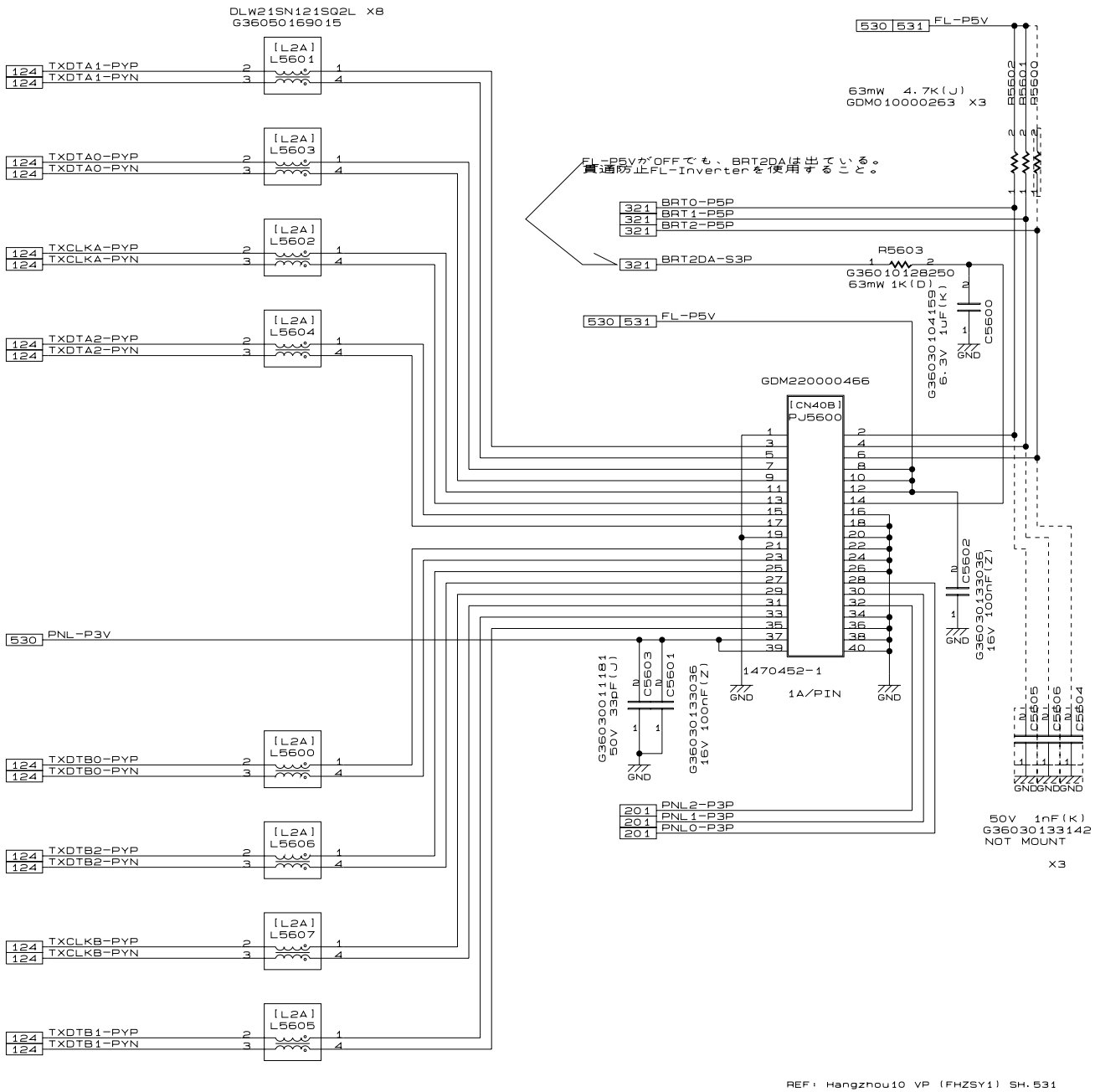
3)以下の差信号ペア同士は+/-0.5mm以内に配線。

ChAのCLK :ICLKAP&ICLKAM ChBのCLK :ICLKBP&ICLKBM
ChAのDATA:IYAP0&IYAM0 ChBのDATA:IYBP0&IYBM0
IYAP1&IYAM1 IYBP1&IYBM1
IYAP2&IYAM2 IYBP2&IYBM2

4)線長調整はMontara-GMLのPackage Lengthを含んだ値である。
詳細はIntel RDDPを参照。

その他、詳細はIntel RDDPを参照。

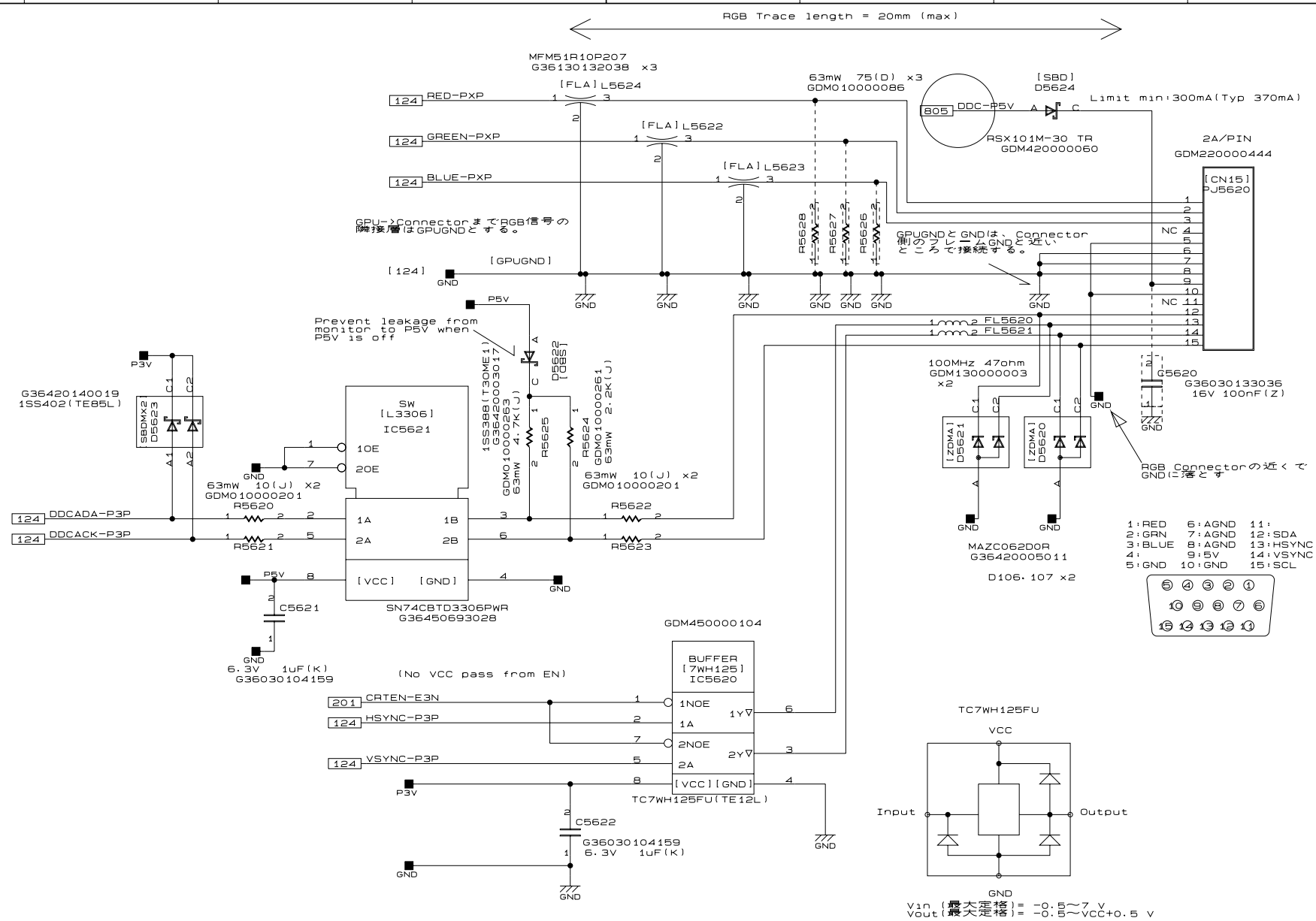




REF: Hangzhou10 VP (FHZSY1) SH.531

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
H. Chiba 2003/8/8	FLM1M2	LCD CONN.	531	79	00	360048117

2003.11.17 14:10



ロケーション番号をLumos10Gに合わせる。2003/8/14 Y.Shimazaki

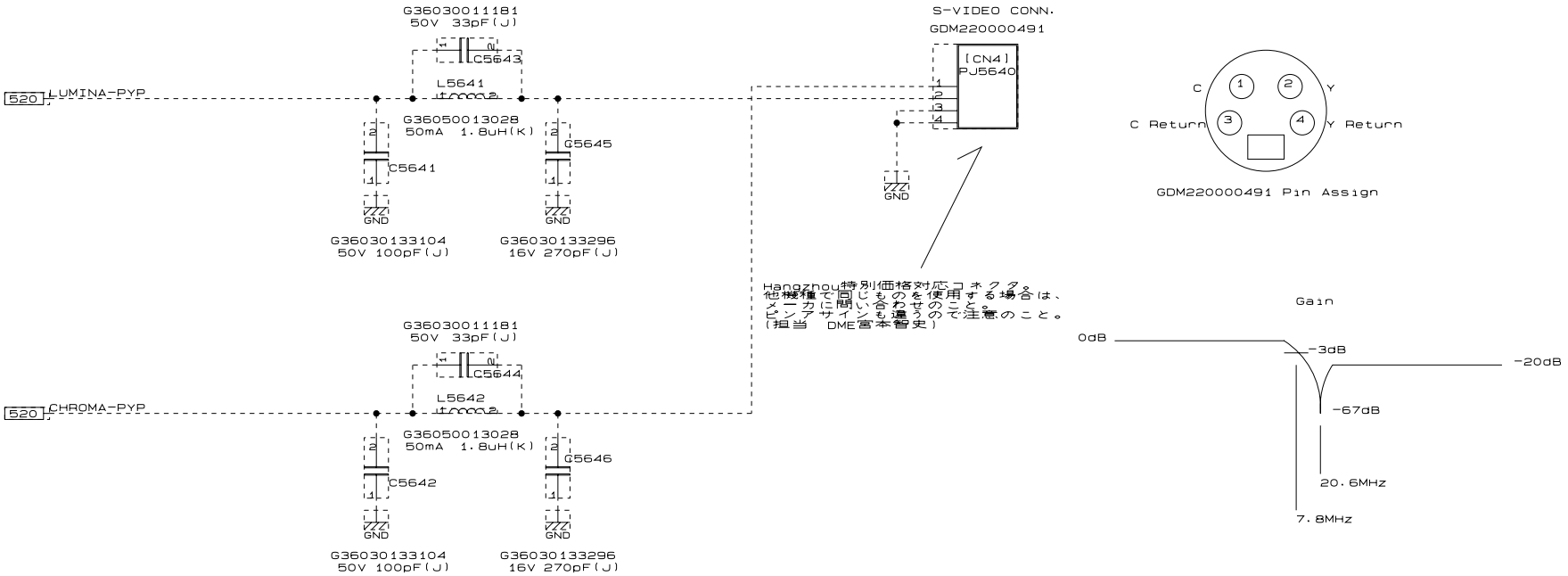
IC5620 属性表示。2003/8/11 H.Chiba

REF:HAWAII10 CS(FWISY1) SH.532

TOSHIBA CONFIDENTIAL

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
H. Chiba 2003/8/8	FLM1M2	CRT I/F	532	80	00	360048117

2003.11.17 14:10



GDM220000491 Pin Assign

[04]
TV非サポート決定。全部品未実装。
H.Chiba 2003/10/17

ロケーション番号をLumos10Gと合わせる。 2003/8/14 Y.Shimazaki

PJ5640の上下をNormalに戻す。 2003/8/11 H.Chiba
PJ5640をGDM220000491(Hangzhou用)にする。 2003/8/8 H.Chiba

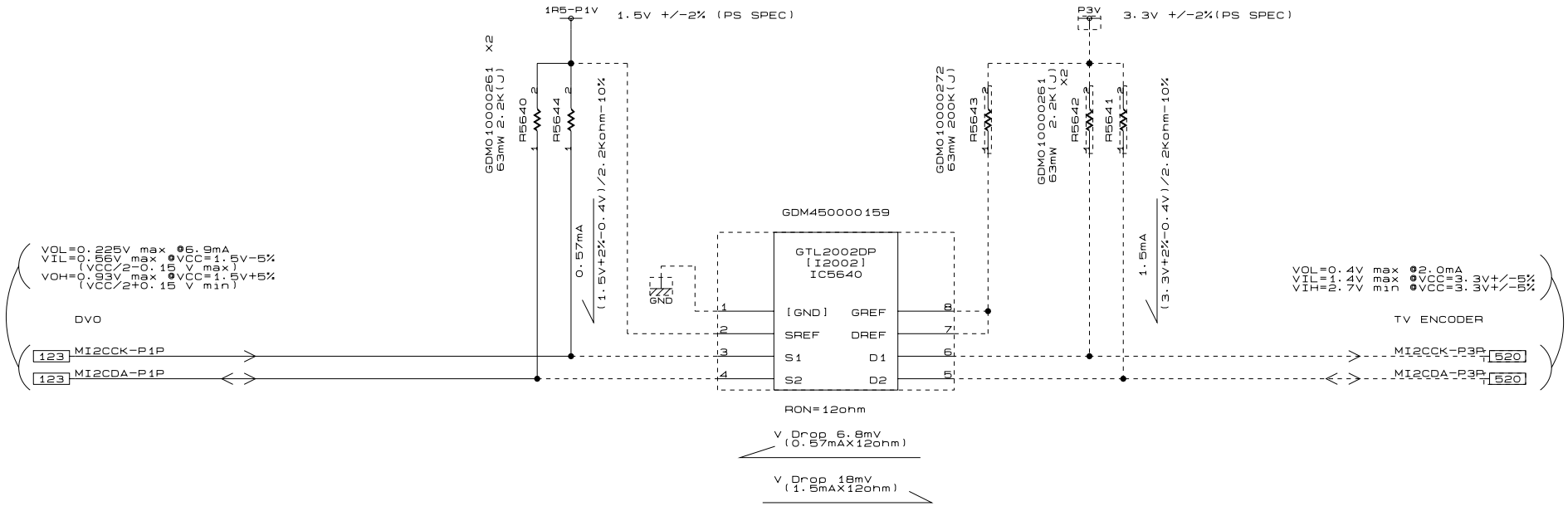
REF:HAYATE10 VP(FHZSY1) SH.534

TOSHIBA CONFIDENTIAL

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
H. Chiba 2003/10/17	FLM1M2	TV CONN.	534	81	04	360048117

2003.11.17 14:10

TOSHIBA CORPORATION



[04]
TV非サポート決定。IC5640.R5641-R5643未実装。
H.Chiba 2003/10/17

REF:HAYATE10 VP(FHZSY1) SH.535

TOSHIBA CONFIDENTIAL

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
H. Chiba 2003/10/17	FLM1M2	Level Shift	535	82	04	360048117

2003.11.17 14:10

TOSHIBA CORPORATION

AC-Linkの信号名のSuffixを変更
(Odem/Montara-GM系に合わせる)
M97SY1-P3P / M97OT1-P3P / M97RS1-S3N
M97BCK-P3P / M97IN1-P3P
2003/7/11 島崎

AC-Linkの分岐抵抗をSH63へ移動
10.11.6Pin SH63に合わせて信号名変更
2002/10/1 細萱

GDM460000323

AD1981B
[U1981]
IC6000

X14SND-P3P
M97SY1-P3P
M97OT1-P3P
M97RS1-S3N
MICIN-PXP
CDAUDL-PXP
CDAUDR-PXP
CD-GND
A-GND
P3V
GND
A4R7-P4V

X97BCK-P3P → ICH4-M/MDC
M97IN1-P3P → ICH4-M
OUTL-PXP
OUTR-PXP

R6006 63mW 33(J) GDM010000249

	STAC9750	AD1981B	
2pin	W6003 Not Mount		1005
2pin	W6006 G36010119012		1005
2pin	X6000 Not Mount		
3pin	W6011 G36010119012		1005
45pin	R6005 Not Mount	1k(J) G36010116097	1005
46pin	R6006 Not Mount	1k(J) G36010116097	1005
43pin	W6004 Not Mount		1005
43pin	C6009 Not Mount	100n G36030133265	1005
34pin	C6016 Not Mount	100n G36030133265	1005
34pin	W6005 Not Mount		1005
42pin	W6007 JP G36010119012		1005
44pin	W6008 Not Mount		1005
33pin	W6009 Not Mount		1005
32pin	C6106 1U G36030104159		1005
32pin	C6020 100n G36030011419	270p G36030133296	1005
31pin	C6019 Not Mount	270p G36030133296	1005
30pin	C6018 820P G36030011785	270p G36030133296	1005
29pin	C6017 820P G36030011785	270p G36030133296	1005

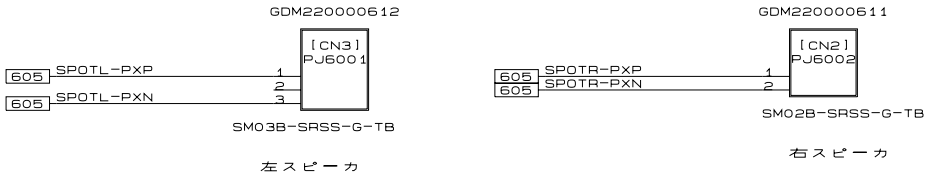
短く引く ☆

DESIGNED BY 2003/8/4	TITLE	FUNCTION	SH. NO. / PAGE NO. / REV. MARK / DRAWING. NO.
K. Saito	FLM1M2	AD1981B	600 / 83 / 00 / 360048117

2003.11.17 14:10

TOSHIBA CORPORATION

5T03655D

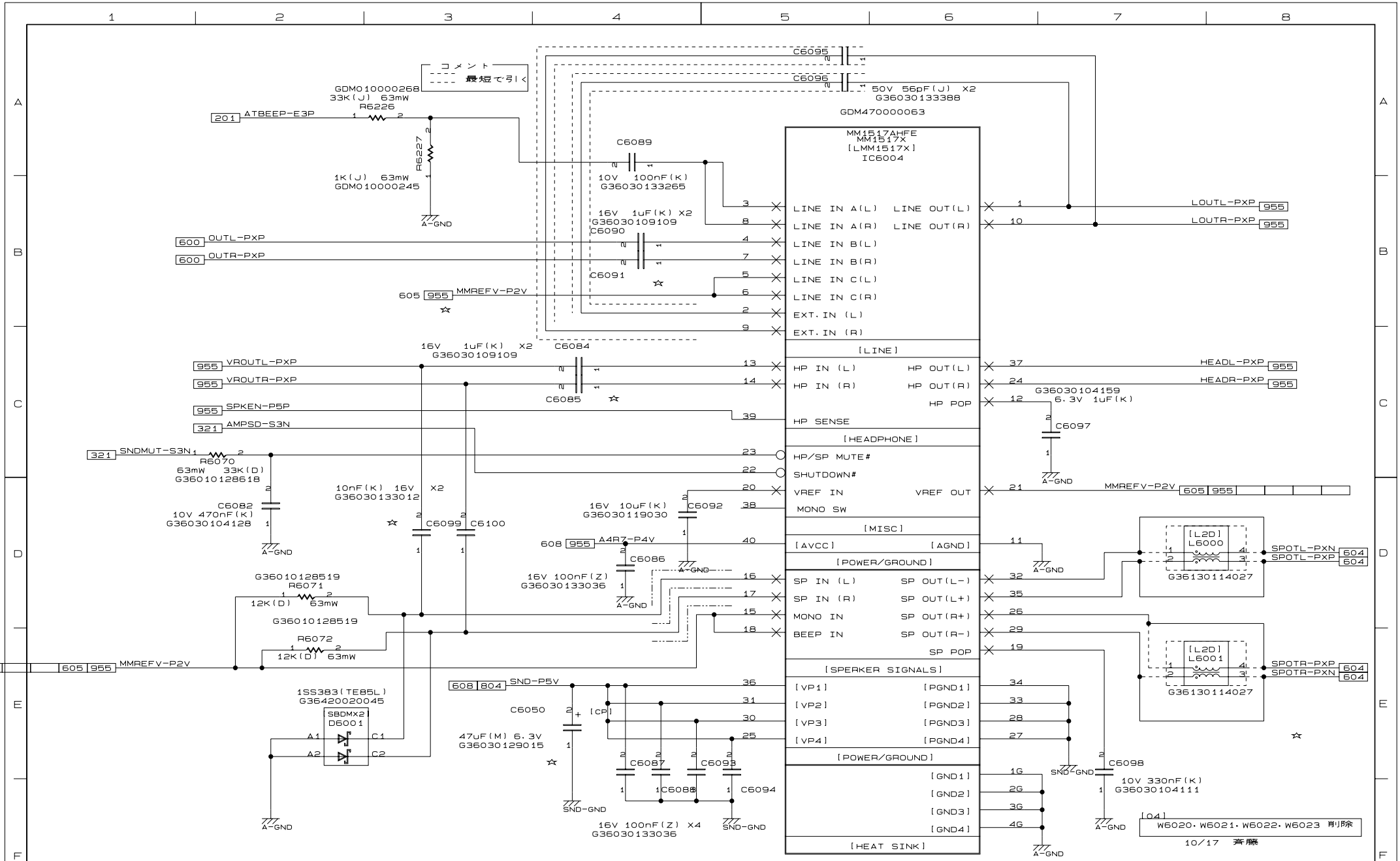


コネクタ新規部品へ変更　8/11　斉藤

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/8/11 K. Saito	FLM1M2	SPEAKER CON	604	84	00	360048117

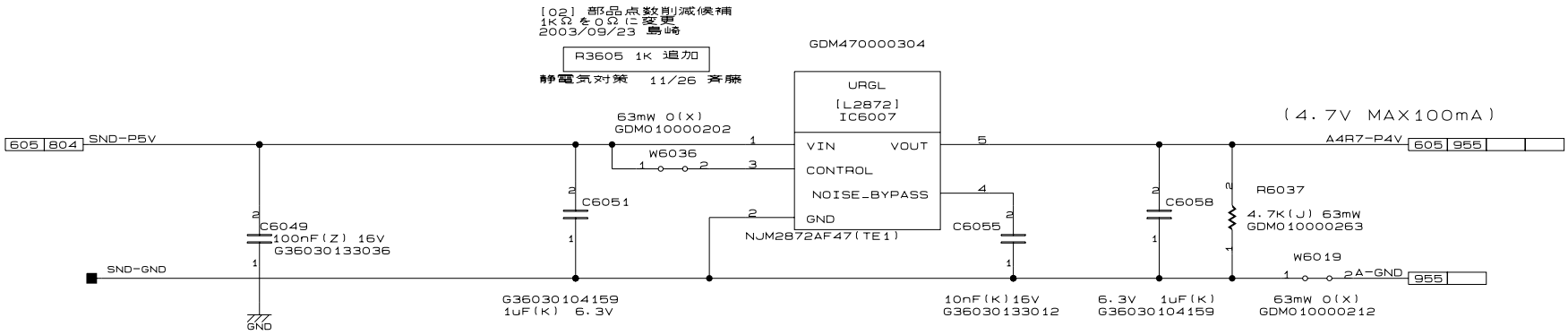
2003. 11. 17　14:10

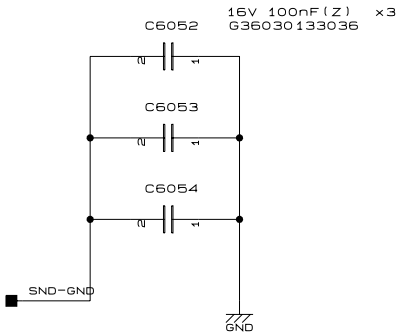
TOSHIBA CORPORATION

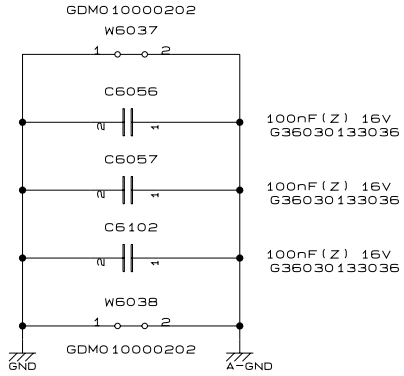


DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/10/17 K. Saito	FLM1M2	AMP MM1517A	605	85	04	360048117

2003. 11. 17 14:10







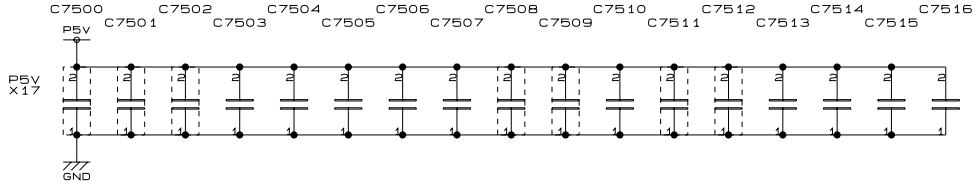
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/10/17 K. Saito	FLM1M2	ANALOG POWER	608	86	04	360048117

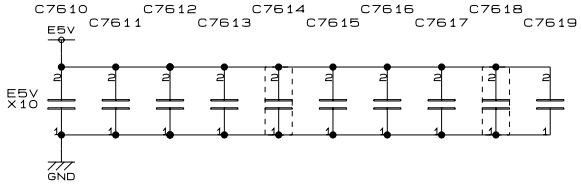
2003.11.17 14:10

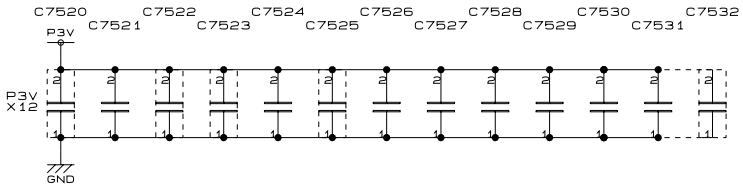
G36030133036 1005 16V. 100nF(Z)

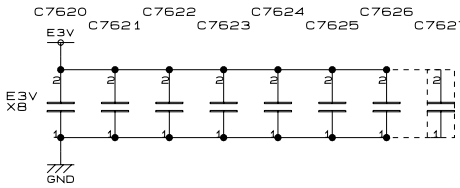
[02] 部品点数削減
C7500-C7502. C7507-C7512.
C7516. C7520-C7525. C7528.
C7532. C7560. C7612. C7618.
C7619. C7627. C7651. C7540.
C7672. C7673. C7684
Not Mount
2003/09/23 島崎

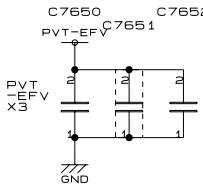
[03] PHY-CR-03A_C-083
C7507. C7510. C7516. C7521.
C7524. C7528. C7560. C7612.
C7619. C7672. C7673. C7684
Mount
C7561. C7614
Not Mount
島崎

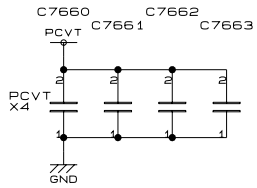


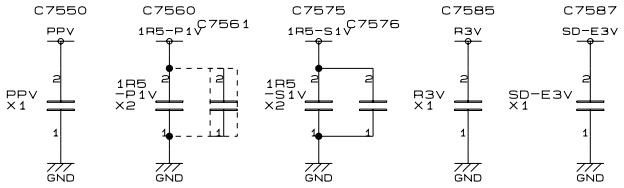


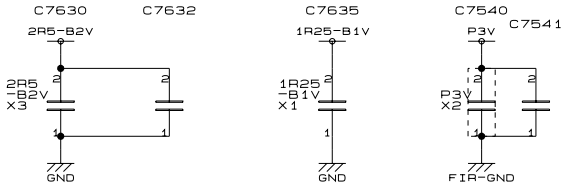


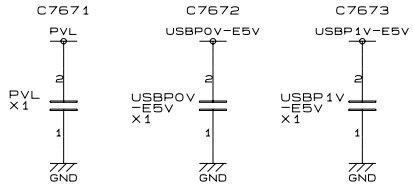


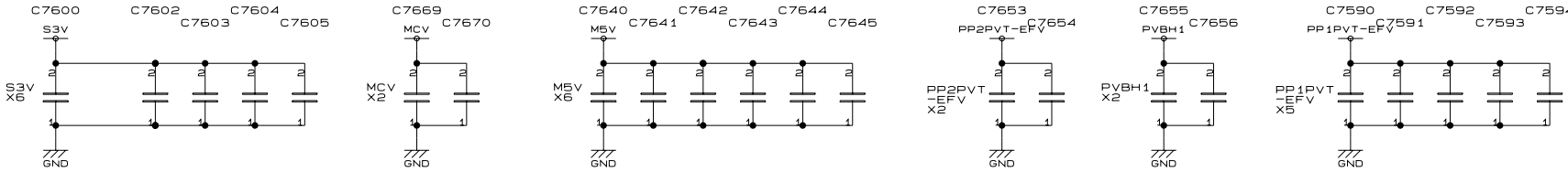


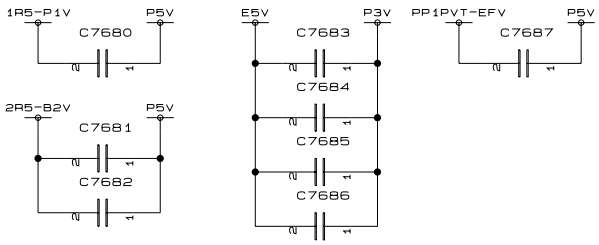






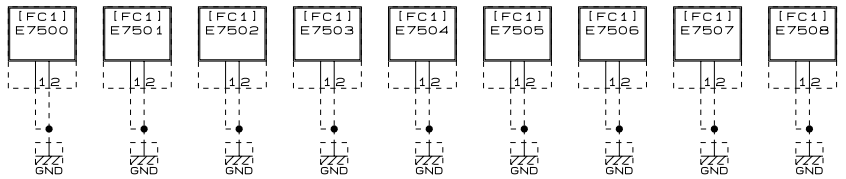






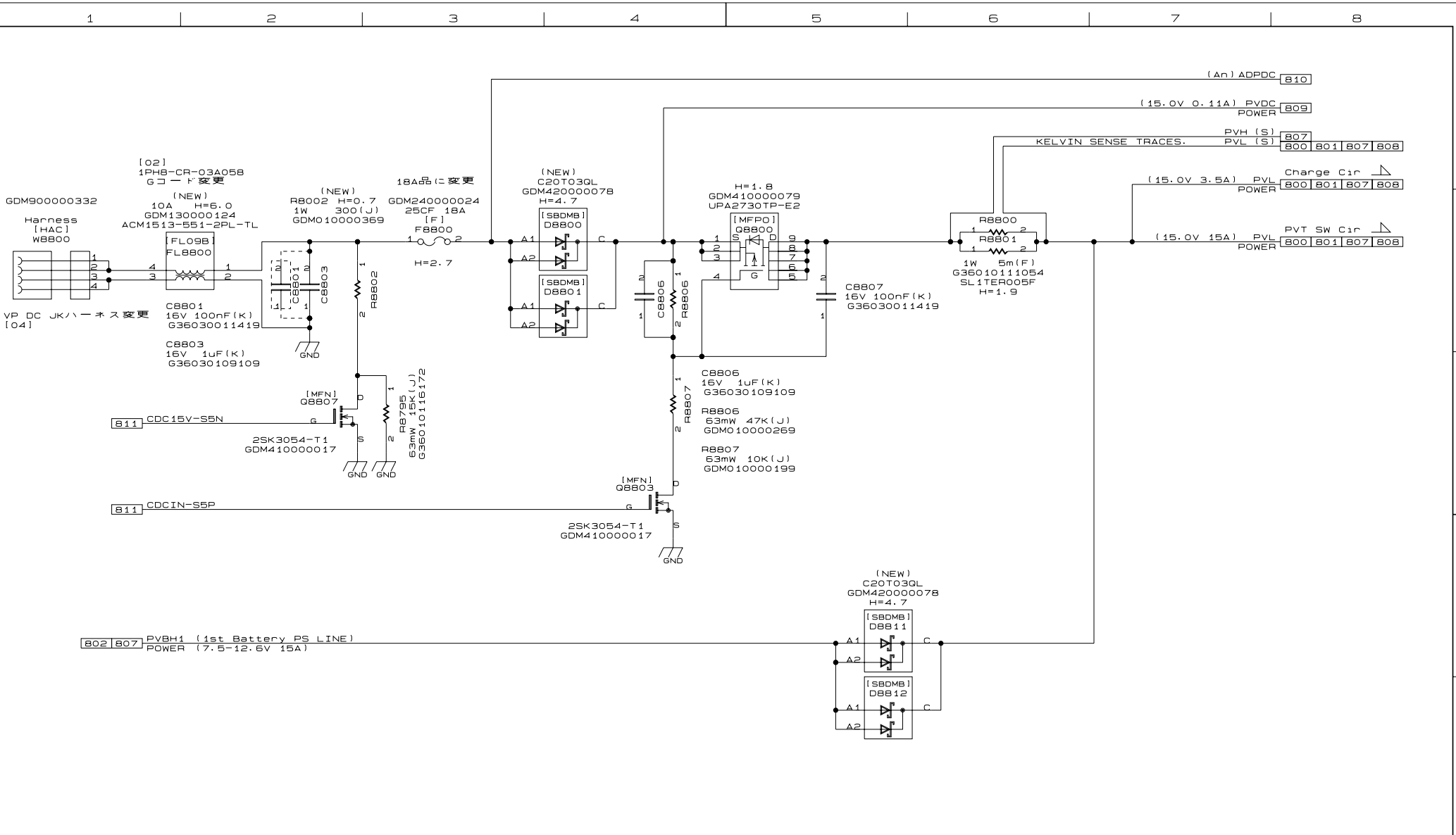
[02] E7500-E7508 Not Mount
2003/09/22 島崎

NOT MOUNT
GDM290000002 x9



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/8/21 Y. Shimazaki	FLM1M2	EMI CAPS	750	87	03	360048117

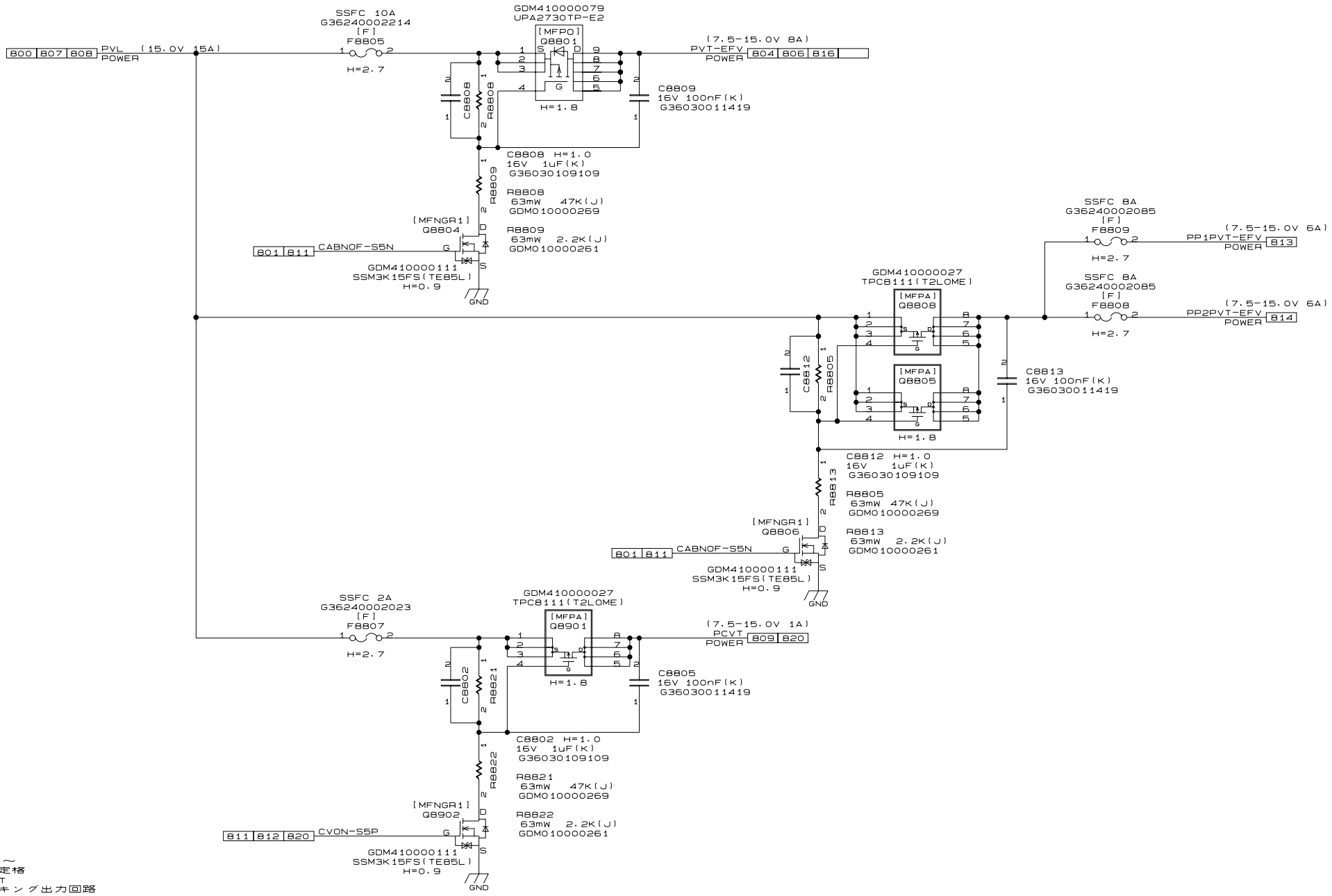
2003.11.17 14:10



～チェック～
□ 部品定格
○ダイオード
○ヒューズ
○FET
○コモンモードコイル
□ 引き抜き抵抗
○抵抗値
○許容損失
□ ドッキング入力回路
□ バッテリー入力回路

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/06/18 S. HACHIYA	FLM1M2	[PS] DC-IN	800	88	04	360048117

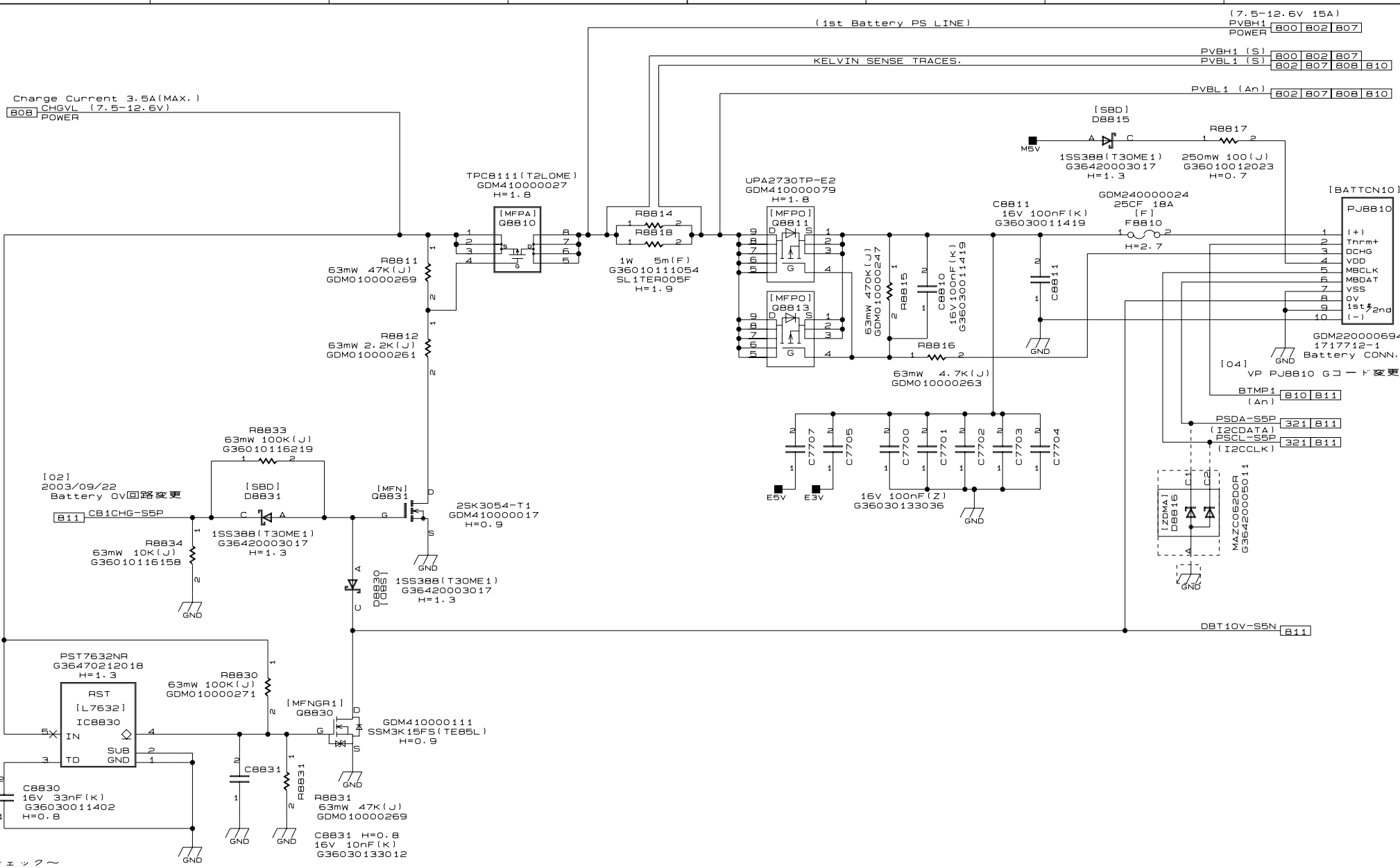
2003.11.17 14:10



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/06/18 S. HACHIYA	FLM1M2	[PS] PVT SW	801	89	00	360048117

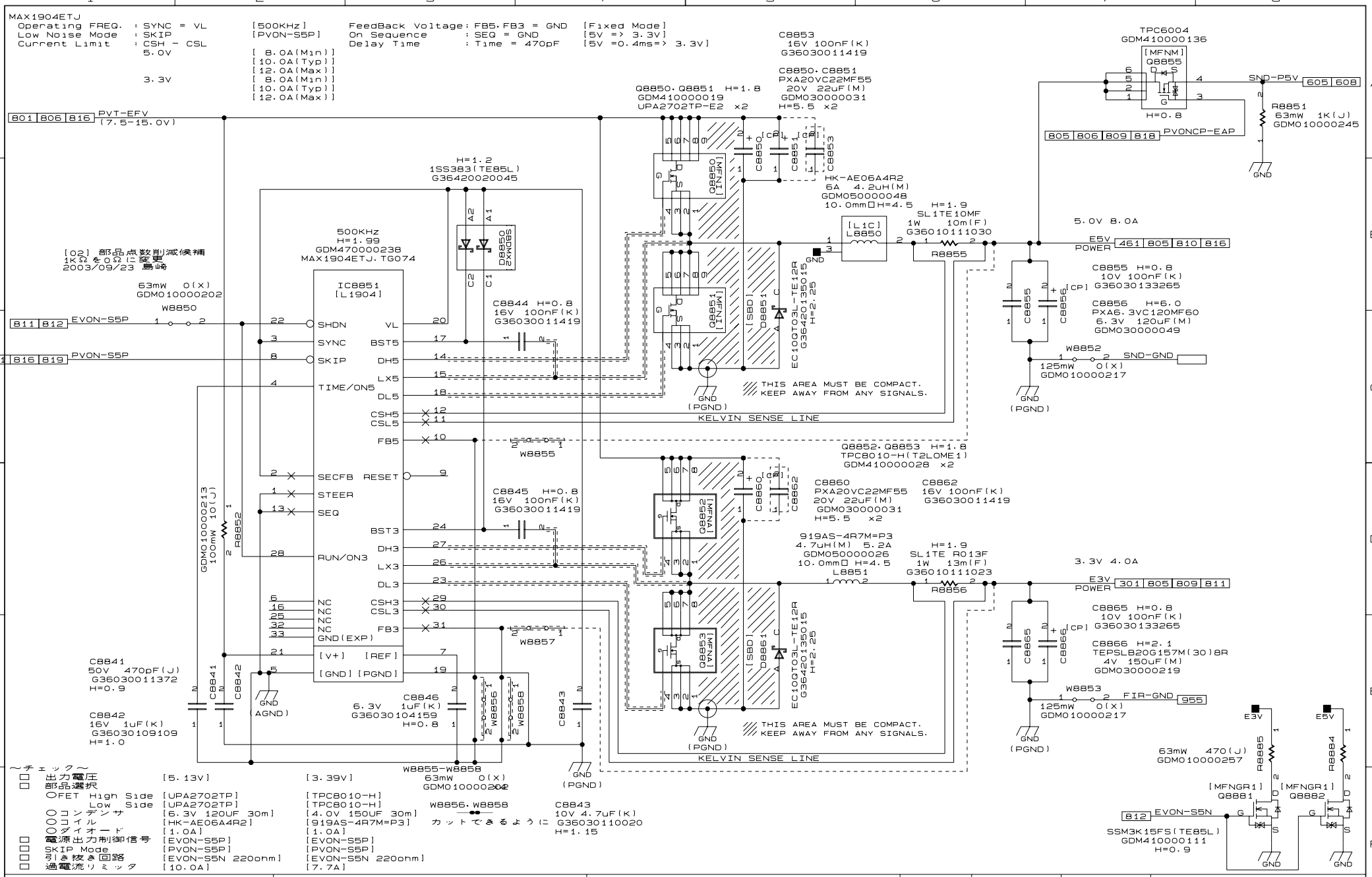
2003.11.17 14:10

TOSHIBA CORPORATION



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/06/18 S. HACHIYA	FLM1M2	[PS] 1st Battery	802	90	04	360048117

2003.11.17 14:10



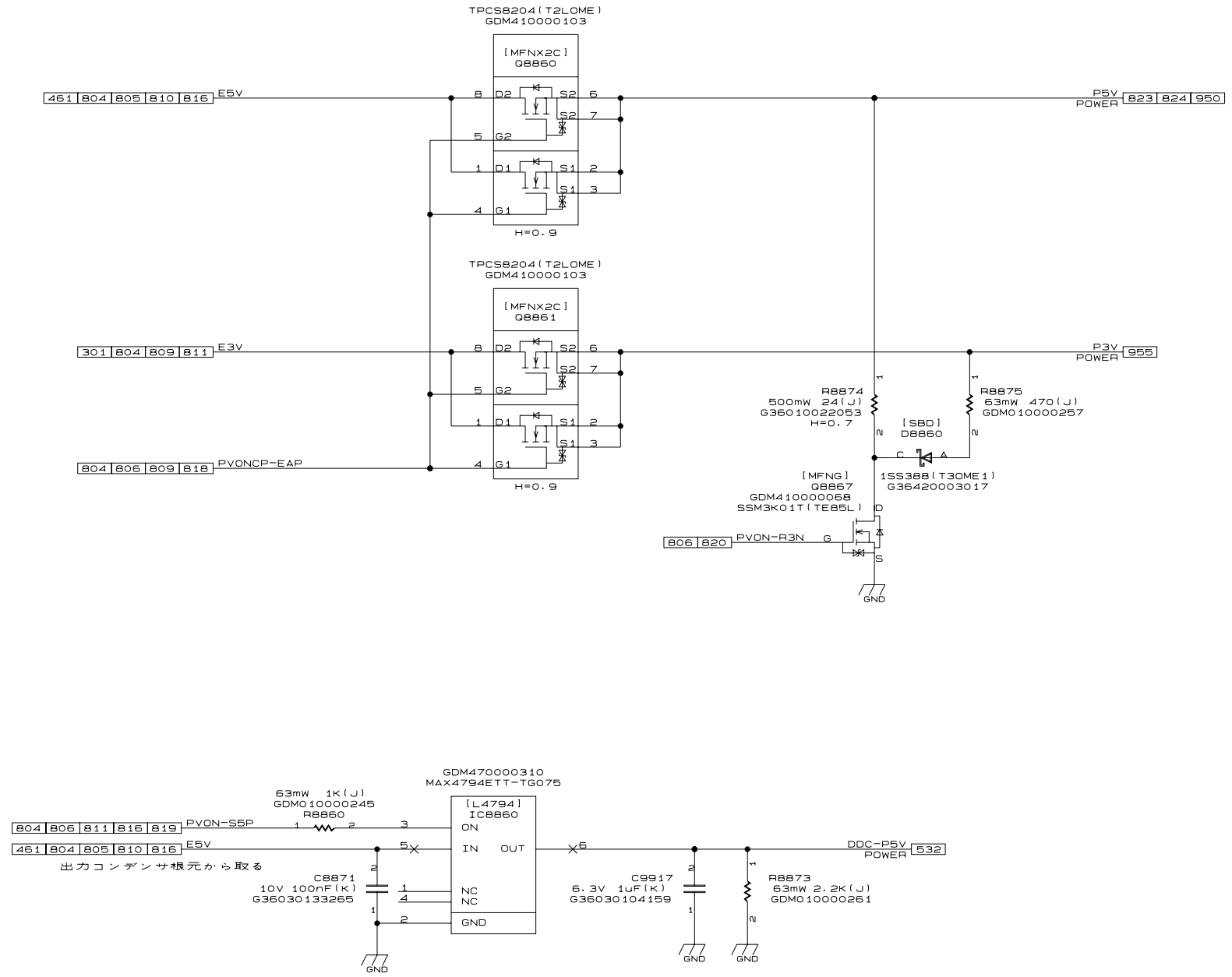
〜チェック〜

□	出力電圧	[5.13V]	[3.39V]
□	部品選択		
	○FET High Side	[UPA2702TP]	[TPC8010-H]
	Low Side	[UPA2702TP]	[TPC8010-H]
	○コンデンサ	[6.3V 120UF 30m]	[4.0V 150UF 30m]
	○コイル	[HK-AE06A4R2]	[919AS-4R7M=P3]
	○ダイオード	[1.0A]	[1.0A]
□	電源出力制御信号	[EVON-S5P]	[EVON-S5P]
□	SKIP Mode	[PVON-S5P]	[PVON-S5P]
□	引き抜き回路	[EVON-S5N 220ohm]	[EVON-S5N 220ohm]
□	過電流リミッタ	[10.0A]	[7.7A]

[02] 部品点数削減候補
1KΩ を 0Ω に変更
2003/09/23 島崎

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/06/18 S. HACHIYA	FLM1M2	[PS] E5V/E3V	804	91	02	360048117

2003.11.17 14:10

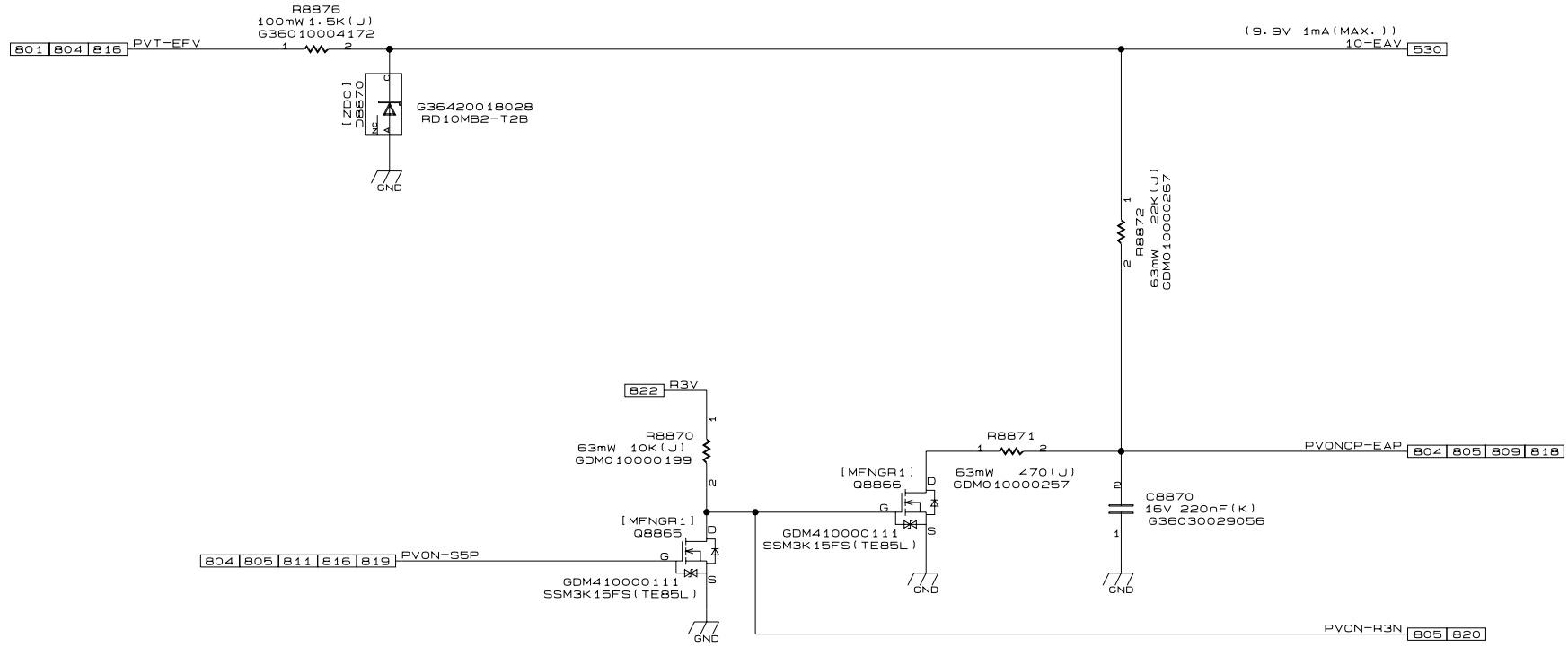


～チェック～
- □ 入力電源名
- □ 出力電源名
- □ 制御信号.駆動電圧
- □ FETの電圧降下/許容損失
- □ 引き抜き回路(100ms以内)

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/06/18 S. HACHIYA	FLM1M2	[PS] PW SW	805	92	00	360048117

2003.11.17 14:10

5T03655D

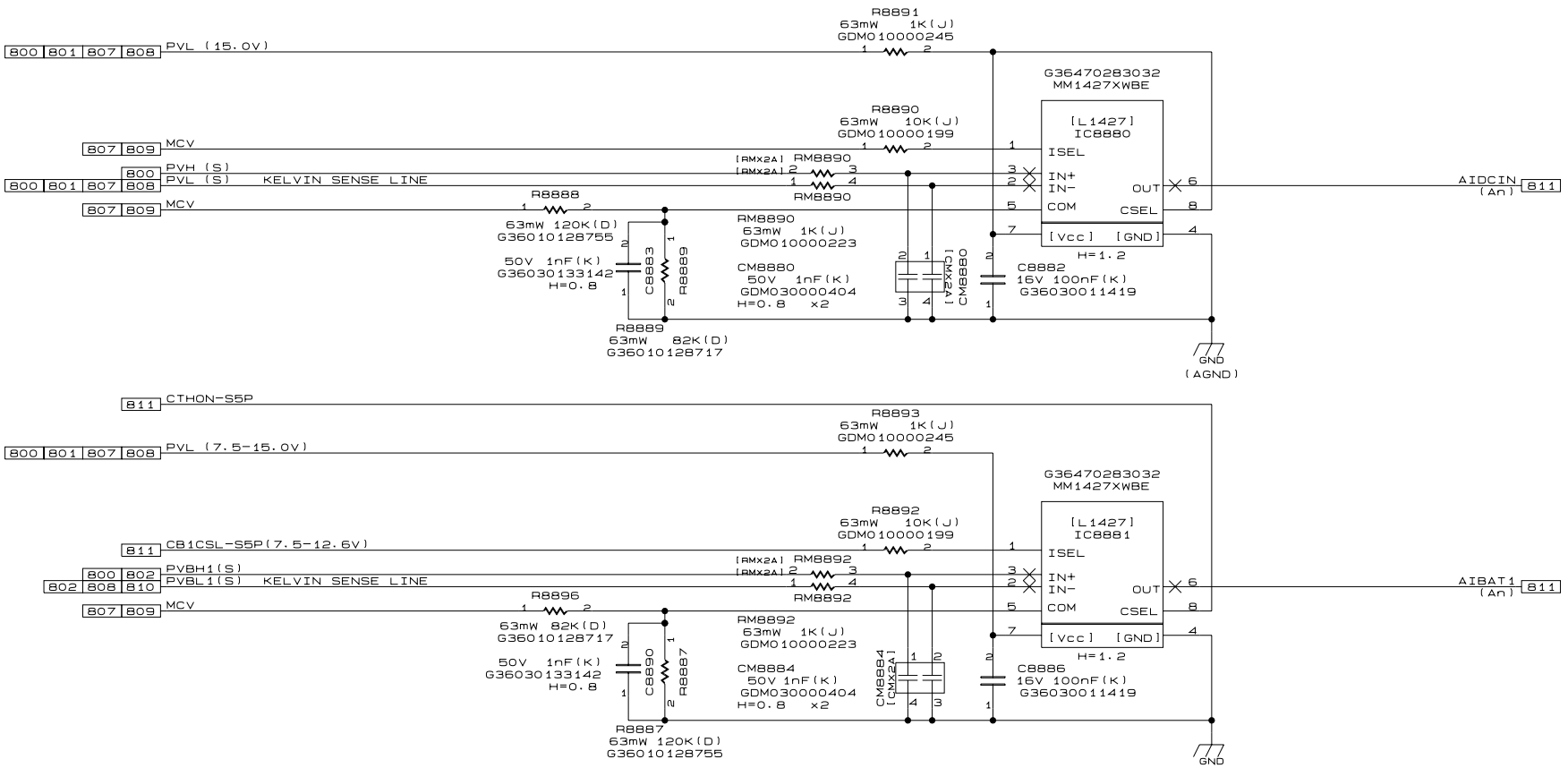


～チェック～
- □ 入力電圧 [5.0V]
- □ 出力電圧 [10.0V]
- □ 負荷電流 [1mA Max]
- ○ 負荷抵抗
- ○ FETゲート漏れ電流
- □ 出力時定数 [4.7ms]

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/06/18 S. HACHIYA	FLM1M2	[PS] E10V	806	93	00	360048117

2003.11.17 14:10

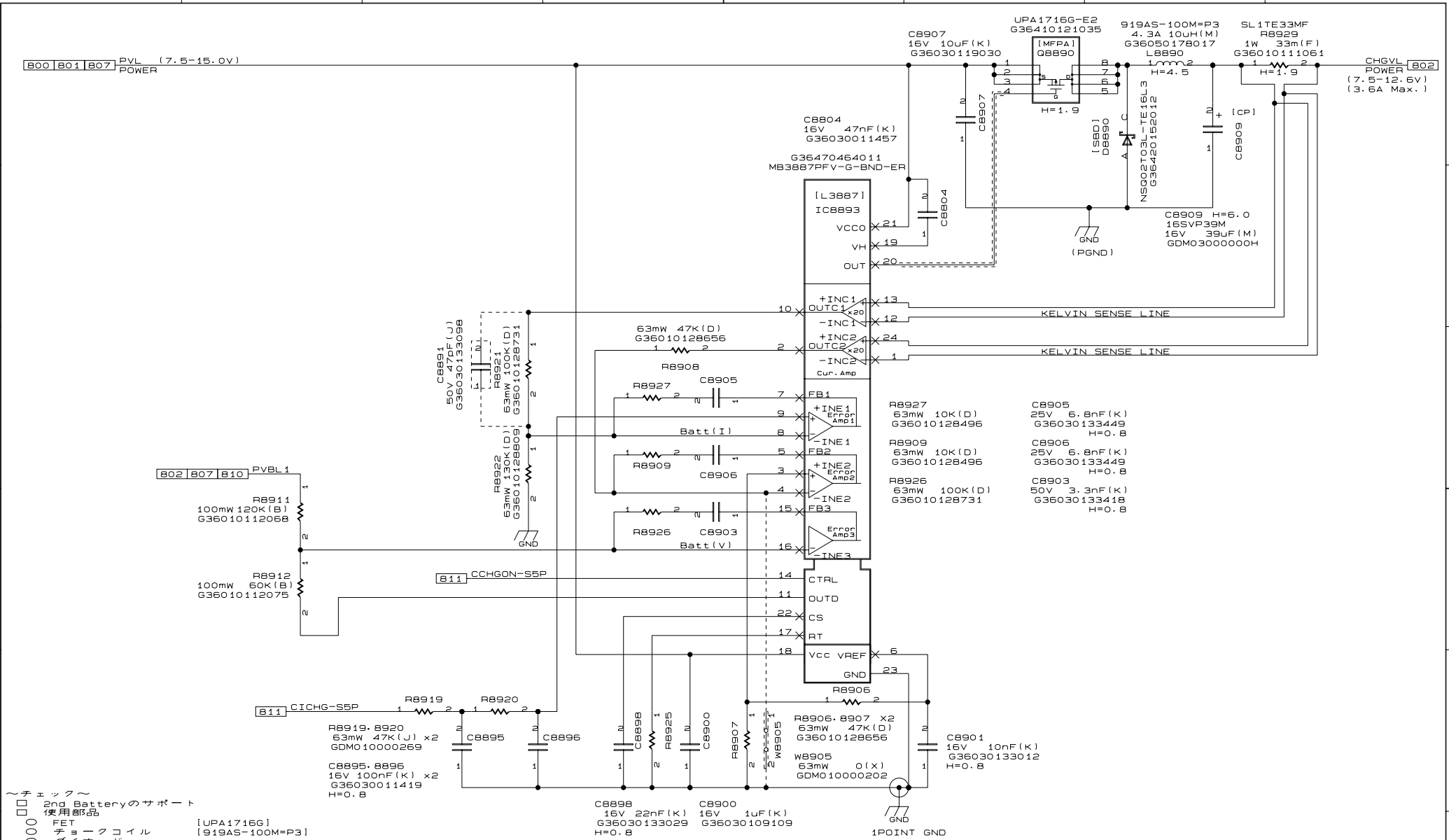
TOSHIBA CORPORATION



～チェック～
□ 2nd-Battery 有り/無し
□ 電流検出範囲
○○ 電流検出抵抗値
○○ 検出ICのGain
○○ PSC入力範囲

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/06/18 S. HACHIYA	FLM1M2	[PS] CURRENT AMP	807	94	00	360048117

2003.11.17 14:10



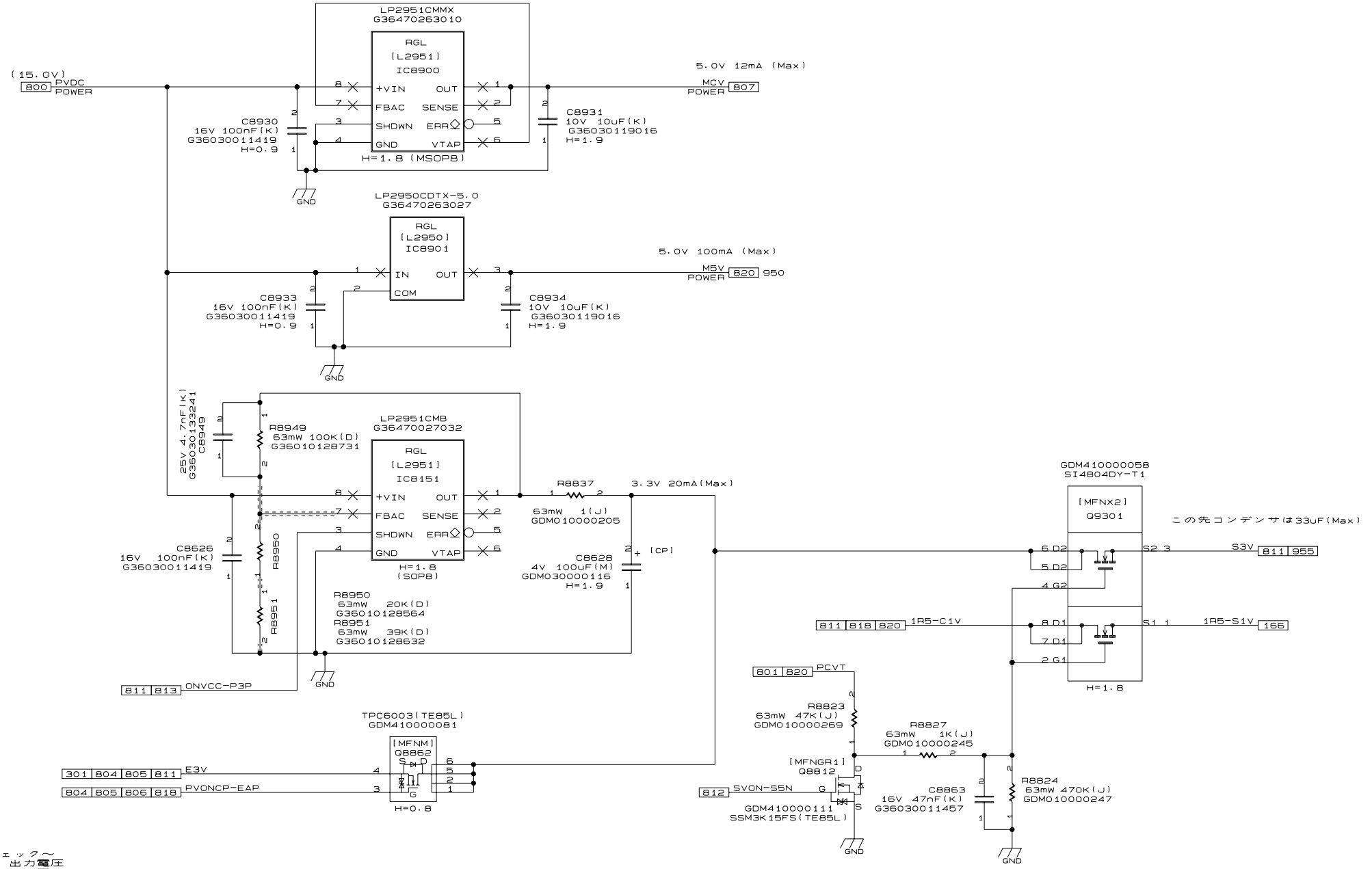
～チェック～

- □ 2nd Batteryのサポート
- □ 使用部品
  - ○ FET [UPA1716G]
  - ○ チョークコイル [919AS-100M=P3]
  - ○ ダイオード
  - ○ コンデンサ [16V 39uF]
    セラコンに変更する場合、ESRを調整すること
  - ○ 電流検出抵抗 [1W 33mohm]
  - □ 電流検出アンプ High:100KΩ Low:130KΩ
  - □ 電圧検出アンプ High:120KΩ Low:60KΩ
  - □ 過電流リミッタ設定値

[R8906/(R8906+R8907)*VREF/20]/R8929 = ILIMT
(0.033mΩ*3.78A)*20 = 5.0V*47kΩ/(47kΩ+47kΩ)

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
S. HACHIYA 2003/06/18	FLM1M2	[PS] CHARGE	808	95	00	360048117

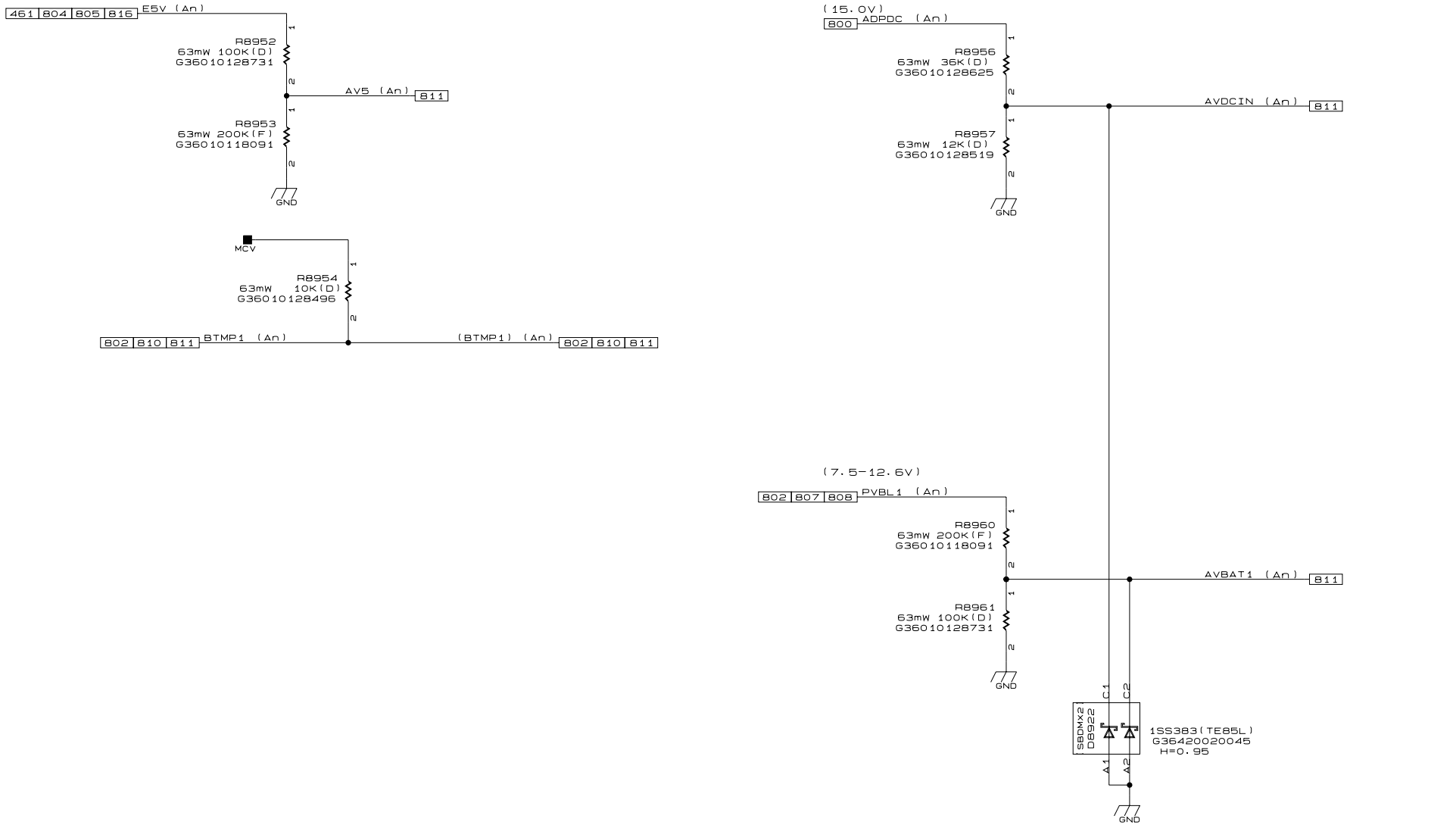
2003.11.17 14:10



～チェック～
□ 出力電圧
□ 出力電流定格

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/06/18 S. HACHIYA	FLM1M2	[PS] MCV/S*V	809	96	00	360048117

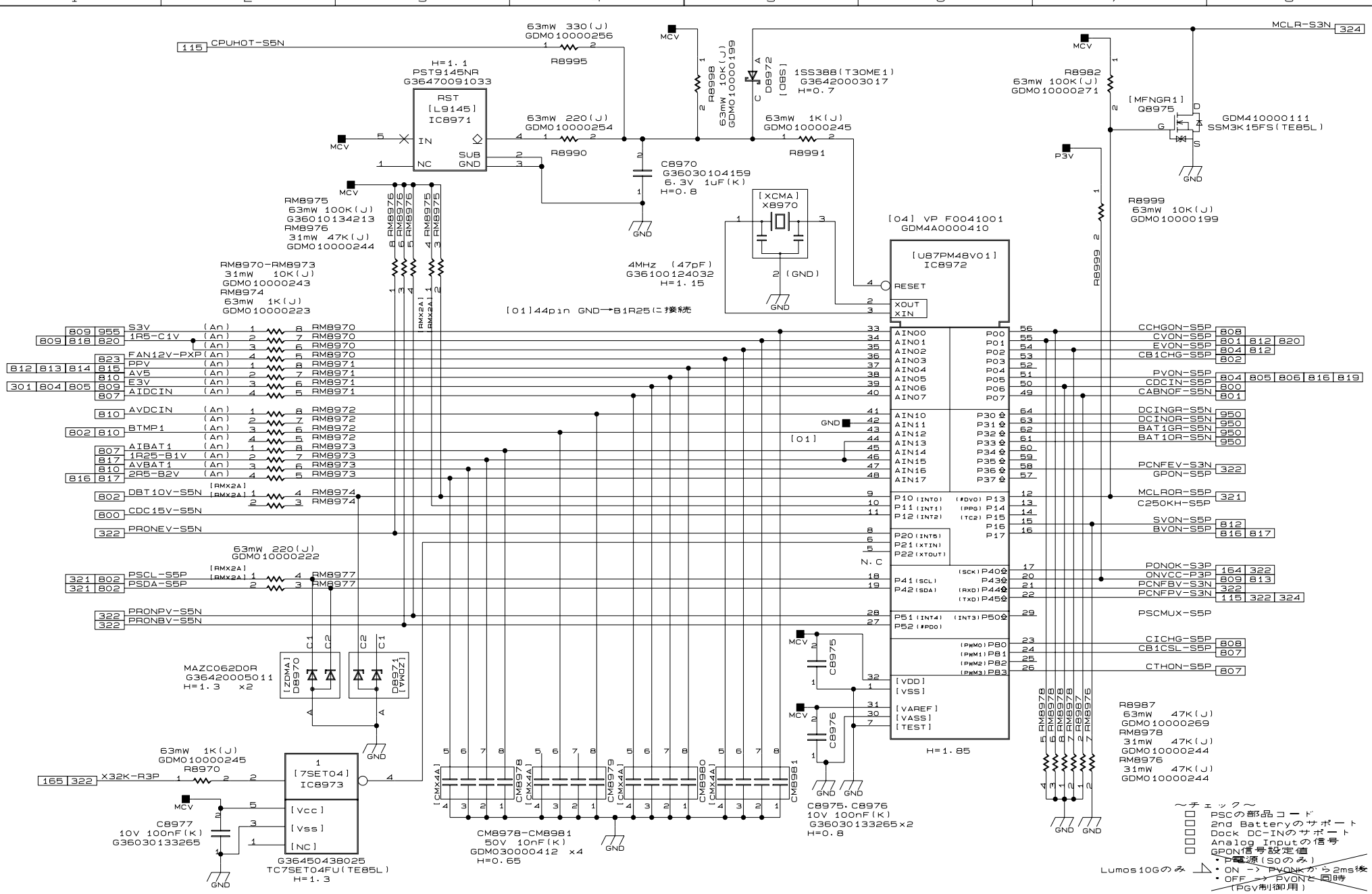
2003.11.17 14:10



～チェック～
- □ 5V Analog Input
- □ 1st Battery 温度
- □ 2nd Battery 温度
- □ 1st Battery 電圧
- □ 2nd Battery 電圧
- □ PC DC-IN 電圧
- □ Dock DC-IN 電圧
- □ Analog Input マルチプレクサ
- ○ すべてのPWM電源を把握
- ○ マルチプレクサ制御信号
- ○ 入力信号、出力信号

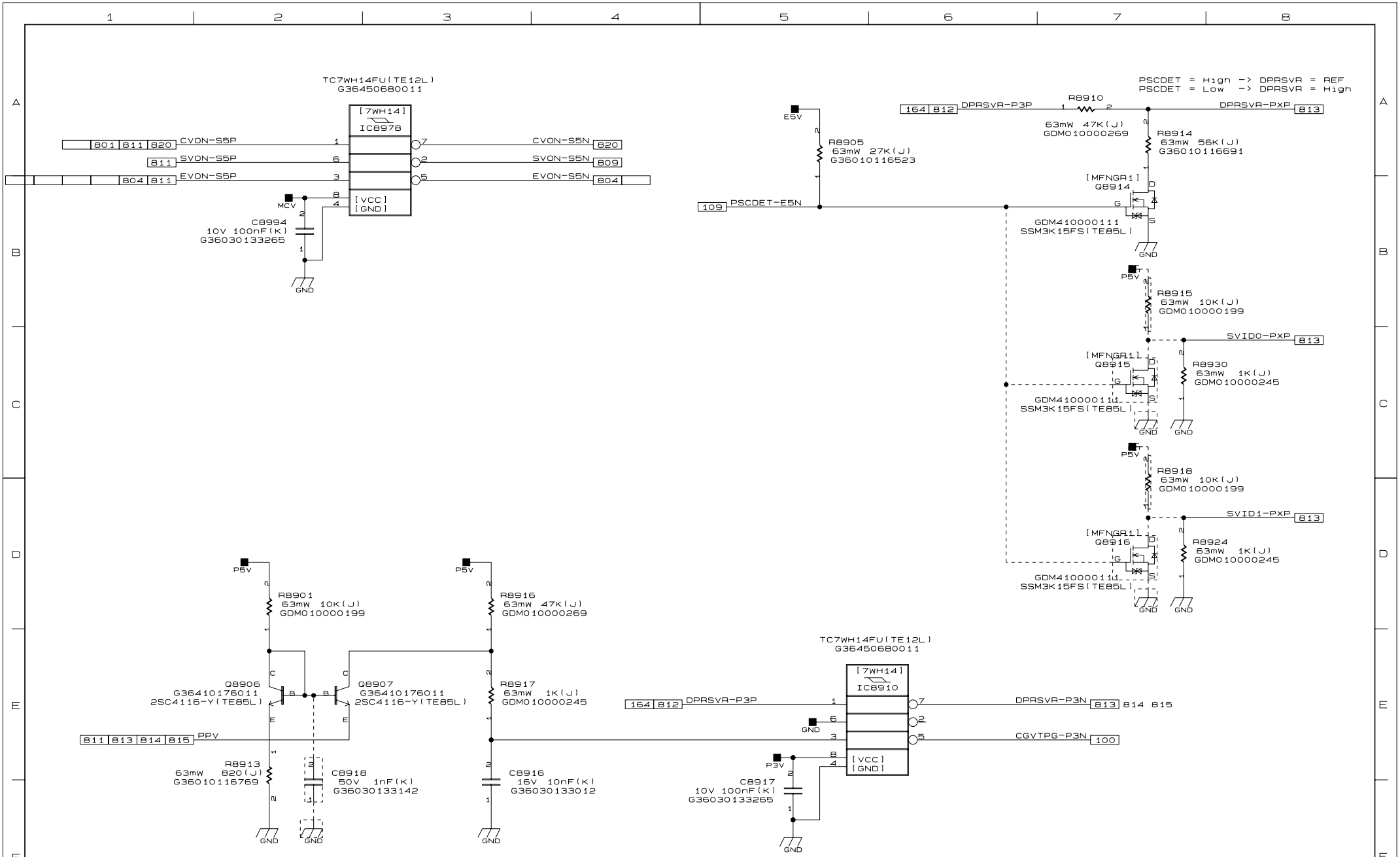
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
S. HACHIYA 2003/06/18	FLM1M2	[PS] Analog Input	810	97	00	360048117

2003.11.17 14:10



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/09/02 S. HACHIYA	FLM1M2	[PS] PSC(1)	811	98	04	360048117

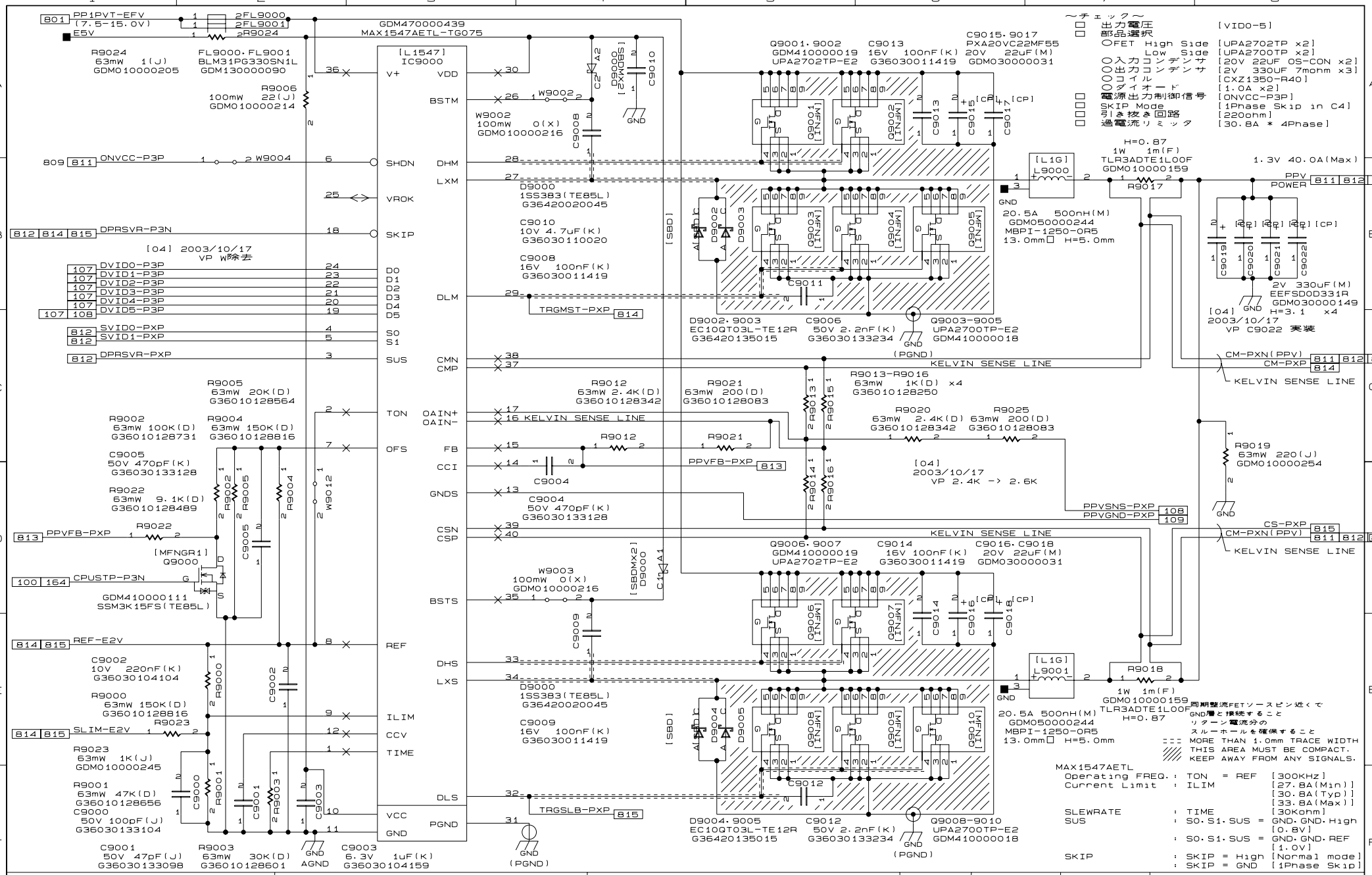
2003.11.17 14:10



DESIGNED BY	2003/06/18	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
S. HACHIYA		FLM1M2	[PS] PSC(2)	812	99	00	360048117

2003.11.17 14:10

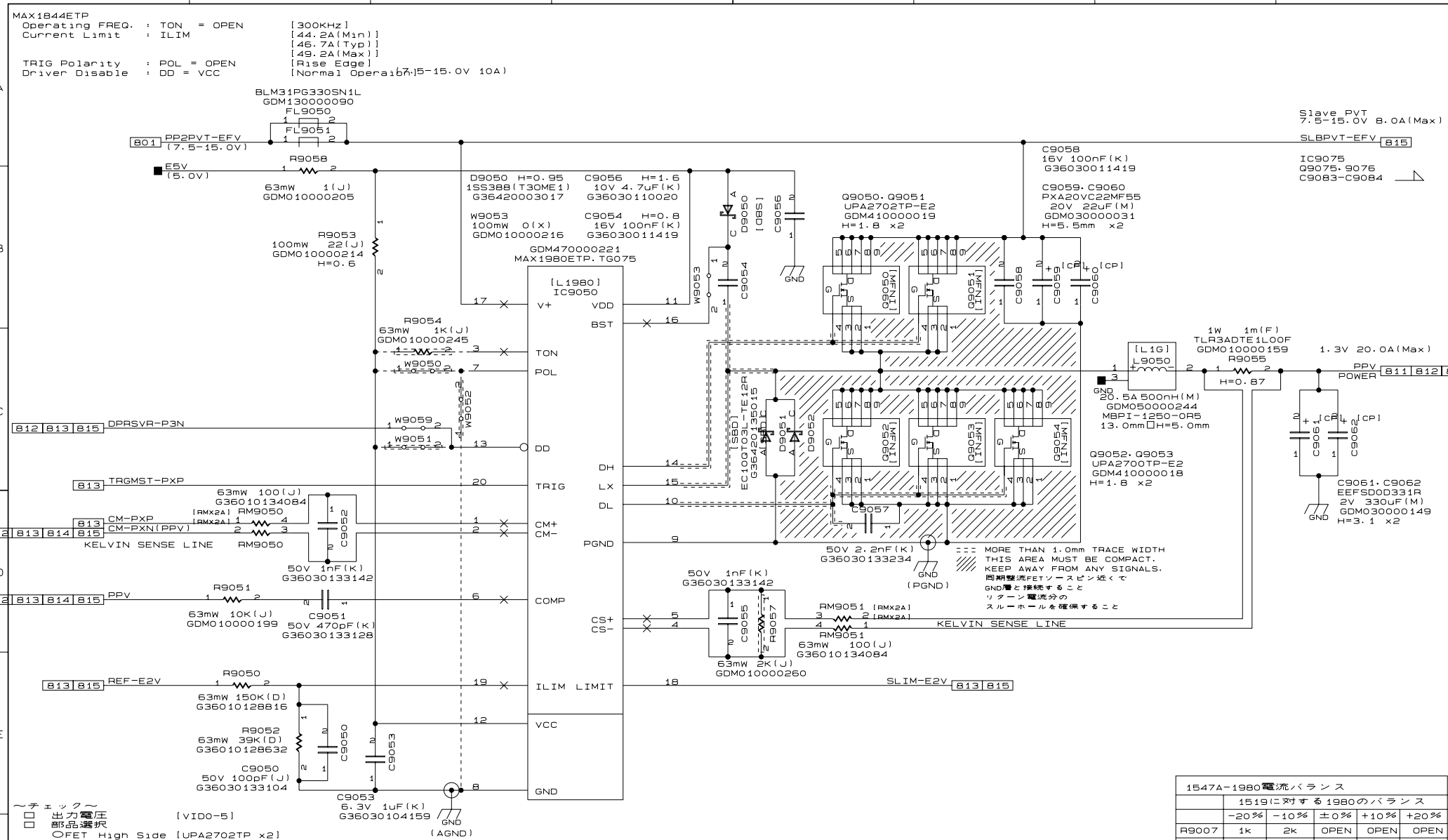
5T03655D



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/06/18 S. HACHIYA	FLM1M2	[PS] CPUVCC(1)	813	100	04	360048117

2003.11.17 14:10

TOSHIBA CORPORATION



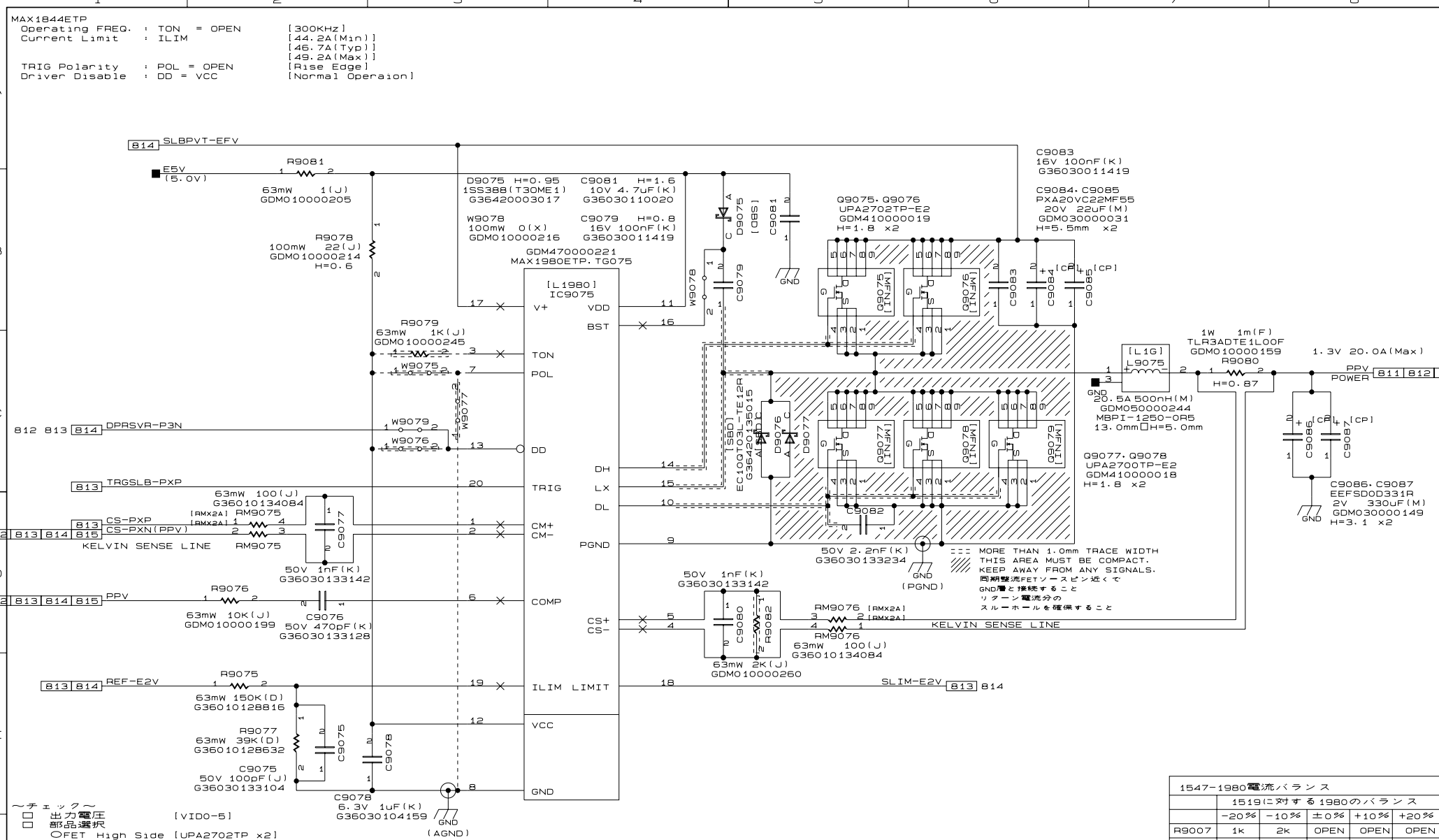
〜チェック〜
- □ 出力電圧 [VID0-5]
- □ 部品選択
  - ○FET High Side [UPA2702TP x2]
  - Low Side [UPA2700TP x2]
  - ○入力コンデンサ [20V 22UF OS-CON x2]
  - ○出力コンデンサ [2V 330UF 7mohm x2]
  - ○コイル [CXZ1350-R40]
  - ○ダイオード [1.0A x2]
- □ 電源出力制御信号 [PVON-S5P]
- □ SKIP Mode [N/A]
- □ 引き抜き回路 [N/A]
- □ 過電流リミッタ [46.7A / Phase]

1547A-1980電流バランス

1519に対する1980のバランス	−20%	−10%	±0%	+10%	+20%
R9007	1k	2k	OPEN	OPEN	OPEN
R9008	1k	2k	OPEN	OPEN	OPEN
R9057	OPEN	OPEN	OPEN	2K	1K
R9082	OPEN	OPEN	OPEN	2K	1K
R9027 R9028	200	390	620	820	1.1K

※10%で2°C、20%で4°C変化

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/06/18 S. HACHIYA	FLM1M2	[PS] CPUVCC(2)	814	101	00	360048117



〜チェック〜
- □ 出力電圧 [VID0-5]
- □ 部品選択
  - ○FET High Side [UPA2702TP x2]
  - Low Side [UPA2700TP x2]
  - ○入力コンデンサ [20V 22UF OS-CON x2]
  - ○出力コンデンサ [2V 330UF 7mohm x2]
  - ○コイル [CXZ1350-R40]
  - ○ダイオード [1.0A x2]
- □ 電源出力制御信号 [PVON-S5P]
- □ SKIP Mode [N/A]
- □ 引き抜き回路 [N/A]
- □ 過電流リミッタ [46.7A / Phase]

1547-1980電流バランス

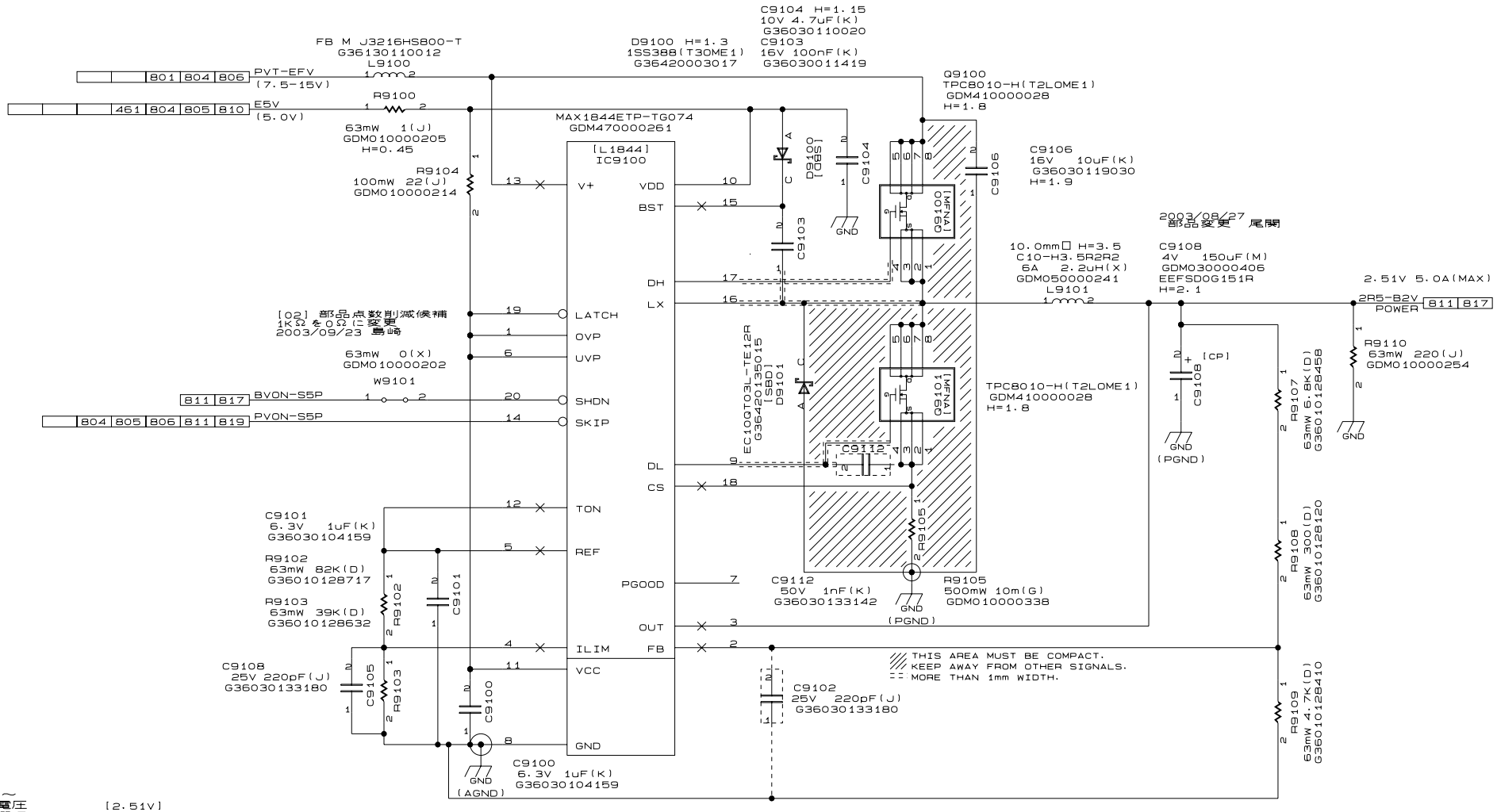
	1519に対する1980のバランス				
	−20%	−10%	±0%	+10%	+20%
R9007	1k	2k	OPEN	OPEN	OPEN
R9008	1k	2k	OPEN	OPEN	OPEN
R9057	OPEN	OPEN	OPEN	2K	1K
R9082	OPEN	OPEN	OPEN	2K	1K
R9027 R9028	200	390	620	820	1.1K

※10%で2℃、20%で4℃変化

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/06/18 S. HACHIYA	FLM1M2	[PS] CPUVCC(3)	815	102	00	360048117

2003. 11. 17 14:10

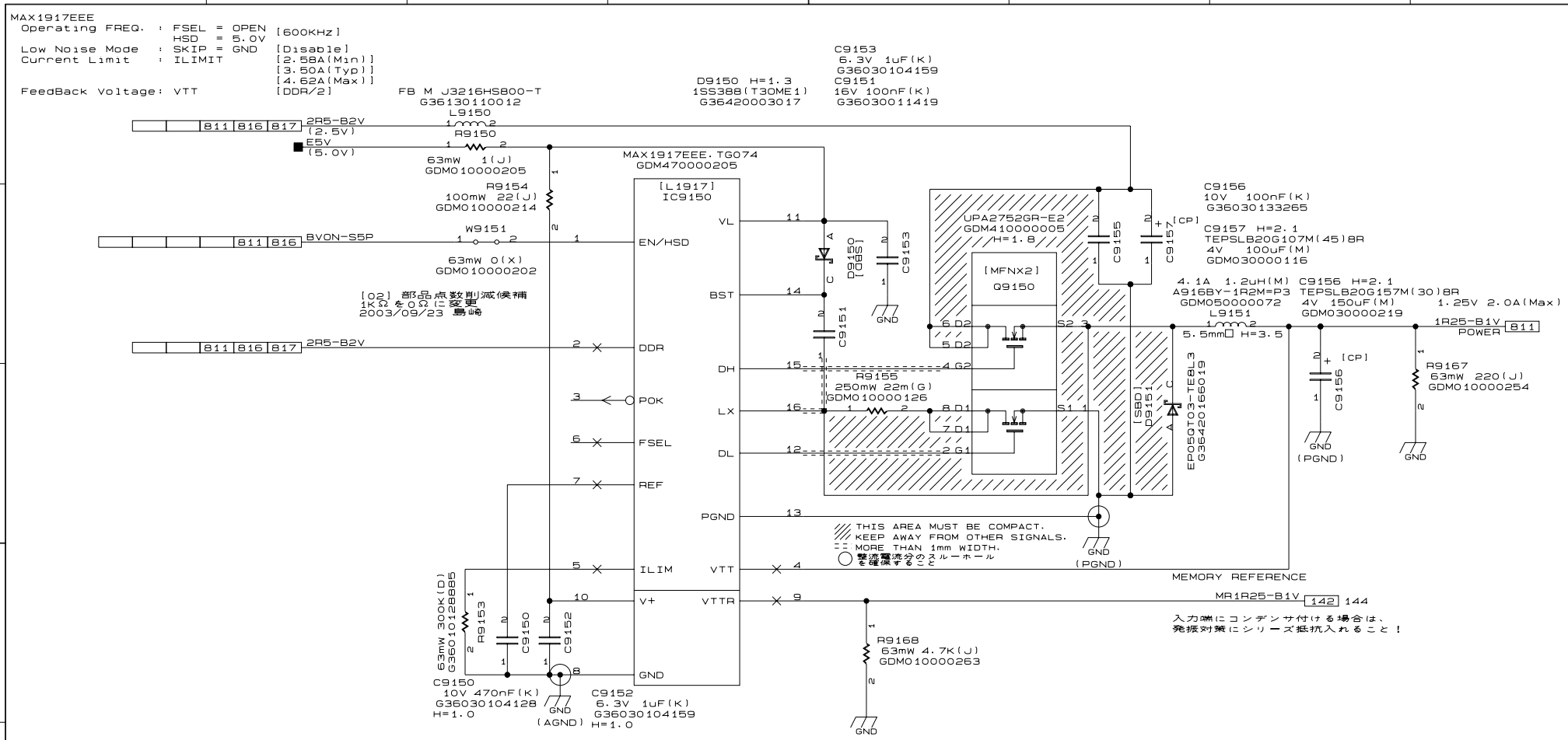
MAX1844ETP
Operating FREQ. : TON = REF [450KHz]
Low Noise Mode : SKIP = PVON [Enable]
Current Limit : ILIMIT [6.7A(Min)]
[7.7A(Typ)]
[8.7A(Max)]
FeedBack Voltage: 1.0 V(Typ.)
OV Protection : Disable
UV Protection : Enable
Latch : Disable



～チェック～
□ 出力電圧 [2.51V]
□ 部品選択
○FET High Side [TPC8010-H]
Low Side [TPC8010-H]
○コンデンサ [4.0V 150UF 7m]
○コイル [C10-H3.5R2R2]
○ダイオード [1.0A]
□ 電源出力制御信号 [BVON-S5P]
□ SKIP Mode [PVON-S5P]
□ 引き抜き回路 [220ohm]
□ 過電流リミッタ [7.7A]

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/06/06 S. HACHIYA	FLM1M2	[PS]2R5-B2V	816	103	02	360048117

2003.11.17 14:10

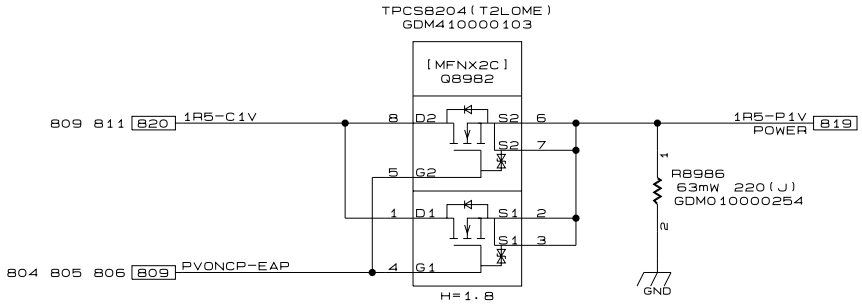


～チェック～

- □ 出力電圧 [1.25V]
- □ 部品選択
  - ○FET [UPA2752GR]
  - ○入力コンデンサ [4.0V 150UF 30m]
  - ○出力コンデンサ [4.0V 150UF 30m]
  - ○コイル [A916BY-1R2M=P3]
  - ○ダイオード [0.5A]
- □ 電源出力制御信号 [PVON-S5P]
- □ SKIP Mode [N/A]
- □ 引き抜き回路 [220ohm]
- □ 過電流リミッタ [3.5A]

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/06/18 S. HACHIYA	FLM1M2	[PS] 1R25-B1V	817	104	02	360048117

2003.11.17 14:10

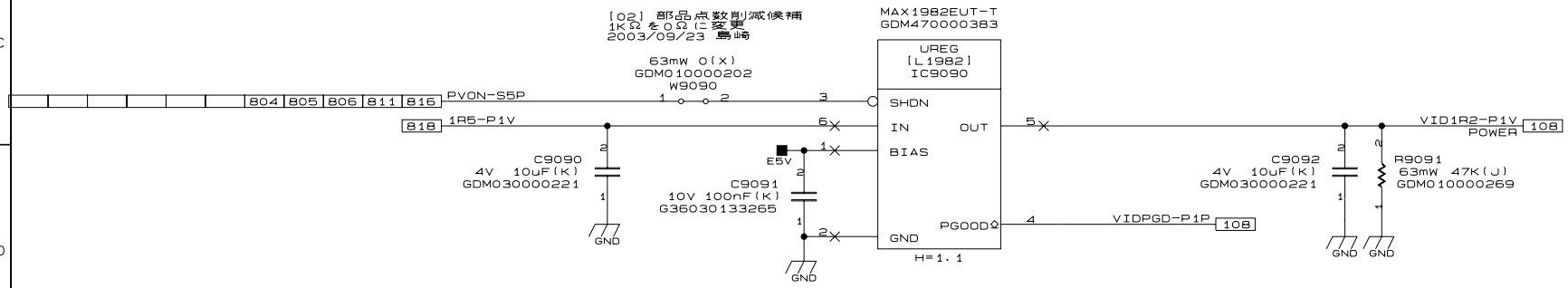


～チェック～
- □ 入力電源名
- □ 出力電源名
- □ 制御信号.駆動電圧
- □ FETの電圧降下/許容損失
- □ 引き抜き回路(100ms以内)

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/06/18 S. HACHIYA	FLM1M2	[PS] PW-SW	818	105	00	360048117

2003.11.17 14:10

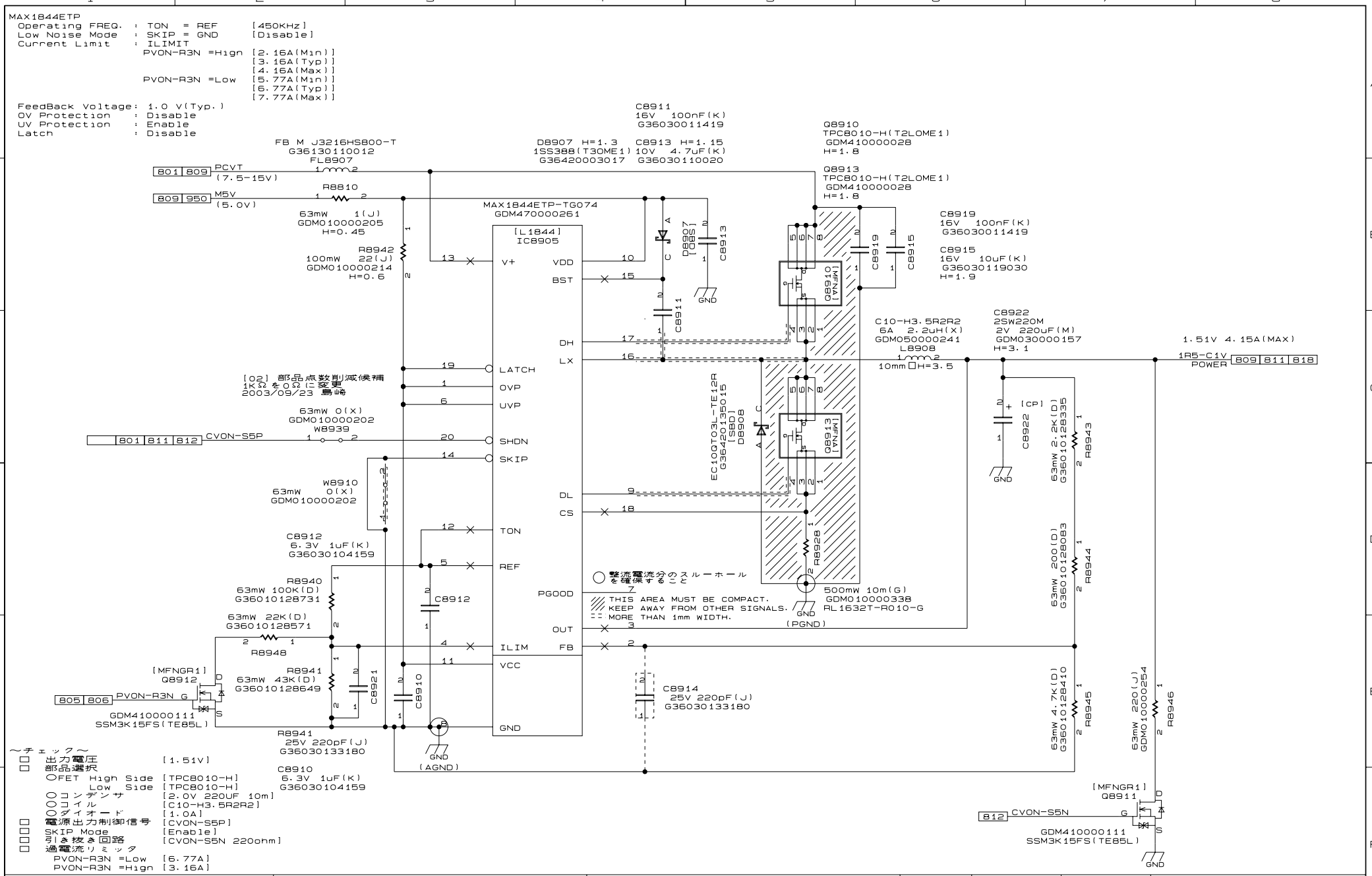
MAX1982EUT-T
Dropout Voltage [350mV @350mA]
[175mV @150mA]
Current Limit [330mA(Min)]
[600mA(Typ)]
[1400mA(Max)]



～チェック～
□ 出力電圧 [1.2V]
□ 部品選択
○入力コンデンサ [4.0V 10UF MLCC]
○出力コンデンサ [4.0V 10UF MLCC]
□ 電源出力制御信号 [PVON-S5P]
□ 引き抜き回路 [47Kohm]
□ 過電流リミッタ [600mA]

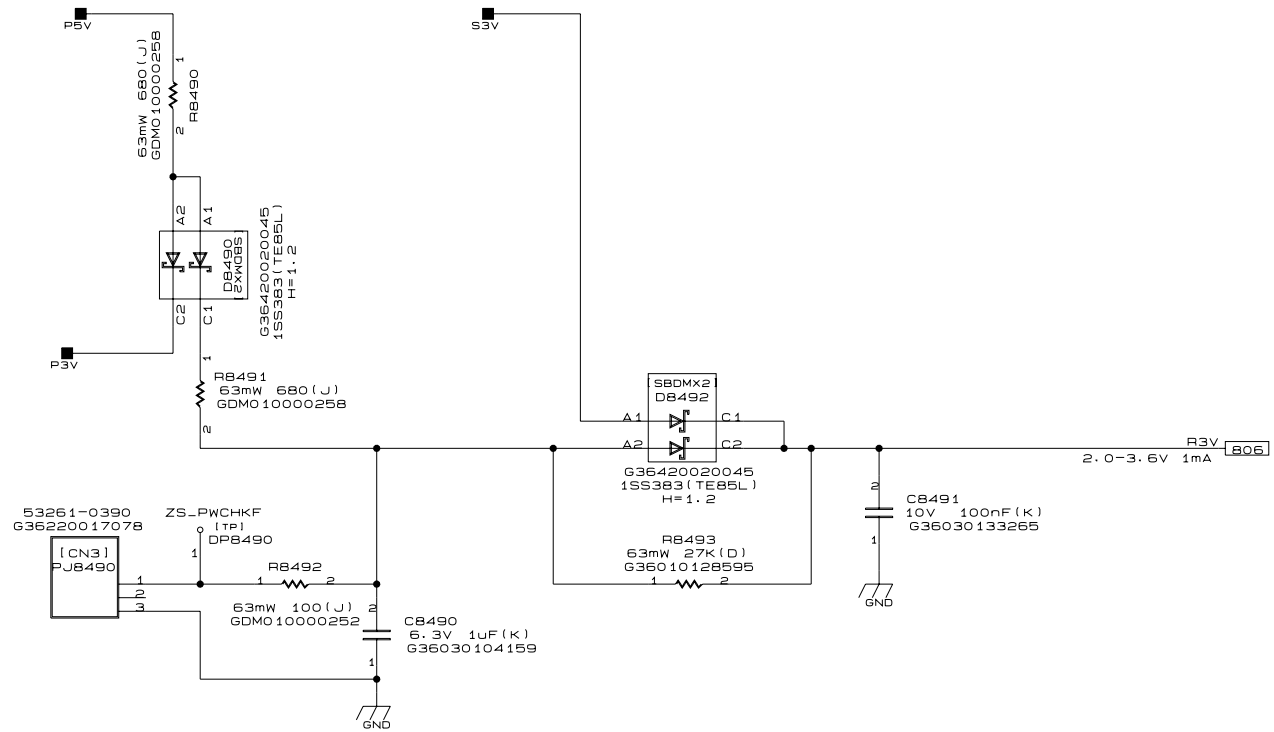
DESIGNED BY 2003/06/18	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
S. HACHIYA	FLM1M2	[PS] VID1R2-P1V	819	106	02	360048117

2003.11.17 14:10



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/06/18 S. HACHIYA	FLM1M2	[PS] 1R5-C1V	820	107	02	360048117

2003.11.17 14:10

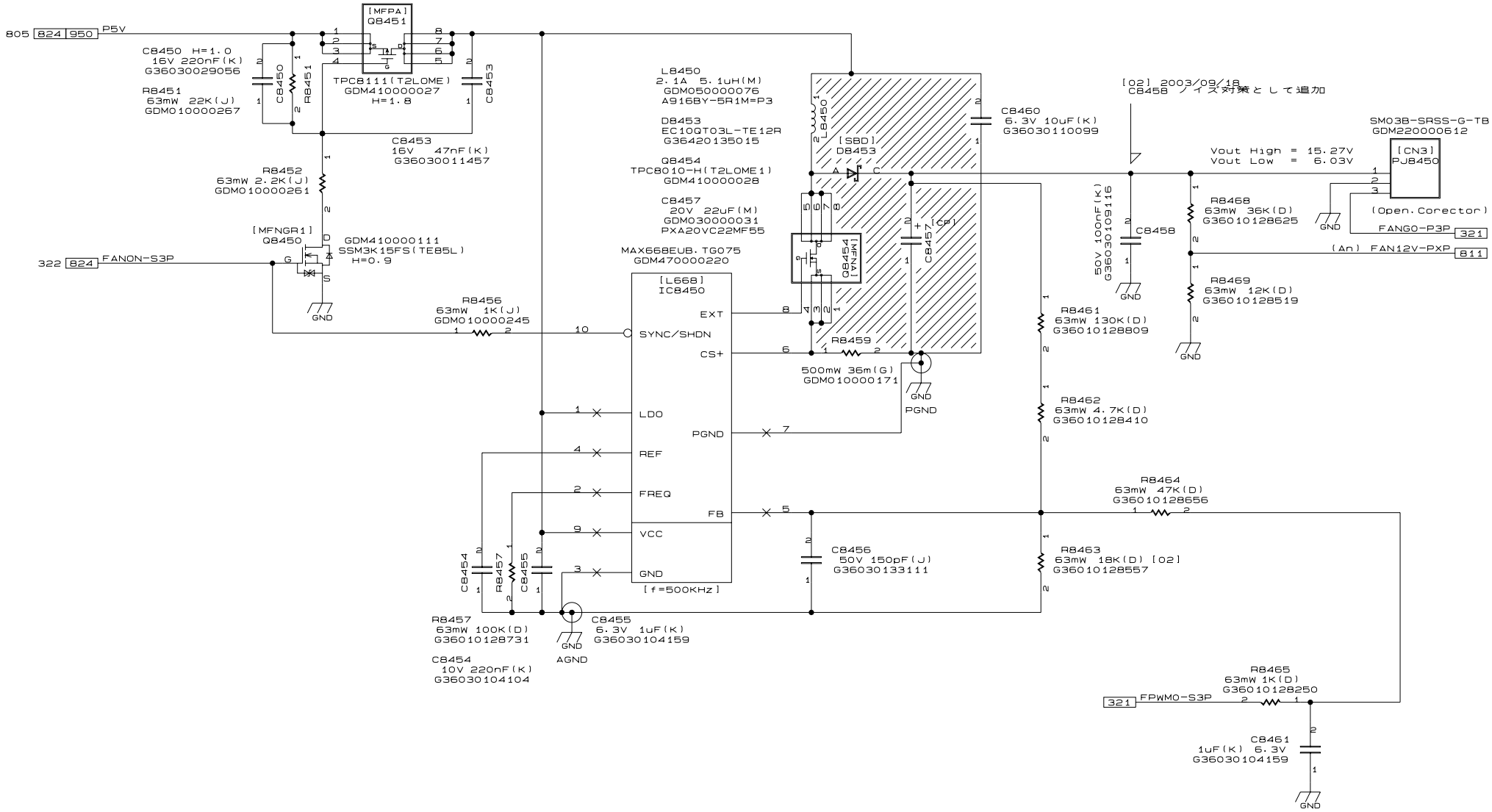


～チェック～
□ コネクタ
□ Battery仕様(H1-MH/L1-ON)

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/06/18 S. HACHIYA	FLM1M2	[PS] RTCVCC	822	108	00	360048117

2003.11.17 14:10

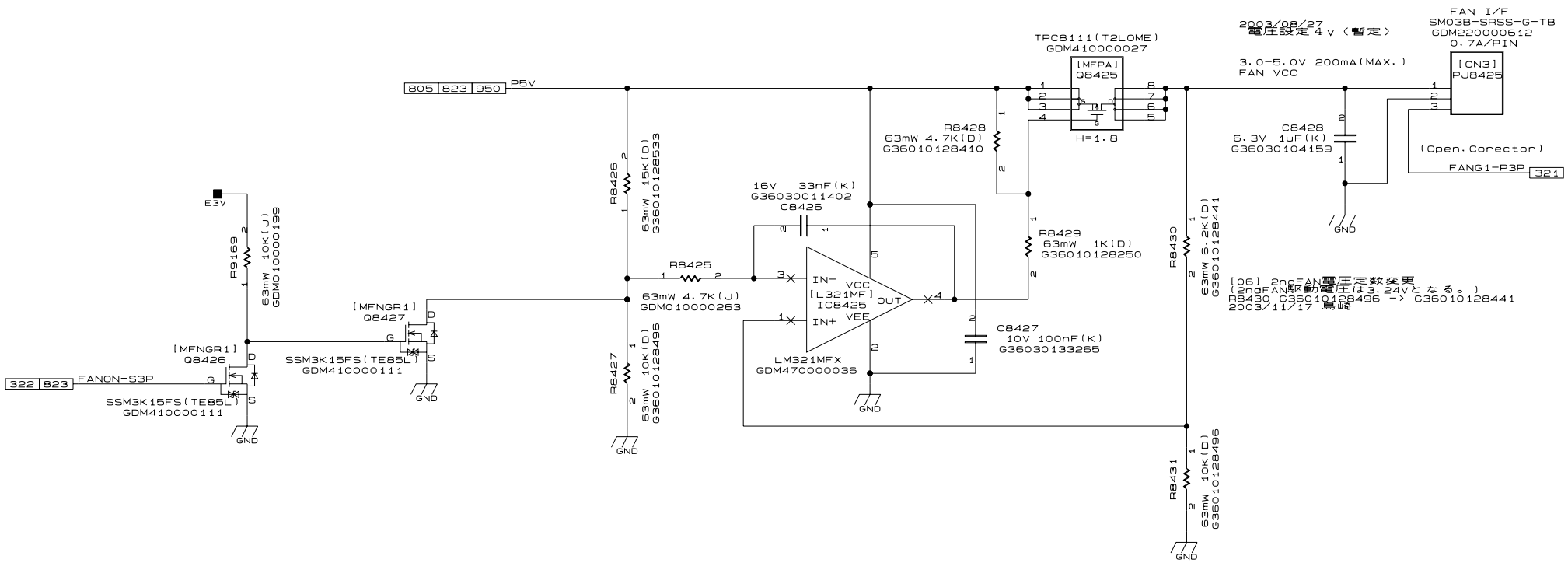
TOSHIBA CORPORATION



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2001/11/20 S. HACHIYA	FLM1M2	[PS] FANVCC	823	109	02	360048117

2003.11.17 14:10

TOSHIBA CORPORATION



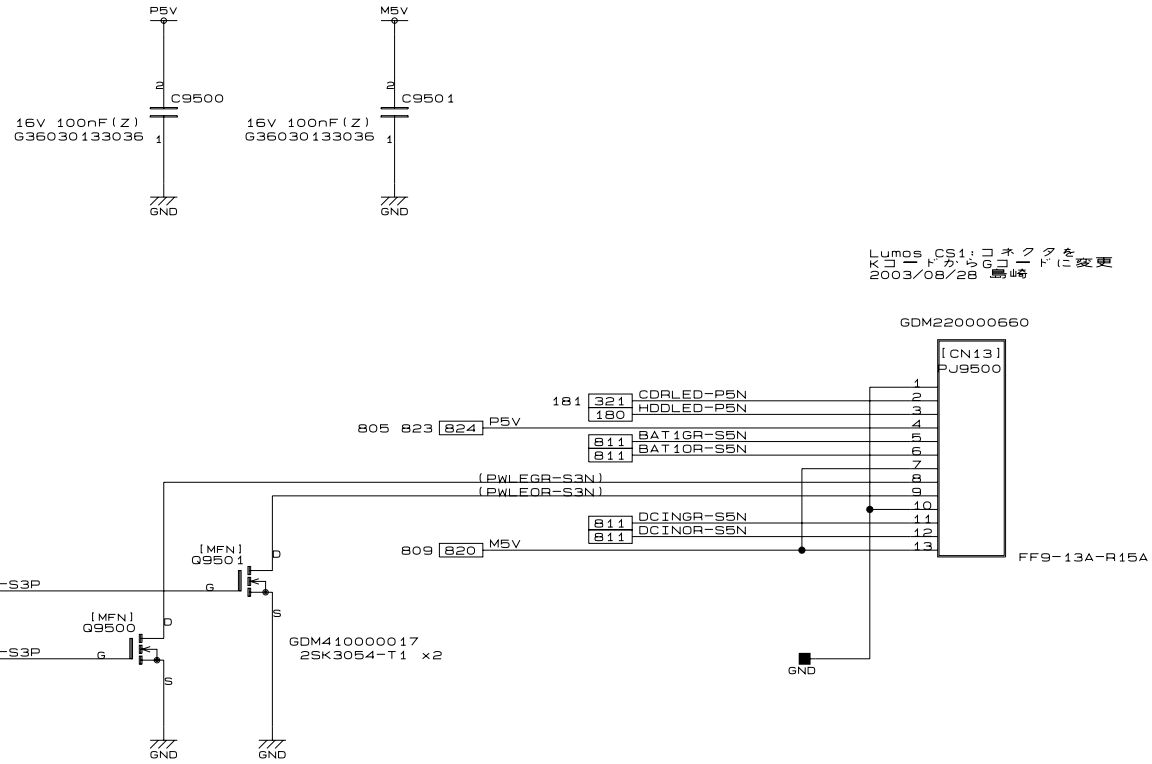
～チェック～

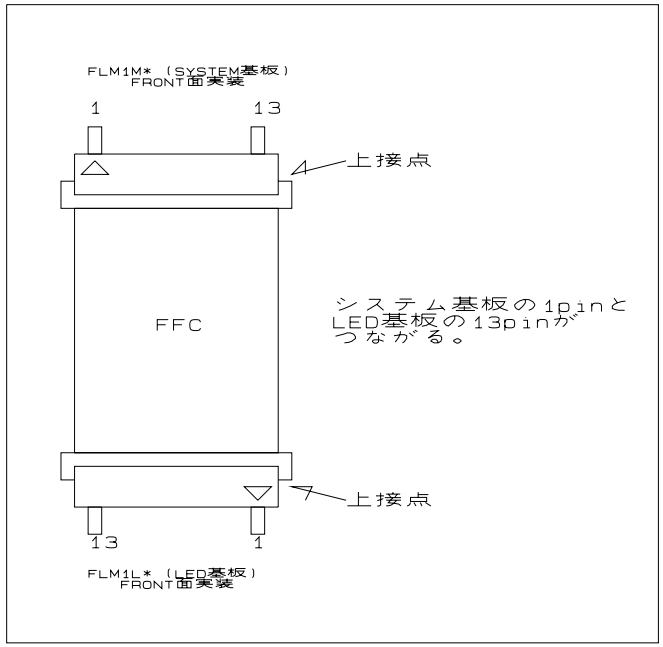
- □ FAN電圧 [5.0V]
- □ コネクタ
- □ PWM信号電圧

FPWM	3.3V	5.0V
R8771	6.8k(D)	10k(D)
R8772	22k(D)	10k(D)

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/06/18 S. HACHIYA	FLM1M2	[PS] FANVCC(1)	824	110	06	360048117

2003.11.17 14:10

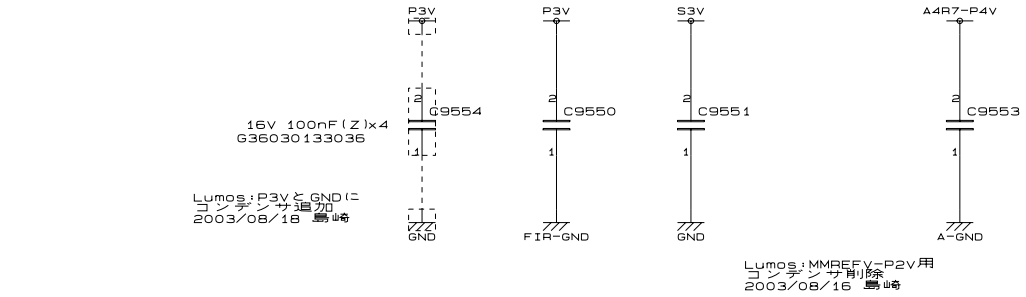


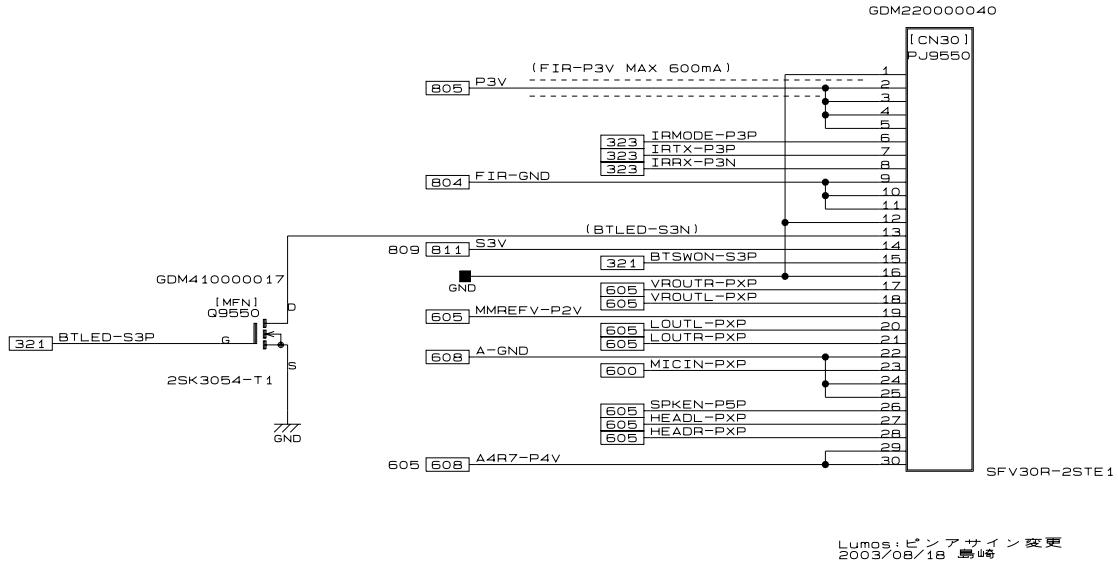


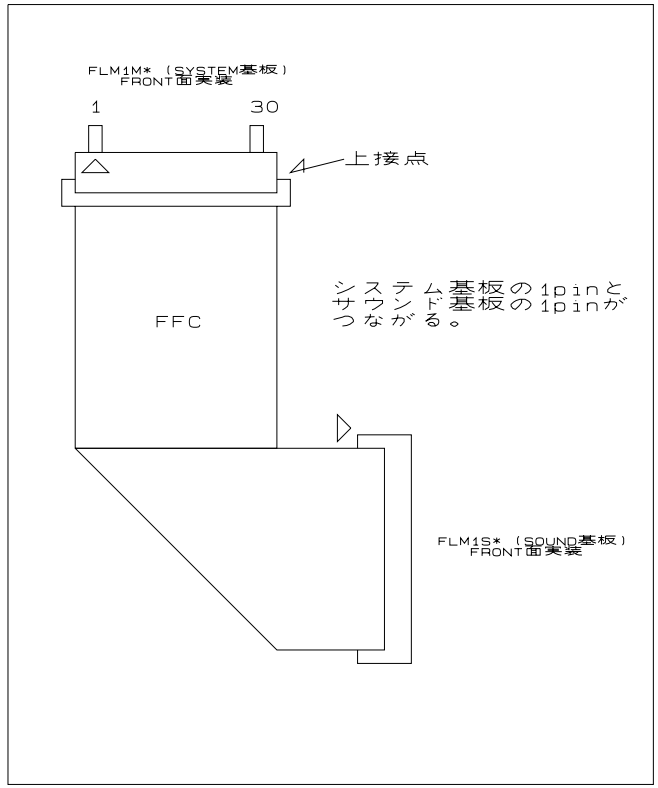
REF:Original

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/07/24 Y.Shimazaki	FLM1M2	FLM1L* I/F	950	111	00	360048117

2003.11.17 14:10



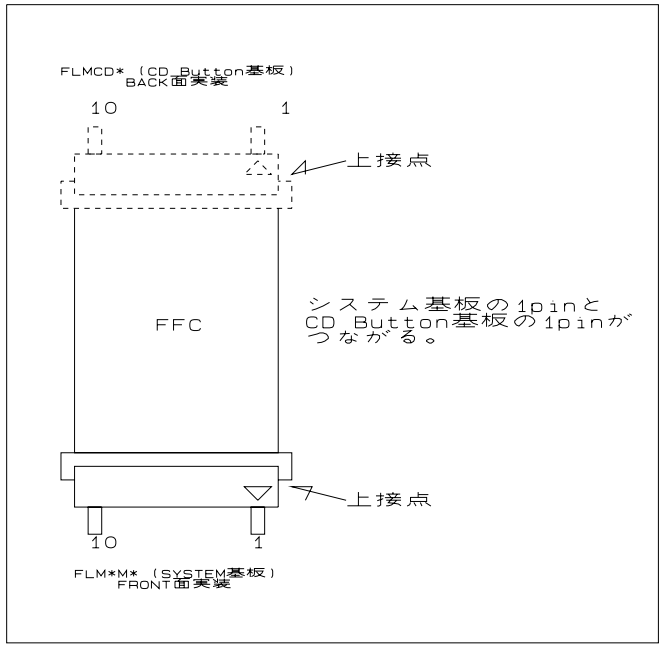




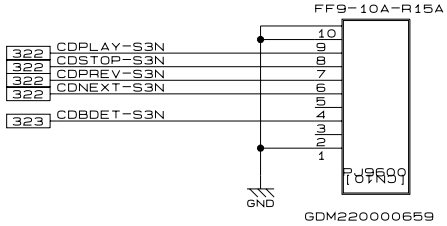
REF: Original

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/07/24 Y.Shimazaki	FLM1M2	FLM1S* I/F	955	112	00	360048117

2003.11.17 14:10



Lumos CS1:コネクタを
Kコードから Gコードに変更
2003/08/28 島崎



GDM220000659

Lumos:接続図に合わせて、コネクタピン
アサイン変更。
2003/08/16 島崎

REF:Original

DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
2003/07/28 Y.Shimazaki	FLM1M2	FLMCD* I/F	960	113	00	360048117

2003.11.17 14:10