

SCHEMATIC DIAGRAMS

PRODUCT NAME Henderson10/10C (VP)
(System 基板)

UNIT NAME FHNSY1

PWA NAME _____

PWB NAME _____

UNIT NO. DPL99060G01
A5A002688010

PWA NO. _____

PWB NO. _____

PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV
1	---	COVER SHEET	00	26	167	Ibex Peak-M (8)	00	51	231	DOCKING I/F 2	00	76	402	LAN EEPROM(8103EL)	00
2	---	COVER & REV. SHEET	00	27	168	Ibex Peak-M (9)	00	52	251	EXPRESSCARD	00	77	403	LAN I/F&LED(8111DL/8103E	00
3	---	BLOCK DIAGRAM	00	28	169	Ibex Peak-M (10)	00	53	252	EXPRESSCARD I/F	00	78	443	Bluetooth Power	00
4	---	PCB Connection	00	29	170	Ibex Peak-M (11)	00	54	259	WLAN／WiMAX Power	00	79	444	Bluetooth I/F	00
5	100	CK505 32pin	00	30	171	Ibex Peak-M (12)	00	55	260	PCI-E MINI CARD	00	80	460	USB POWER	00
6	105	rPGA989 (1)	00	31	172	Ibex Peak-M (13)	00	56	261	PCI-E MINI Card(3G)	00	81	462	USB I/F	00
7	106	rPGA989 (2)	00	32	173	Ibex Peak-M (14)	00	57	267	3G(WWAN) LED	00	82	465	USB CHARGER 3/4(1)	00
8	107	rPGA989 (3)	00	33	174	Ibex Peak-M (15)	00	58	300	FLASH ROM	00	83	466	USB CHARGER 3/4(2)	00
9	108	rPGA989 (4)	00	34	175	IMVP-6.5 POWER OK	00	59	301	MDC I/F	00	84	530	LCD PS	00
10	109	rPGA989 (5)	00	35	177	PCH ITP-XDP 24pin	00	60	310	PC Health PJ	00	85	539	LCD I/F	00
11	110	rPGA989 (6)	00	36	190	SATA HDD I/F	00	61	311	F/T PJ	00	86	540	CRT RGB	00
12	111	rPGA989 (7)	00	37	191	Dedicated SSD I/F	00	62	320	EC/KBC(1)	00	87	541	CRT DDC	00
13	112	rPGA989 (8)	00	38	195	SATA ODD I/F	00	63	321	EC/KBC(2)	00	88	542	CRT H/VSync	00
14	113	rPGA989 (9)	00	39	197	eSATA/USB I/F	00	64	322	EC/KBC(3)	00	89	550	DVI Level Shifter	00
15	115	THERMAL SENSOR	00	40	198	SATA REPEATER	00	65	323	GPIO Expander	00	90	551	DVI Jumper	00
16	117	Proc.ITP-XDP 24pin	00	41	200	PC CARD CONT(1)	00	66	324	KB I/F	00	91	560	Displayport Dongle	00
17	144	DDR3 SO-DIMM A	00	42	201	PC CARD CONT(2)	00	67	325	LED	00	92	569	Displayport I/F	00
18	146	DDR3 SO-DIMM B	00	43	202	PC CARD CONT(3)	00	68	326	WIRELESS SW,LED	00	93	603	ALC268	00
19	160	Ibex Peak-M (1)	00	44	210	PC-Card POWER	00	69	330	TPM CONT	00	94	607	Int-MIC	00
20	161	Ibex Peak-M (2)	00	45	211	PC-CARD I/F	00	70	335	ACCELEROMETER (FRONT)	00	95	615	AN12947AA(1)	00
21	162	Ibex Peak-M (3)	00	46	214	MEDIA BRIDGE I/F	00	71	336	AMP for AXIS	00	96	616	AN12947AA(2)	00
22	163	Ibex Peak-M (4)	00	47	216	Smart Card Reader	00	72	338	FAN I/F	00	97	645	ANALOG POWER	00
23	164	Ibex Peak-M (5)	00	48	217	Smart Card I/F	00	73	340	SUPER I/O	00	98	648	Pass-con	00
24	165	Ibex Peak-M (6)	00	49	220	CARD CONT	00	74	400	Ethernet PHY(Hanksville)	00	99	700	Reduce S3 Power	00
25	166	Ibex Peak-M (7)	00	50	230	DOCKING I/F 1	00	75	401	LAN CONTROLLER(8103EL)	00	100	750	EMI CAP.	00

APPROVED BY	CHECKED BY	DESIGNED BY	TITLE	TOTAL	PAGE NO.	REV.MARK	DRAWING.NO.
[PC設](PC設7) K.Yamamori	[PC設](PC設7) T.Miyairi	[PC設](PC設7) T.Ichimura	FHNSY1	131	001	00	360069769

SCHEMATIC DIAGRAMS

PRODUCT NAME

UNIT NAME

UNIT NO.

PWA NAME

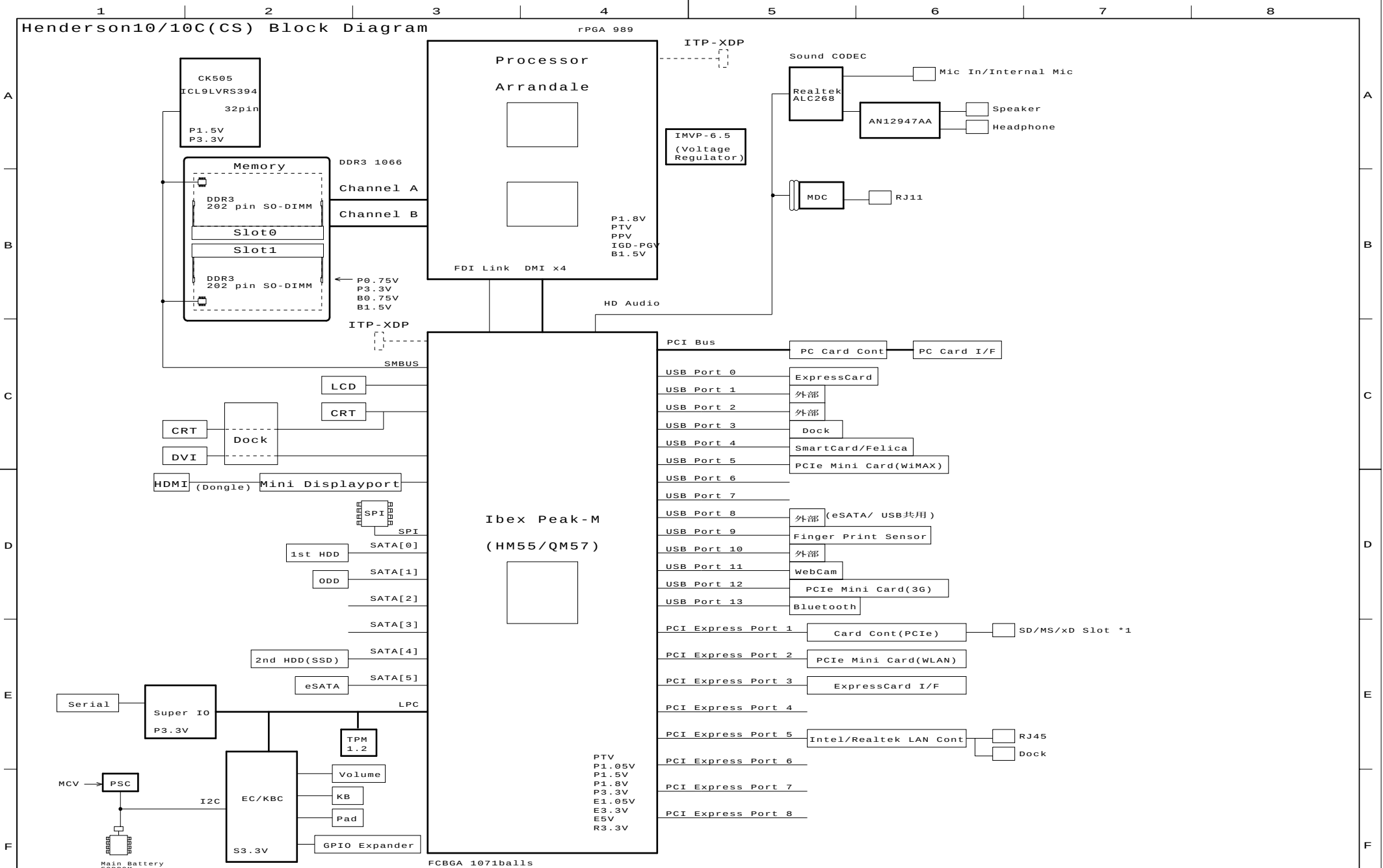
PWA NO.

PWB NAME

PWB NO.

PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV
101	800	[PS]DC-IN	00	126	953	FHNJK* I/F	00
102	801	[PS]PVT-SW	00	127	954	WebCam I/F	00
103	802	[PS]1st Battery	00	128	955	FHNFS* I/F	00
104	804	[PS]E5V/E3V	00	129	957	FHNSM* I/F	00
105	805	[PS] B*V/P*V	00	130	970	FELICA I/F	00
106	806	[PS]E10V	00	131	990	Accessories	00
107	807	[PS]CURRENT AMP	00	132			
108	808	[PS] CHARGE	00	133			
109	809	[PS]S5V/S3V	00	134			
110	810	[PS]AnalogInput	00	135			
111	811	[PS]PSC(1)	00	136			
112	812	[PS]PSC(2)	00	137			
113	813	[PS]CPUVCC	00	138			
114	815	[PS]1R8-P1V	00	139			
115	818	[PS]LOAD SW2	00	140			
116	819	[PS]PTV	00	141			
117	820	[PS]1R5-B1V,0R75-POV	00	142			
118	821	[PS]1R05-E1V	00	143			
119	822	[PS]RTC VCC	00	144			
120	832	[PS]IGD-PGV	00	145			
121	841	[PS]LAN-PS	00	146			
122	901	EC TEST PAD	00	147			
123	950	FHNSW* I/F	00	148			
124	951	RGB Unit I/F	00	149			
125	952	Serial Unit I/F	00	150			

REV. MARK	CONTENTS	APPROVED BY	REVISED BY	REGISTERED
00	ISSUE(Release for VP)	2009/10/15 [PC設] (PC設7) K.Yamamori	2009/10/15 [PC設] (PC設7) T.Ichimura	



DESIGNED BY
S. Fujiwara

TITLE
FHNSY1

FUNCTION
BLOCK DIAGRAM

SH.NO.
003

PAGE NO.
003

REV.MARK
00

DRAWING.NO.
360069769

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TOSHIBA CORPORATION

FPC
FHNSS* : 2nd SSD FPC

FHNSW*
(Switch)

FFC

FHNSM*
(SIM)

FFC

CN9500

CN9570

FHNSR*
(Serial Port)

FHNJK*

FHNSS*
(2nd SSD FPC)

FHNSY*
FHVSY*
(System)

FHNFS*
(Finger&Pad Button)

DESIGNED BY
T. Ichimura

TITLE	FHNSY1
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FUNCTION
PCB Connection

SH. NO.	004
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PAGE NO. 004

REV. MARK	00
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DRAWING . NO .
360069769

A

B

C

D

E

F

A

B

C

D

E

F

				GDM250000249		Henderson-CS		Henderson-CS	
						IS1050變更 → K2500331		IS1050變更 → GDM250000249	
						■ 2009/06/15		■ 2009/06/29	
Arrandale/Clarksfield									
ARRANDALE 1/10									
[SOCKET989A]									
IS1050									
144	ADQ00-B1P	A10	↔	SA_DQ[0]	SA_CK[0]	↔	AA6	XACK0-B1P	
144	ADQ01-B1P	C10	↔	SA_DQ[1]	SA_CK#[0]	↔	AA7	XACK0-B1N	144
144	ADQ02-B1P	C7	↔	SA_DQ[2]	SA_CK[1]	↔	Y6	XACK1-B1P	144
144	ADQ03-B1P	A7	↔	SA_DQ[3]	SA_CK#[1]	↔	Y5	XACK1-B1N	144
144	ADQ04-B1P	B10	↔	SA_DQ[4]					144
144	ADQ05-B1P	D10	↔	SA_DQ[5]					
144	ADQ06-B1P	E10	↔	SA_DQ[6]	SA_CKE[0]	↔	P7	ACKE0-B1P	
144	ADQ07-B1P	A8	↔	SA_DQ[7]	SA_CKE[1]	↔	P6	ACKE1-B1P	144
144	ADQ08-B1P	D8	↔	SA_DQ[8]	SA_ODT[0]	↔	AD8	AODT0-B1P	144
144	ADQ09-B1P	F10	↔	SA_DQ[9]	SA_ODT[1]	↔	AF9	AODT1-B1P	144
144	ADQ10-B1P	E6	↔	SA_DQ[10]					
144	ADQ11-B1P	F7	↔	SA_DQ[11]	SA_CS#[0]	↔	AE2	ACS0-B1N	144
144	ADQ12-B1P	E9	↔	SA_DQ[12]	SA_CS#[1]	↔	AE8	ACS1-B1N	144
144	ADQ13-B1P	B7	↔	SA_DQ[13]					
144	ADQ14-B1P	E7	↔	SA_DQ[14]	SA_RAS#	↔	AB3	ARAS-B1N	144
144	ADQ15-B1P	C6	↔	SA_DQ[15]	SA_CAS#	↔	AE1	ACAS-B1N	144
144	ADQ16-B1P	H10	↔	SA_DQ[16]	SA_WE#	↔	AE9	AWE-B1N	144
144	ADQ17-B1P	G8	↔	SA_DQ[17]					
144	ADQ18-B1P	K7	↔	SA_DQ[18]	SA_MA[0]	↔	Y3	AMA00-B1P	144
144	ADQ19-B1P	J8	↔	SA_DQ[19]	SA_MA[1]	↔	W1	AMA01-B1P	144
144	ADQ20-B1P	G7	↔	SA_DQ[20]	SA_MA[2]	↔	AA8	AMA02-B1P	144
144	ADQ21-B1P	G10	↔	SA_DQ[21]	SA_MA[3]	↔	AA3	AMA03-B1P	144
144	ADQ22-B1P	J7	↔	SA_DQ[22]	SA_MA[4]	↔	V1	AMA04-B1P	144
144	ADQ23-B1P	J10	↔	SA_DQ[23]	SA_MA[5]	↔	AA9	AMA05-B1P	144
144	ADQ24-B1P	L7	↔	SA_DQ[24]	SA_MA[6]	↔	V8	AMA06-B1P	144
144	ADQ25-B1P	M6	↔	SA_DQ[25]	SA_MA[7]	↔	T1	AMA07-B1P	144
144	ADQ26-B1P	M8	↔	SA_DQ[26]	SA_MA[8]	↔	Y9	AMA08-B1P	144
144	ADQ27-B1P	L9	↔	SA_DQ[27]	SA_MA[9]	↔	U6	AMA09-B1P	144
144	ADQ28-B1P	L6	↔	SA_DQ[28]	SA_MA[10]	↔	AD4	AMA10-B1P	144
144	ADQ29-B1P	K8	↔	SA_DQ[29]	SA_MA[11]	↔	T2	AMA11-B1P	144
144	ADQ30-B1P	N8	↔	SA_DQ[30]	SA_MA[12]	↔	U3	AMA12-B1P	144
144	ADQ31-B1P	P9	↔	SA_DQ[31]	SA_MA[13]	↔	AG8	AMA13-B1P	144
144	ADQ32-B1P	AH5	↔	SA_DQ[32]	SA_MA[14]	↔	T3	AMA14-B1P	144
144	ADQ33-B1P	AF5	↔	SA_DQ[33]	SA_MA[15]	↔	V9	AMA15-B1P	144
144	ADQ34-B1P	AK6	↔	SA_DQ[34]					
144	ADQ35-B1P	AK7	↔	SA_DQ[35]	SA_BS[0]	↔	AC3	ABS0-B1P	144
144	ADQ36-B1P	AF6	↔	SA_DQ[36]	SA_BS[1]	↔	AB2	ABS1-B1P	144
144	ADQ37-B1P	AG5	↔	SA_DQ[37]	SA_BS[2]	↔	U7	ABS2-B1P	144
144	ADQ38-B1P	AJ7	↔	SA_DQ[38]					
144	ADQ39-B1P	AJ6	↔	SA_DQ[39]	SA_DM[0]	↔	B9	ADM0-B1P	144
144	ADQ40-B1P	AJ10	↔	SA_DQ[40]	SA_DM[1]	↔	D7	ADM1-B1P	144
144	ADQ41-B1P	AJ9	↔	SA_DQ[41]	SA_DM[2]	↔	H7	ADM2-B1P	144
144	ADQ42-B1P	AL10	↔	SA_DQ[42]	SA_DM[3]	↔	M7	ADM3-B1P	144
144	ADQ43-B1P	AK12	↔	SA_DQ[43]	SA_DM[4]	↔	AG6	ADM4-B1P	144
144	ADQ44-B1P	AK8	↔	SA_DQ[44]	SA_DM[5]	↔	AM7	ADM5-B1P	144
144	ADQ45-B1P	AL7	↔	SA_DQ[45]	SA_DM[6]	↔	AN10	ADM6-B1P	144
144	ADQ46-B1P	AK11	↔	SA_DQ[46]	SA_DM[7]	↔	AN13	ADM7-B1P	144
144	ADQ47-B1P	AL8	↔	SA_DQ[47]					
144	ADQ48-B1P	AN8	↔	SA_DQ[48]	SA_DQS[0]	↔	C8	ADQS0-B1P	144
144	ADQ49-B1P	AM10	↔	SA_DQ[49]	SA_DQS#[0]	↔	C9	ADQS0-B1N	144
144	ADQ50-B1P	AR11	↔	SA_DQ[50]	SA_DQS[1]	↔	F9	ADQS1-B1P	144
144	ADQ51-B1P	AL11	↔	SA_DQ[51]	SA_DQS#[1]	↔	F8	ADQS1-B1N	144
144	ADQ52-B1P	AM9	↔	SA_DQ[52]	SA_DQS[2]	↔	H9	ADQS2-B1P	144
144	ADQ53-B1P	AN9	↔	SA_DQ[53]	SA_DQS#[2]	↔	J9	ADQS2-B1N	144
144	ADQ54-B1P	AT11	↔	SA_DQ[54]	SA_DQS[3]	↔	M9	ADQS3-B1P	144
144	ADQ55-B1P	AP12	↔	SA_DQ[55]	SA_DQS#[3]	↔	N9	ADQS3-B1N	144
144	ADQ56-B1P	AM12	↔	SA_DQ[56]	SA_DQS[4]	↔	AH8	ADQS4-B1P	144
144	ADQ57-B1P	AN12	↔	SA_DQ[57]	SA_DQS#[4]	↔	AH7	ADQS4-B1N	144
144	ADQ58-B1P	AM13	↔	SA_DQ[58]	SA_DQS[5]	↔	AK10	ADQS5-B1P	144
144	ADQ59-B1P	AT14	↔	SA_DQ[59]	SA_DQS#[5]	↔	AK9	ADQS5-B1N	144
144	ADQ60-B1P	AT12	↔	SA_DQ[60]	SA_DQS[6]	↔	AN11	ADQS6-B1P	144
144	ADQ61-B1P	AL13	↔	SA_DQ[61]	SA_DQS#[6]	↔	AP11	ADQS6-B1N	144
144	ADQ62-B1P	AR14	↔	SA_DQ[62]	SA_DQS[7]	↔	AR13	ADQS7-B1P	144
144	ADQ63-B1P	AP14	↔	SA_DQ[63]	SA_DQS#[7]	↔	AT13	ADQS7-B1N	144

[SSTL1.5]

[DDR3 INTERFACE A]

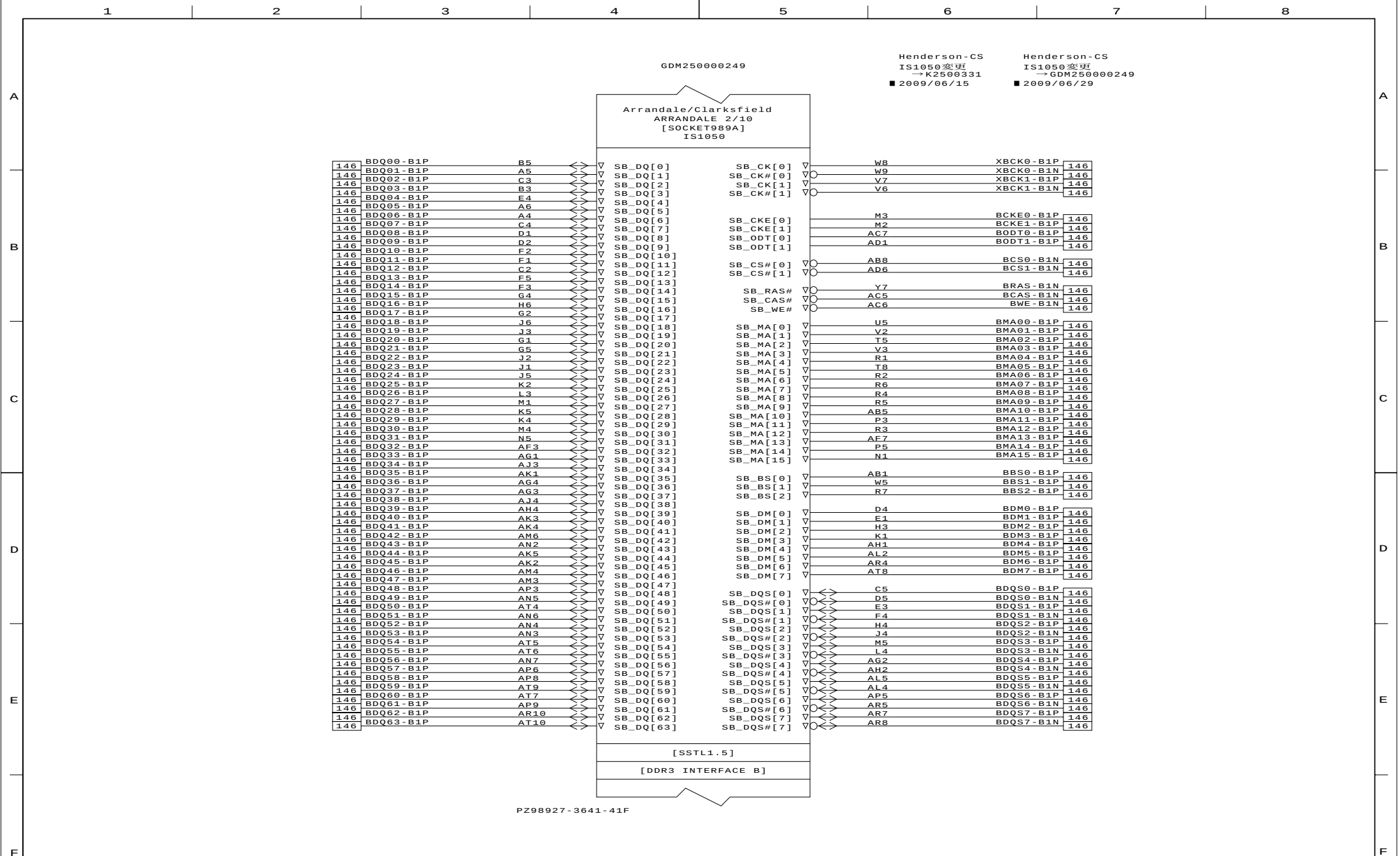
PZ98927-3641-41F

■ REF: Calpella Platform Base. FCLBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	rPGA989 (1)	105	006	00	360069769

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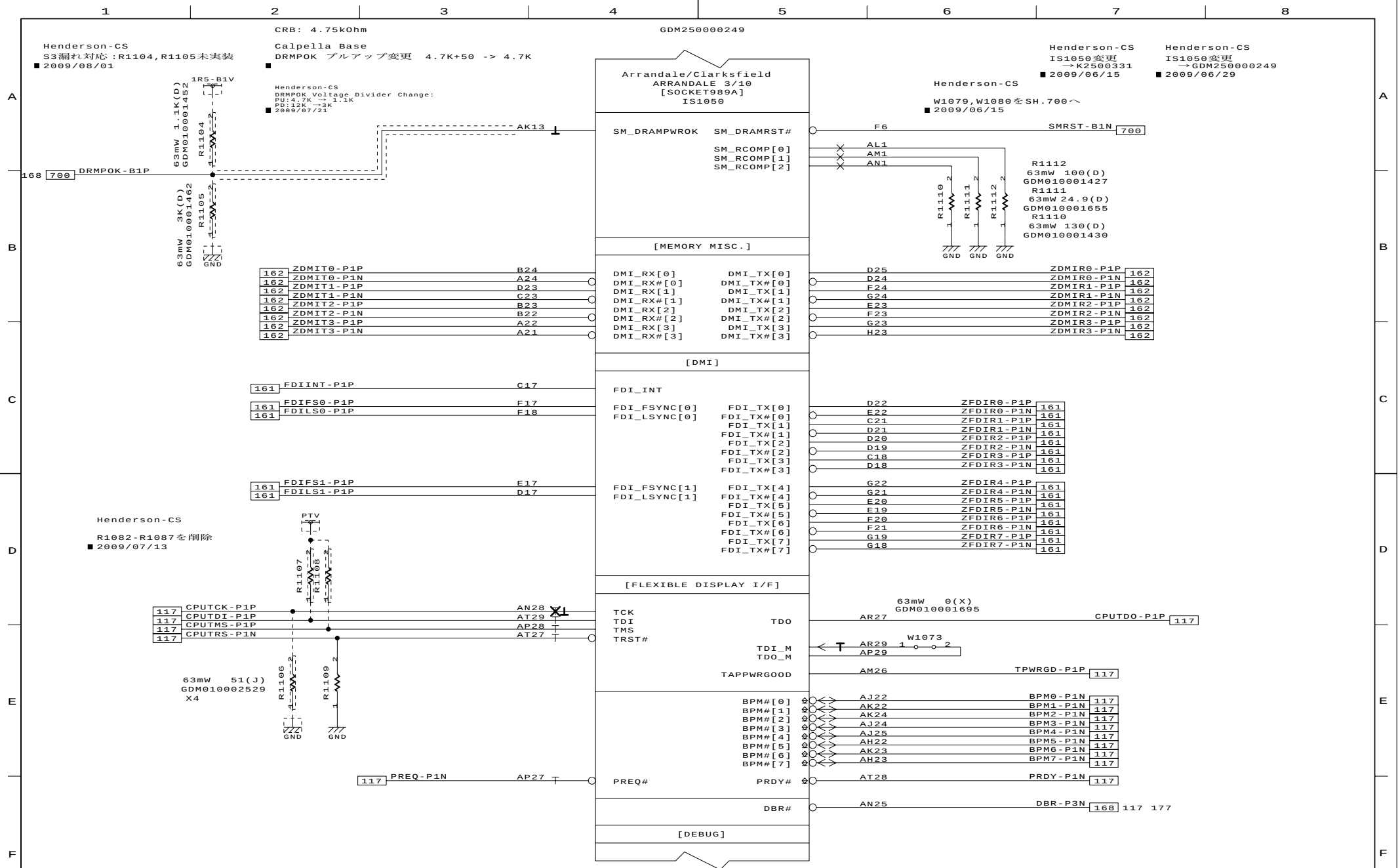
TOSHIBA CORPORATION



REF: Calpella Platform Base. FCLBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura	FHNSY1	rPGA989 (2)	106	007	00	360069769
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REF: Calpella Platform Base. FCLBS0

PZ98927-3641-41F

DESIGNED BY

T.Naruse/T.Ichimura

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TITLE

FHNSY1

FUNCTION

rPGA989 (3)

SH.NO.

107

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008

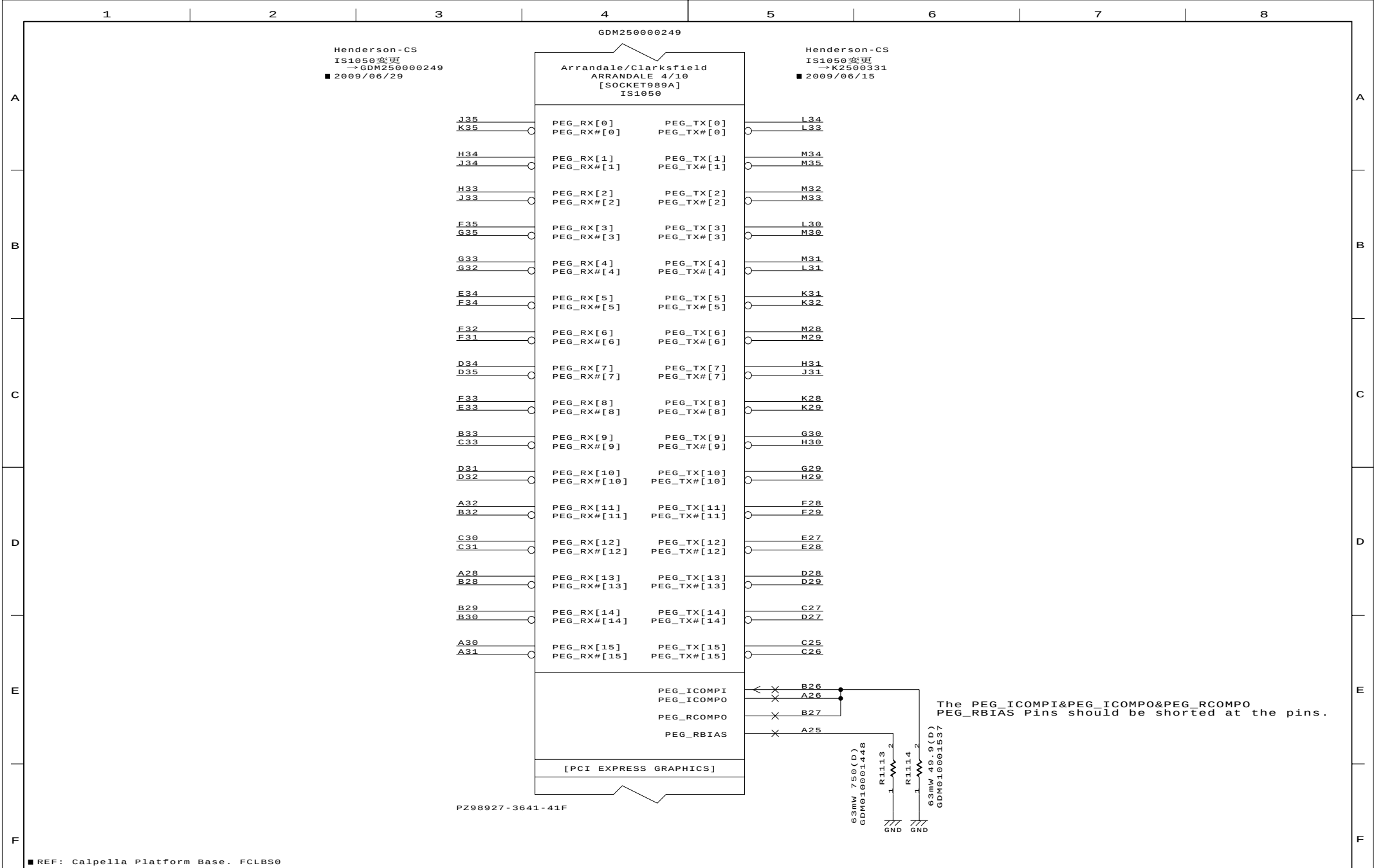
REV.MARK

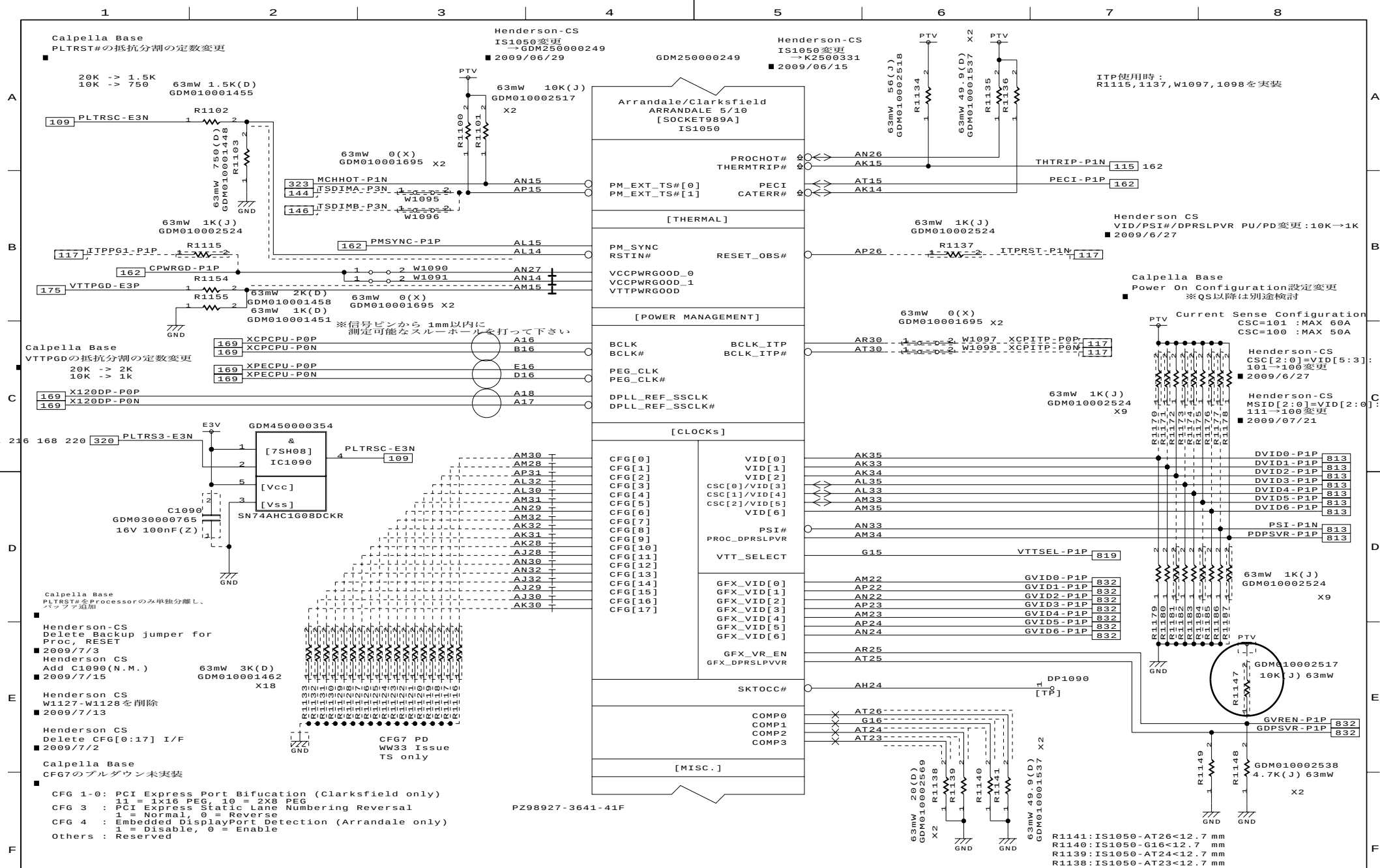
00

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DESIGNED BY

T.Naruse/T.Ichimura

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TITLE

FHNSY1

FUNCTION

rPGA989 (5)

SH.NO.

109

PAGE NO.

010

REV.MARK

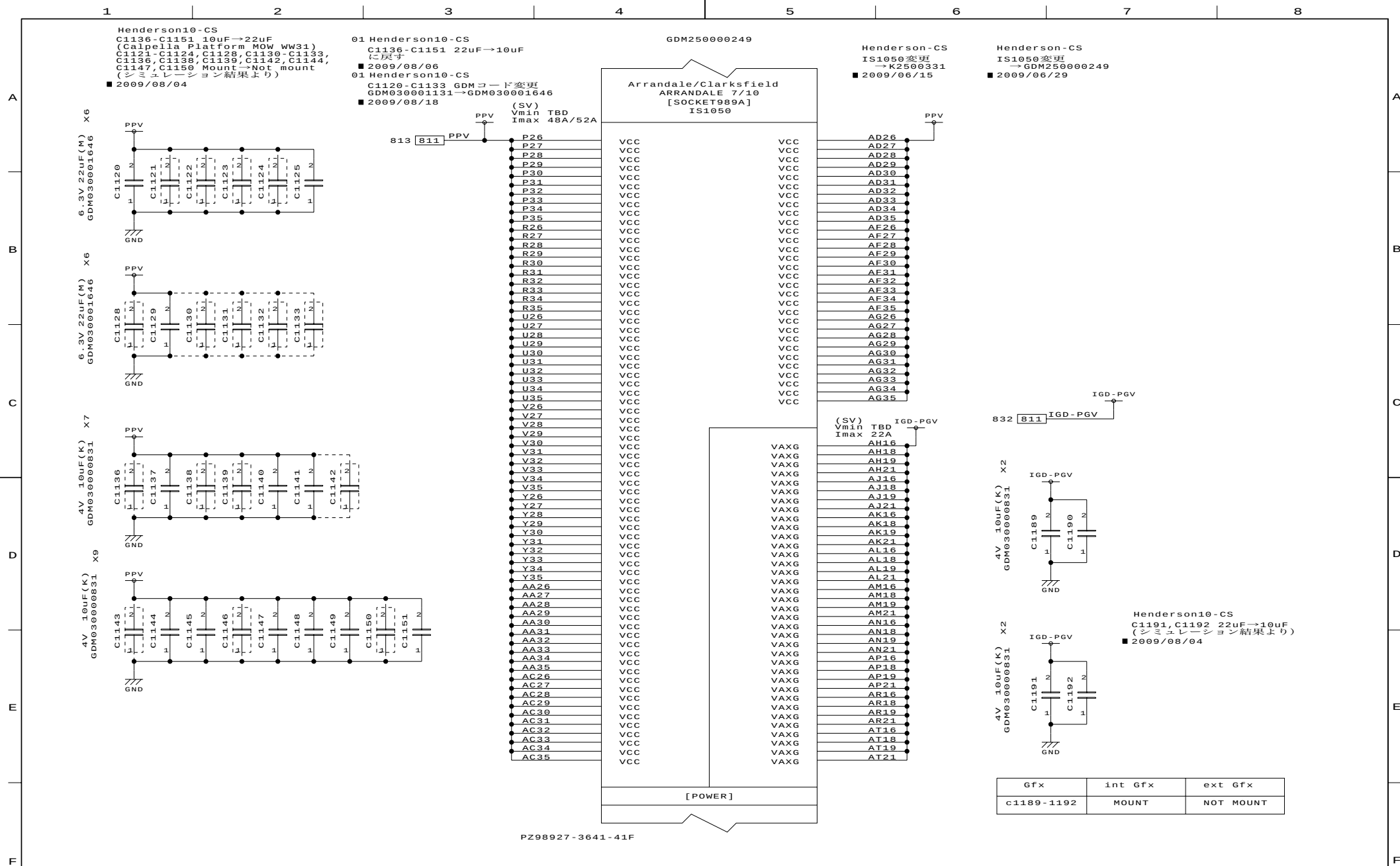
00

DRAWING.NO.

360069769

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■ REF: Calpella Platform Base. FCLBS0

DESIGNED BY

T.Naruse/T.Ichimura

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TITLE

FHNSY1

FUNCTION

rPGA989 (7)

SH. NO.

111

PAGE NO.

012

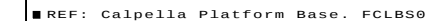
REV. MARK

00

DRAWING. NO.

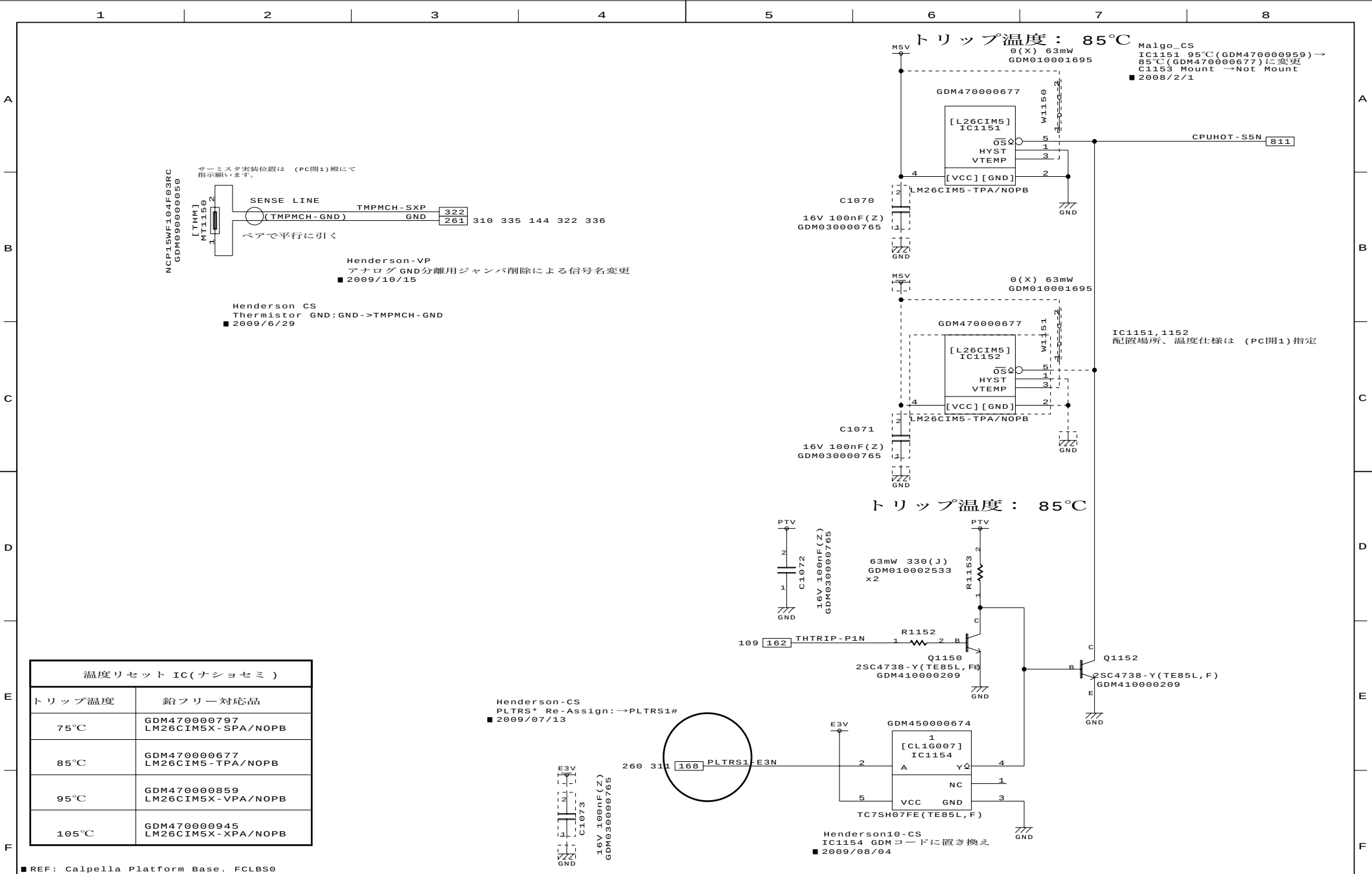
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TOSHIBA CORPORATION

TOSHIBA CORPORATION



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TP:CN1170の近くに配置

107	BPM0-P1N	DP1200
107	BPM1-P1N	DP1201
107	BPM2-P1N	DP1202
107	BPM3-P1N	DP1203

107	BPM4-P1N	DP1206
107	BPM5-P1N	DP1207
107	BPM6-P1N	DP1208
107	BPM7-P1N	DP1209

168	SMBDT0-P3P	DP1210
168	SMBCK0-P3P	DP1211

Henderson CS
Delete CFG[0:17] I/F
■ 2009/7/2

Henderson CS
Delete W1172-W1175,W1178-W1179
Change to direct connection
■ 2009/07/15

Henderson-CS
DP1206-1209をDCK55Fに変更
■ 2009/08/03

Calpella SFF TS
60pin→24pin不足信号に DP追加。
■ 2009/5/18

Calpella SFF TS
DBR-P3N:PUをN.Mに。
■ 2009/6/1

Lillehammer Temp
XDP 60pin → XDP-SFF 24pin
■ 2009/3/4

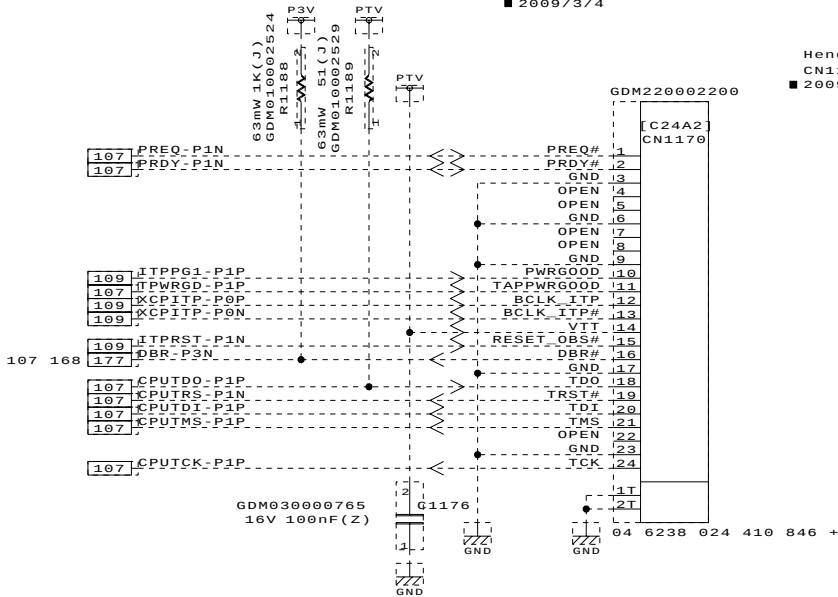
Henderson-CS
CN1170:G-CODE化
■ 2009/08/04

Henderson CS
N.M.CN1170,C1176,R1189
■ 2009/6/27

Henderson CS
CPU-PTV->PTV
■ 2009/6/27

Lilleham Temp
DBR-E3N→DBR-P3N
■ 2009/3/24

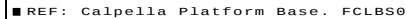
107 168



■ REF: Calpella SFF TS. FCLST0

DESIGNED BY Ling.Yu	TITLE FHNSY1	FUNCTION Proc.ITP-XDP 24pin	SH.NO. 117	PAGE NO. 016	REV.MARK 00	DRAWING.NO. 360069769
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2009.10.15 17:09 G11



DRAWING . NO .
360069769

仕向け設定有

Docker

	Yes	No
C1600-C1607 R1798	Mount NotMount	NotMount Mount

Mini-DP

	Yes	No
C1608-C1615 W1611	Mount Mount	NotMount Mount

Henderson-VP
HPDにプルダウン抵抗追加。
■ 2009/09/24

BH45
BE45
BG48
BJ48
BG46
BJ46
BJ44
BG44
AU38

SDVO_INTN
SDVO_INTN
SDVO_STALLP
SDVO_STALLN
SDVO_TVCLKINP
SDVO_TVCLKINN

GDM460002418

IBEX PEAK PCH 2/16
[UIBEXPEAKA]
*IC1600

DIGITAL DISPLAY SIGNALS

[VccIO]

Digital Port

HDMI

DDPB_0 TMDSB_DATA2
DDPB_1 TMDSB_DATA1
DDPB_2 TMDSB_DATA0
DDPB_3 TMDSB_CLK

Digital Port

Displayport

DDPC_0 DDPC_0
DDPC_1 DDPC_1
DDPC_2 DDPC_2
DDPC_3 DDPC_3

DG ver1.6より

10V 100nF(K)
GDM030000786

550 DPBHPD-P3P

*R1798
GDM010002517
10K(J) 63mW

560 ZICAUX-P1P
560 ZICAUX-P1N
560 W1610
560 W1611

569 323 HPDDPG-S3P
323 HPDDPE-S3P

GDM010001695
0(X) 63mW

Henderson-CS
W1610, W1611, HPDDPE-P3P追加
■ 2009/07/23

Henderson-CS
HPDDPG-P3P->HPDDPG-S3P
■ 2009/7/23

Henderson-CS
HPDDPE-P3P->HPDPE-S3P
■ 2009/7/23

GDM010002517
10K(J) 63mW
R1799
1 2
777 GND

BD46
BC46
AT38

DDPC_AUXP
DDPC_AUXN
DDPC_HPD

DDPD_AUXP
DDPD_AUXN
DDPD_HPD

DDPB_0P
DDPB_0N
DDPB_1P
DDPB_1N
DDPB_2P
DDPB_2N
DDPB_3P
DDPB_3N

BC42
BD42
BG42
BJ42
BA40
BB40
BA38
AW38

*C1600 ZIDPB2-P1P 550
*C1601 ZIDPB2-P1N 550
*C1602 ZIDPB1-P1P 550
*C1603 ZIDPB1-P1N 550
*C1604 ZIDPB0-P1P 550
*C1605 ZIDPB0-P1N 550
*C1606 XIDPBC-P1P 550
*C1607 XIDPBC-P1N 550

DVI

Displayport

DDPC_0P
DDPC_0N
DDPC_1P
DDPC_1N
DDPC_2P
DDPC_2N
DDPC_3P
DDPC_3N

BD40
BE40
BH41
BF41
BC38
BD38
BA36
BB36

*C1608 ZDPTX0-P1P 569
*C1609 ZDPTX0-P1N 569
*C1610 ZDPTX1-P1P 569
*C1611 ZDPTX1-P1N 569
*C1612 ZDPTX2-P1P 569
*C1613 ZDPTX2-P1N 569
*C1614 ZDPTX3-P1P 569
*C1615 ZDPTX3-P1N 569

FDI

[VccIO]

FDI_INT

FDI_FSYNC0
FDI_LSYNC0

BJ14 FDIINT-P1P 107

BF13 FDIIS0-P1P 107
BJ12 FDIIS0-P1P 107

FDI_FSYNC1
FDI_LSYNC1

BH13 FDIIS1-P1P 107
BG14 FDIIS1-P1P 107

107 ZFDIR0-P1P BB18
107 ZFDIR0-P1N BA18
107 ZFDIR1-P1P BF17
107 ZFDIR1-P1N BH17
107 ZFDIR2-P1P BC16
107 ZFDIR2-P1N BD16
107 ZFDIR3-P1P BG16
107 ZFDIR3-P1N BJ16

107 ZFDIR4-P1P AW16
107 ZFDIR4-P1N BA16
107 ZFDIR5-P1P BD14
107 ZFDIR5-P1N BE14
107 ZFDIR6-P1P BB14
107 ZFDIR6-P1N BA14
107 ZFDIR7-P1P BD12
107 ZFDIR7-P1N BC12

FDI_RXP0
FDI_RXN0
FDI_RXP1
FDI_RXN1
FDI_RXP2
FDI_RXN2
FDI_RXP3
FDI_RXN3

FDI_RXP4
FDI_RXN4
FDI_RXP5
FDI_RXN5
FDI_RXP6
FDI_RXN6
FDI_RXP7
FDI_RXN7

BD82QM57 SLGZQ

■ REF: Calpella Platform Base. FCLBS0

DESIGNED BY

T. Naruse

TITLE

FHNSY1

FUNCTION

Ibex Peak-M (2)

SH.NO.

161

PAGE NO.

020

REV.MARK

00

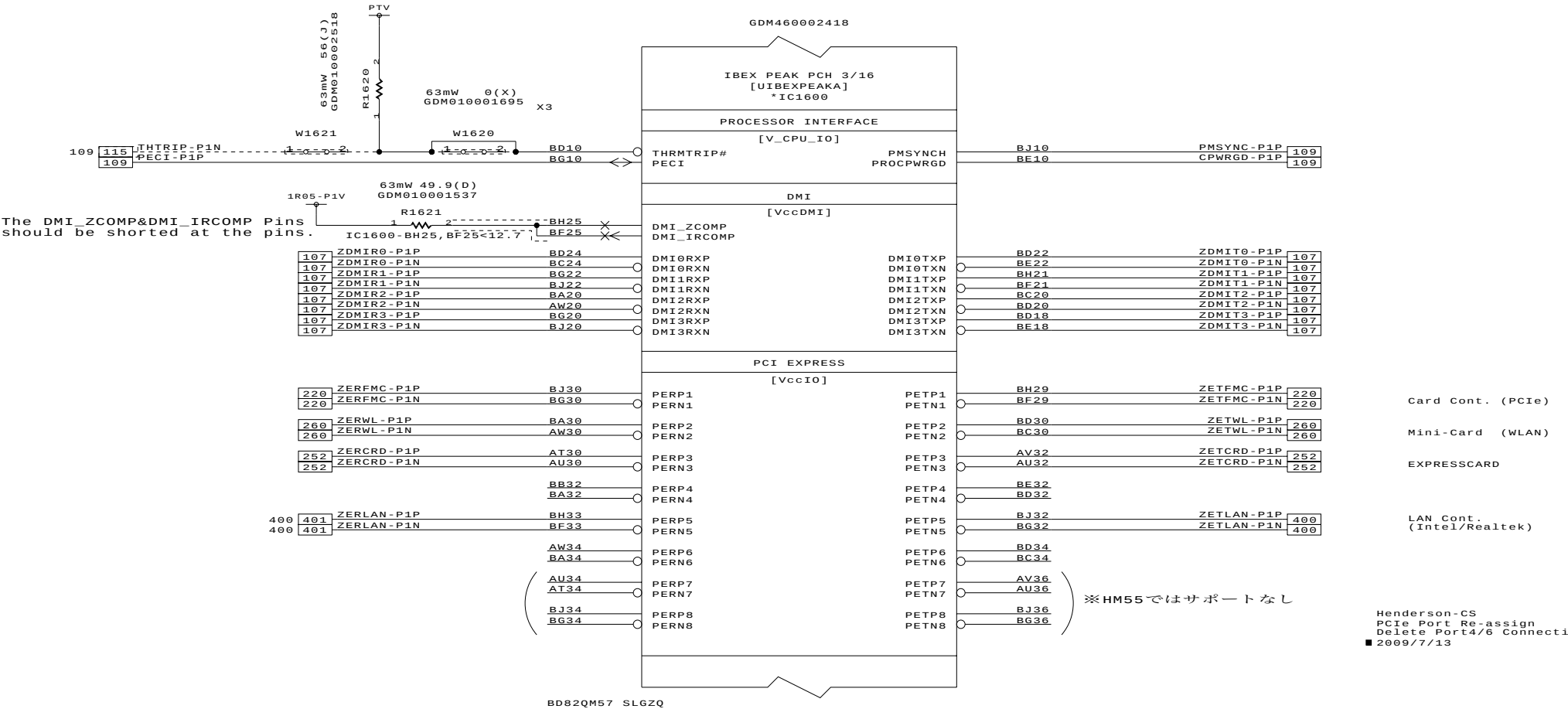
DRAWING.NO.

360069769

2009.10.15 17:09 G11

TOSHIBA CORPORATION

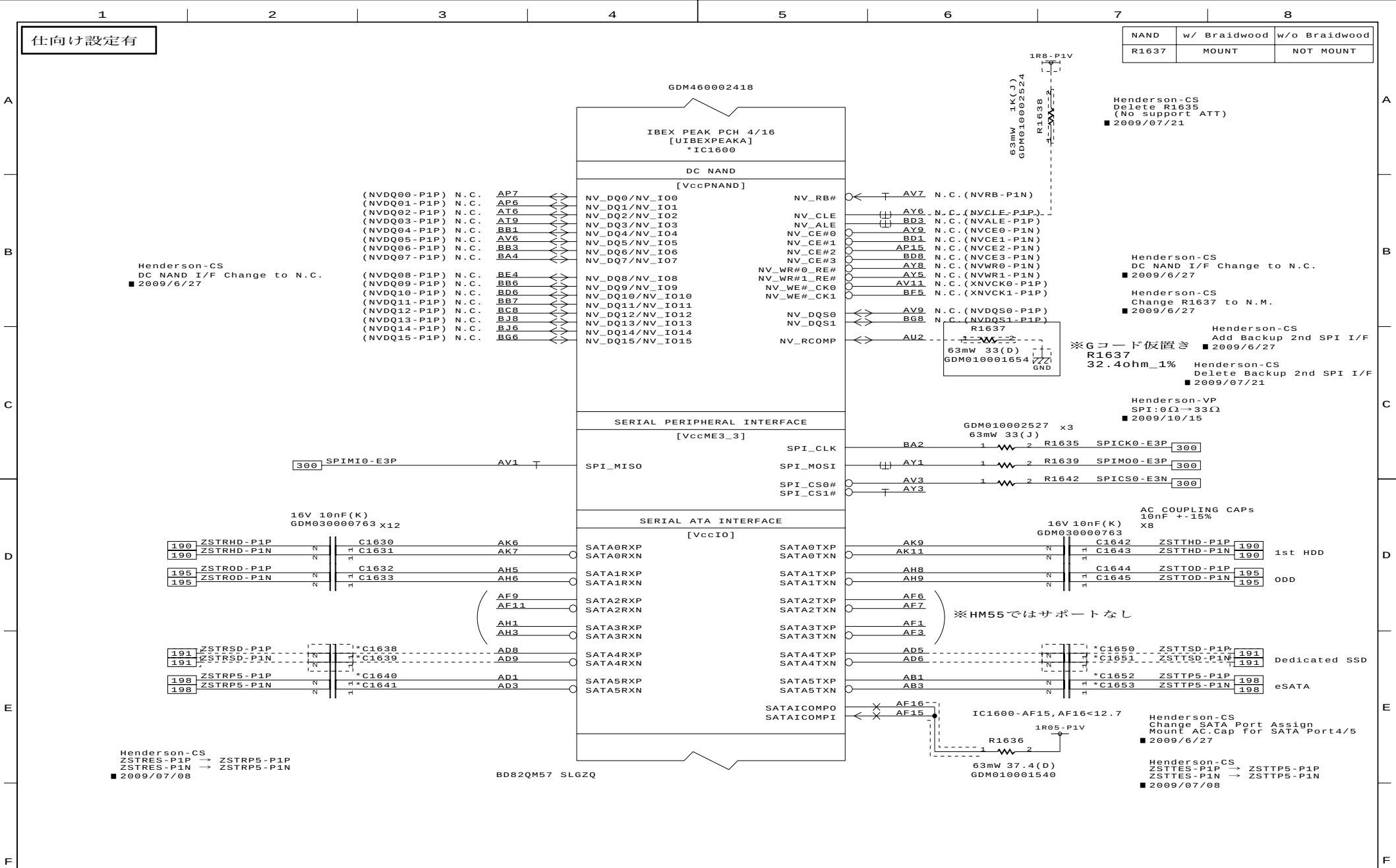
仕向け設定有

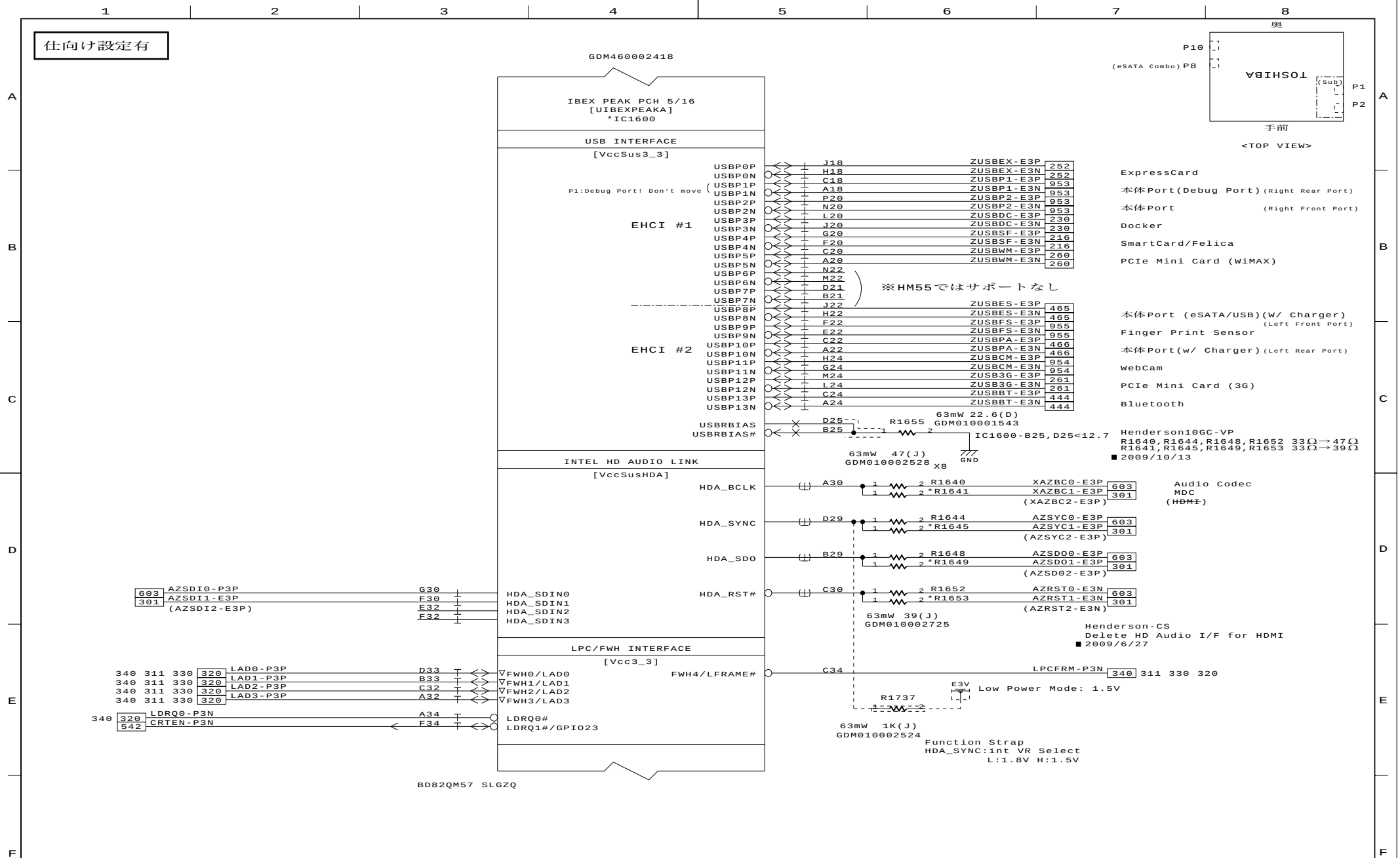


REF: Calpella Platform Base. FCLBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/L.Yu	FHNSY1	Ibex Peak-M (3)	162	021	00	360069769

2009.10.15 17:09 G11





DESIGNED BY

T. Ichimura/L.Yu

TITLE

FHNSY1

FUNCTION

Ibex Peak-M (5)

SH.NO.

164

PAGE NO.

023

REV.MARK

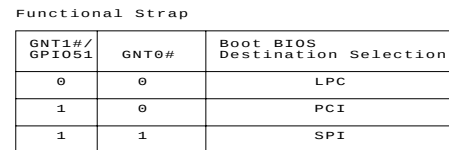
00

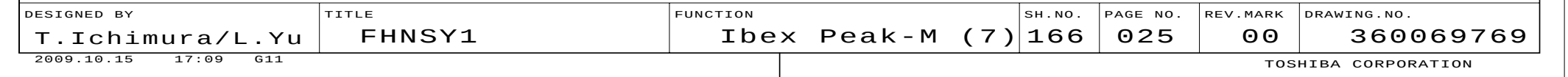
DRAWING.NO.

360069769

2009.10.15 17:09 G11

TOSHIBA CORPORATION



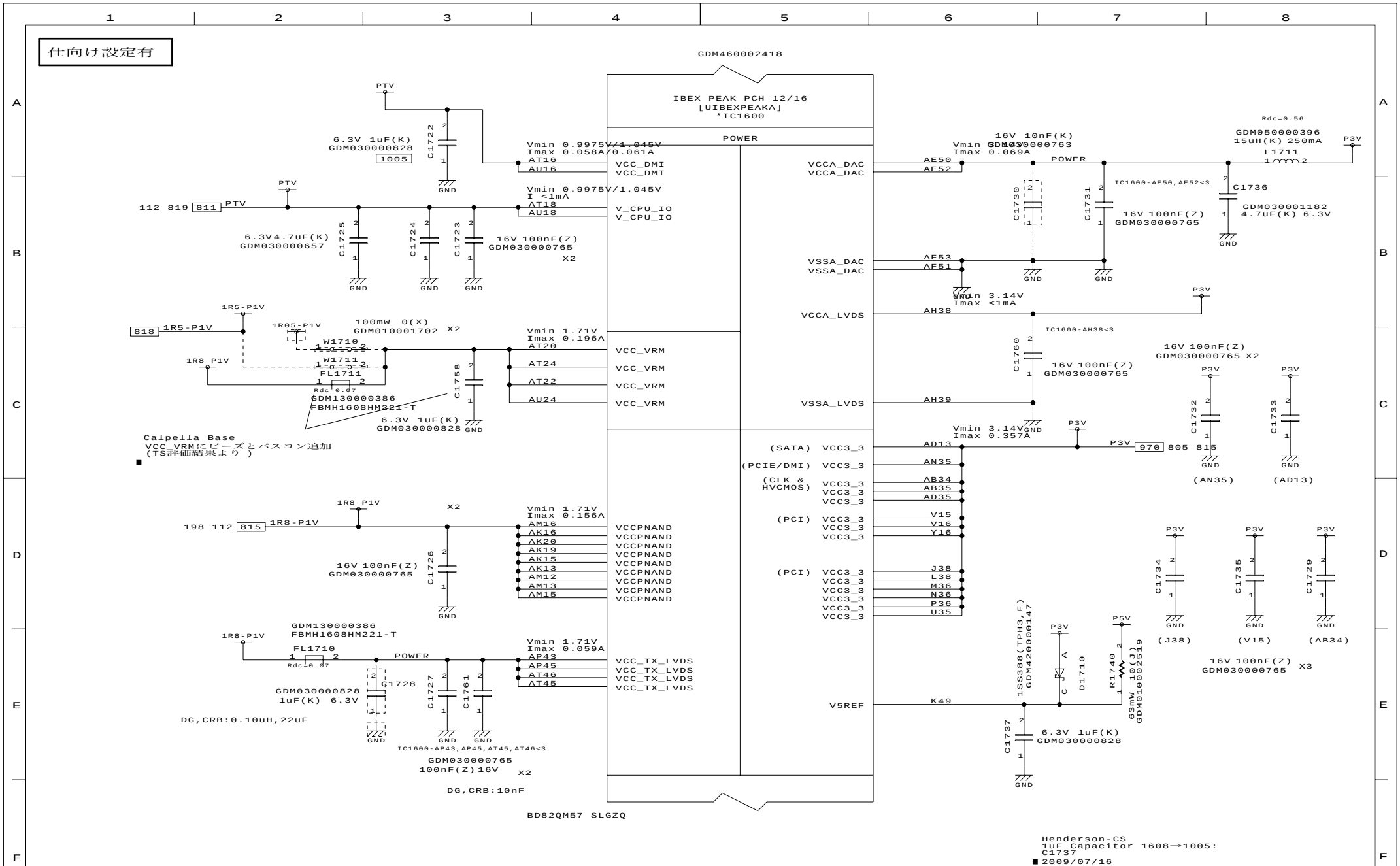










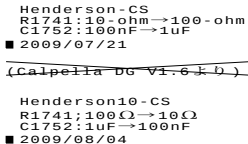


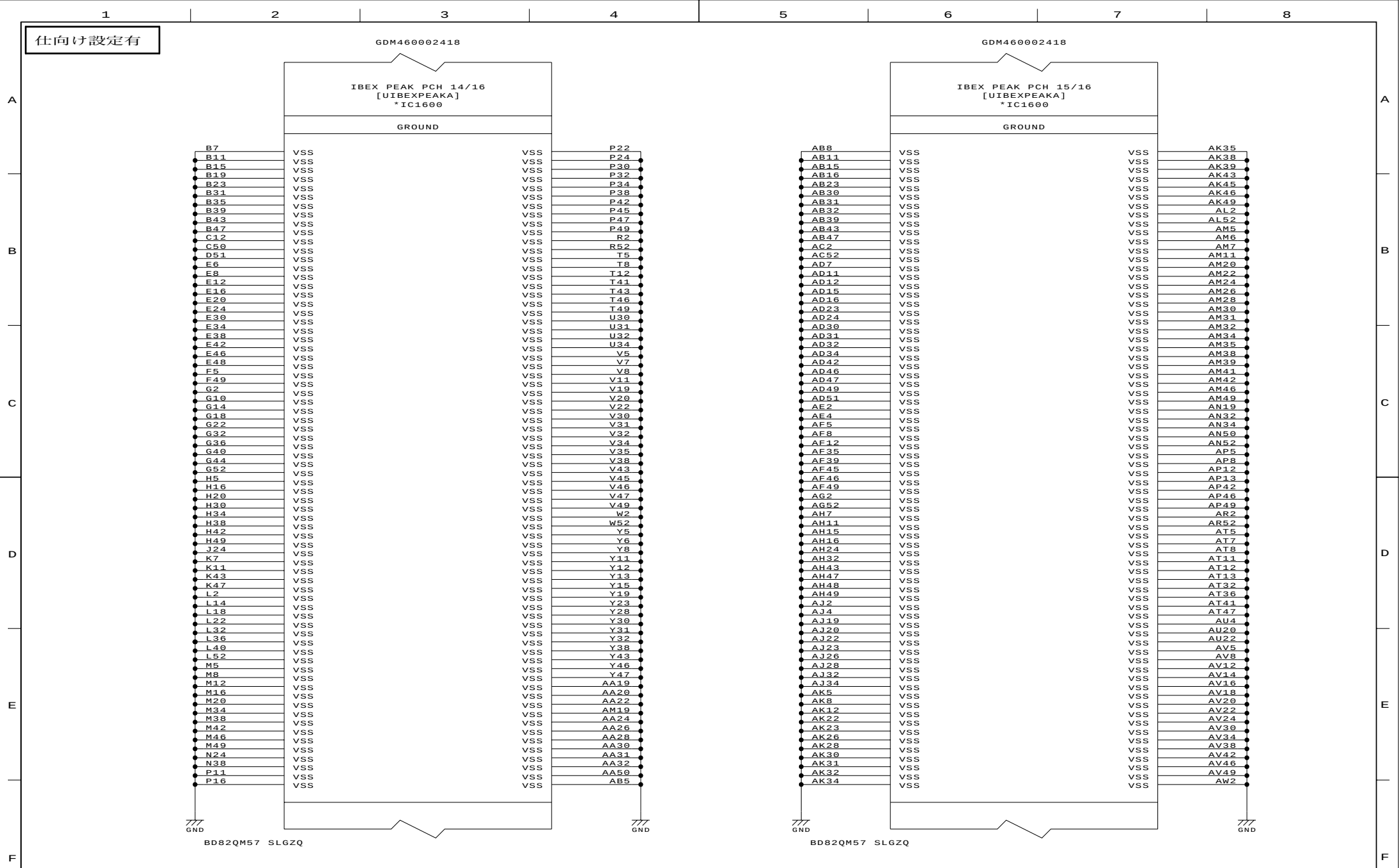
REF: Calpella Platform Base. FCLBS0

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse/T.Ichimura	FHNSY1	Ibex Peak-M (12)	171	030	00	360069769

2009.10.15 17:09 G11

TOSHIBA CORPORATION





GDM460002418

IBEX PEAK PCH 16/16
[UIBEXPEAKA]
*IC1600

GROUND

TP, NC, NCTF

AW14	VSS	TP1	BA22	H	DP1709
AW18	VSS	TP2	AW22	HTB	DP1701
AW32	VSS	TP3	BB22	HTB	DP1702
AW36	VSS	TP4	AY45	HTB	DP1703
AW40	VSS	TP5	AY46	HTB	DP1704
AW52	VSS	TP6	AV43	HTB	DP1705
AY7	VSS	TP7	AV45	HTB	DP1706
AY11	VSS	TP8	AE12	HTB	DP1707
AY43	VSS	TP9	M18	HTB	DP1708
AY47	VSS	TP10	N18	HTB	DP1709
BA12	VSS	TP11	AJ24	HTB	DP1710
BA42	VSS	TP12	AK41	HTB	DP1711
BB5	VSS	TP13	AK42	HTB	DP1712
BB10	VSS	TP14	M32	HTB	DP1713
BB12	VSS	TP15	N32	HTB	DP1714
BB16	VSS	TP16	N30	HTB	DP1715
BB20	VSS	TP17	N30	HTB	DP1716
BB24	VSS	TP18	H12	HTB	DP1717
BB30	VSS	TP19	AA23	HTB	DP1718
BB34	VSS			[TP]	
BB38	VSS				
BB42	VSS				
BB44	VSS				
BB49	VSS				
BC2	VSS	NC	AB45		
BC10	VSS	NC	AB38		
BC14	VSS	NC	AB42		
BC18	VSS	NC	AB41		
BC22	VSS	NC	T39		
BC32	VSS				
BC36	VSS	VSS_NCTF	A4		
BC40	VSS	VSS_NCTF	A5		
BC44	VSS	VSS_NCTF	B2		
BC52	VSS	VSS_NCTF	B4		
BD5	VSS	VSS_NCTF	D1		
BD48	VSS	VSS_NCTF	D2		
BD49	VSS	VSS_NCTF	E1		
BE6	VSS				
BE8	VSS				
BE12	VSS				
BE16	VSS				
BE20	VSS	VSS_NCTF	A49		
BE24	VSS	VSS_NCTF	A50		
BE30	VSS	VSS_NCTF	A52		
BE34	VSS	VSS_NCTF	A53		
BE38	VSS	VSS_NCTF	B52		
BE42	VSS	VSS_NCTF	B53		
BE46	VSS	VSS_NCTF	D53		
BE48	VSS	VSS_NCTF	E53		
BE50	VSS				
BF3	VSS				
BF9	VSS				
BF49	VSS	VSS_NCTF	BE1		
BF51	VSS	VSS_NCTF	BF1		
BG4	VSS	VSS_NCTF	BH1		
BG12	VSS	VSS_NCTF	BH2		
BG18	VSS	VSS_NCTF	BJ1		
BG24	VSS	VSS_NCTF	BJ2		
BG50	VSS	VSS_NCTF	BJ4		
BH7	VSS	VSS_NCTF	BJ5		
BH9	VSS				
BH11	VSS				
BH15	VSS	VSS_NCTF	BE53		
BH19	VSS	VSS_NCTF	BF53		
BH23	VSS	VSS_NCTF	BH52		
BH31	VSS	VSS_NCTF	BH53		
BH35	VSS	VSS_NCTF	BJ49		
BH39	VSS	VSS_NCTF	BJ50		
BH43	VSS	VSS_NCTF	BJ52		
BH47	VSS	VSS_NCTF	BJ53		

※TP必要

Henderson CS
Delete TP for NC/VSS_NCTF
■ 2009/7/2

BD82QM57 SLGZQ



GND

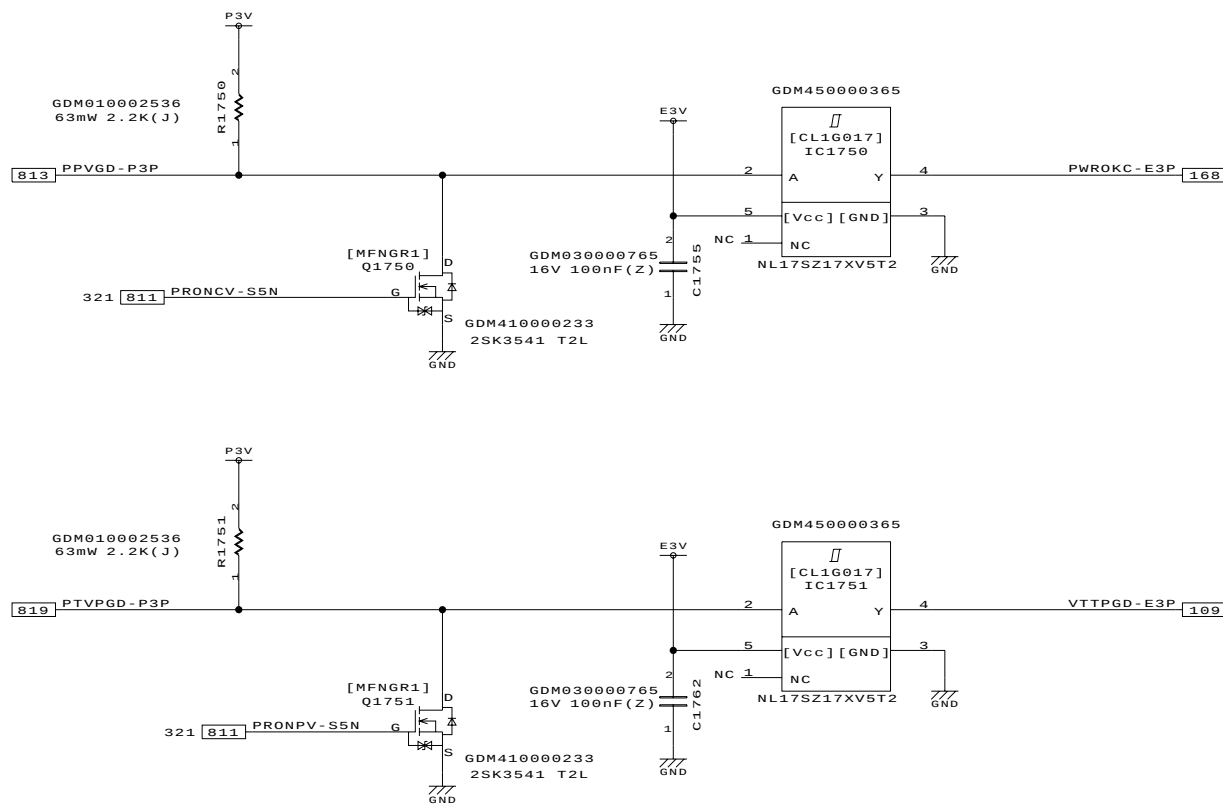
■ REF: Calpella Platform Base. FCLBS0

DRAWING . NO .

360069769

2009.10.15 17:09 G11

TOSHIBA CORPORATION



■ REF: Calpella Platform Base. FCLBS0

DESIGNED BY

T. Ichimura

TITLE

FHNSY1

FUNCTION

IMVP-6.5 POWER OK

SH.NO.

175

PAGE NO.

034

REV.MARK

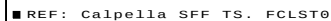
00

DRAWING.NO.

360069769

2009.10.15 17:09 G11

TOSHIBA CORPORATION



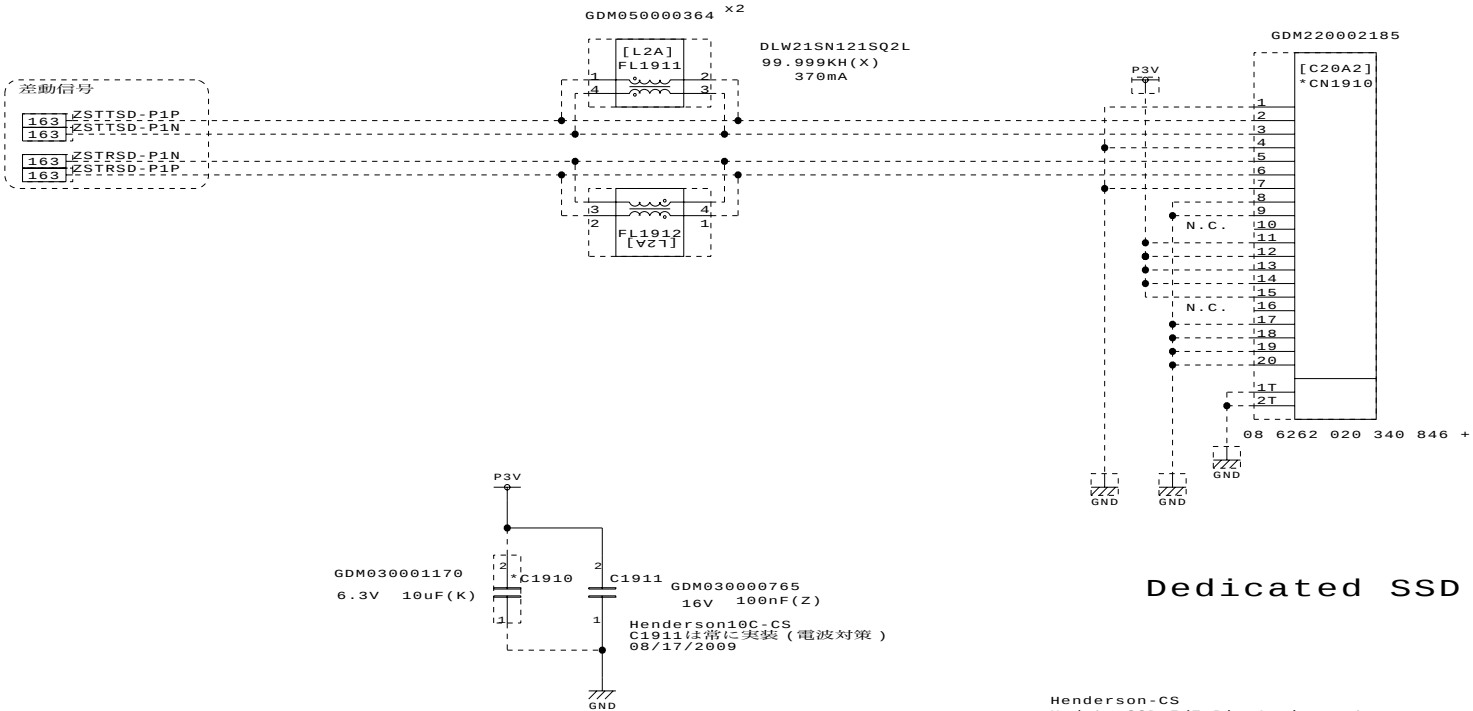
2009.10.15 17:09 G11



仕向け設定有

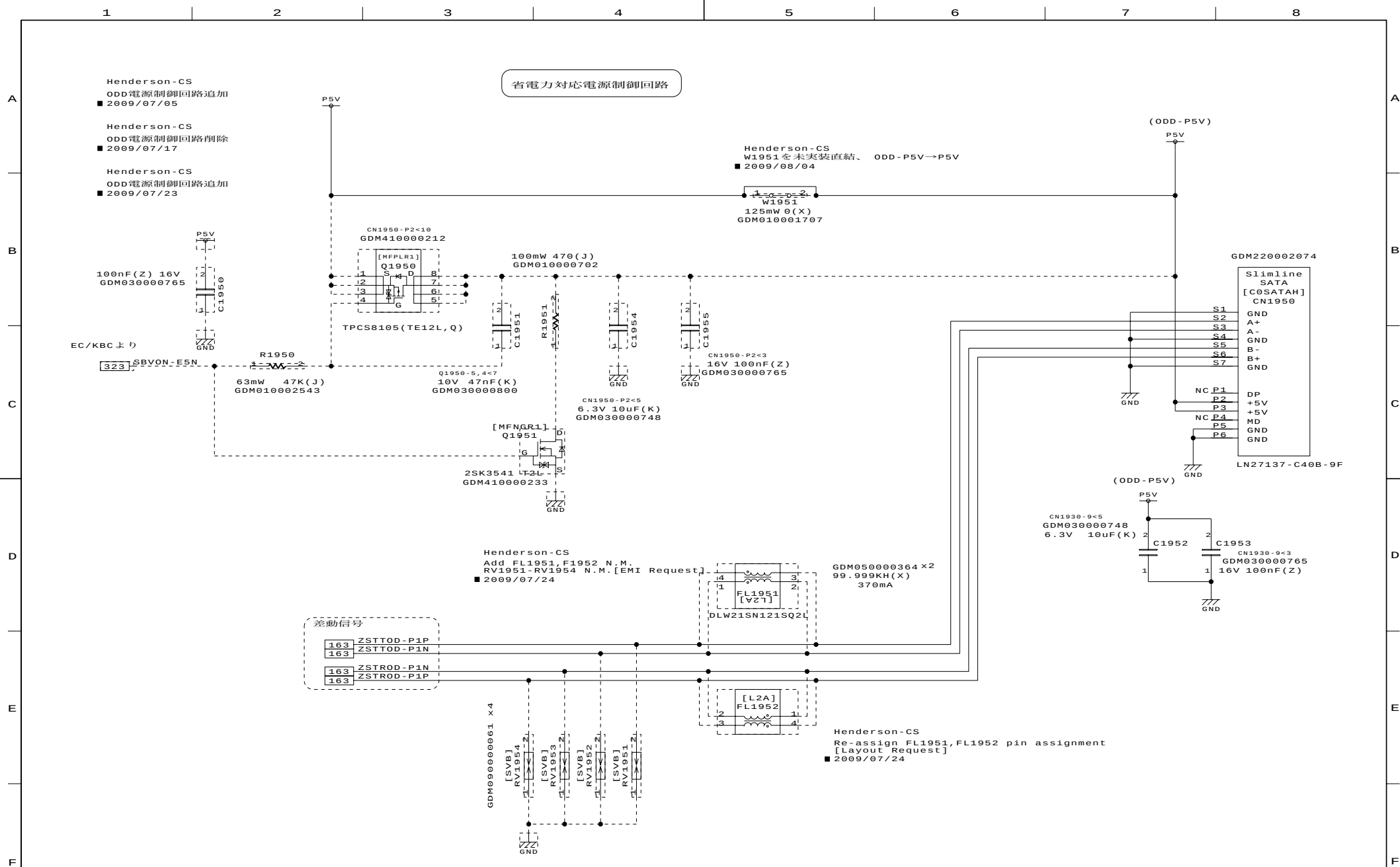
Henderson-CS
Add FL1911, F1912 N.M.
[EMI Request]
■ 2009/07/24

SSD コネクタ : ハーネス経由用



Henderson-CS
Update SSD I/F Pin Assignment
■ 2009/07/13

DESIGNED BY Ling.Yu		TITLE FHNSY1	FUNCTION Dedicated SSD I/F 191	SH.NO. 037	PAGE NO. 00	REV.MARK 00	DRAWING.NO. 360069769
2009.10.15 17:09 G11		TOSHIBA CORPORATION					



DESIGNED BY

T. Ichimura

TITLE

FHNSY1

FUNCTION

SATA ODD I/F

SH.NO.

195

PAGE NO.

038

REV.MARK

00

DRAWING.NO.

360069769

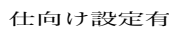
2009.10.15 17:09

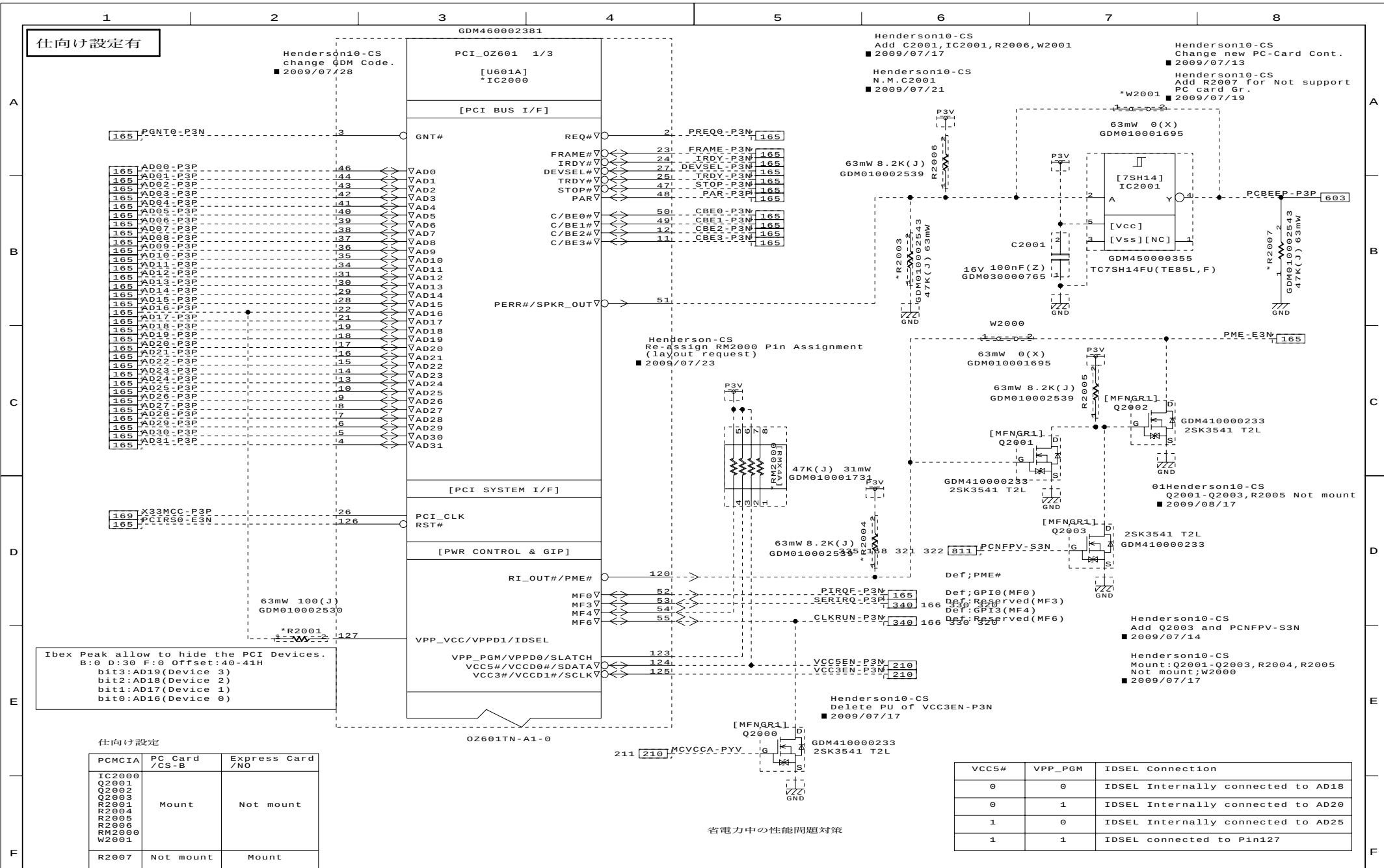
TOSHIBA CORPORATION

5



DESIGNED BY T. Ichimura	TITLE FHNSY1	FUNCTION eSATA/USB I/F	SH. NO. 197	PAGE NO. 039	REV. MARK 00	DRAWING. NO. 360069769
2009.10.15 17:09			TOSHIBA CORPORATION			

TOSHIBA CORPORATION

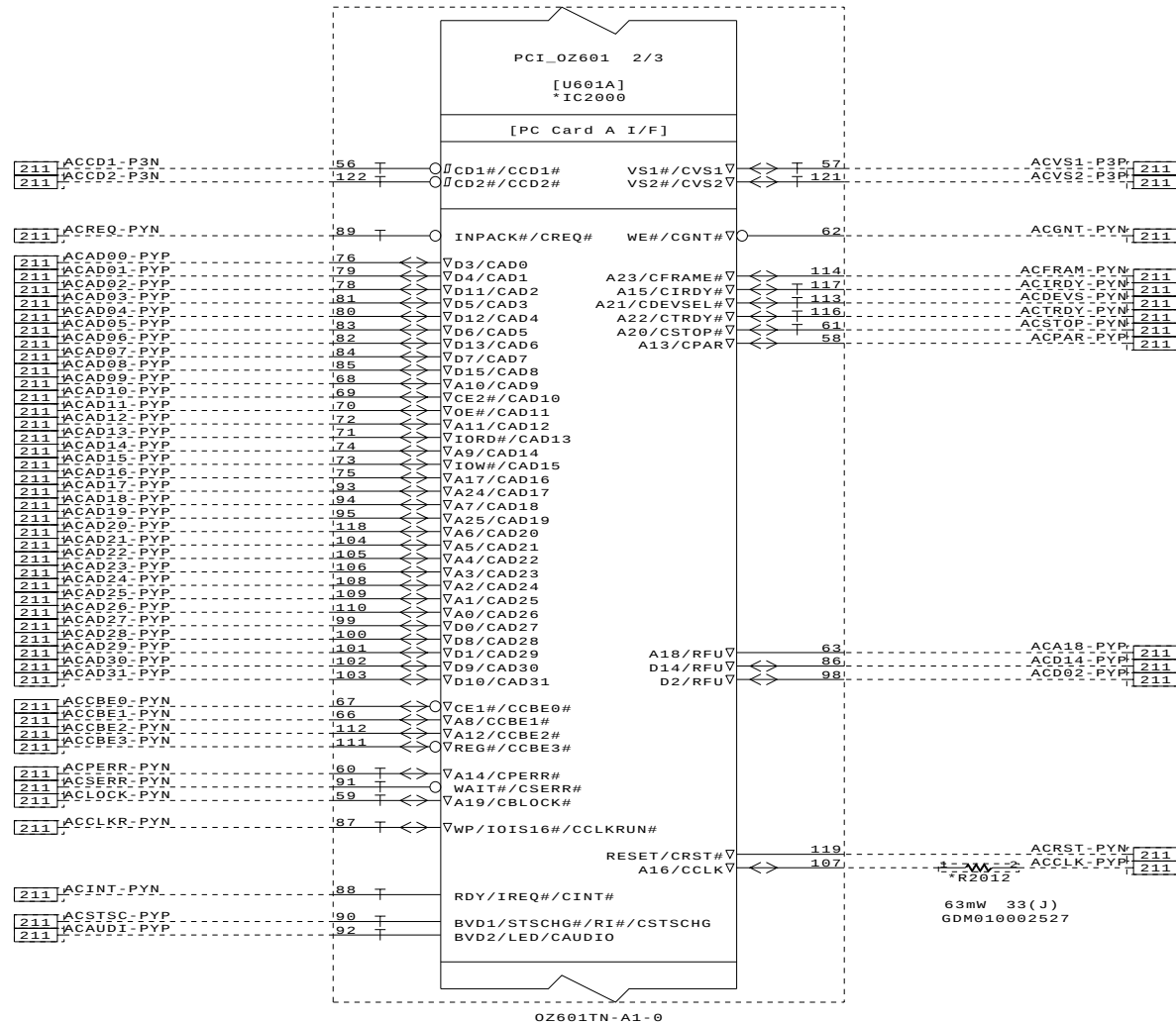


仕向け設定有

PCMCIA	PC Card /CS-B	Express Card /NO
IC2000 R2012	Mount	Not mount

Henderson10-CS
change GDM Code.
■ 2009/07/28

GDM460002381



■ REF: NEW

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.OCHIAI	FHNSY1	PC CARD CONT(2)	201	042	00	360069769

2009.10.15 17:09 G11

TOSHIBA CORPORATION

仕向け設定有

Henderson10-CS
Change new PC-Card Cont.
■ 2009/07/13

PCMCIA	PC Card /CS-B	Express Card /NO
IC2000	Mount	Not mount

A

B

C

D

E

F

A

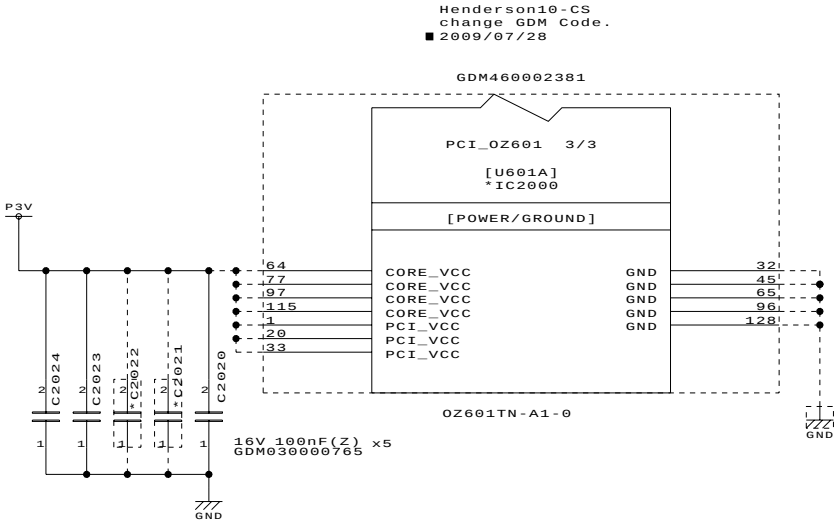
B

C

D

E

F



■ REF: NEW

DESIGNED BY

T.OCHIAI

TITLE

FHNSY1

FUNCTION

PC CARD CONT(3)

SH.NO.

202

PAGE NO.

043

REV.MARK

00

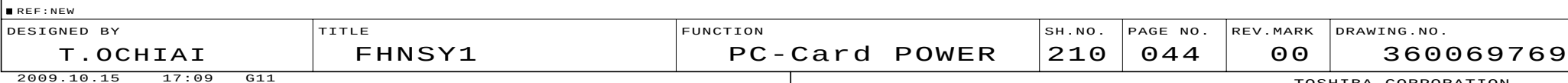
DRAWING.NO.

360069769

2009.10.15 17:09 G11

TOSHIBA CORPORATION

Henderson10-CS
Change new PC-Card Power SW
■ 2009/07/13

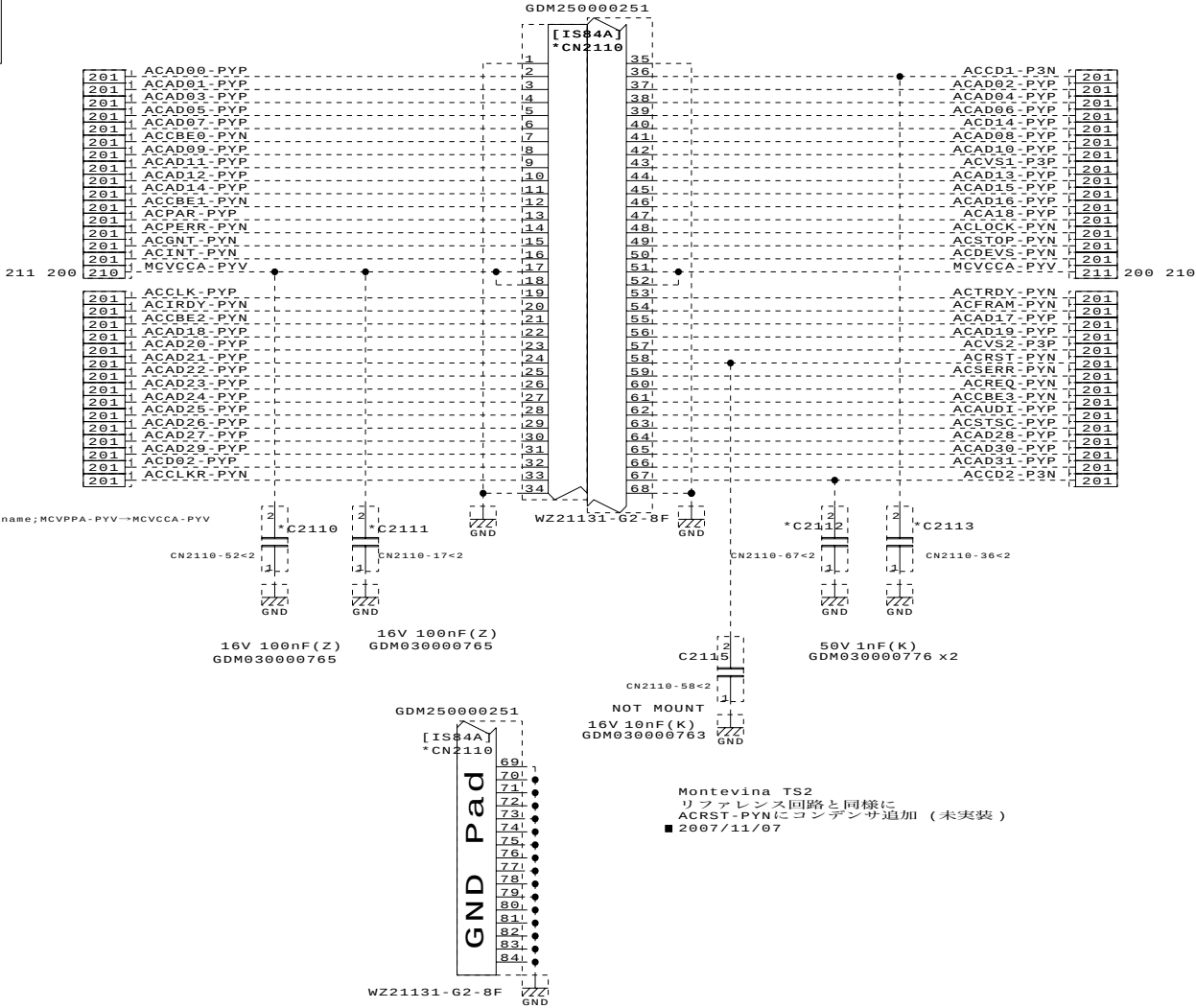


仕向け設定有

PCMCIA	PC Card /CS-B	Express Card /NO
C2110 C2111 C2112 C2113 CN2110	Mount	Not mount

Henderson-CS
CN2110変更
→K2500337
■ 2009/06/29

Henderson10-CS
CN2110 GDMコードに置き換え
■ 2009/08/04



Henderson10-CS
PC Card Headerを
Kコードに変更
■ 2009/03/30

Henderson-CS
Change Signal name;MCVPPA-PYV→MCVCCA-PYV
■ 2009/7/13

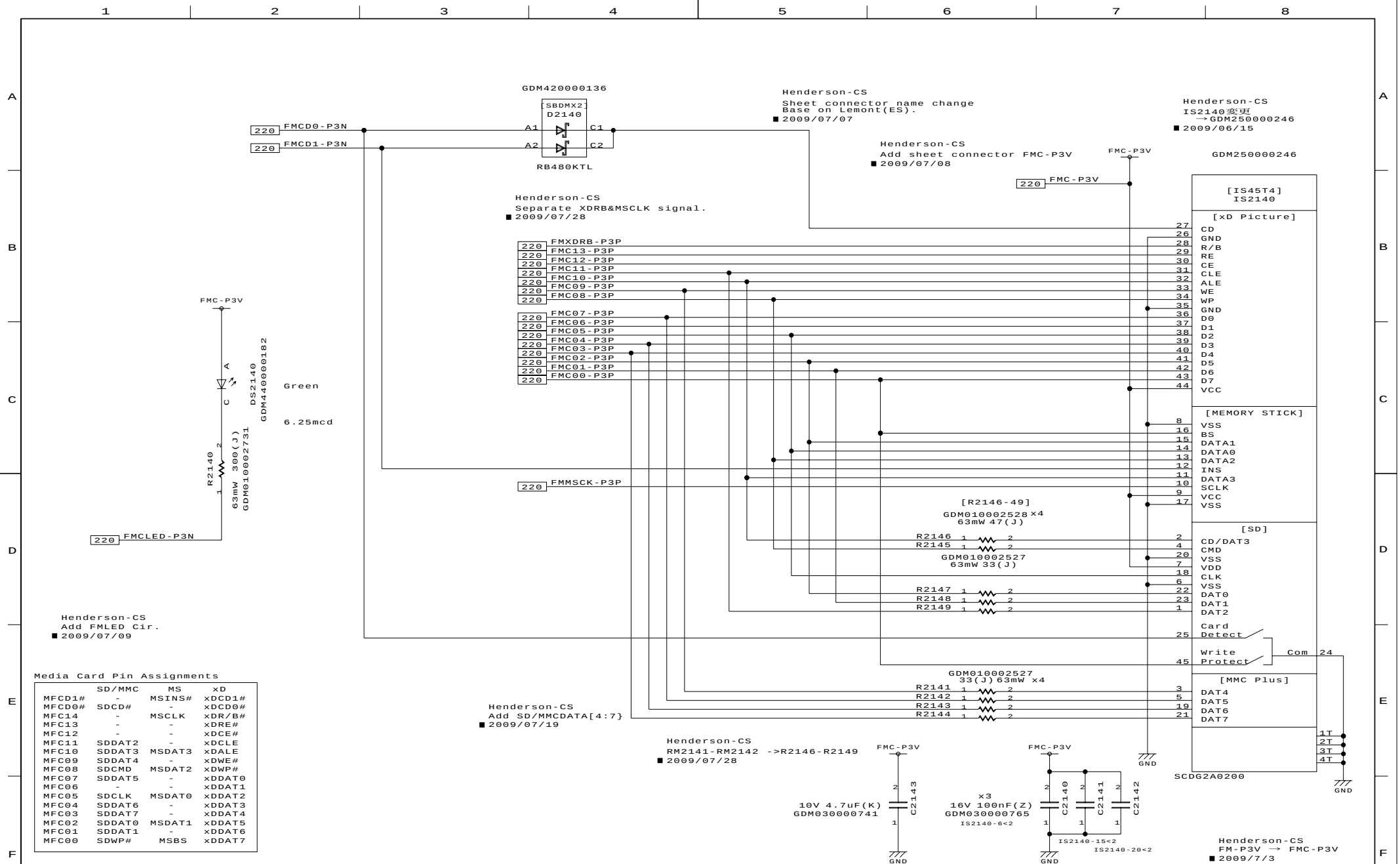
Montevina TS2
リファレンス回路と同様に
ACRST-PYNにコンデンサ追加 (未実装)
■ 2007/11/07

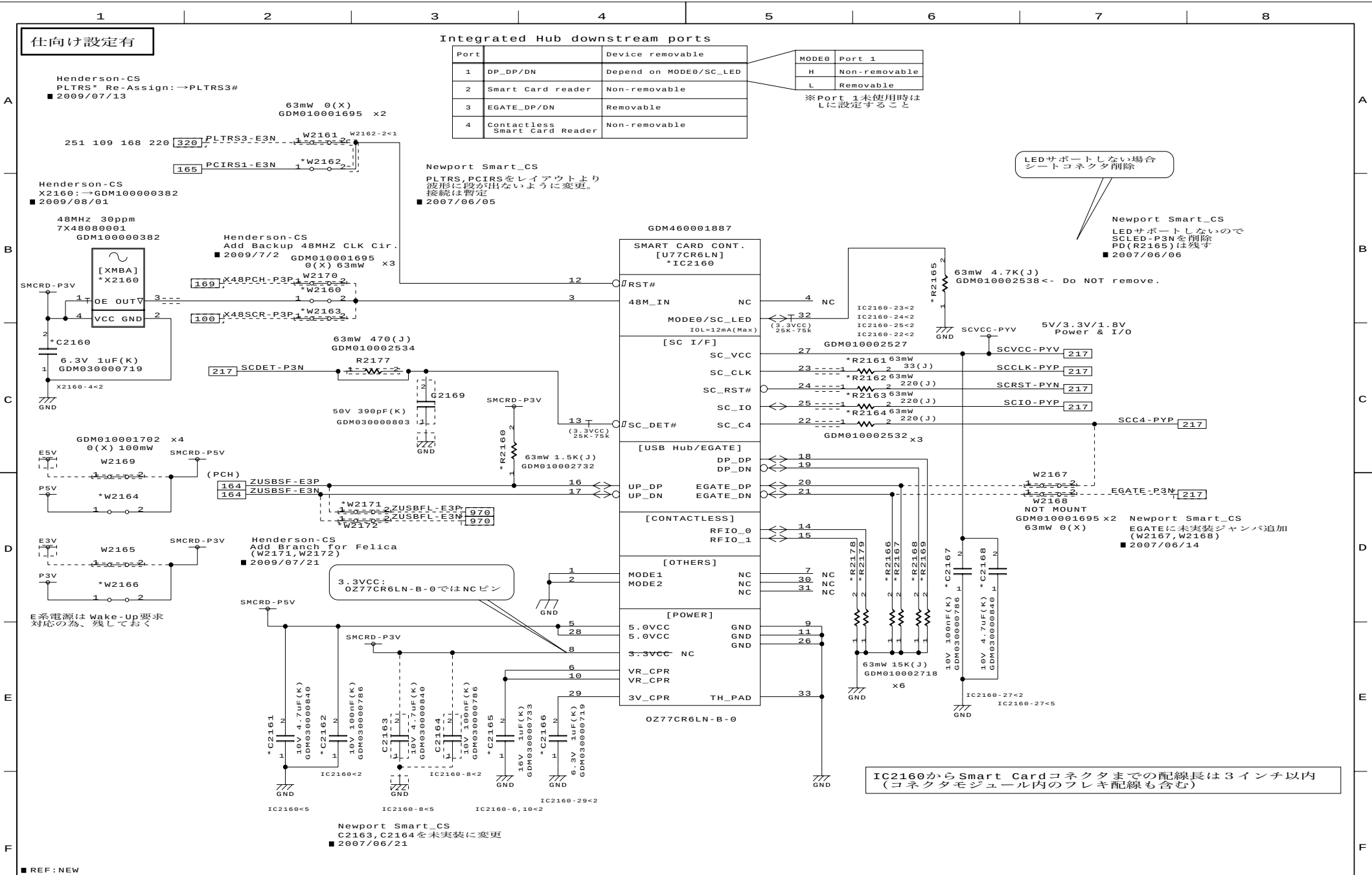
Henderson-CS
Add PC-Card/Ex-Card Module Detection Circuit
■ 2009/7/6

■ REF:Santa Rosa Base . FSRBS0

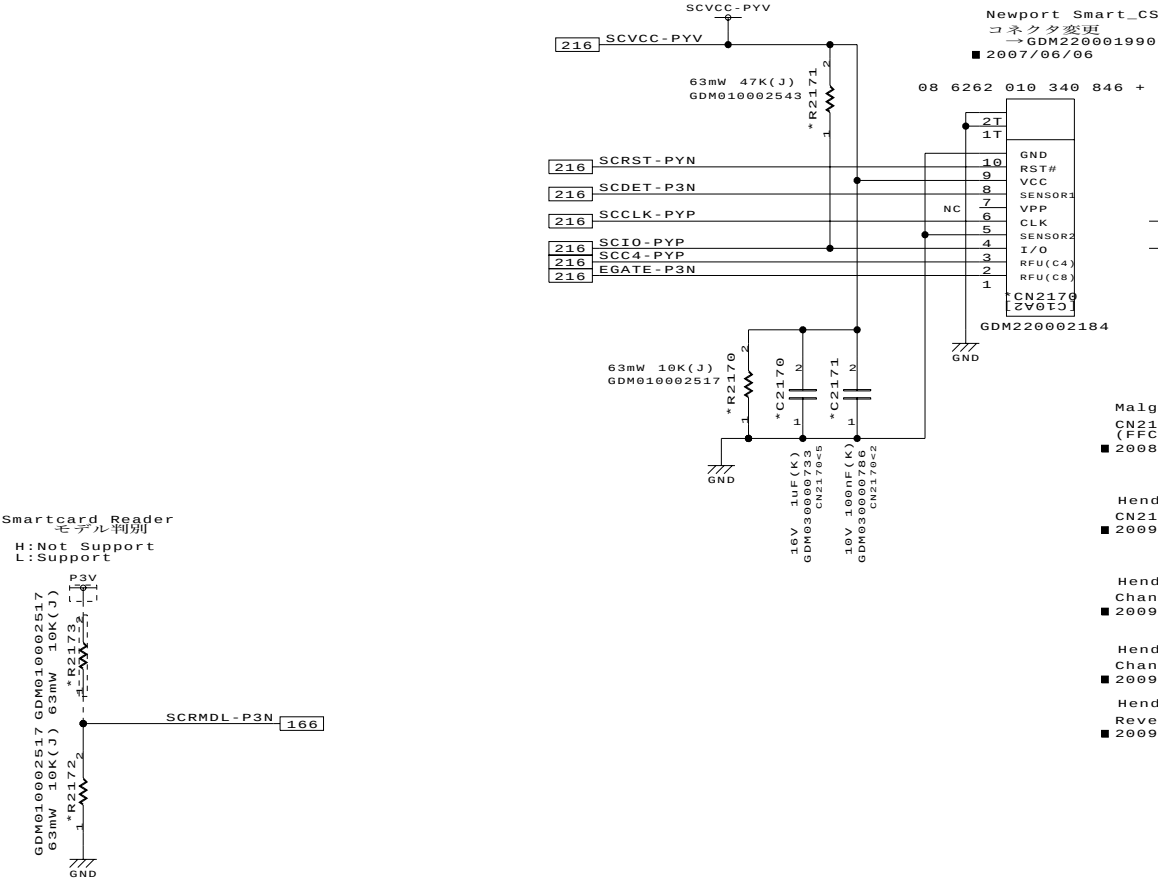
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/L.Yu	FHNSY1	PC-CARD I/F	211	045	00	360069769

2009.10.15 17:09 G11





仕向け設定有



Smart Card Socket

Malgow_VP
CN2170をGDM220002079に変更
(FFC→FPC変更対応)
■ 2008/03/24

Henderson-CS
CN2170をGDM220002184に変更
■ 2009/6/29

Henderson-CS
Change CN2170 Pin Assignment
■ 2009/07/02

Henderson-CS
Change CN2170 Pin Assignment
■ 2009/07/14

Henderson-CS
Reverse CN2170 Pin Assignment
■ 2009/07/15

Smart Card Socketのコンタクトのアサイン

Contact No.	Assignment	Contact No.	Assignment
C1	VCC	C5	GND
C2	RST	C6	VPP
C3	CLK	C7	I/O
C4	AUX1	C8	AUX2

AUX1 is used by function code (FCB) for type 2 synchronous cards (ISO/IEC 7816-10).
If an I/F device provides a USB I/F, VCC shall be used by VBUS, AUX1 by D+ and AUX2 by D-.
The use of AUX1 and AUX2 by the USB I/F does not preclude the use of these contacts by other protocols or other I/F devices defined in this International Standard series.
Such use is reserved by ISO/IEC JTC1/SC17.

ISO/IEC 7816-2 AMENDMENT 1 (2004-06-01)より

■ REF:NEW

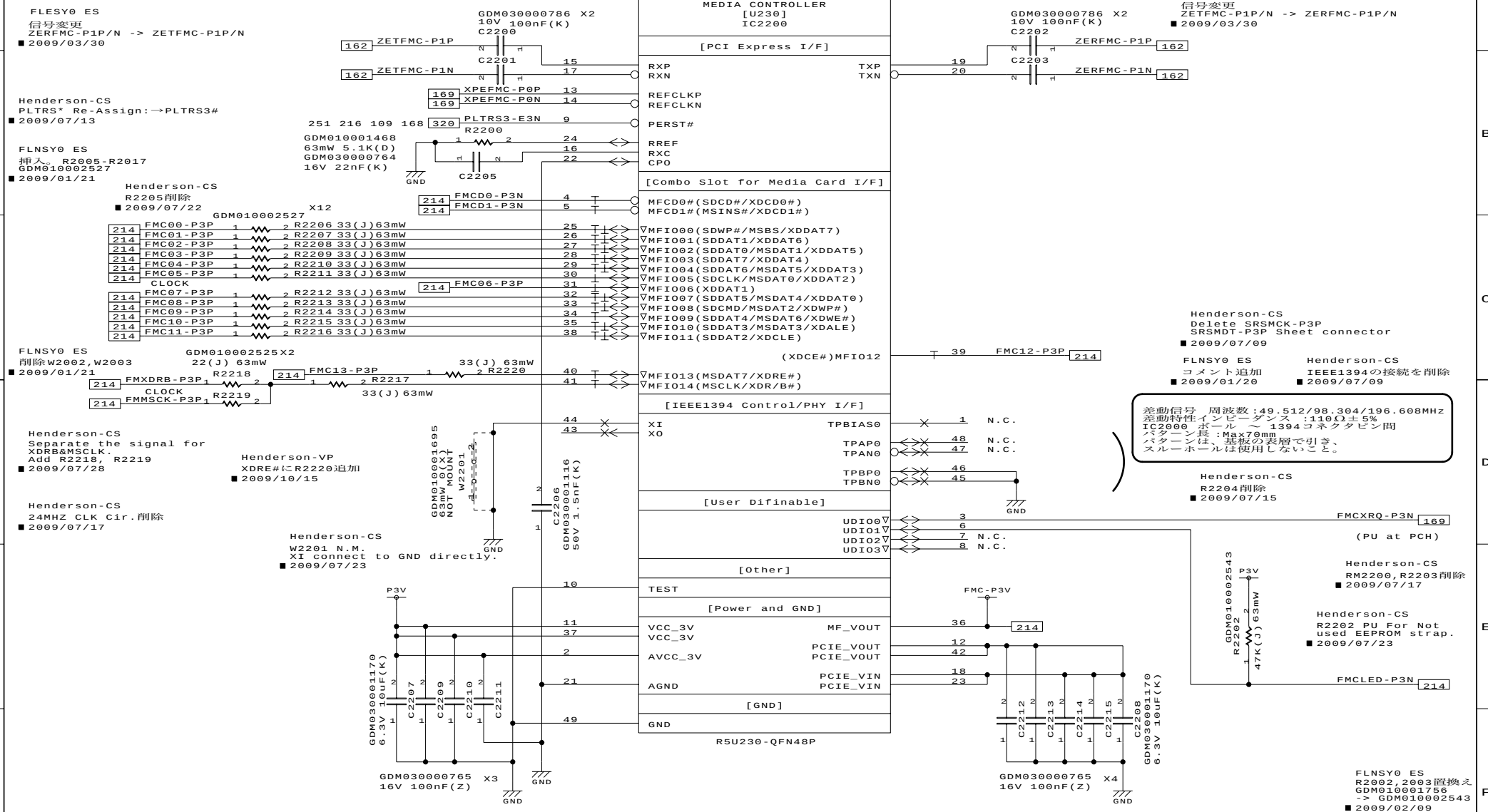
DESIGNED BY T. Ichimura/L. Yu 2009.10.15 17:09 G11	TITLE FHNSY1	FUNCTION Smart Card I/F	SH.NO. 217	PAGE NO. 048	REV.MARK 00	DRAWING.NO. 360069769
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FLNSY0 ES
IC2000 Gコードに置換え。
■ 2009/02/12

GDM460002286

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仕向け設定有

Docker	YES	NO
CN2300 C2310 R2314-R2316	Mount	Not Mount

	GM W/ Dock	GM/GL W/o Dock	PM W/ Dock
R2310-R2312 FL2310-FL2312 IC2311	Mount NotMount	NotMount NotMount	NotMount Mount

Aspen10
コネクタ GDMコードに変更
05/10/7

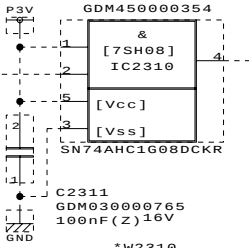
Canary10_CS:
コネクタ変更 GDM220001699→GDM220001756 06/2/23
コネクタ変更 GDM220001756→GDM220001699 06/2/28

[02] Canary10_VP:
コネクタ変更 GDM220001699→GDM220001756
(ピンスルー対応)TEMECO-0001096) 06/4/6

Henderson-CS
CN2300変更
→GDM220001756
■ 2009/06/15

表示周りのピンアサイン

Signal	pin
DRED-PXP	92
DBLUE-PXP	90
DGREEN-PXP	126
DHSYNC-P3P	113
DVSYNC-P3P	114
SCRTSC-P3P	96
SCRTSD-P3P	95

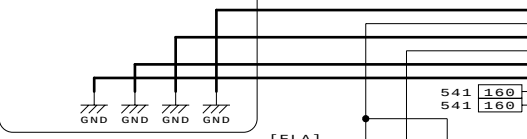


Henderson-CS
Add DOCVON# Backup Cir.
■ 2009/07/22

Malgow_CS
DOCVON# VA対応
反転ゲート削除、信号レベル・極性変更 S5N→S3P
■ 2008/02/14

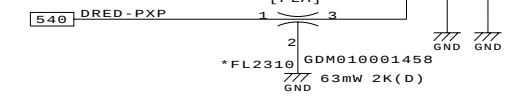
Malgow_CS
DOCVON#のシリーズ抵抗追加 (R2317)
■ 2008/03/03

Malmo_CS
GM W/ Dockの仕向けを
A-SW使用に変更
■ 2008/2/15



GPU->DOCK Connectorまで、
RGB信号の隣接層は
GNDとする。

NFW31SP207X1E4L
GDM130000213 x2

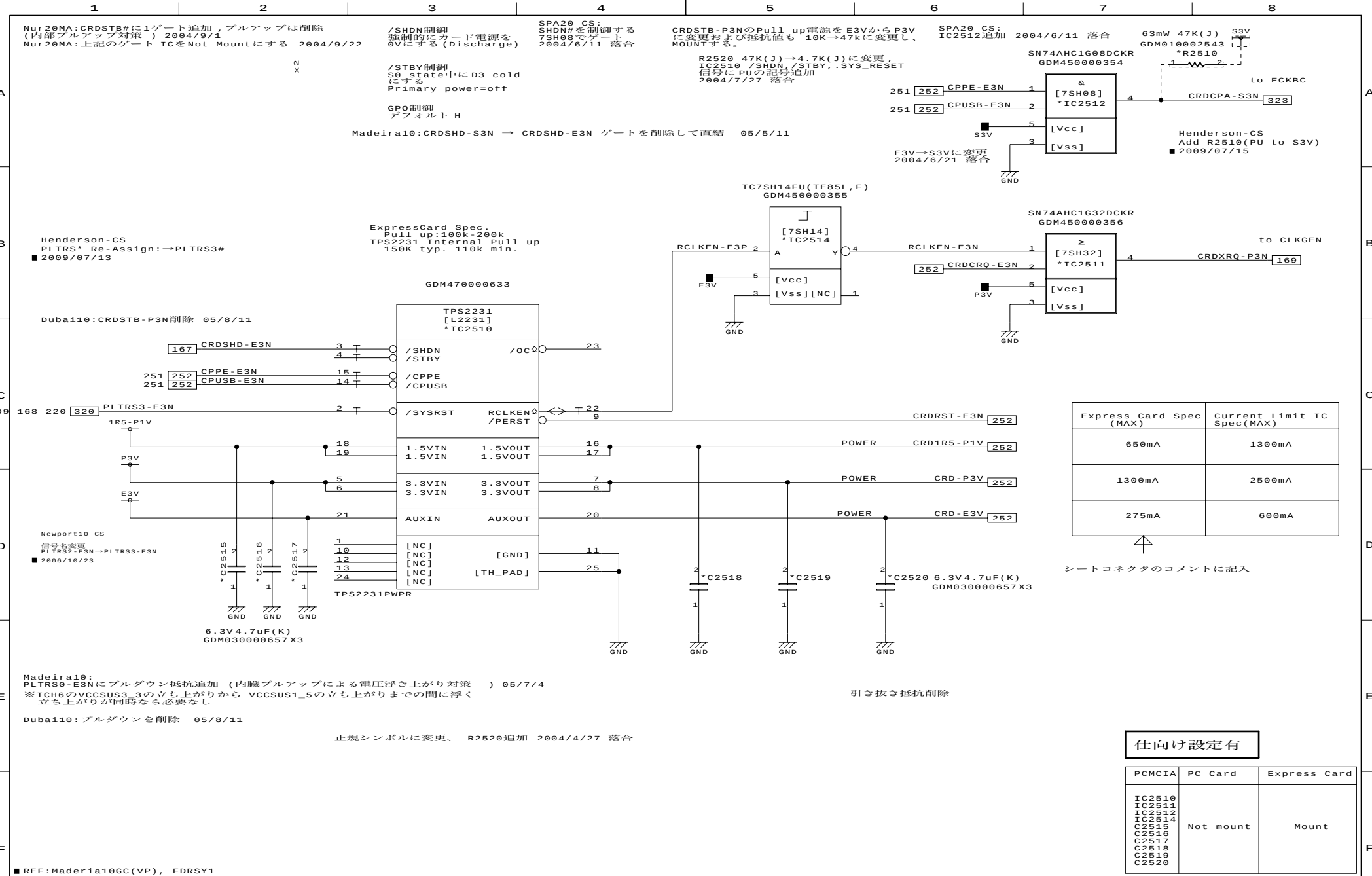


FilterのGNDパットから
最短でGND層に接続。

■ REF:Malgow(VP) . FGGIN2

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/T.Naruse	FHNSY1	DOCKING I/F 2	231	051	00	360069769

2009.10.15 17:09 G11



仕向け設定有

PCMCIA	PC Card	Express Card	NO	CS-B
R2520 R2522 RM2520 C2521 C2523 C2525 C2526 C2527 C2528	Not mount	Mount	Not mount	Mount
R2521	Mount	Not mount	Mount	Not mount
IS2520	Not mount	Mount	Not mount	Not mount

A

B

C

D

E

F

A

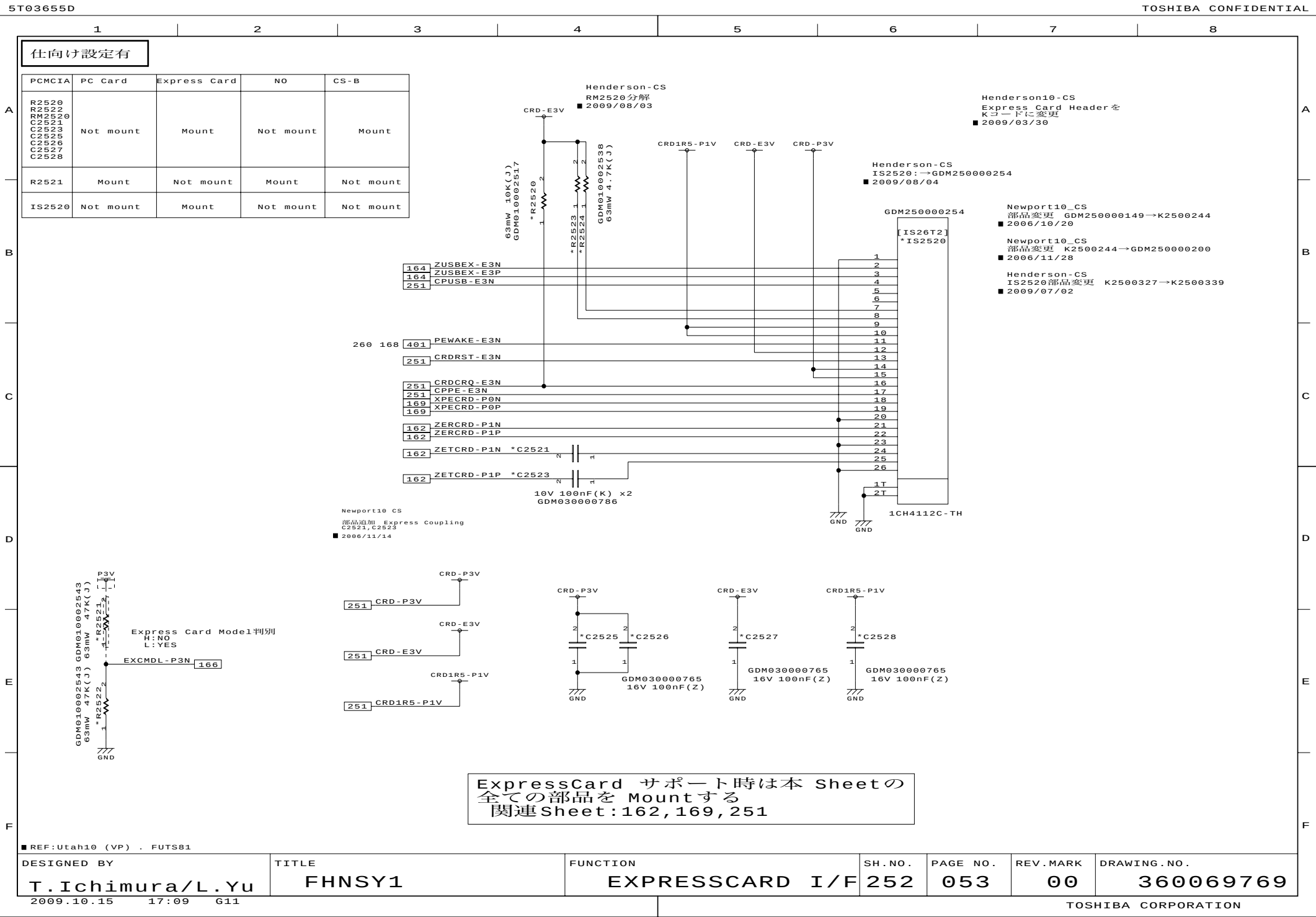
B

C

D

E

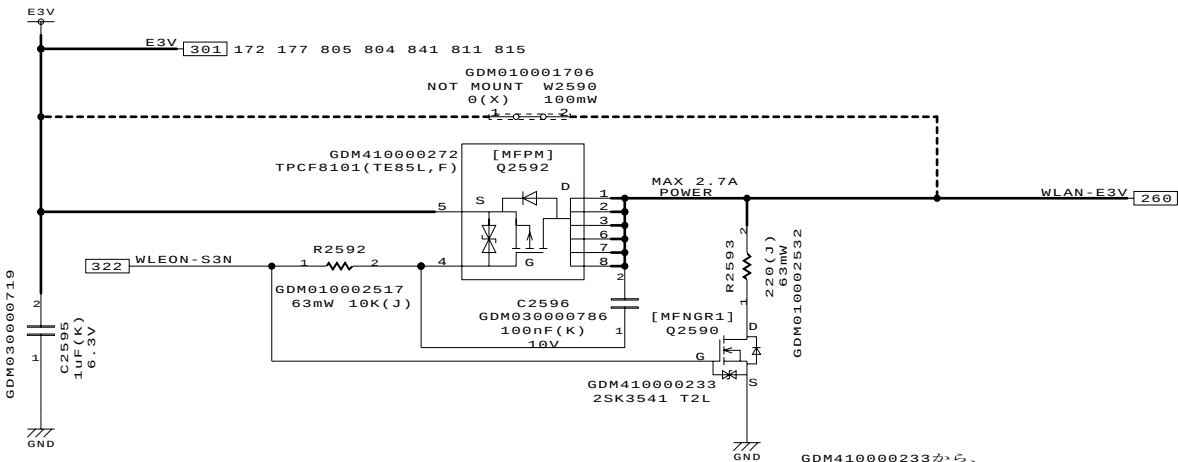
F



ExpressCard サポート時は本 Sheet の
全ての部品を Mount する
関連Sheet: 162, 169, 251

WLAN/WiMAX電源

TOSHIBA CONFIDENTIAL



GDM410000233から、
GDM410000267の2個使いへ変更
hosokawa070112a

PCI Express(TM) Mini Card Electromechanical Specification
Rev 1.0 (June 2, 2003)より

Power Rail	Voltage Tolerance	D0-D2 D3 hot Power(*)		Auxiliary Pow(**)	
		Peak(max)mA	Normal(max)mA	Peak(max)mA	Normal(max)mA
3.3Vaux	± 9%	2750	1100	1750 (wake enabled)	250(wake enabled) 5(not wake enabled)
+1.5V	± 5%	500	375	N/A	N/A

*:When available, the total power drawn by a PCI Express Mini Card function for the sum of +3.3V and 3.3Vaux shall not exceed 750mA(Normal max) and 1,000mA(Peak max)

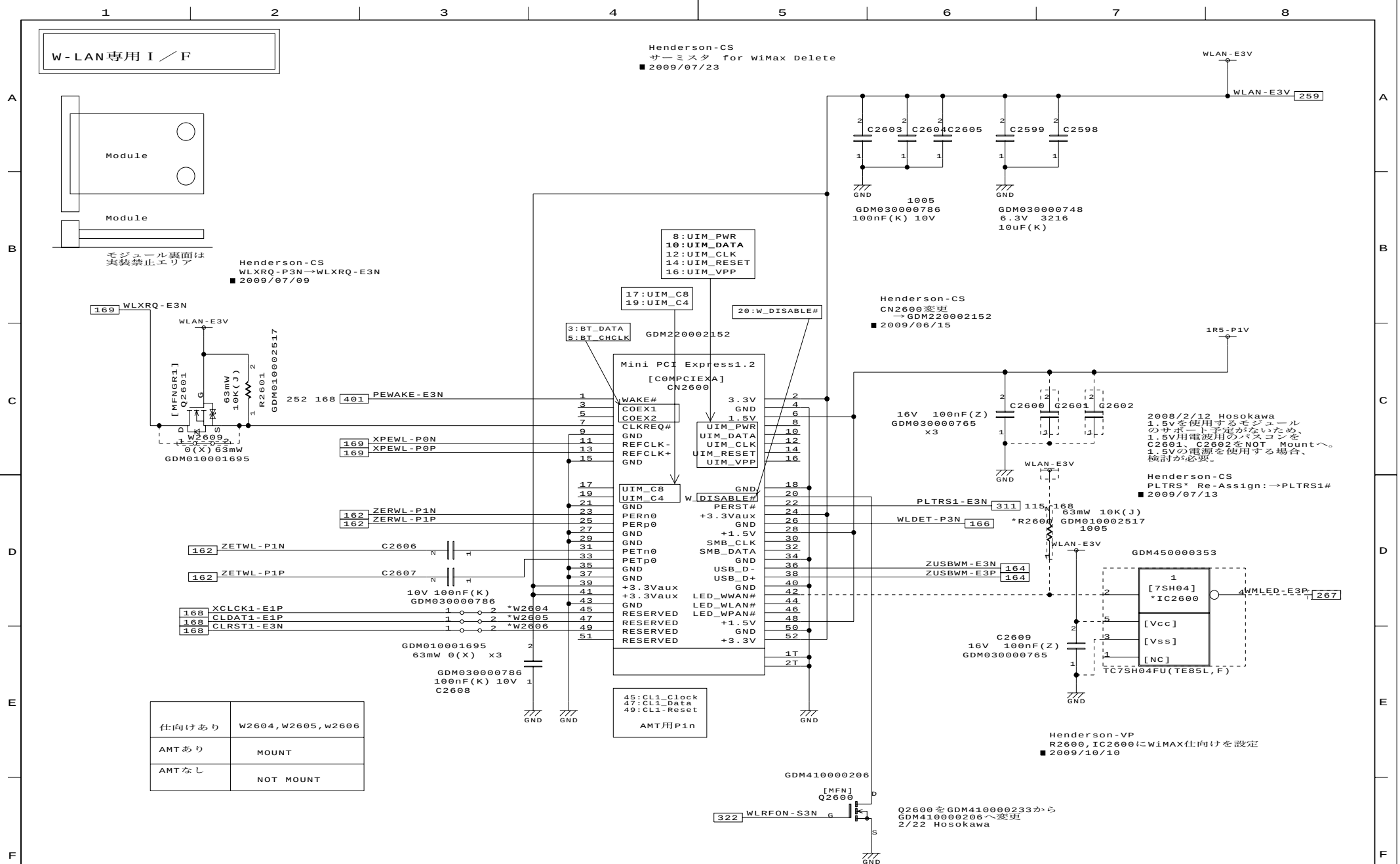
***:The auxiliary current limit only applies when the primary +3.3V and +1.5V Vvoltage sources are not available, i.e., the card is in a low power D3 state.

Definitions:
Peak - The highest averaged current value over any 10-millisecond period.
Normal - The highest averaged current value over any 1-second period.

REF: Moldau10(CS) . FMUSY0

DESIGNED BY T. Hosokawa	TITLE FHNSY1	FUNCTION WLAN/WiMAX Power	SH.NO. 259	PAGE NO. 054	REV.MARK 00	DRAWING.NO. 360069769
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2009.10.15 17:09 G11



DESIGNED BY

T. Hosokawa

TITLE

FHNSY1

FUNCTION

PCI-E MINI CARD

SH.NO.

260

PAGE NO.

055

REV.MARK

00

DRAWING.NO.

360069769

2009.10.15 17:09

TOSHIBA CORPORATION

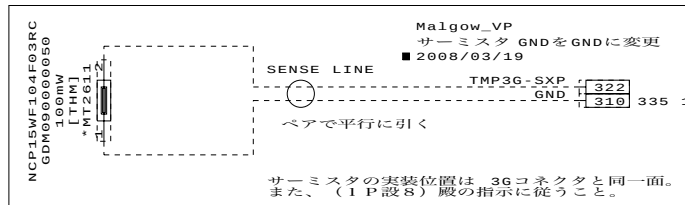
3 G Module

TOSHIBA CONFIDENTIAL

仕向け設定有

3G/Robson	YES	NO
R2610	Mount	Mount
W2614	Not Mount	Not Mount
上記部品以外全て	Mount	Not Mount

Henderson-VP
アナログ GND分離用ジャンパ削除による信号名変更
■ 2009/10/15

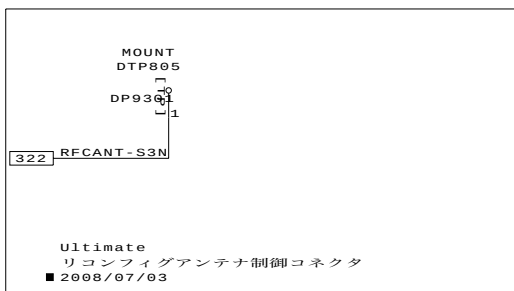


Henderson-CS
GND -> TMP3G-GND
■ 2009/07/08

Henderson-CS
2610変更
→GDM220002152
■ 2009/06/15

Gr.			
3G	有	無	
C2613, C2614 C2616, C2617 C2619, C7700 CN2610 MT2611, Q2610 Q2611, Q2612 R2611, R2612	MOUNT	NOT MOUNT	

Henderson-CS
Robson I/F削除
■ 2009/7/3



Malgow-CS
Q2613を未実装
(G-S逆転、VPで修正)
■ 2008/03/06

37, 43pinをGNDに追加
06/04/07

Malgow-CS
C2611常に実装(電波)
■ 2008/02/20

■ REF: Newport10(VP) . FNPSY1

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
S.Anwar/T.OCHIAI	FHNSY1	PCI-E MINI Card(3G)	261	056	00	360069769

2009.10.15 17:09 G11

TOSHIBA CORPORATION

8:UIM_PWR
10:UIM_DATA
12:UIM_CLK
14:UIM_RESET
16:UIM_VPP

17:UIM_C8
19:UIM_C4

20:W_DISABLE#

3:BT_DATA
5:BT_CHCLK

Mini PCI Express1.2
[COMPCIEXA]
*CN2610

WAKE#
COEX1
COEX2
CLKREQ#
GND
REFCLK-
REFCLK+
GND

UIM_C8
UIM_C4
GND
PERST#
PERn0
PERp0
GND
PBTn0
PETp0
GND
USB_D-
USB_D+
GND
+3.3Vaux
+3.3Vaux
GND
RESERVED
RESERVED
RESERVED
RESERVED

UIM_PWR
UIM_DATA
UIM_CLK
UIM_RESET
UIM_VPP

W_DISABLE#
PERST#
PERn0
PERp0
GND
SMB_CLK
SMB_DATA
GND
USB_D-
USB_D+
GND
+3.3Vaux
+3.3Vaux
GND
LED_WLAN#
LED_WLAN#
GND
RESERVED
RESERVED
RESERVED
RESERVED

AS0B226-S40T-7H

E3V
GND

GDM030000765
16V 100nF(Z)

FETをGDM410000233->GDM410000206に変更
2007/12/13 Anwar

3G用PCI-E Mini Cardソケットと
SIM CARD ソケット間のライン長は200mm以内として下さい

GDM010001706
NOT MOUNT W2614
0(X) 100mW

GDM410000272
TPCF8101(TE85L,F)

*R2611
GDM010002517
63mW 10K(J)

*C2619
GDM030000786
100nF(K)
10V

*Q2612
GDM410000233
2SK3541 T2L

Canary18 CS:
C7700(バスコン) 追加
2006/2/24

Malmo_CS
C7700削除
■ 2007/11/20

UIMPWR-E3V
UIMDAT-E3P
UIMCLK-E3P
UIMRST-E3P

GDM030000748
10uF(K) 6.3V x2

GDM030000765
100nF(Z) 16V x3

PLTRS2-E3N
MINDET-P3N

Henderson-CS
PLTRS* Re-Assign:→PLTRS2#
■ 2009/07/13

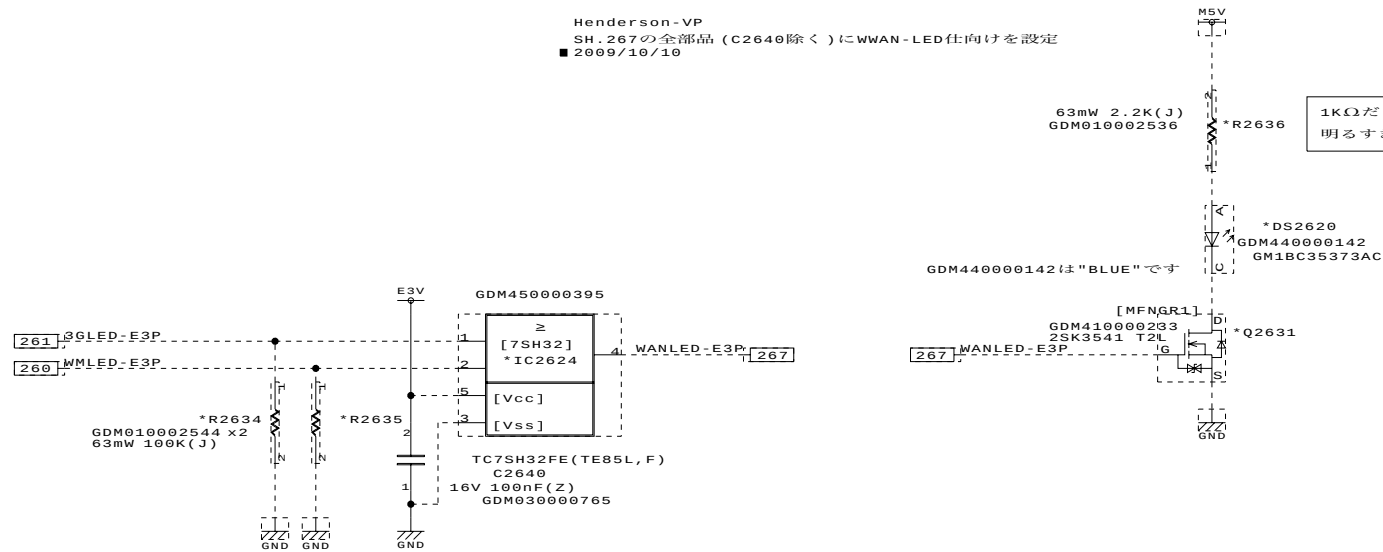
Malmo_CS
UIM-GNDを15pinから18pinに変更
■ 2007/11/20

UIM-GNDを通常のGNDにしました (アンワル)
■ 2008/1/22

UIM-CLK&UIM-Data
UIM-CLKとUIM-Dataの信号はできるだけ (信号ライン2分以上) 離して
(間に GND パターンを挟んで) 引いて頂きたい。(アンワル)
■ 2008/1/22

GDM450000391
[7SH02]
*IC2610
[Vcc]
[Vss]

Henderson-CS
リファレンス変更 :C2620→C2612(重複)
■ 2009/08/04



Ref:Wengen CS

DESIGNED BY	2009/10/15	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
H.Gushiken		FHNSY1	3G(WWAN) LED	267	057	00	360069769

2009.10.15 17:09 G11

TOSHIBA CORPORATION



TOSHIBA CORPORATION

(SPI_Part No:64Mbit/GDM460002384
32Mbit/GDM460001975)

~~Dual Footprint~~要検討

Henderson-CS
IC3000をGコード(生ROM)に変更
■ 2009/08/01

	Ibex Peak	ME Function
16Mbit	PM55	(ME Ignition FW)
32Mbit	HM55, PM57, HM57	PAVP1.5, AT-p, Braidwood
64Mbit	PM57, HM57	PAVP1.5, AT-p, Braidwood, RPAT for Consumer
	QM57	PAVP1.5, AT-p, Braidwood, vPro 6.0 with RPAT, AT-d

仕向け設定有

MDC	有り	無し
CN3010 R3010, R3011 C3011, C3012 IC3010	Mount	Not Mount
L3010 C3010	Not Mount	

Henderson10GC-VP
WebCam用フィルタのレイアウト・スペース確保の為、
■ 2009/10/10 L3010を削除

Hamilton10 CS
CN3010をコネクタリスト
Rev01にあわせる
GDM220001530→GDM220001498
■ 2006/9/26

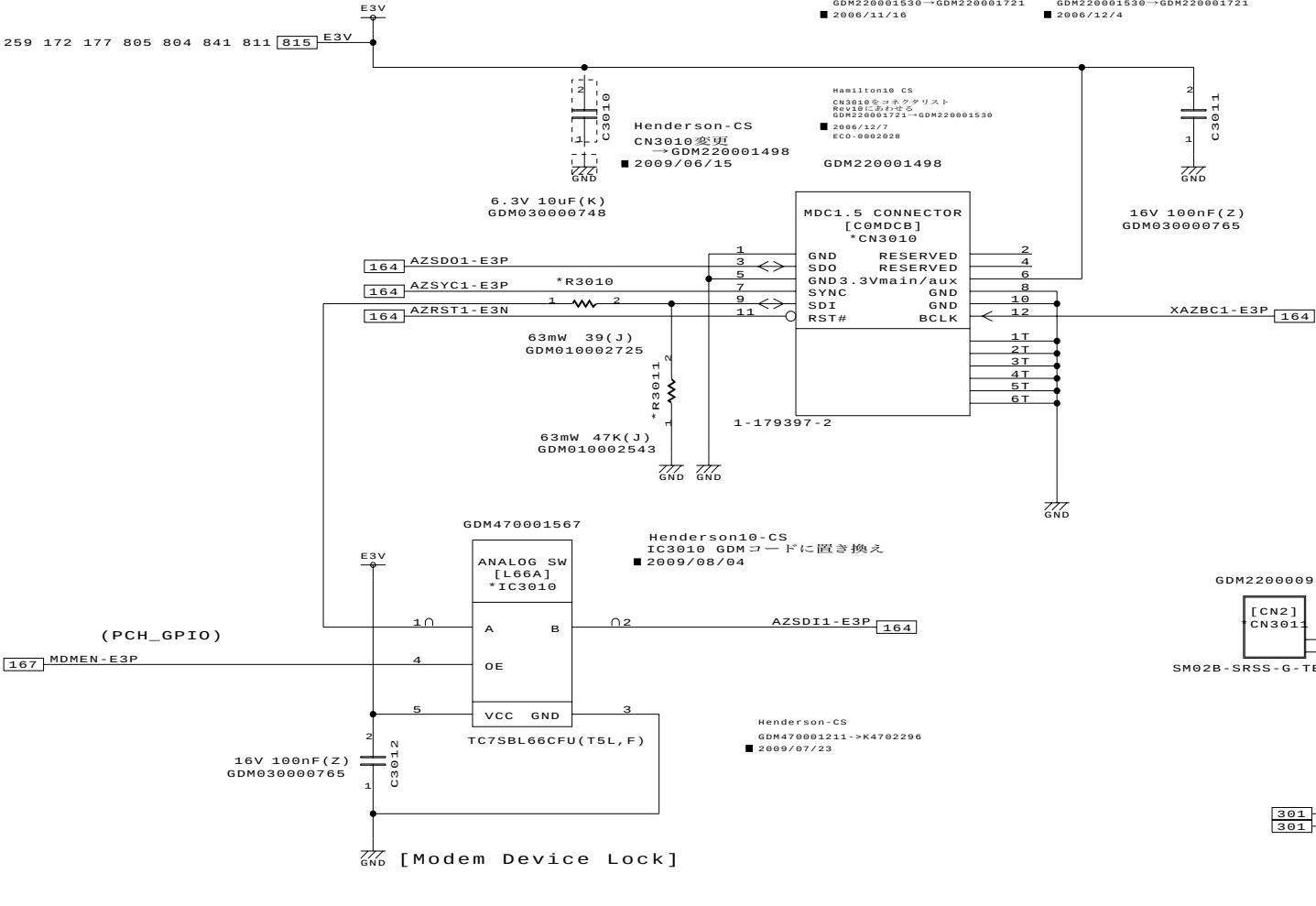
Hamilton10 CS
CN3010をコネクタリスト
Rev02にあわせる
GDM220001498→GDM220001721
■ 2006/10/24

Hamilton10 CS
CN3010をコネクタリスト
Rev03にあわせる
GDM220001721→GDM220001530
■ 2006/11/2

Hamilton10 CS
CN3010をコネクタリスト
Rev06にあわせる
GDM220001530→GDM220001721
■ 2006/11/16

Hamilton10 CS
CN3010をコネクタリスト
Rev07にあわせる
GDM220001721→GDM220001530
■ 2006/11/24

Hamilton10 CS
CN3010をコネクタリスト
Rev08にあわせる
GDM220001530→GDM220001721
■ 2006/12/4



■ REF: Santa Rosa Base . FSRBS0

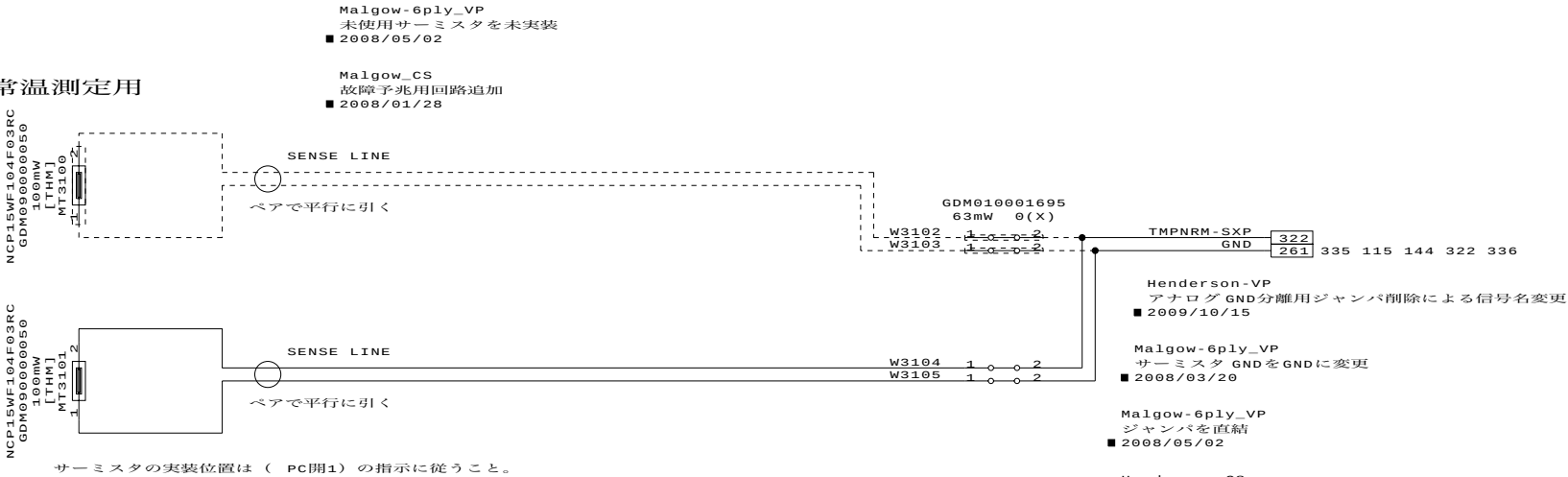
DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
T. Ichimura	FHNSY1	MDC I/F	301	059	00	360069769

2009.10.15 17:09 G11

故障予兆

配置候補地 3

常温測定用



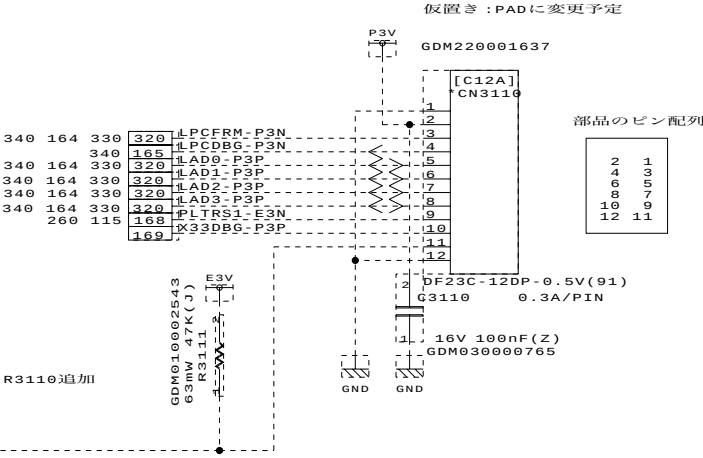
仕向け設定有

Henderson-CS
PLTRS* Re-Assign:→PLTRS1#
■ 2009/07/13

Malgow_VP
R1651をSH.163から移動 ,R3110追加
■ 2008/04/08

167 FTDET-E3N

Henderson-CS
Delete R3110
N.M.R3111,PU:P3V→E3V
FTDET-P3N→FTDET-E3N
■ 2009/07/15



■ REF:Malgow(VP) . F66IN2

DESIGNED BY

T.Iwaki/T.Ichimura

2009.10.15 17:09 G11

TITLE

FHNSY1

FUNCTION

F/T PJ

SH.NO.

311

PAGE NO.

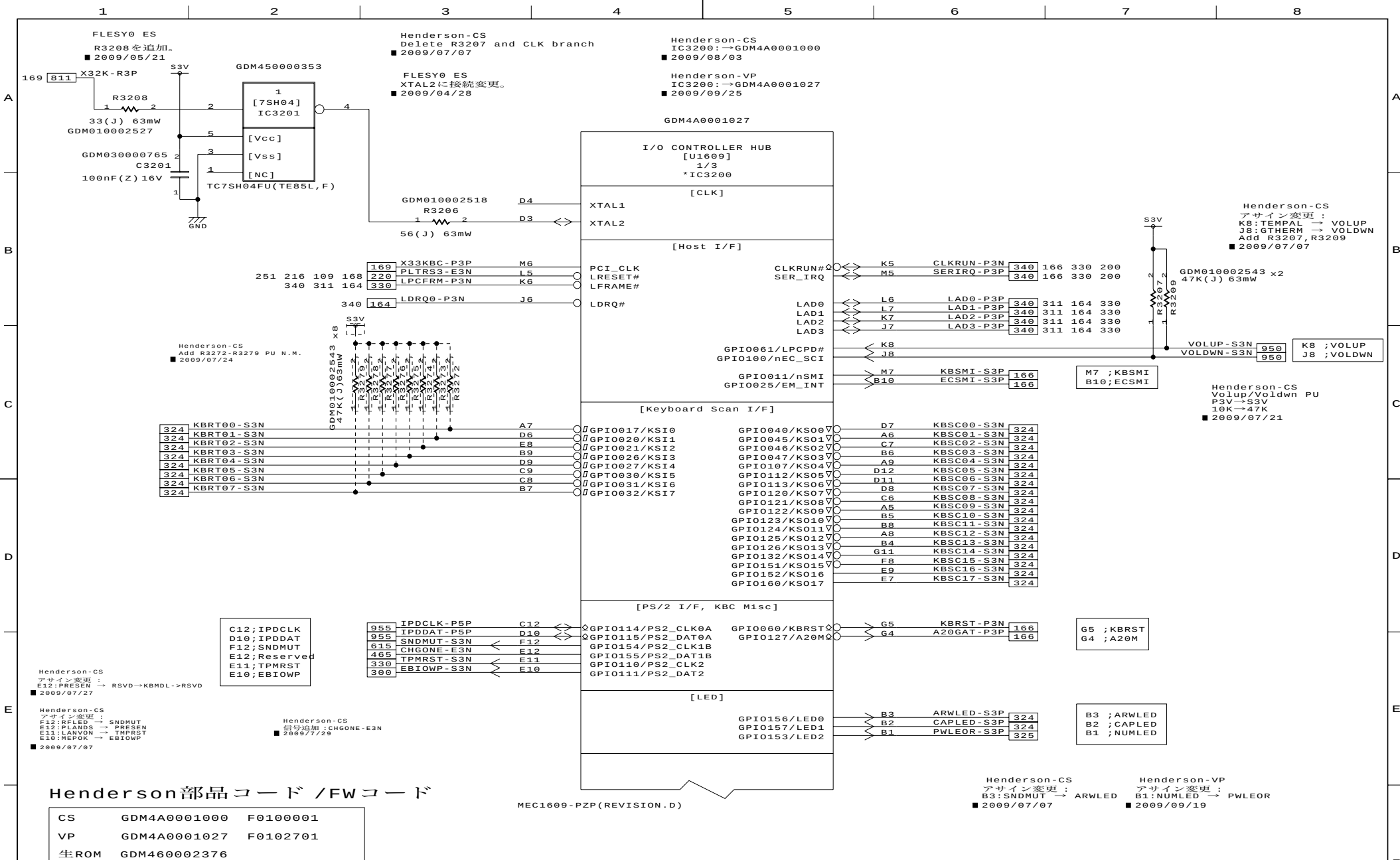
061

REV.MARK

00

DRAWING.NO.

360069769



DESIGNED BY

Ling.Yu

2009.10.15

17:09

G11

TITLE

FHNSY1

FUNCTION

EC/KBC(1)

SH.NO.

320

PAGE NO.

062

REV.MARK

00

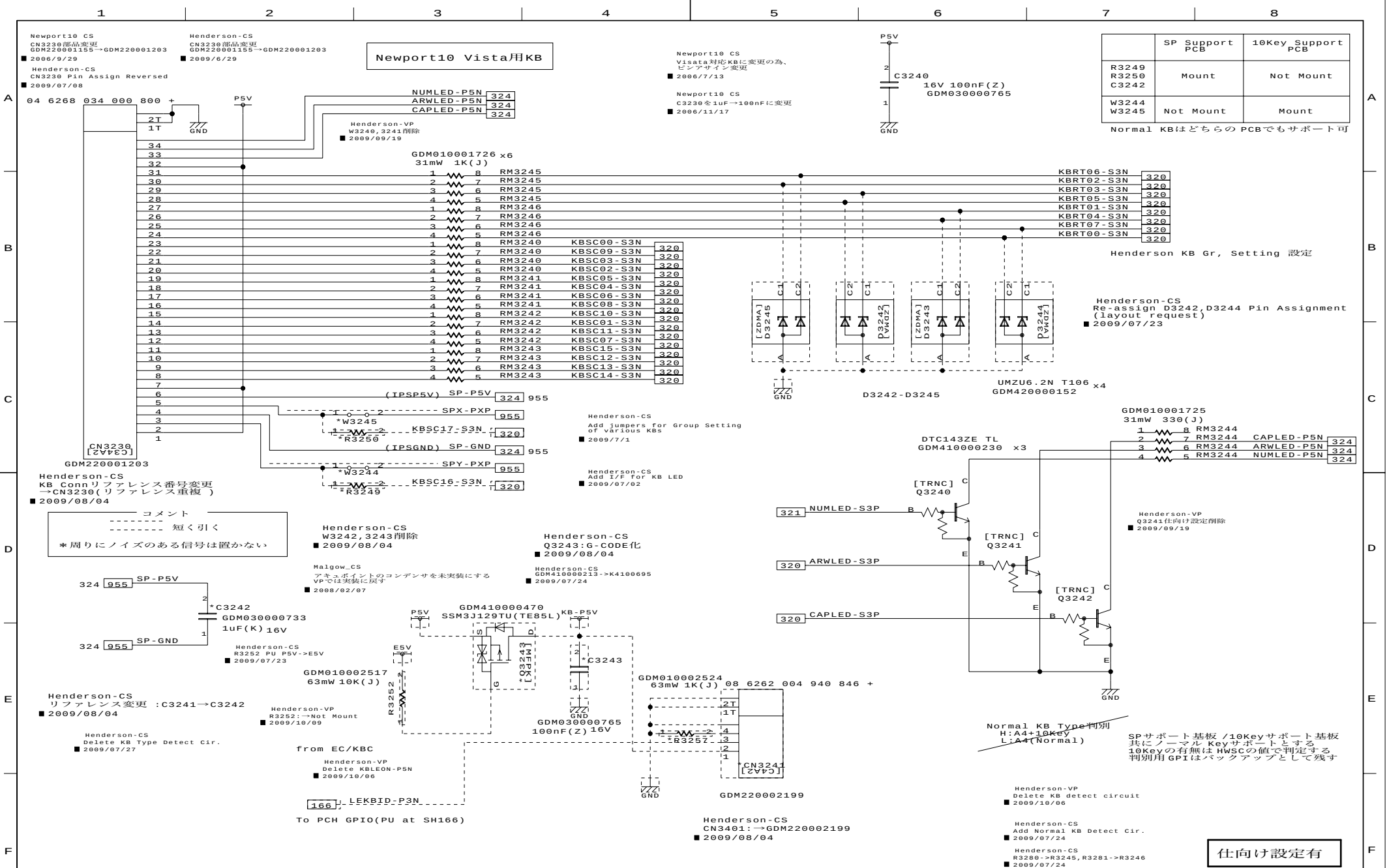
DRAWING.NO.

360069769

TOSHIBA CORPORATION





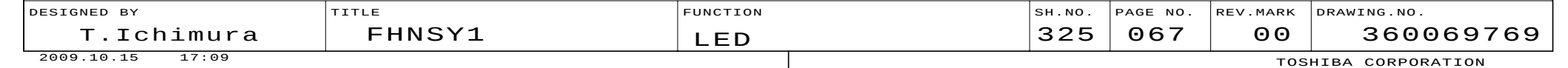


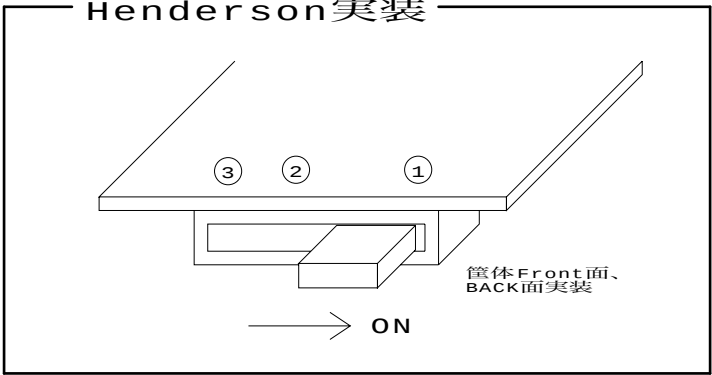
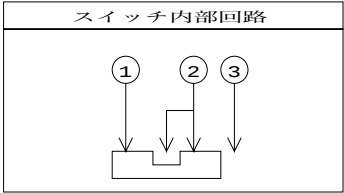
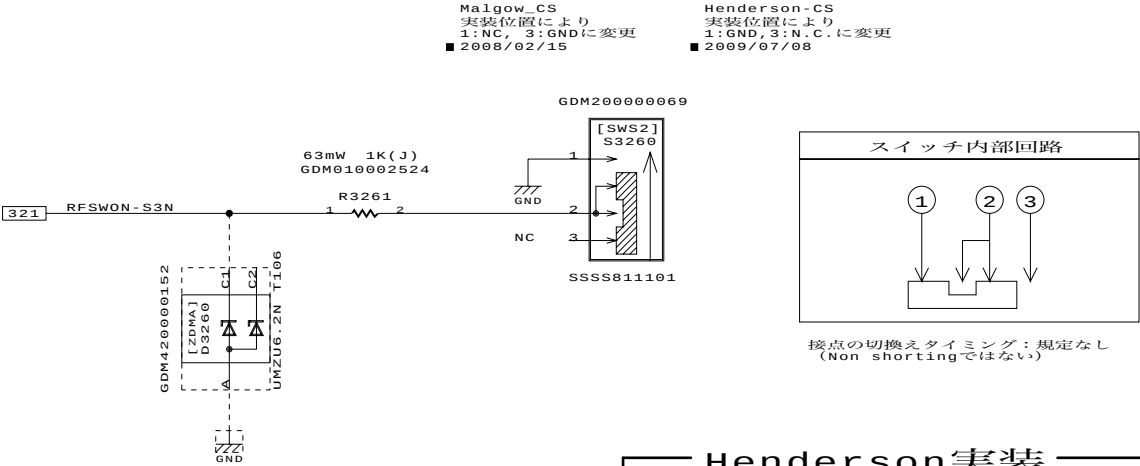
■ REF:Malgow (VP) . FGGIN2

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura/L.Yu	FHNSY1	KB I/F	324	066	00	360069769

2009.10.15 17:09 @Date

TOSHIBA CORPORATION





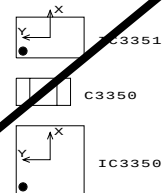


仕向け設定有

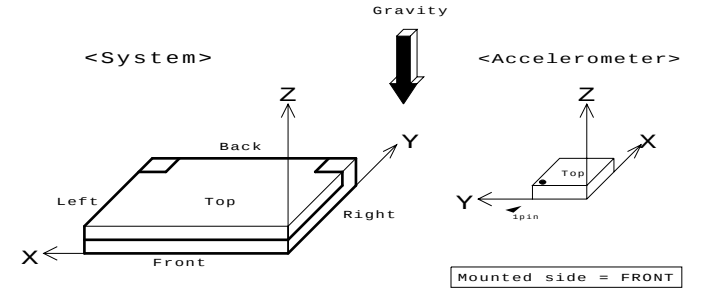
Place FRONT side
Spot and direction of accelerometer depend on (1P-SETSU8)
Don't place nearby exothermic parts for avoid thermal drift

FRONT面に実装。
配置する場所／向きは (1P設8) 指示に従うこと。
温度ドリフトしやすい部品なので、発熱部品の近くへ配置しないこと。

Place them like follow drawing
図のように配置



静的加速度の座標軸



Malgow-DDR3_CS
Kionixを削除し、 FreescaleをSH.336に追加
■ 2009/01/30

Malgow-Andalusia_CS
加速度センサ判別信号追加
L:STMicro
H:Kionix
■ 2008/04/12

Malgow-Andalusia_CS
AXIS-GND→GNDに変更
■ 2008/03/31

Henderson-CS
加速度センサ Delete.
■ 2009/07/08

Malgow-DDR3_CS
IC3550:GDM470001458に変更
■ 2009/01/27

Malgow-DDR3_CS
W3350を削除し、 9PinをS3Vに直結
■ 2009/01/29

ACCEL[X,Y,Z]-SXPは、 EC/KBC側のシートに
コンデンサを設置すること

CAUTION (Circuit)

ACCEL[X,Y,Z]-SXP must have
capacitors near by EC/KBC (SH.32x).

Value of capacitors
(depend on accelerometer)

100nF(K) at Kionix products
(Rout=32Kohms)

33nF(K) at STMicroelectronics products
(Rout=110Kohms)

EC/KBCのVREFピンまでを
できるだけ最短に！

PD(Power Down)
0: normal mode
1: Power-Down mode

FS(Full Scale selection)
0: 2g Full-scale
1: 6g Full-scale

ST(Self Test)
0: normal mode
1: Self-test

PD, STは内部プルダウンされていないので、
レベル不定状態が発生しないように
外部でプルアップ/ダウンすること

261 310 115 144 322 336 GND
(AXIS-GND)

Henderson-VP
アナログ GND分離用ジャンパ削除による信号名変更
■ 2009/10/15

DESIGNED BY

H.Nishioka/T.Ichimura

TITLE

FHNSY1

FUNCTION

ACCELEROMETER (FRONT)

SH.NO.

335

PAGE NO.

070

REV.MARK

00

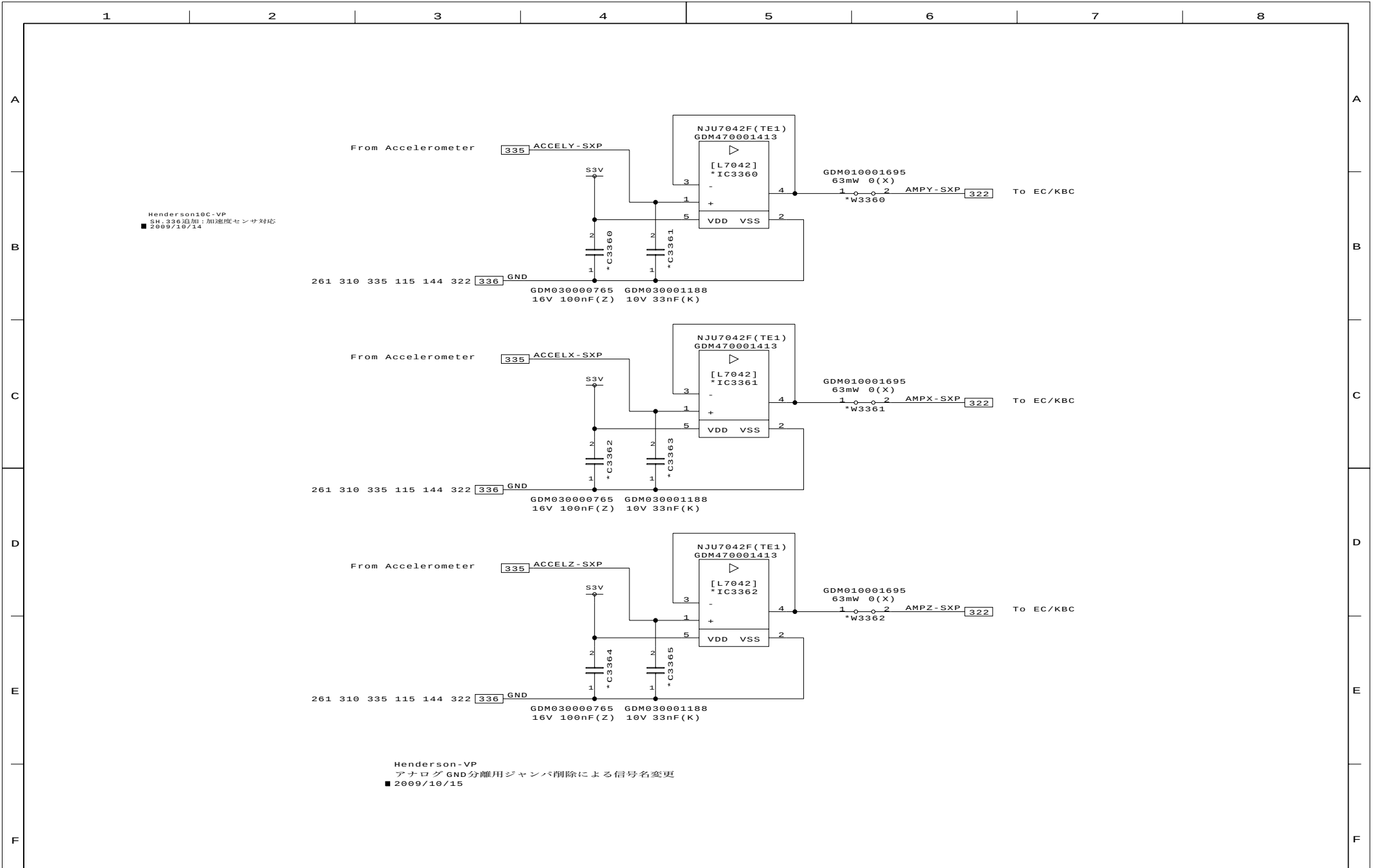
DRAWING.NO.

360069769

2009.10.15 17:09 G11

TOSHIBA CONFIDENTIAL

TOSHIBA CORPORATION



12345678

仕向け設定あり

A

A

B

B

C

C

D

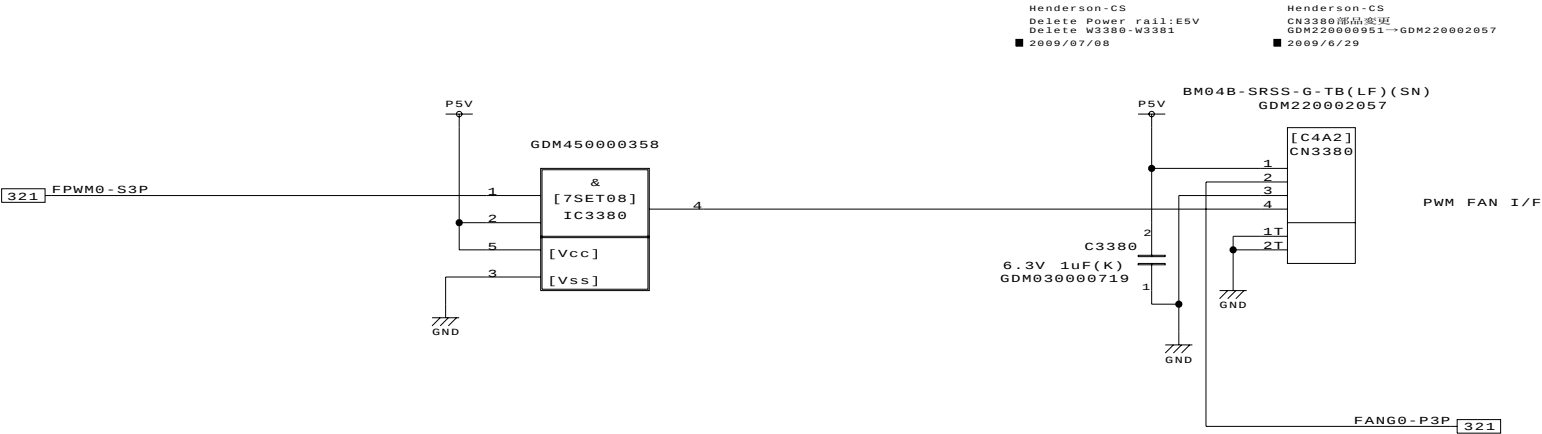
D

E

E

F

F



- ～チェック～
- ☐ FAN電圧 [5.0V]
 - ☐ コネクタ
 - ☐ PWM信号電圧

FPWM	3.3V	5.0V
R8771	5-8k(D)	10k(D)
R8772	22k(D)	10k(D)

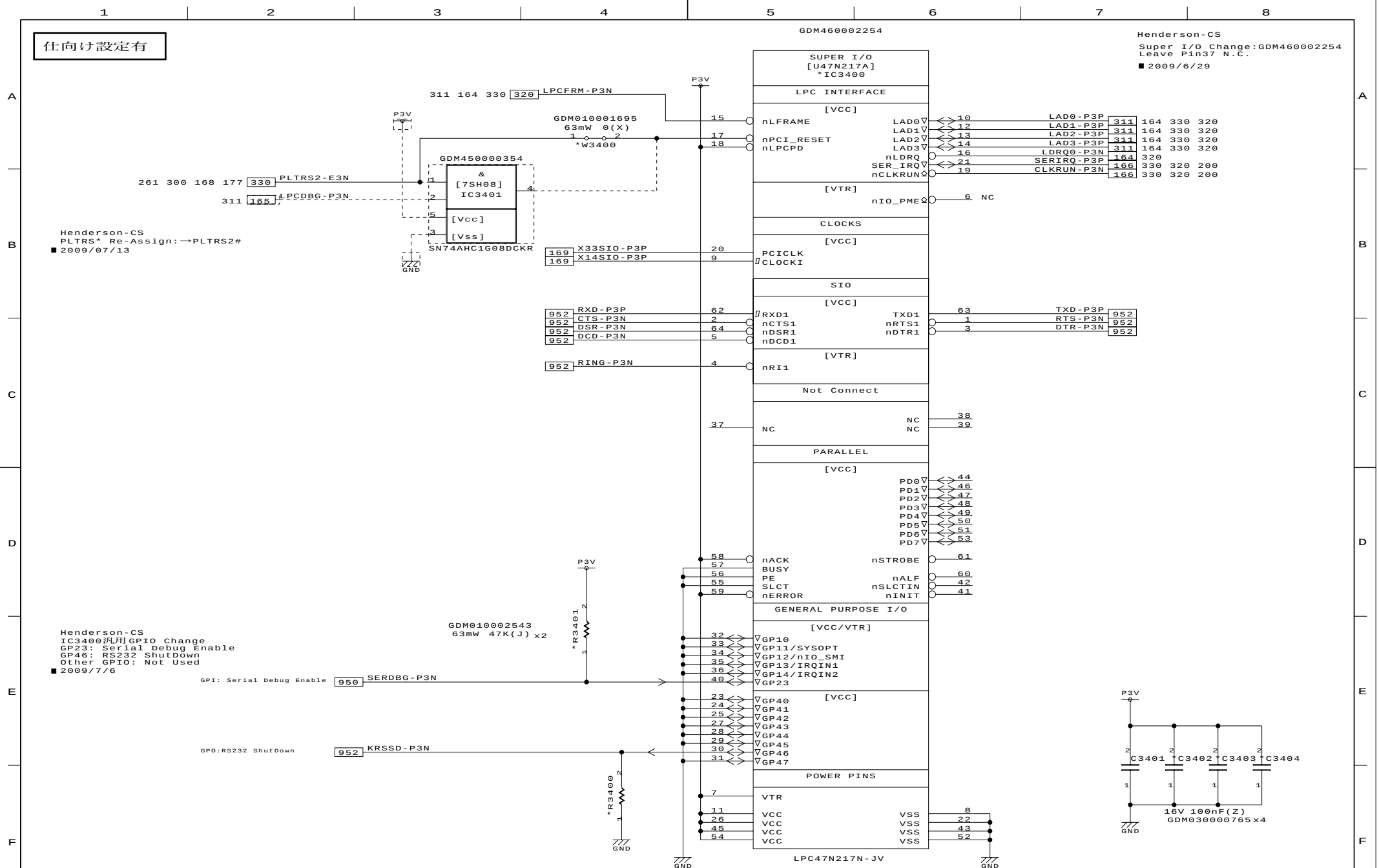
仕向け設定

Group	AMTサポート	AMT非サポート
W3380	Mount	Not Mount
W3381	Not Mount	Mount

REF:Montevina Platform TS (TS2) . FPC9T1

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Ling.Yu	FHNSY1	FAN I/F	338	072	00	360069769

2009.10.15 17:09 G11



DESIGNED BY

T. Ichimura/L.Yu

2009.10.15 17:09

TITLE

FHNSY1

FUNCTION

SUPER I/O

SH.NO.

340

PAGE NO.

073

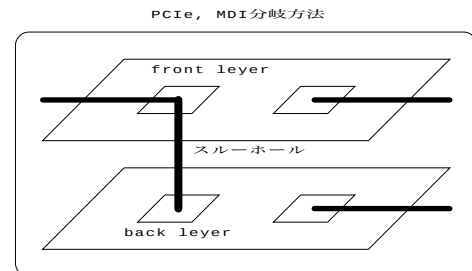
REV.MARK

00

DRAWING.NO.

360069769

TOSHIBA CORPORATION

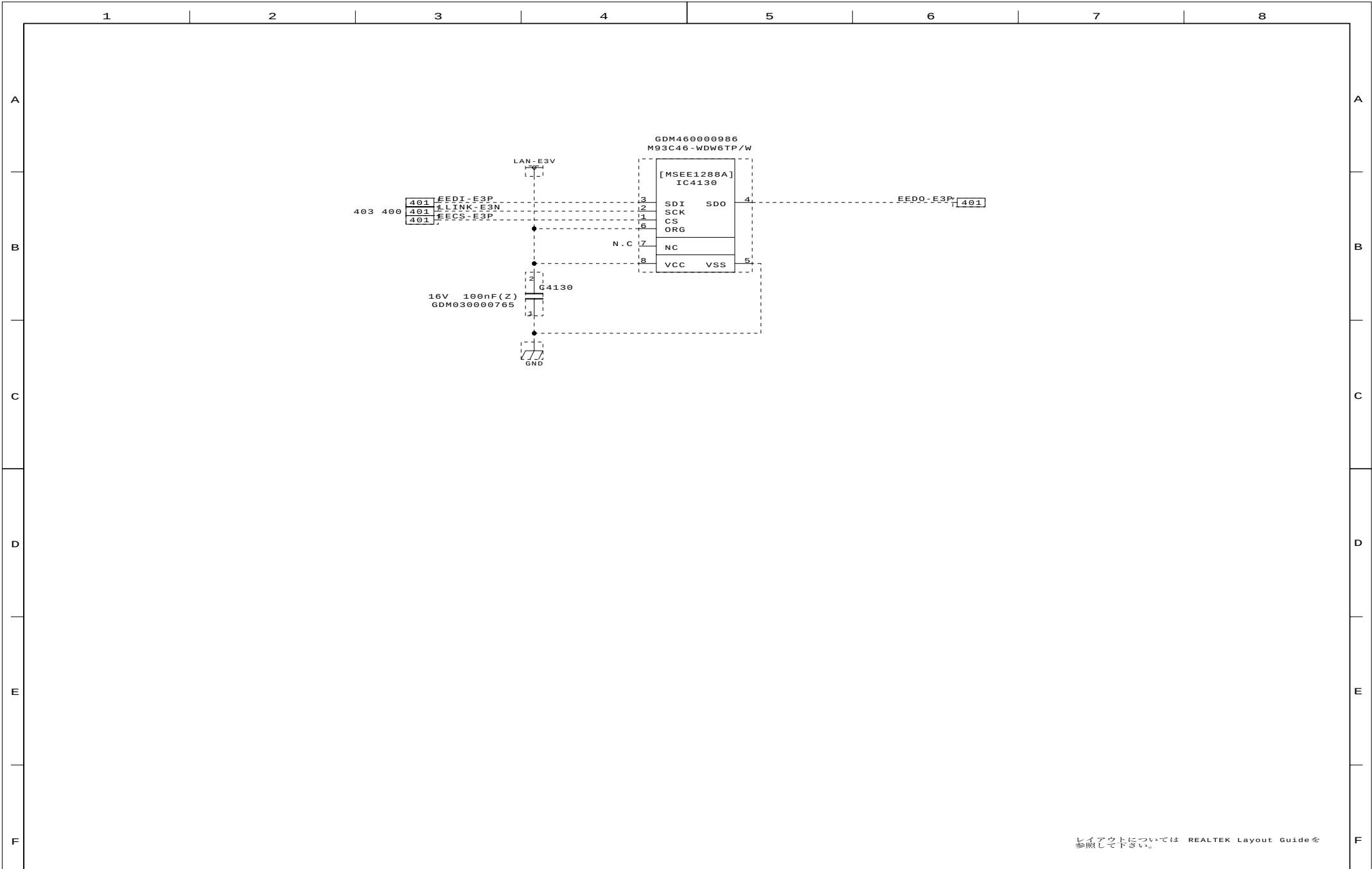


レイアウトについては Calpella Platform Design Guideを参照して下さい。

SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
400	074	00	360069769

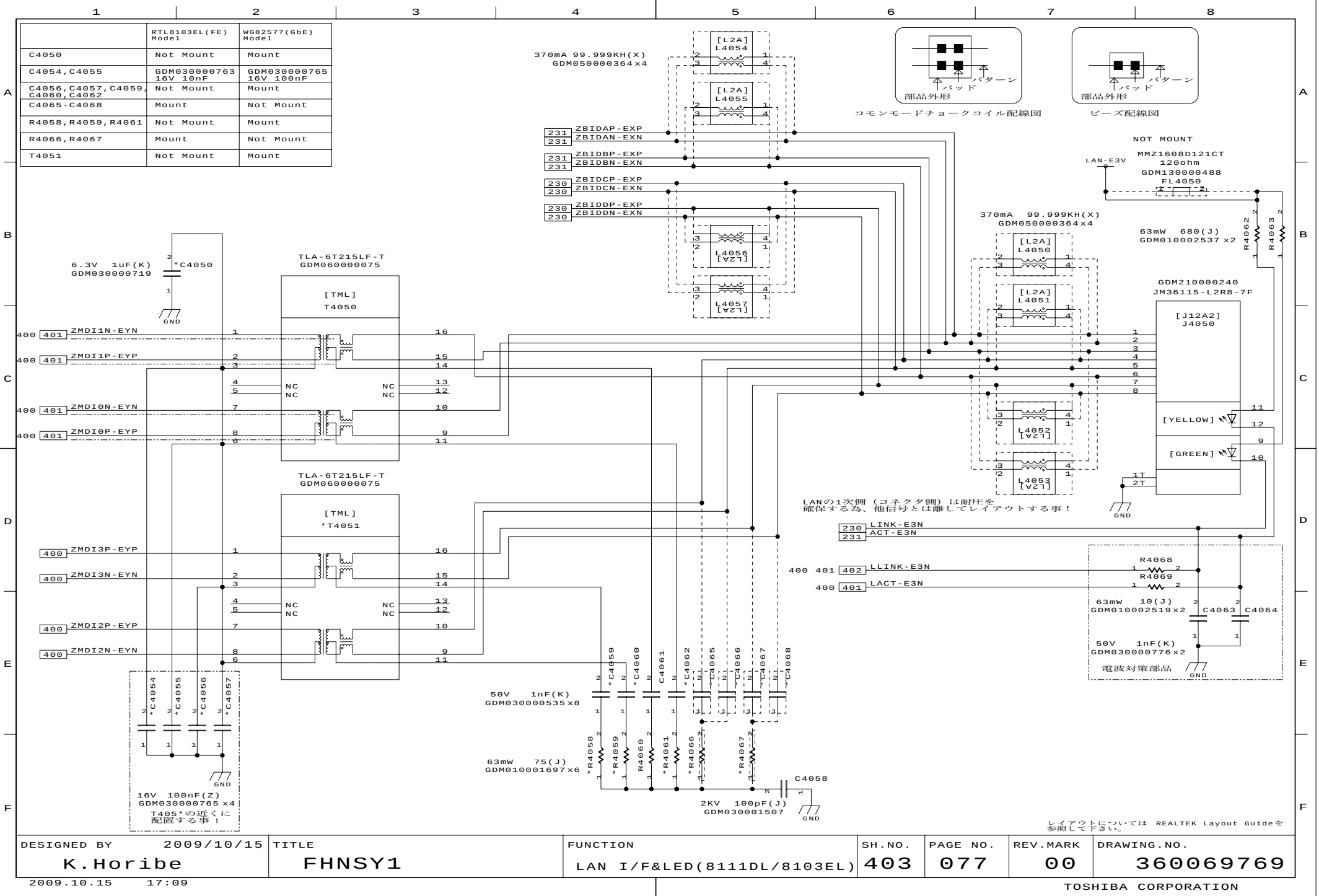


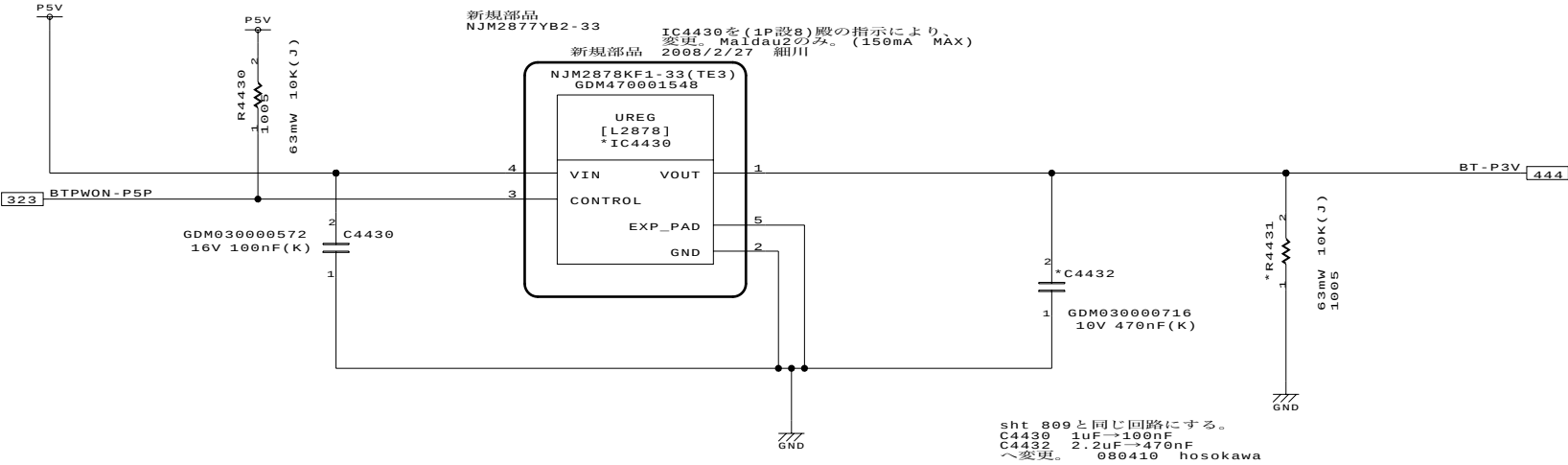
2009.10.15 17:09



レイアウトについては REALTEK Layout Guideを
参照して下さい。

DESIGNED BY	2009/10/15	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
K.Horibe		FHNSY1	LAN EEPROM(8103EL)	402	076	00	360069769
2009.10.15	17:09						TOSHIBA CORPORATION





BTはリセット信号のみで制御する

REF: Moldau2/Sierra10(VP) . FMTSY1

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Hosokawa	FHNSY1	Bluetooth Power 443	443	078	00	360069769

12345678

A

A

B

B

C

C

D

D

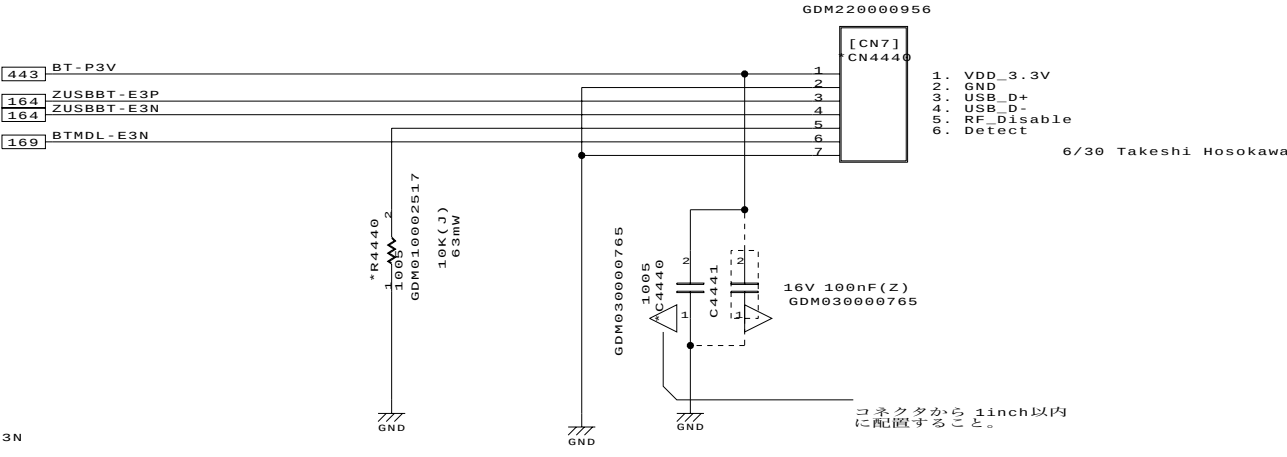
E

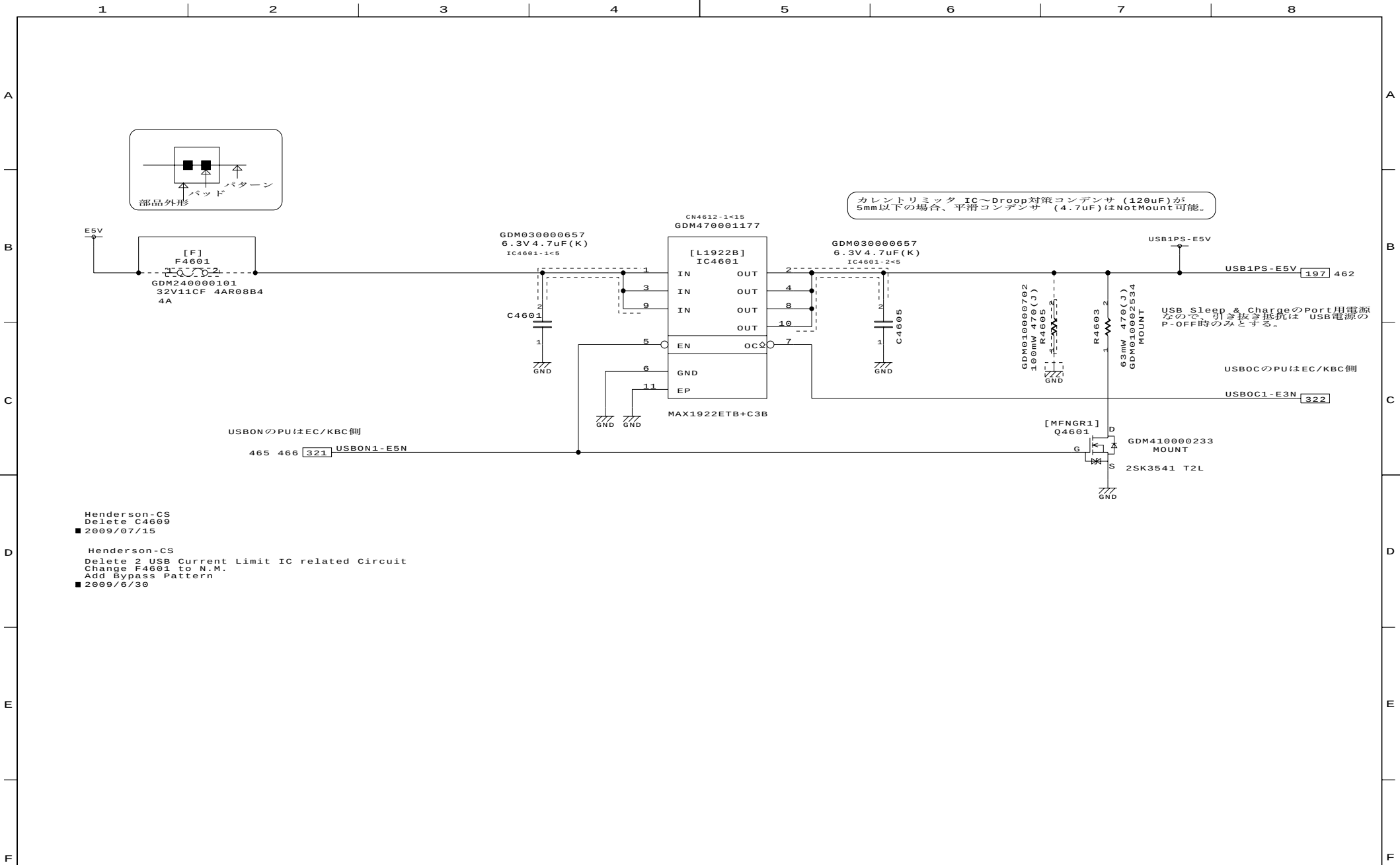
E

F

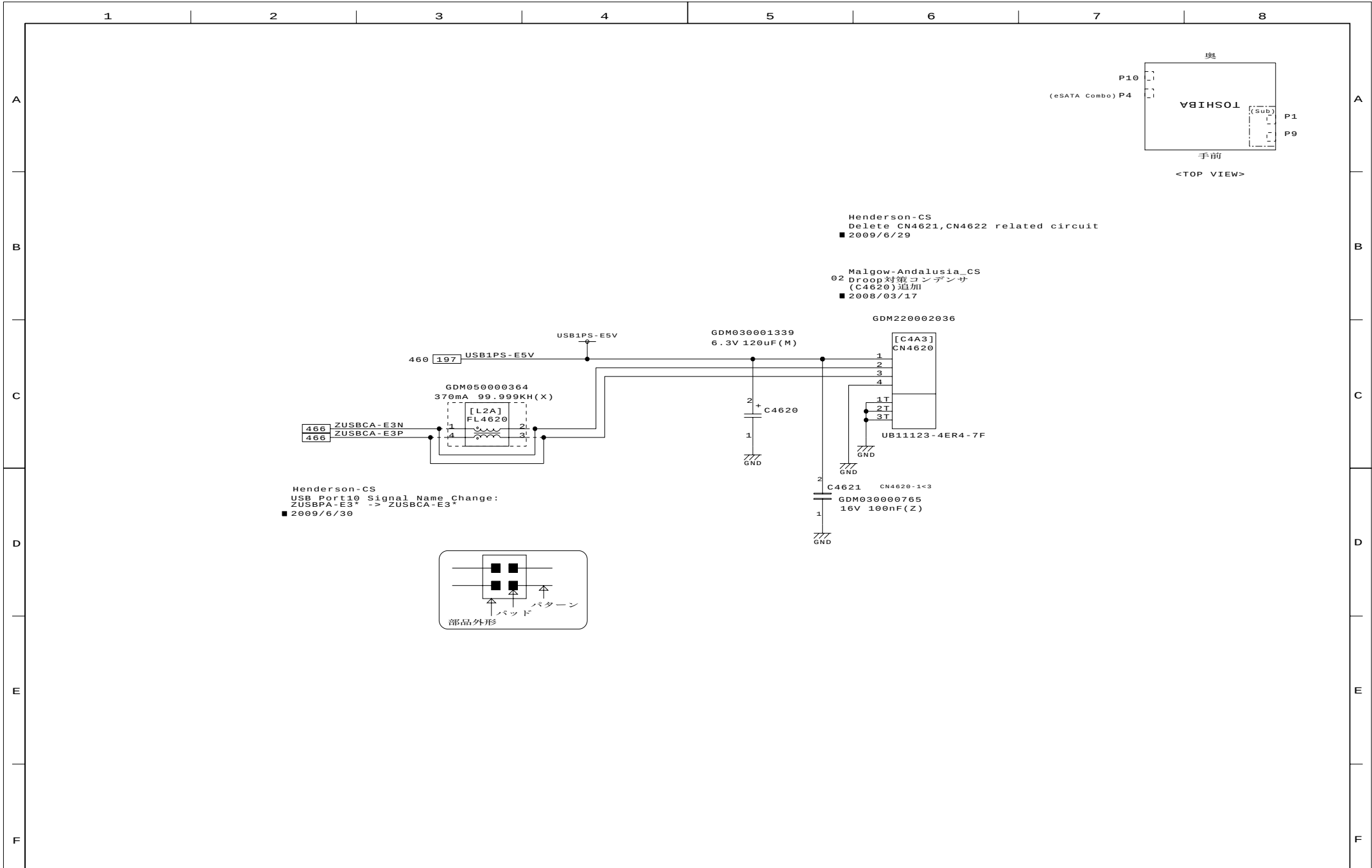
F

BTMDL#信号	
L	Bluetooth基板あり
H	Bluetooth基板無し





DESIGNED BY Ling.Yu		TITLE FHNSY1	FUNCTION USB POWER	SH.NO. 460	PAGE NO. 080	REV.MARK 00	DRAWING.NO. 360069769
2009.10.15 17:09 G11		TOSHIBA CORPORATION					



DESIGNED BY T.Ichimura	TITLE FHNSY1	FUNCTION USB I/F	SH.NO. 462	PAGE NO. 081	REV.MARK 00	DRAWING.NO. 360069769
2009.10.15	17:09	TOSHIBA CORPORATION				

仕向け設定有

USB Port8

特定Device用 or Dedicated Charger

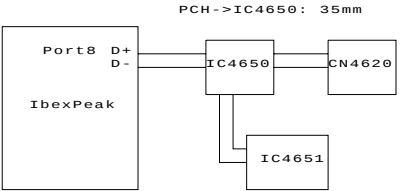
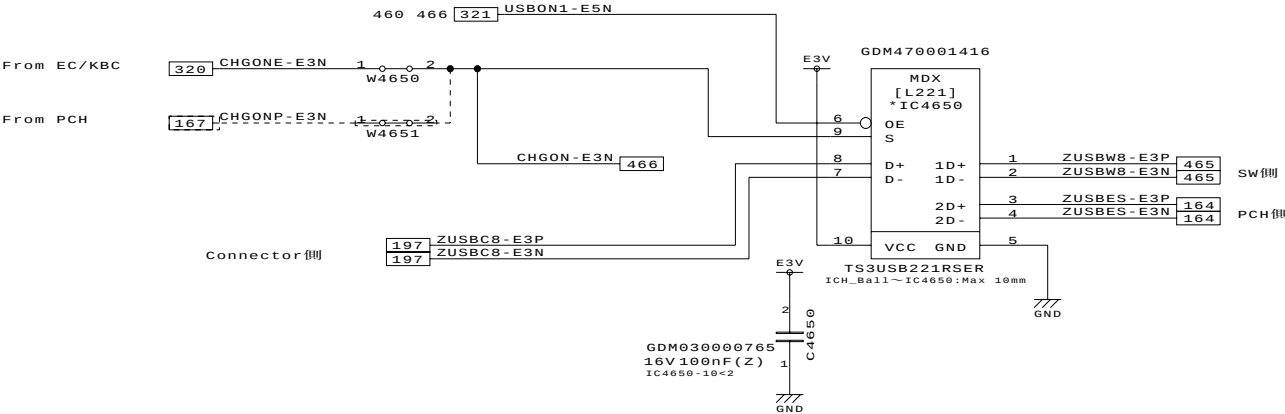
Henderson USB Charge Circuit Layout Guide

Henderson-CS
信号名変更:
USB5C-E3N→CHGONP-E3N
USBDC-E3N→MOD3EN-E3N
USB1P-E3N→MOD4EN-E3N
■ 2009/7/29

Henderson-CS
USB_Charger_ON信号制御元
切り替え用ジャンプ追加
■ 2009/7/29

Cassiopeia10 CS
Delete W4650,W4651
■ 2008/08/01

可能ならば USB0N0-E3Nに

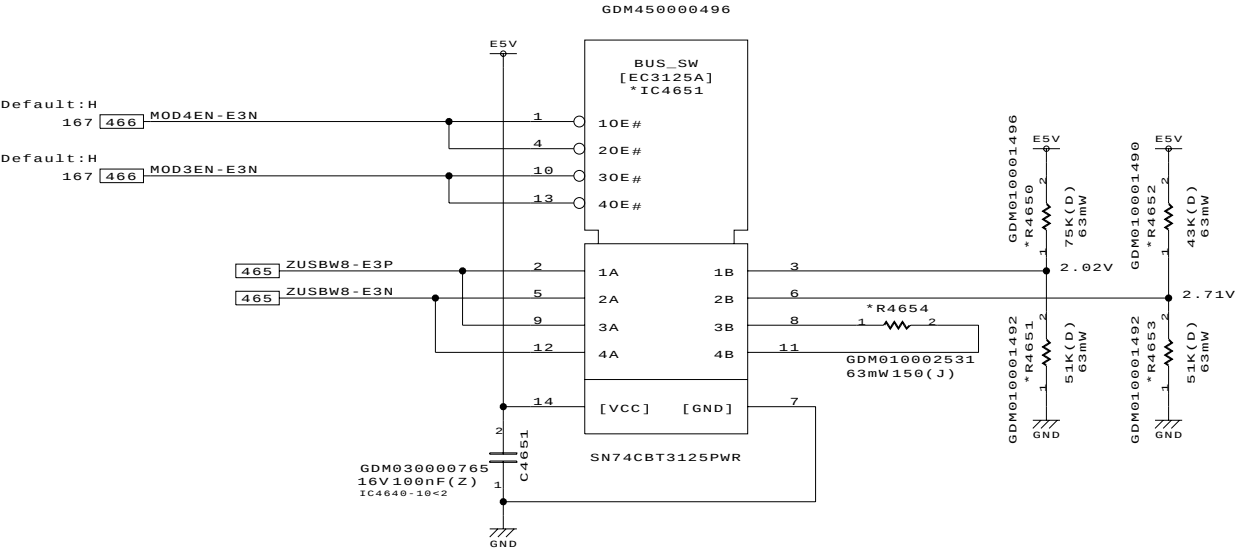


PCH->IC4650: 35mm

S	OE#	Function
X	H	Disconnect
L	L	D=1D
H	L	D=2D

Henderson-CS
USB Port4 Signal Name Change
■ 2009/7/3

Cassiopeia10 CS
USB Port0 Signal Name Change
■ 2008/07/28

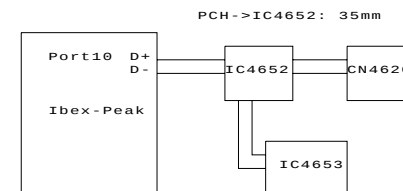


■ REF:Cassiopeia10(VP) . FCASY1

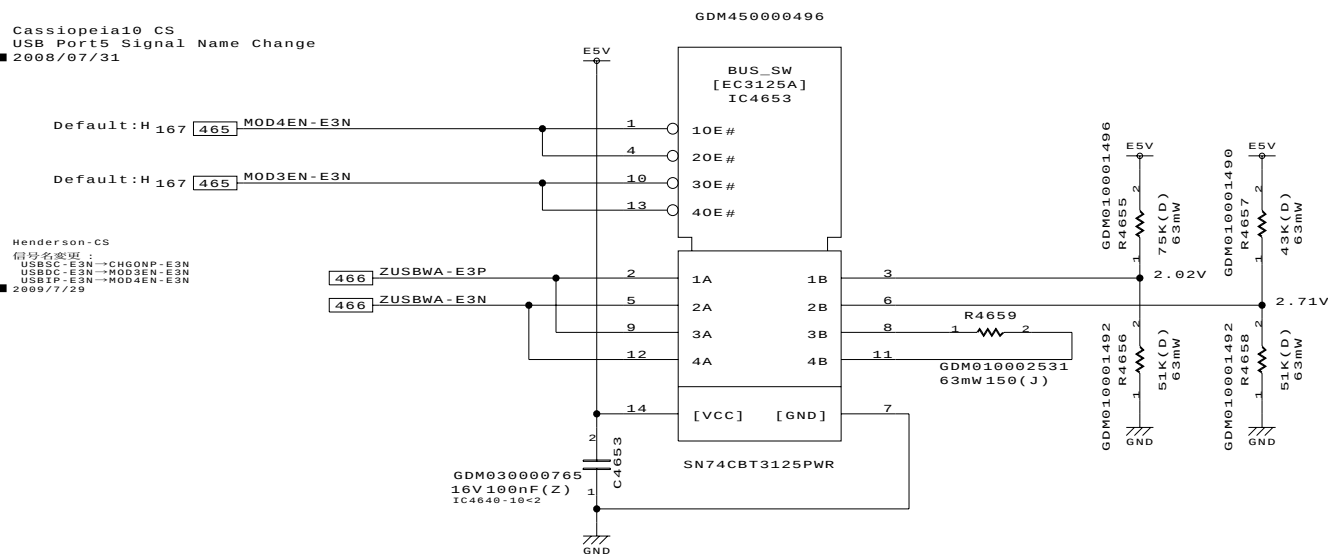
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Ling.Yu	FHNSY1	USB CHARGER 3/4(1)	465	082	00	360069769

2009.10.15 17:09

Henderson USB Charge Circuit Layout Guide



S	OE#	Function
X	H	Disconnect
L	L	D=1D
H	L	D=2D



REF:Cassiopeia10(VP) . FCASY1

DESIGNED BY
Ling.Yu

TITLE
FHNSY1

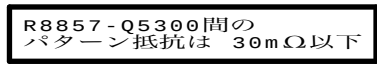
FUNCTION
USB CHARGER 3/4(2)

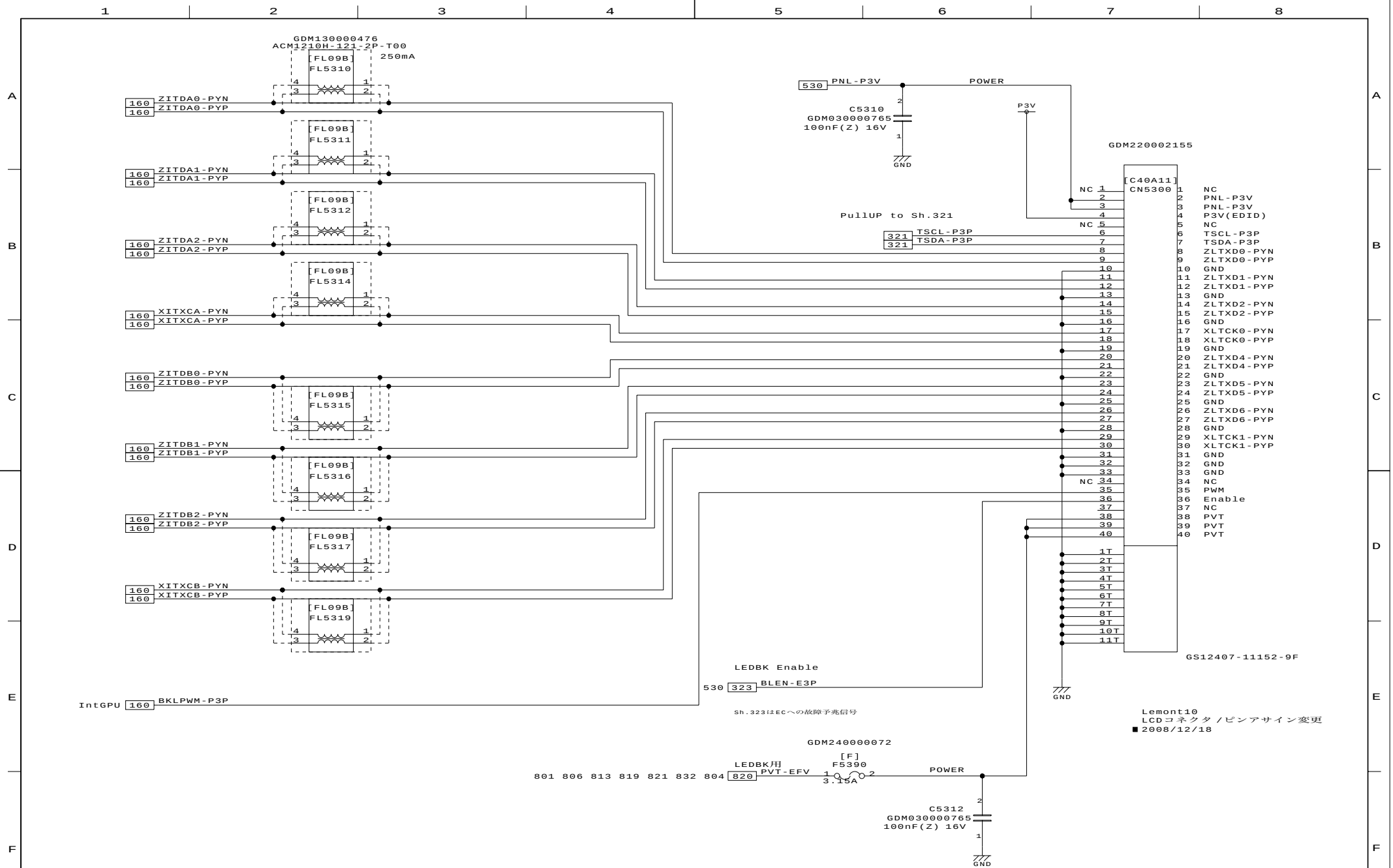
SH. NO.	466
---------	-----

PAGE NO.
083

REV . MARK	00
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DRAWING . NO .
360069769

TOSHIBA CORPORATION



DESIGNED BY

T. Naruse

TITLE

FHNSY1

FUNCTION

LCD I/F

SH. NO.

539

PAGE NO.

085

REV. MARK

00

DRAWING NO.

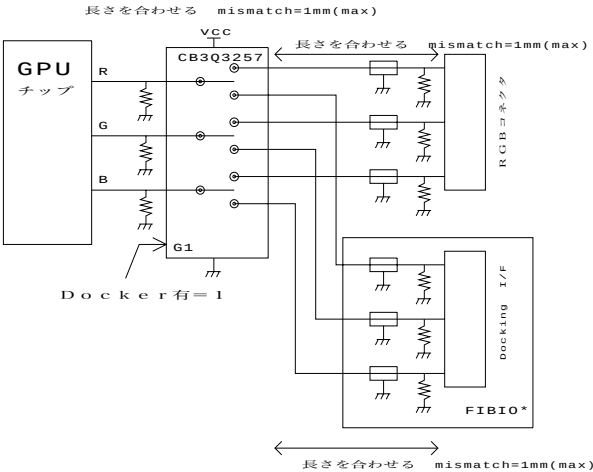
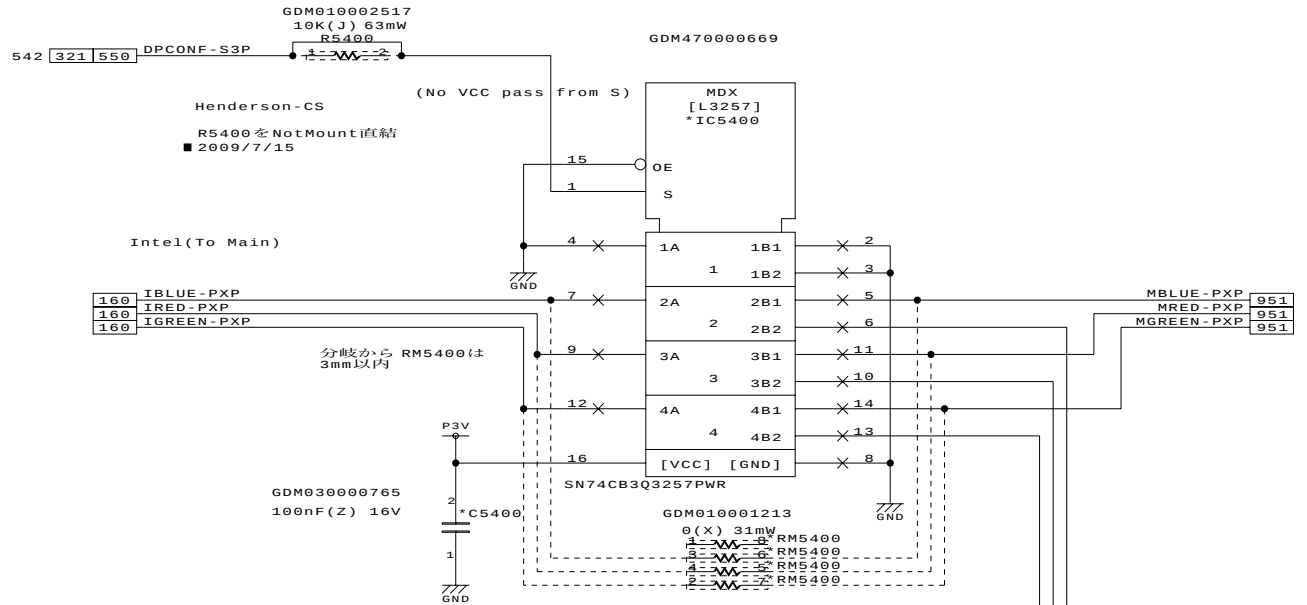
360069769

2009.10.15 17:09 G11

TOSHIBA CORPORATION

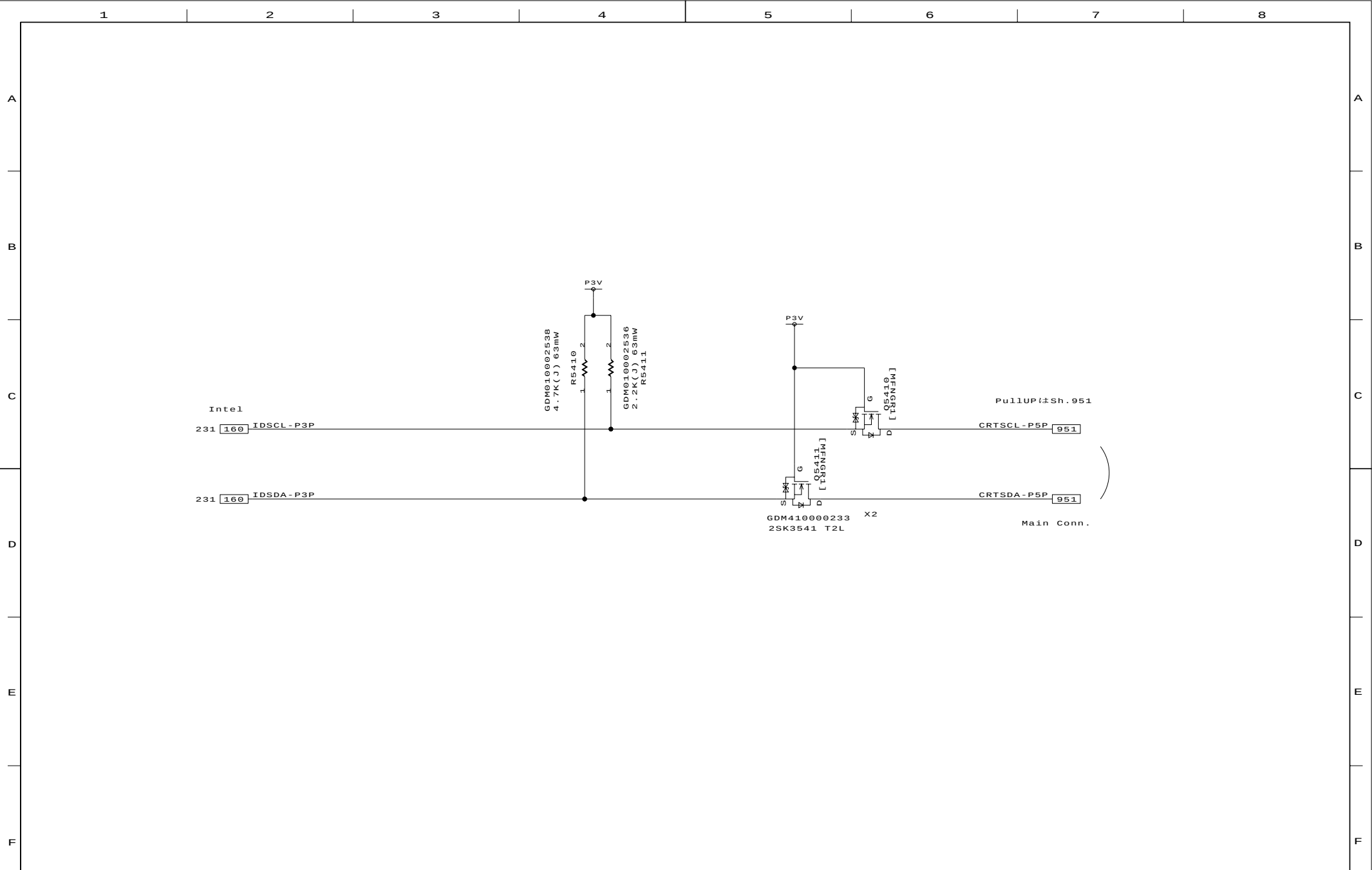
仕向け設定有

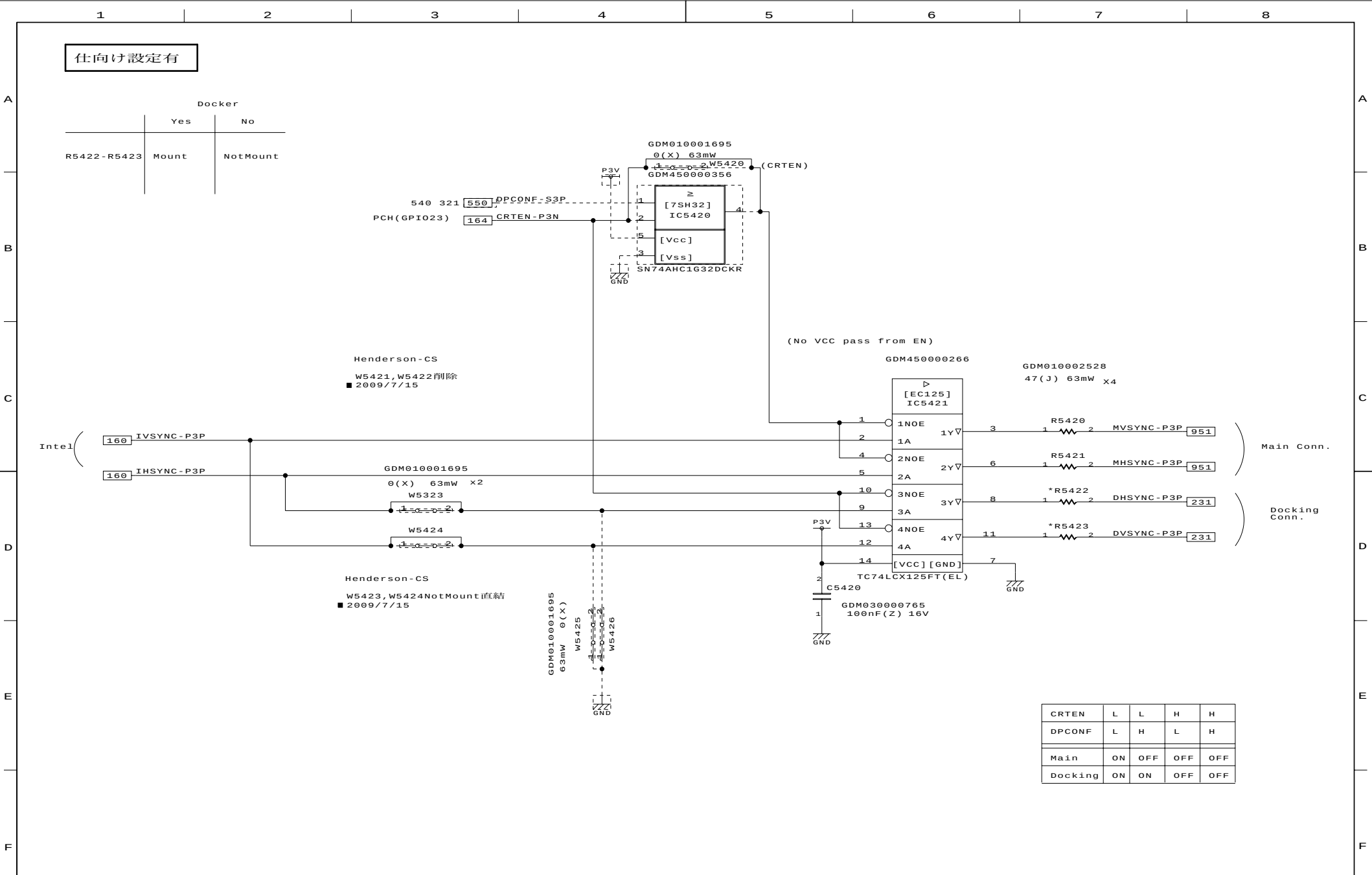
	Docker	
	Yes	No
RM5400	NotMount	Mount
IC5400	Mount	NotMount
C5400	Mount	NotMount



S (DPCONF)	L	H
CB3Q3257 (S1)	ON	OFF
CB3Q3257 (S2)	OFF	ON

TOSHIBA&NVIDIA CONFIDENTIAL





DESIGNED BY

T. Naruse

TITLE

FHNSY1

FUNCTION

CRT H/VSync

SH.NO.

542

PAGE NO.

088

REV.MARK

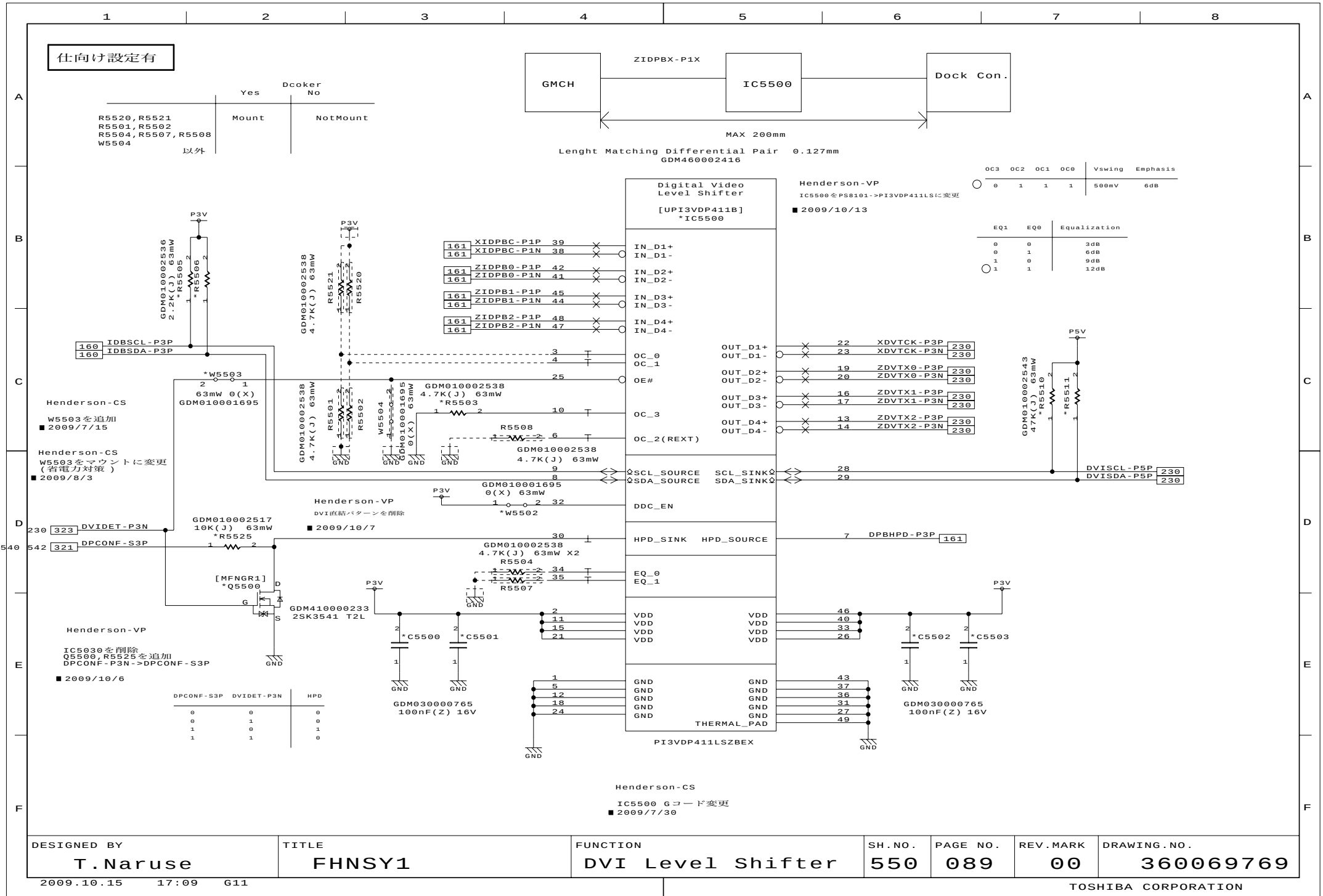
00

DRAWING.NO.

360069769

2009.10.15 17:09 G11

TOSHIBA CORPORATION

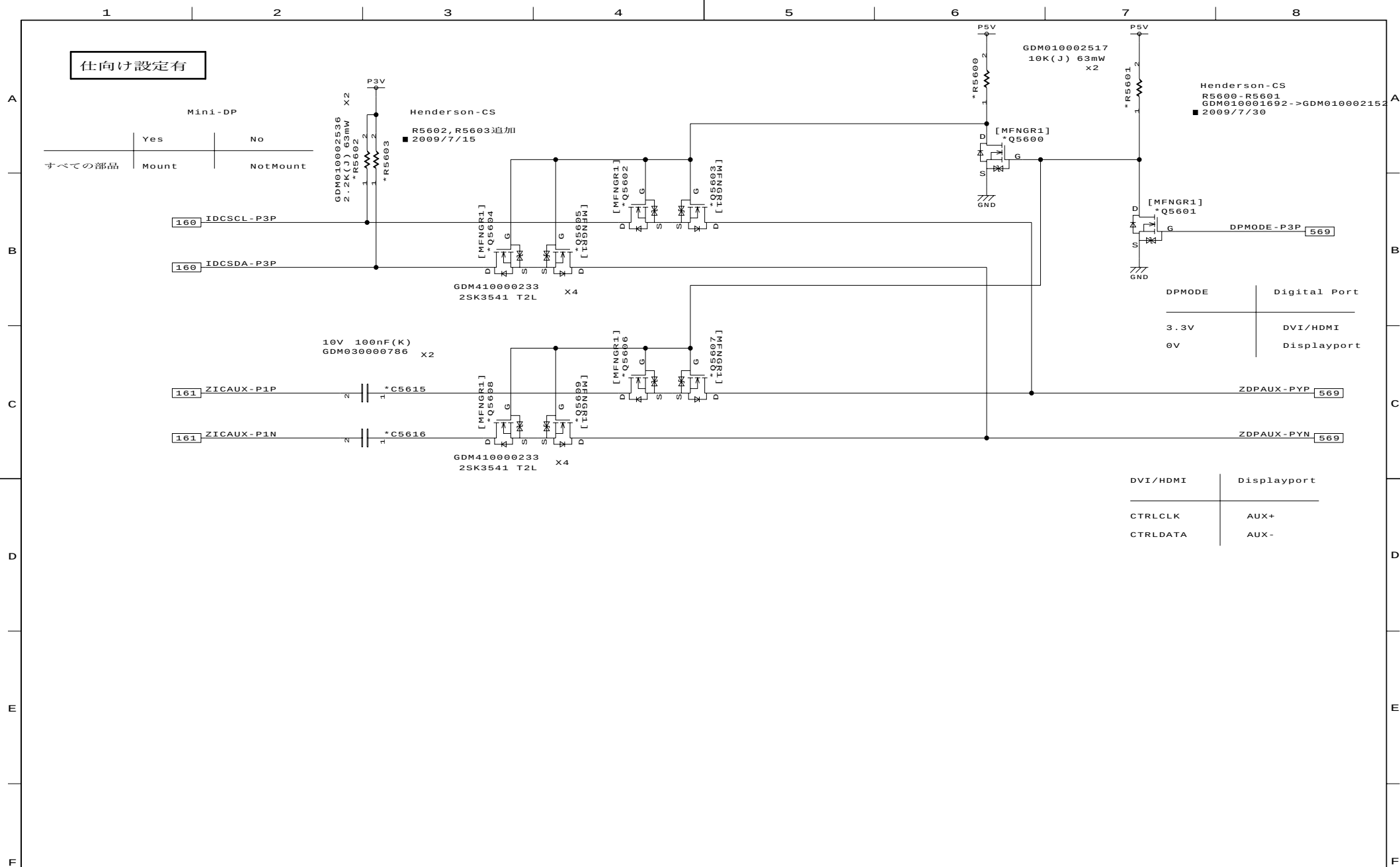


Henderson - VP

DVI直結パターンを削除

■ 2009/10/7

DESIGNED BY T.Naruse	TITLE FHNSY1	FUNCTION DVI Jumper	SH.NO. 551	PAGE NO. 090	REV.MARK 00	DRAWING.NO. 360069769
2009.10.15 17:09 G11			TOSHIBA CORPORATION			



DESIGNED BY

T. Naruse

TITLE

FHNSY1

FUNCTION

Displayport Dongle

SH.NO.

560

PAGE NO.

091

REV.MARK

00

DRAWING.NO.

360069769

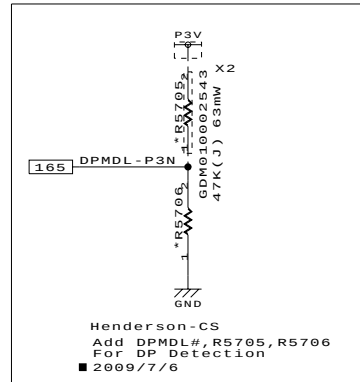
2009.10.15 17:09 G11

TOSHIBA CORPORATION

仕向け設定有

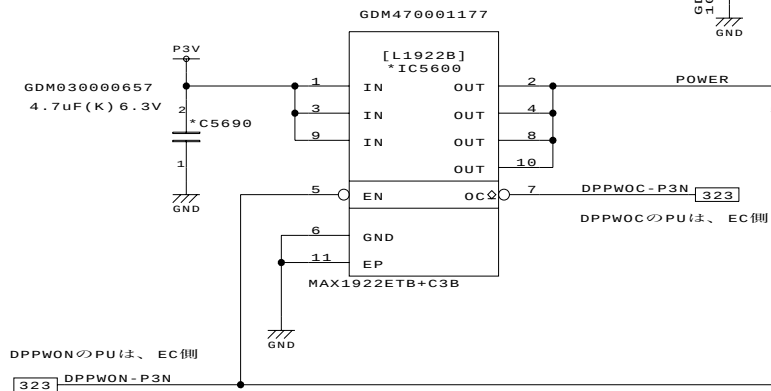
Mini-DP

	Yes	No
D5690以外 R5699, R5700 Q5690, Q5691	Mount	NotMount
R5705 R5706	NotMount	Mount



Henderson-VP
DP_PWR回路変更
■ 2009/10/06

Henderson-VP
DP_PWR回路変更
■ 2009/09/24



161 ZDPTX0-P1P
161 ZDPTX0-P1N
161 ZDPTX1-P1P
161 ZDPTX1-P1N
161 ZDPTX2-P1P
161 ZDPTX2-P1N
161 ZDPTX3-P1P
161 ZDPTX3-P1N

*R5698
GDM010002530
100(J) 63mW
Henderson-CS
R5698追加
■ 2009/7/15

560 ZDPAUX-PYP
560 ZDPAUX-PYN

GDM010002544
100K(J) 63mW
*R5696

Henderson-VP
Diode削除
■ 2009/09/29

GDM010002524
1K(J) 63mW
*R5692

[MFNGR1]
*Q5693

GDM410000233
2SK3541 T2L
*R5693

Henderson-CS
W5690を追加
■ 2009/7/23

GDM010001695
0(X) 63mW

Henderson-CS

IC5691をシュミットから FETに変更
■ 2009/7/15

GDM220002196

[C20A4]
*CN5690

1 GND
2 HPD
3 ML_LANE0+
4 DP_MODE
5 ML_LANE0-
6 CEC
7 GND
8 GND
9 ML_LANE1+
10 ML_LANE3+
11 ML_LANE1-
12 ML_LANE3-
13 GND
14 GND
15 ML_LANE2+
16 AUX_CH+
17 ML_LANE2-
18 AUX_CH-
19 GND
20 DP_PWR

3V112M3-RH3TTB-7H

Henderson-CS

K2203168->GDM220002196
■ 2009/7/24

GDM010002517
10K(J) 63mW x2
*R5699

Henderson-CS

R5699, R5700, Q5690, Q5691をNotMount
■ 2009/7/23

GPU
HPDDPG-S3P 161 323
EC

Henderson-CS
R5694, R5697を削除
■ 2009/7/16

Henderson-CS
HPDDPG-P3P->HPDPG-S3P
■ 2009/7/23

DESIGNED BY

M.Endo/T.Naruse

TITLE

FHNSY1

FUNCTION

Displayport I/F 569

SH.NO.

PAGE NO.

092

REV.MARK

00

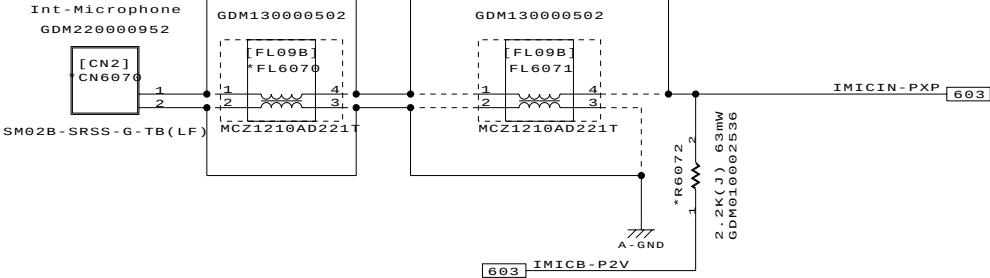
DRAWING.NO.

360069769

2009.10.15 17:09 G11

TOSHIBA CORPORATION

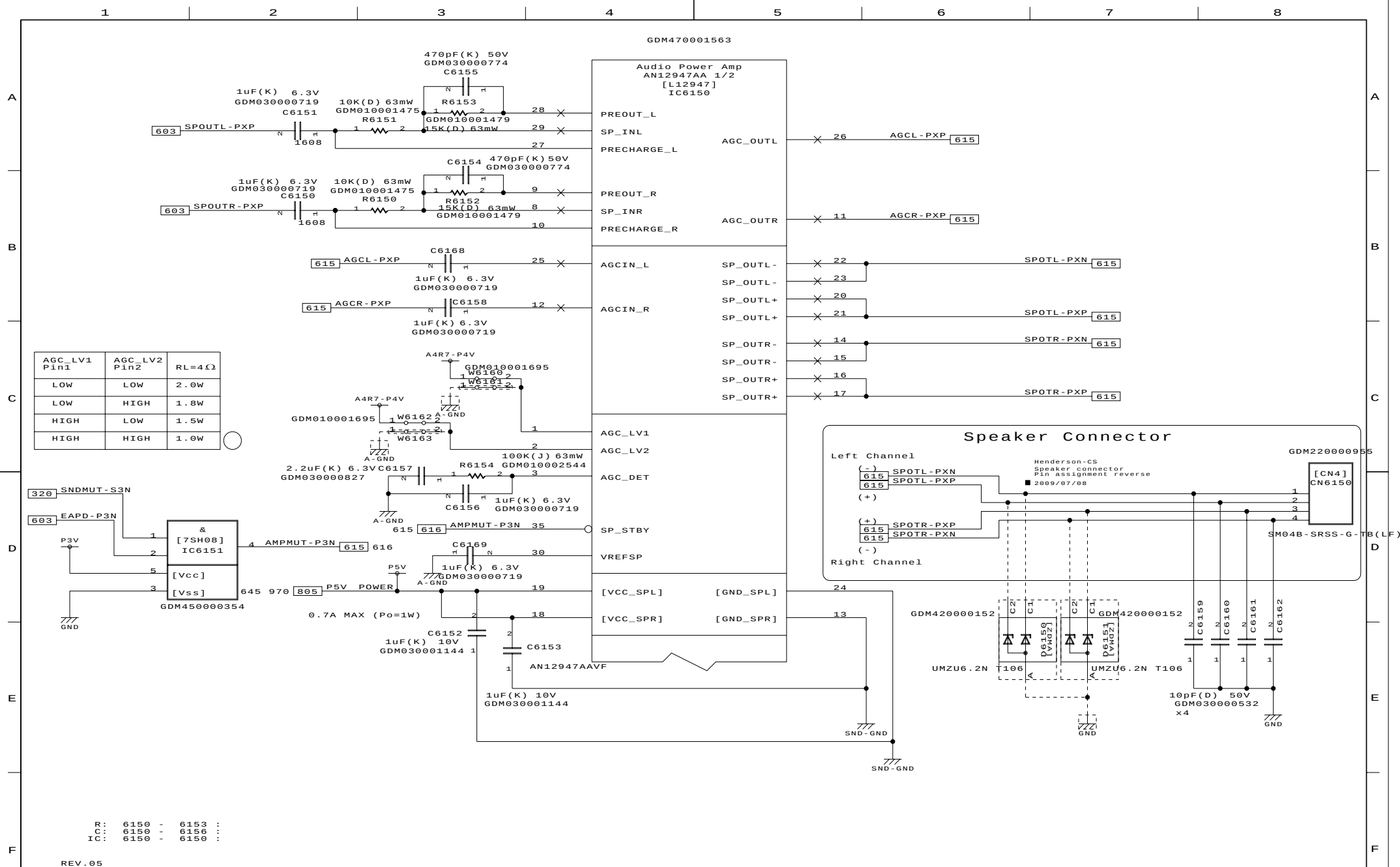
仕向け設定有



FL: 6050 - 6050 :
C: 6050 - 6052 :
R: 6050 - 6053 :
IC: 6050 - 6050 :
W: 6050 - 6050 :
PJ: 6050 - 6051 :

REV.01 鉛フリー済み

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
M.Yamaguchi	FHNSY1	Int-MIC	607	094	00	360069769



DESIGNED BY

M. Yamaguchi

TITLE

FHNSY1

FUNCTION

AN12947AA(1)

SH.NO.

615

PAGE NO.

095

REV.MARK

00

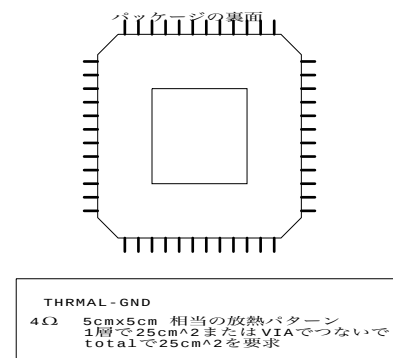
DRAWING.NO.

360069769

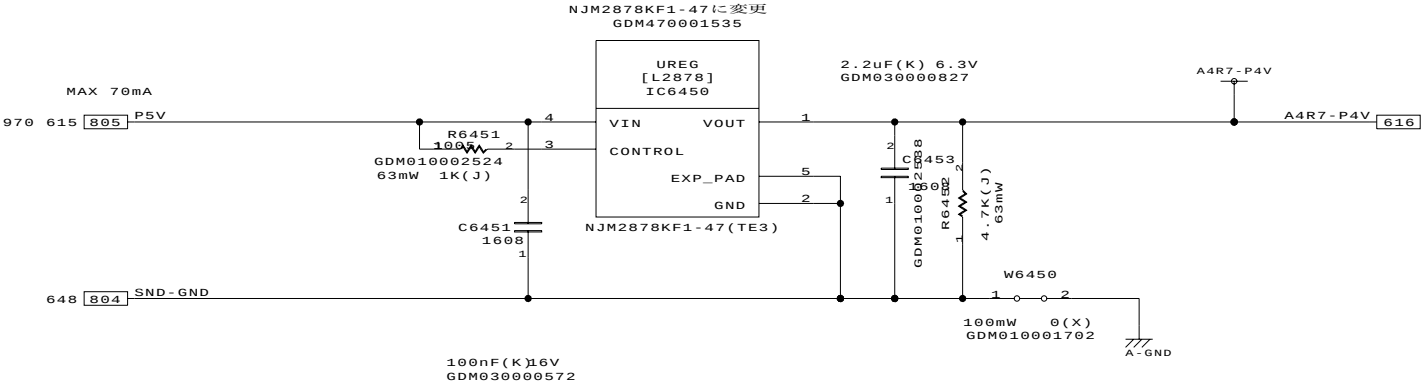
2009.10.15 17:09 G11

TOSHIBA CONFIDENTIAL

TOSHIBA CORPORATION



2009.10.15 17:09 G11



W: 6450 - 6450 :
C: 6451 - 6453 :
R: 6450 - 6451 :
IC: 6450 - 6450 :

REV.01 G対応済み

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
M.Yamaguchi	FHNSY1	ANALOG POWER	645	097	00	360069769

1	2	3	4	5	6	7	8
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A

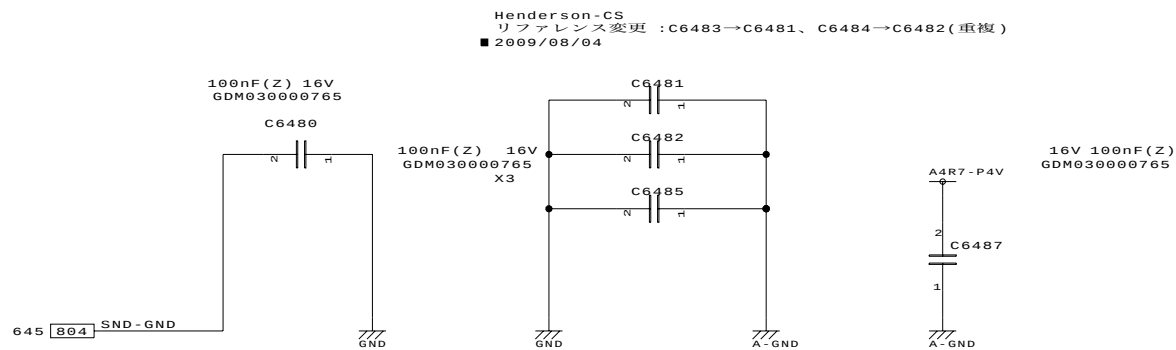
B

C

D

E

F



C: 6480 - 6490 :

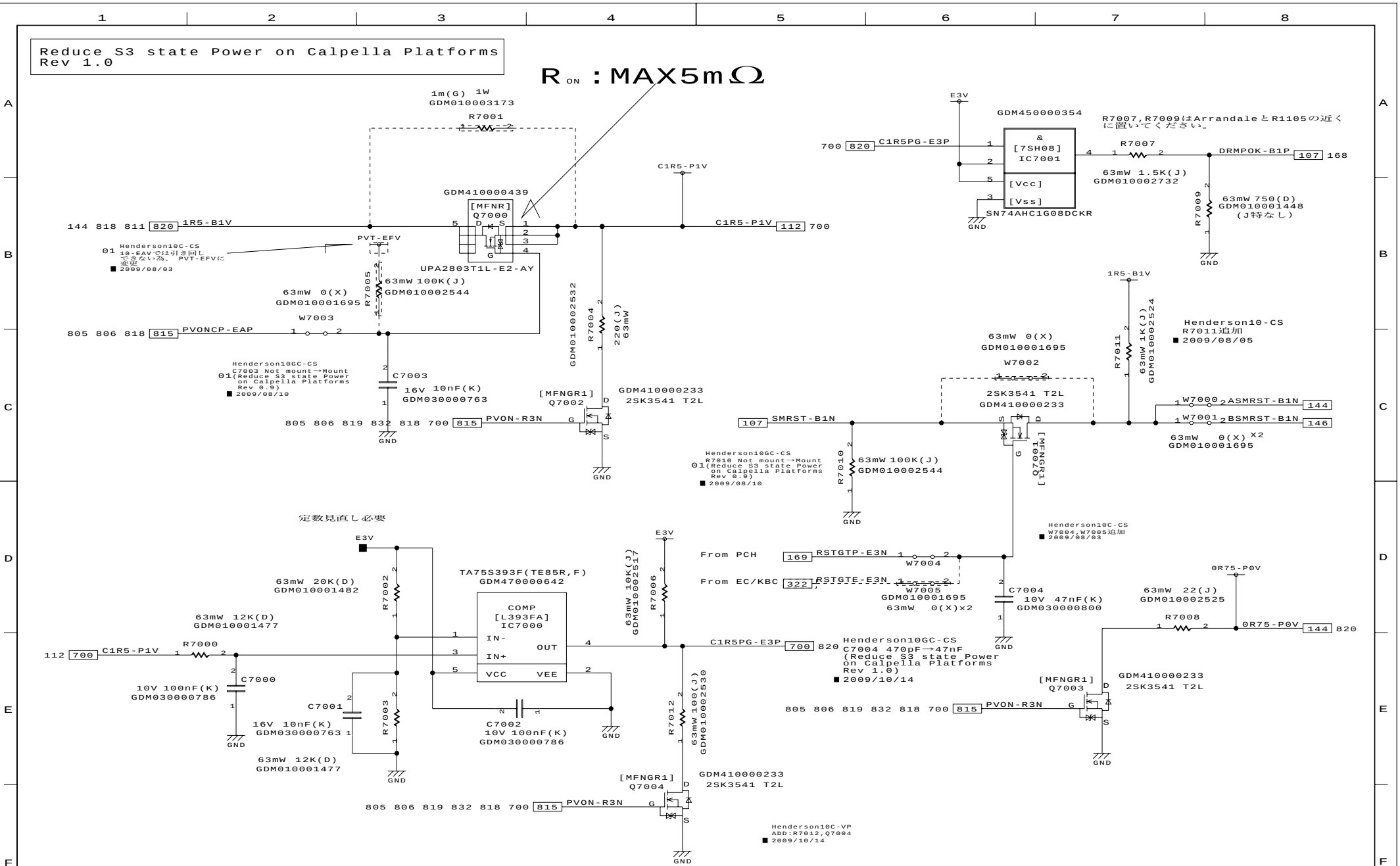
REV.01 G対応済み

DESIGNED BY M.Yamaguchi	TITLE FHNSY1	FUNCTION Pass-con	SH.NO. 648	PAGE NO. 098	REV.MARK 00	DRAWING.NO. 360069769
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2009.10.15 17:09 G11

TOSHIBA CONFIDENTIAL

TOSHIBA CORPORATION



DESIGNED BY
T.Iwai/Y.Horie
/T.Ochiai/T.Ichimura
2009.10.15 17:09

TITLE
FHNSY1

FUNCTION
Reduce S3 Power

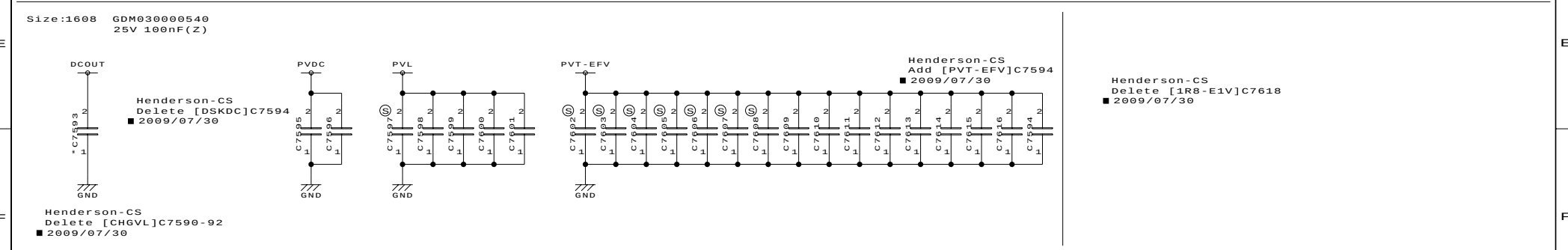
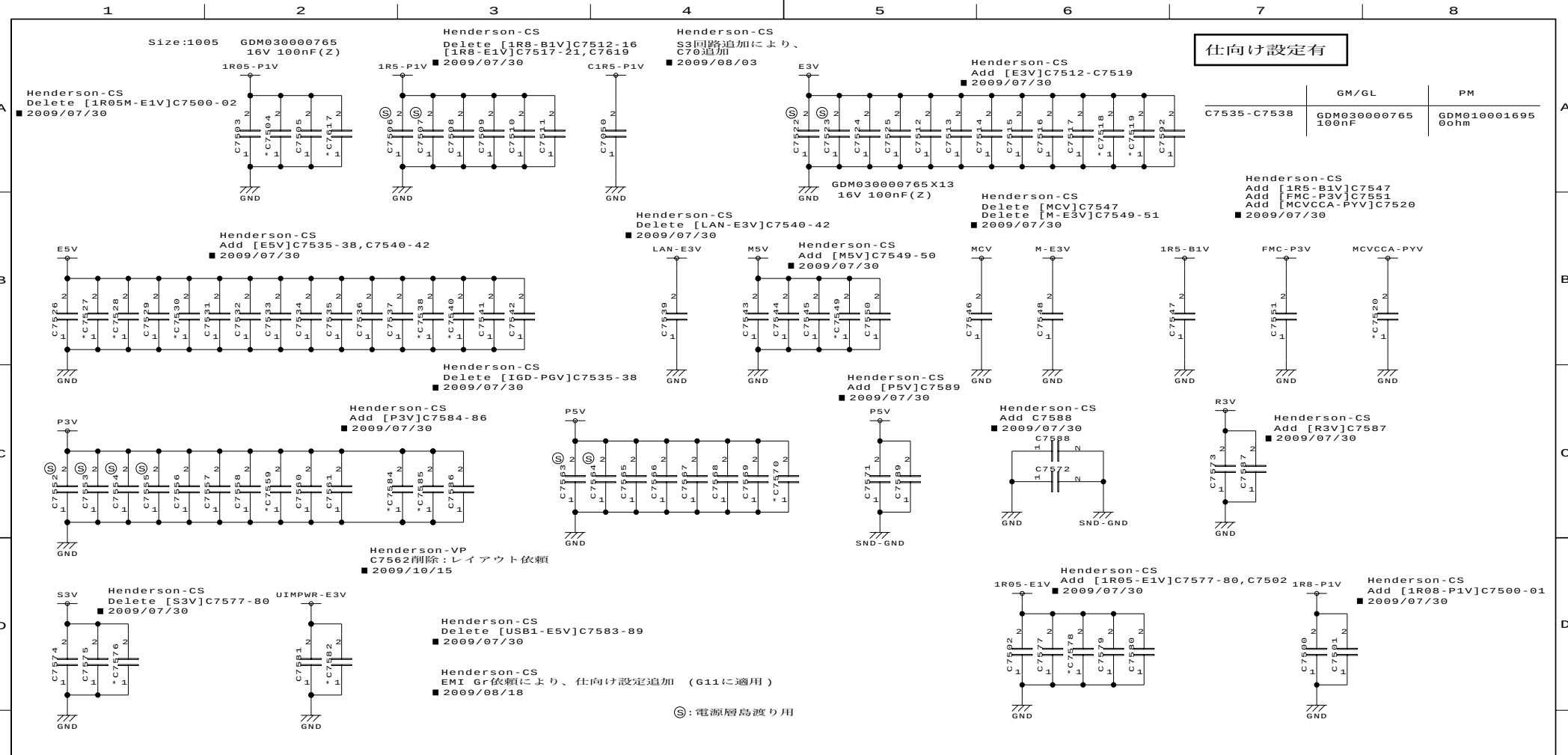
SH.NO.
700

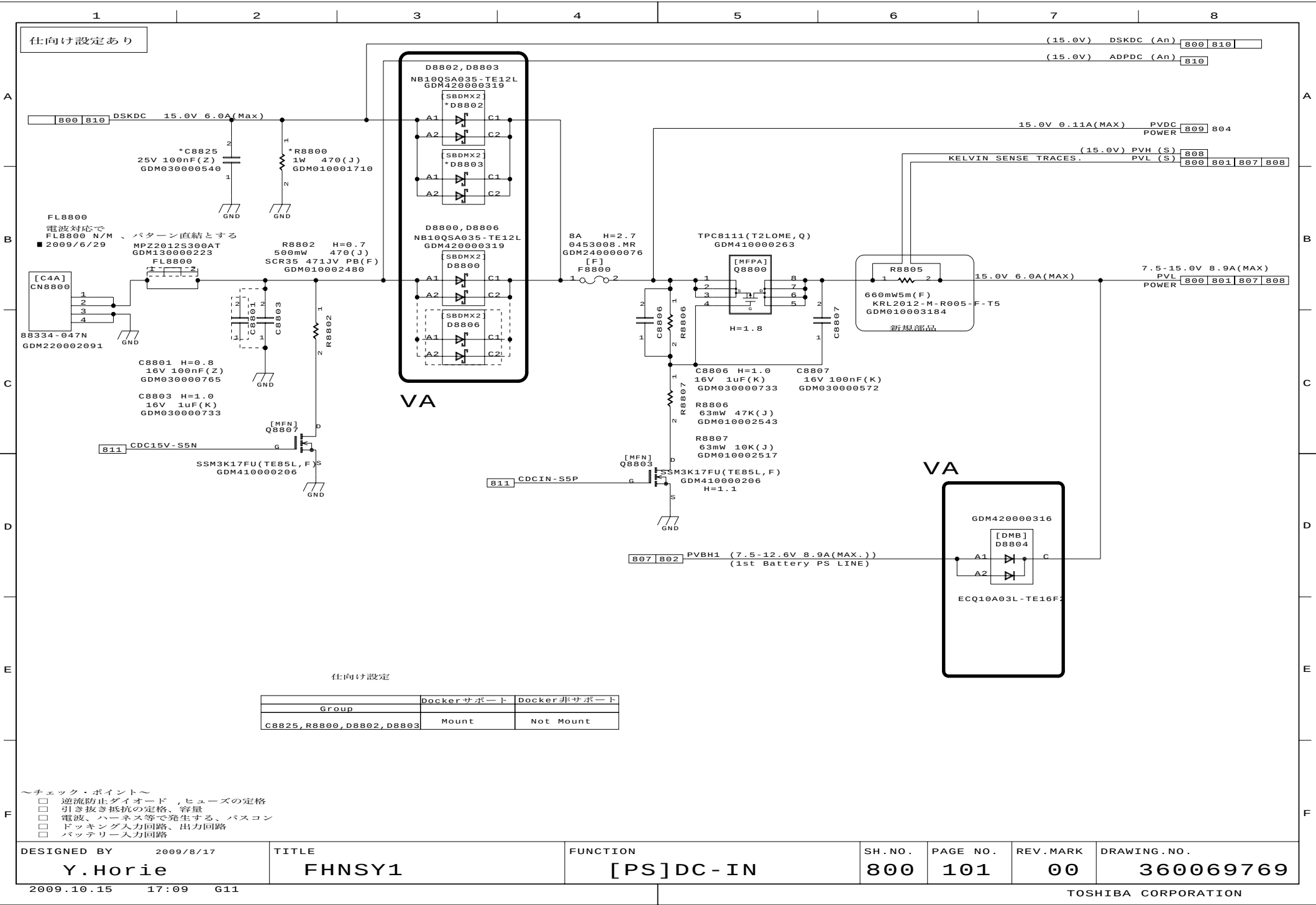
PAGE NO.
099

REV.MARK
00

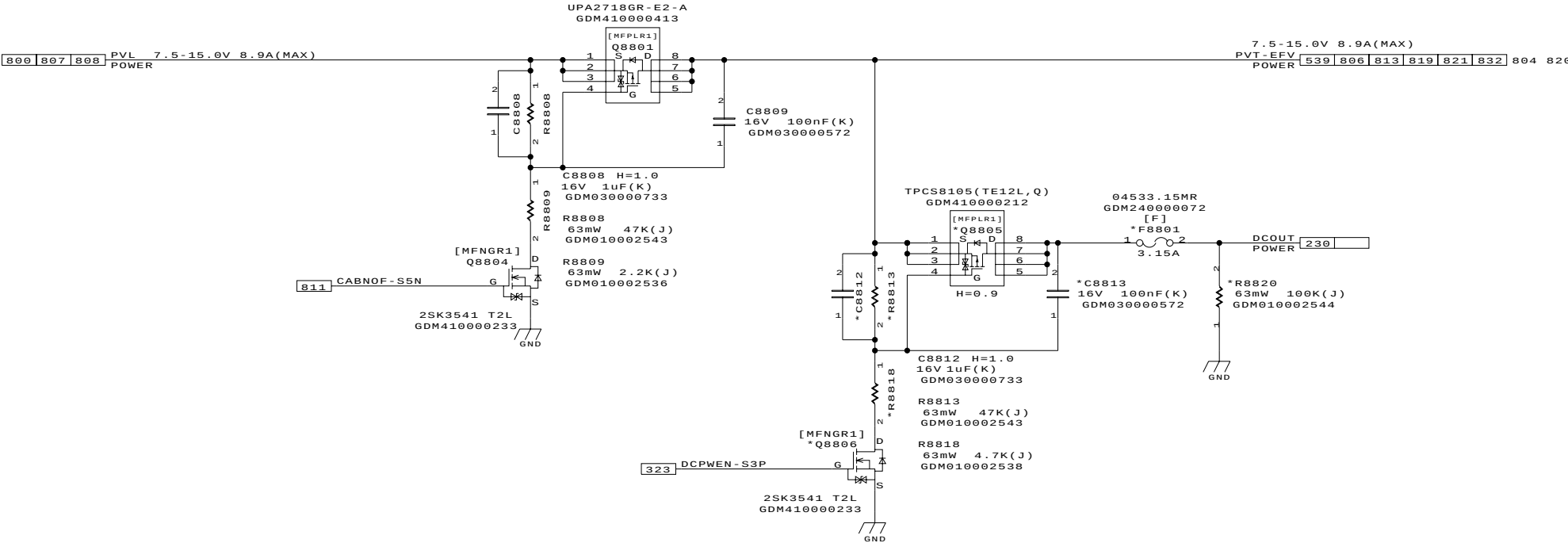
DRAWING.NO.
360069769

TOSHIBA CORPORATION





仕向け設定あり

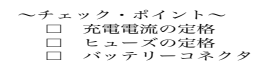


仕向け設定

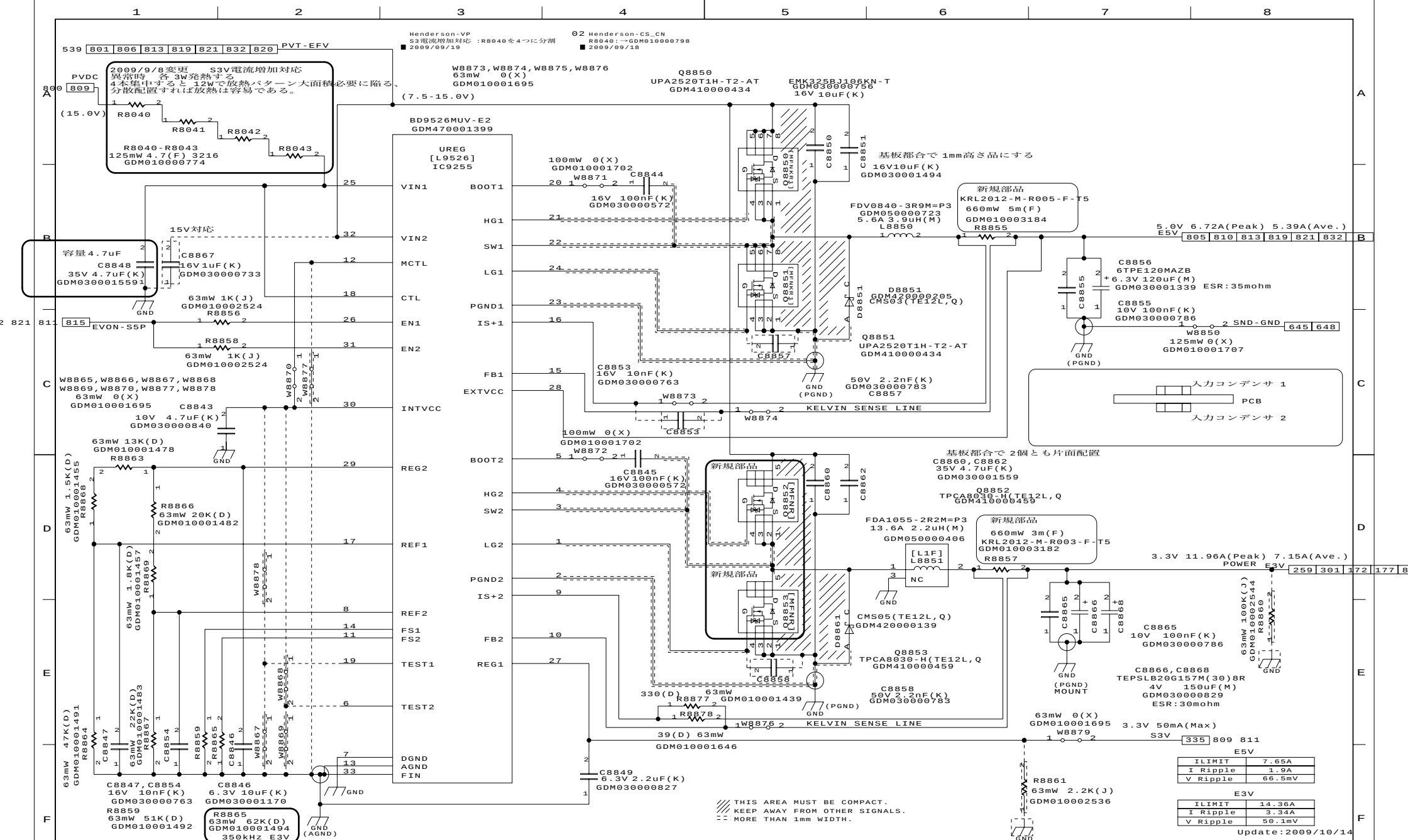
	Dockerサポート	Docker非サポート
Group		
C8812, C8813, F8801, Q8805, Q8806, R8813, R8818, R8820	Mount	Not Mount

- ～チェック～
- ☐ 部品定格
 - ☐ FET
 - ☐ ドッキング出力回路

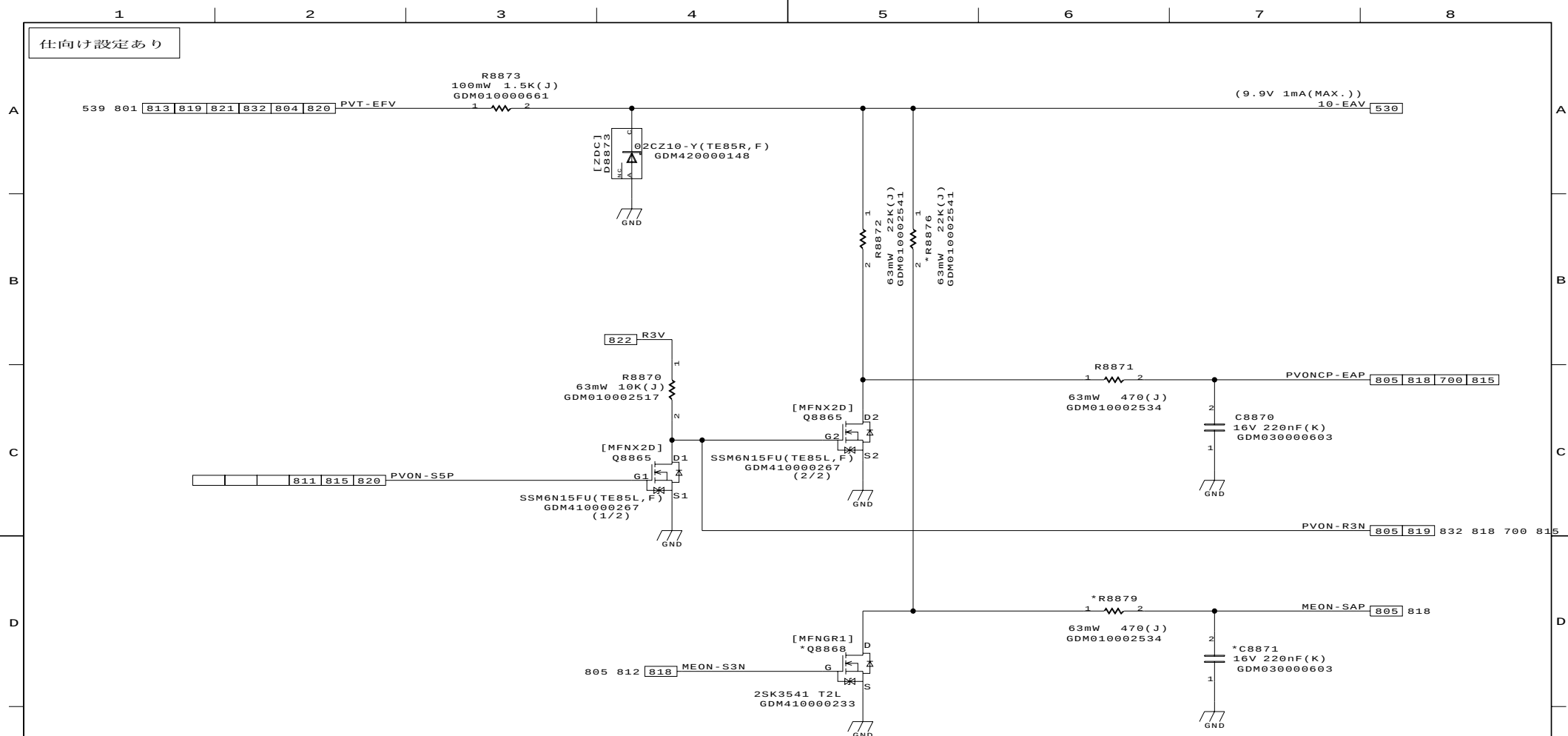
DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Y.Horie	FHNSY1	[PS]PVT-SW	801	102	00	360069769



TOSHIBA CORPORATION







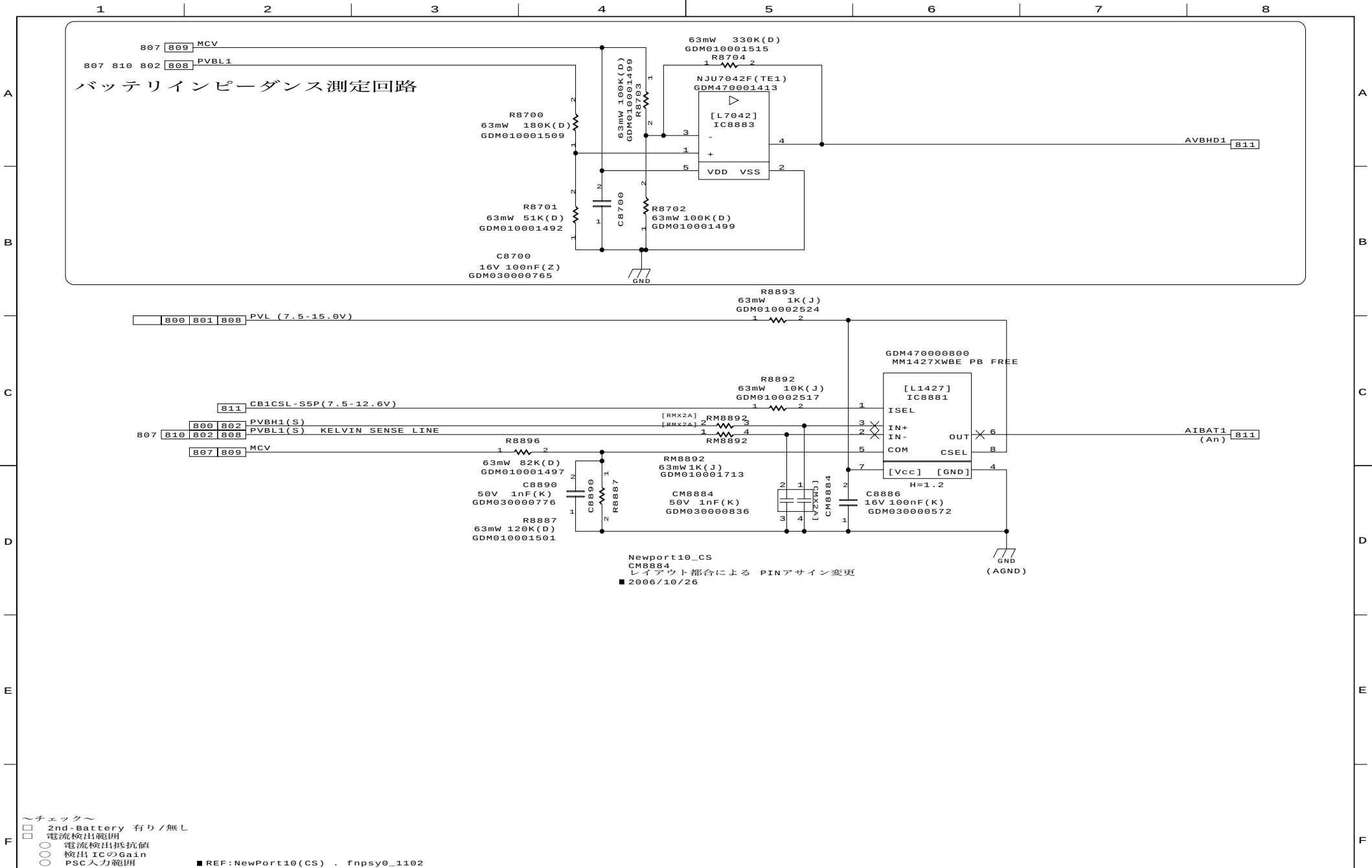
仕向け設定

	AMTサポート	AMT 非サポート
R8876, R8879 C8871, Q8868	Mount	Not Mount

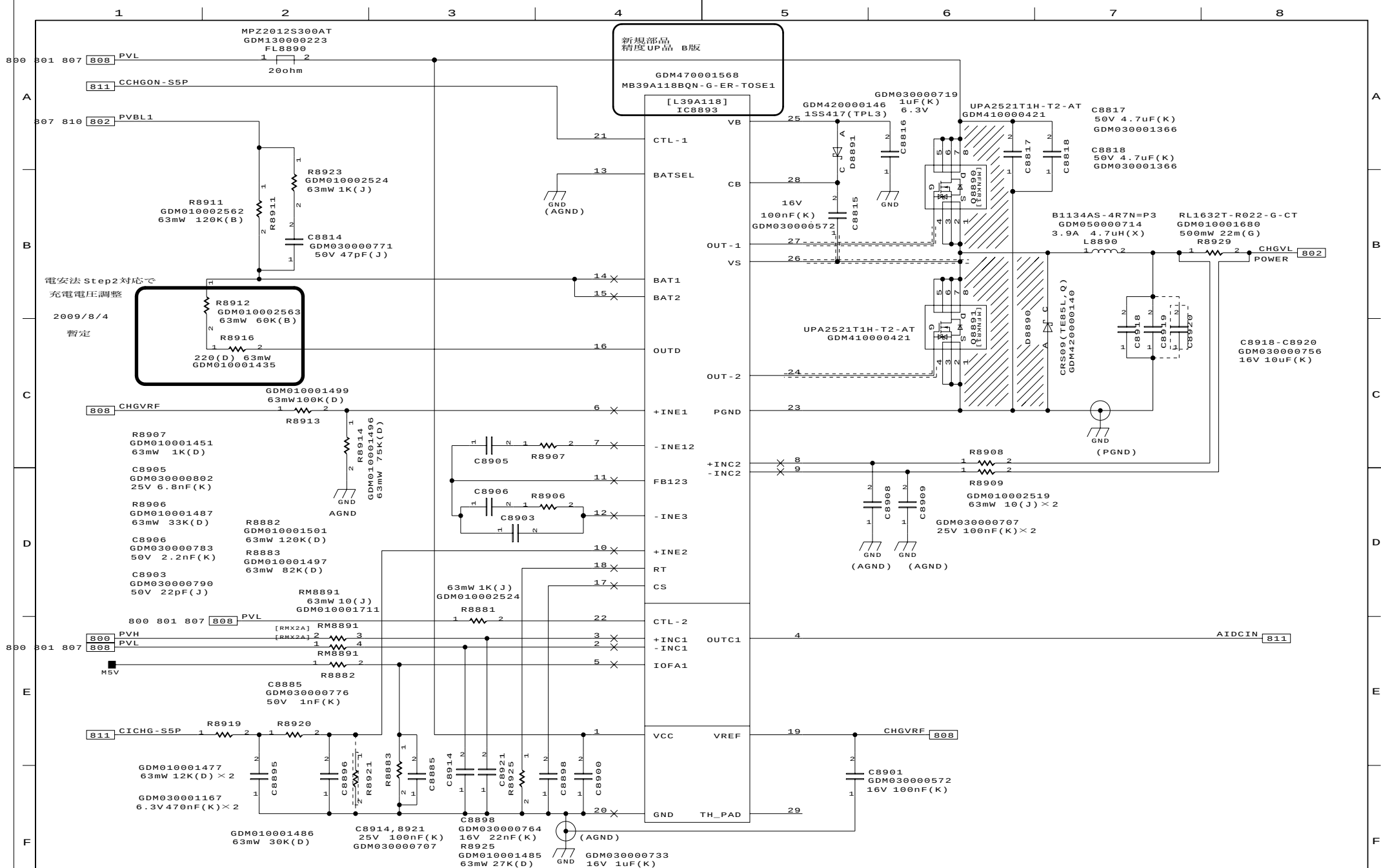
～チェック～

- | | | |
|--------------------------|------------|-----------|
| ☐ | 入力電圧 | [PVT-EFV] |
| ☐ | 出力電圧 | [10.0V] |
| ☐ | 負荷電流 | [1mA Max] |
| ☐ | 負荷抵抗 | |
| ☐ | FETゲート漏れ電流 | |
| ☐ | 出力時定数 | [ms] |

360069769



DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
Y.Horie	FHNSY1	[PS]CURRENT AMP	807	107	00	360069769

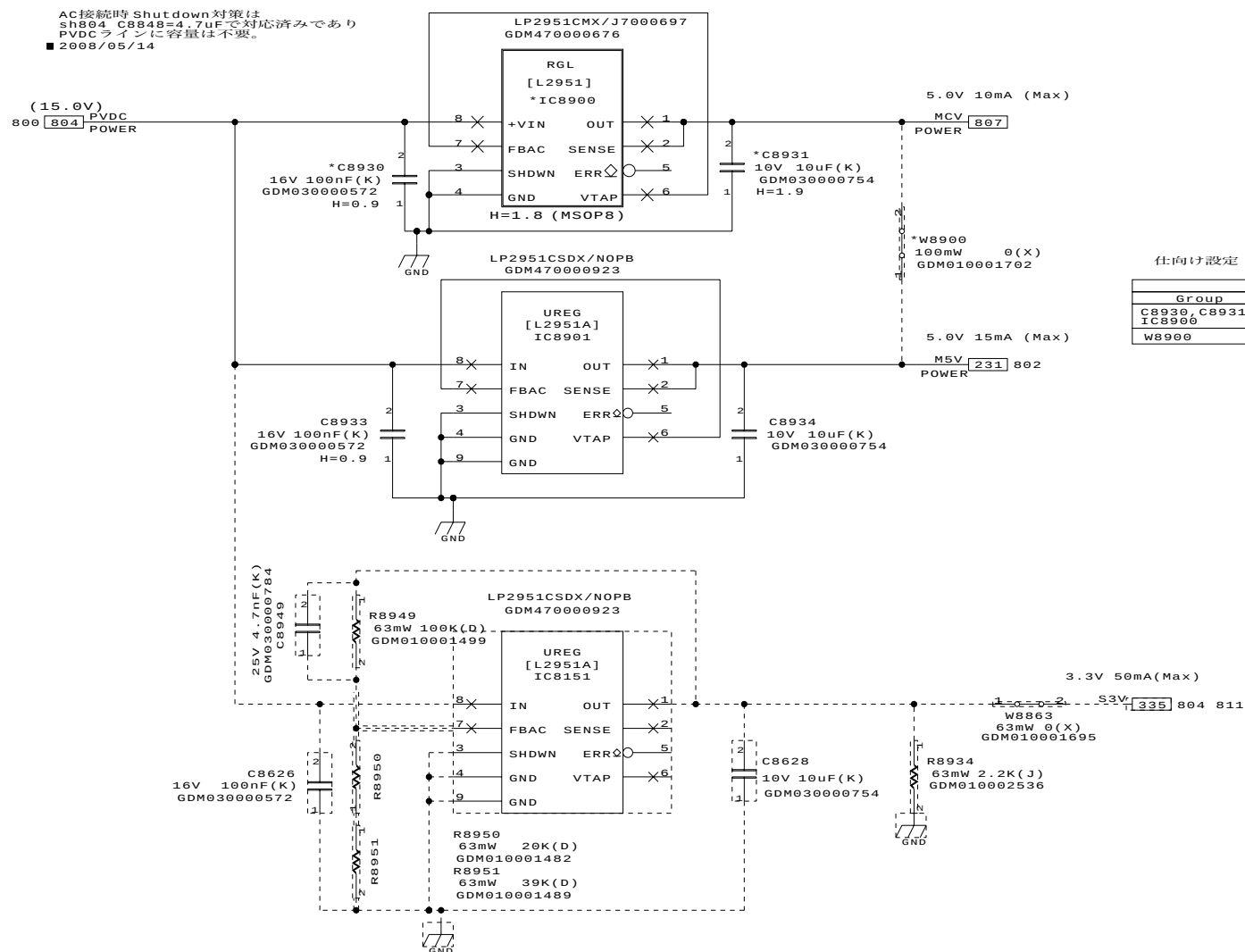


DRAWING . NO .

360069769

仕向け設定あり

M5V Supply current
○ LED : [10mA]
○ Thermal : [1mA x 2 =2mA]
○ IR Controller : [7.5mA]



～チェック～
☐ 出力電圧
☐ 出力電流定格

DESIGNED BY 2009/9/25

Y.Horie

TITLE

FHNSY1

FUNCTION

[PS]S5V/S3V

SH.NO.

809

PAGE NO.

109

REV.MARK

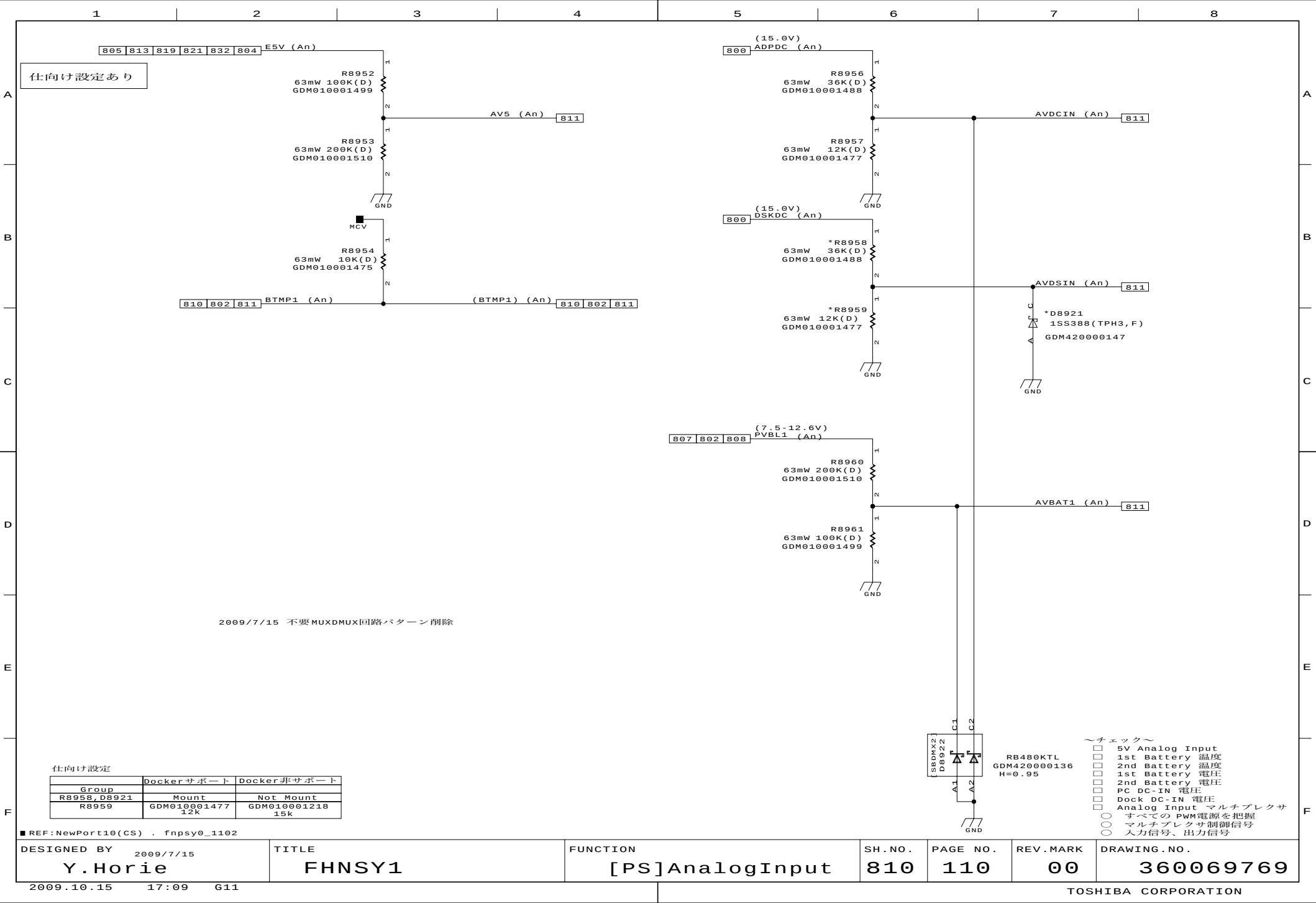
00

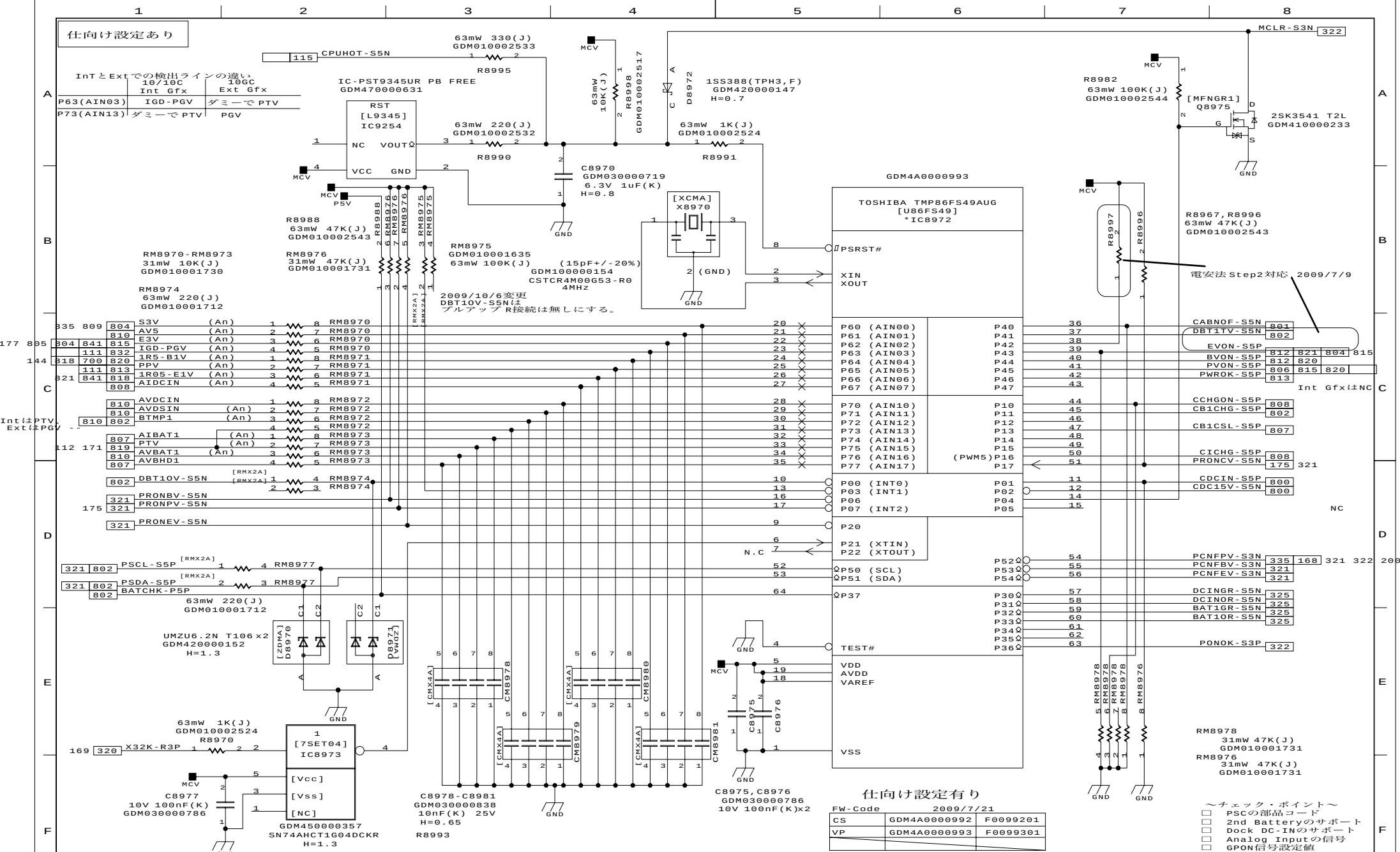
DRAWING.NO.

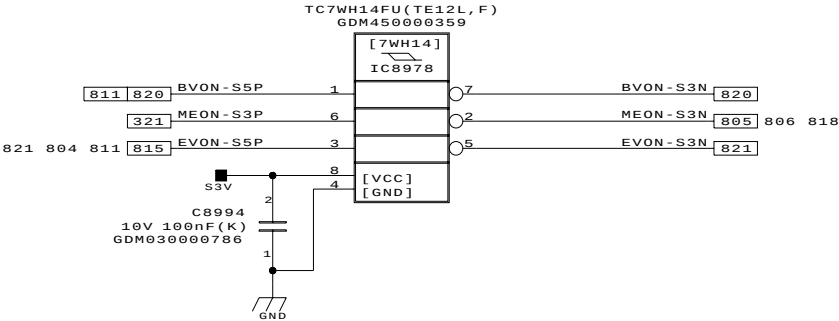
360069769

2009.10.15 17:09 G11

TOSHIBA CORPORATION



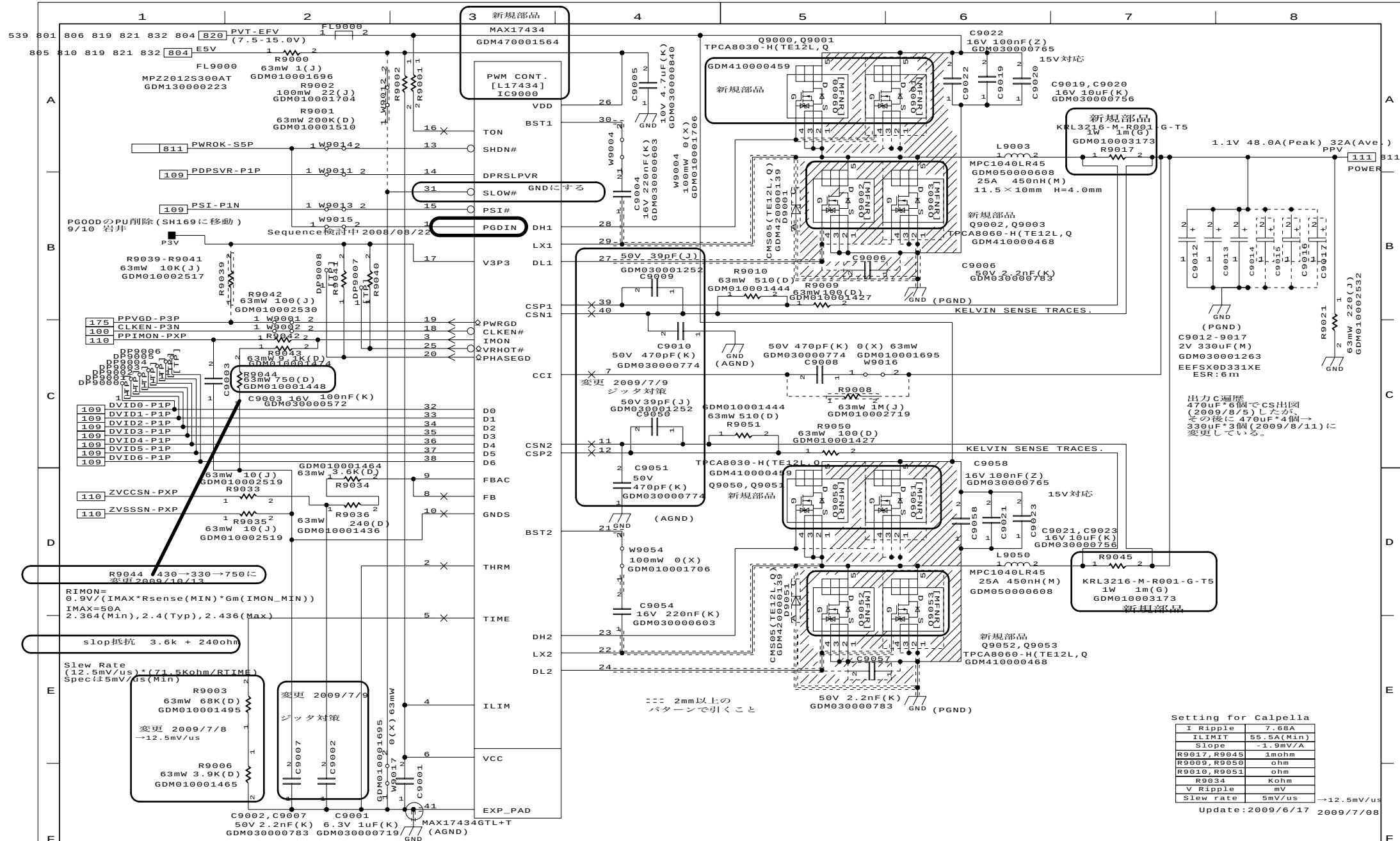




PPVGD-P3P回路削除 2009/7/3

Henderson-CS
Delete VRMPGD Cir.
(Move to SH100)
■2009/07/13

2009/7/3 2個→1個へ変更
このshに置く必要なし、システム側へ移動したい



DESIGNED BY 2009/10/10

A. Kanou

TITLE

FHNSY1

FUNCTION

[PS]CPUVCC

SH. NO.

813

PAGE NO.

113

REV. MARK

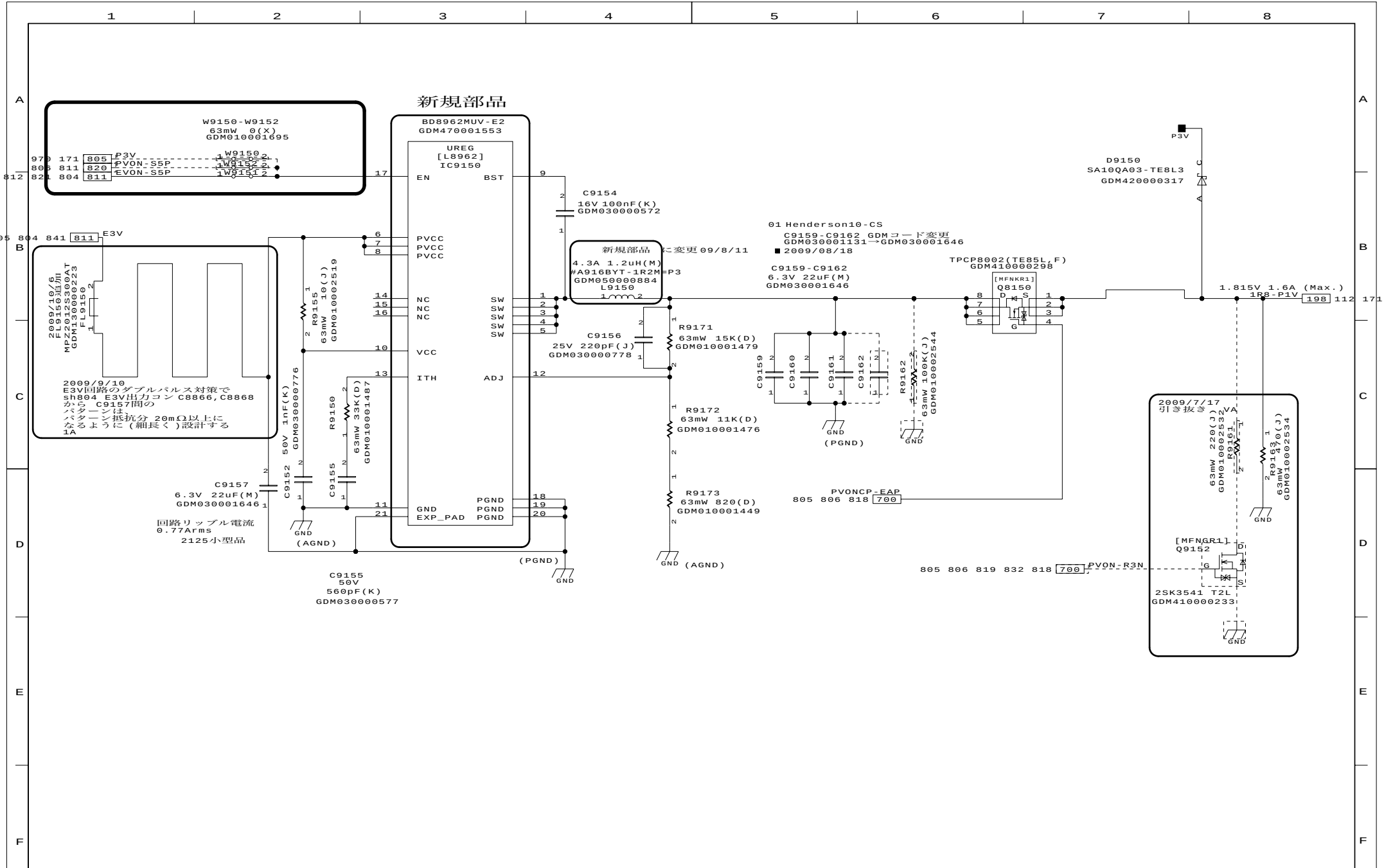
00

DRAWING NO.

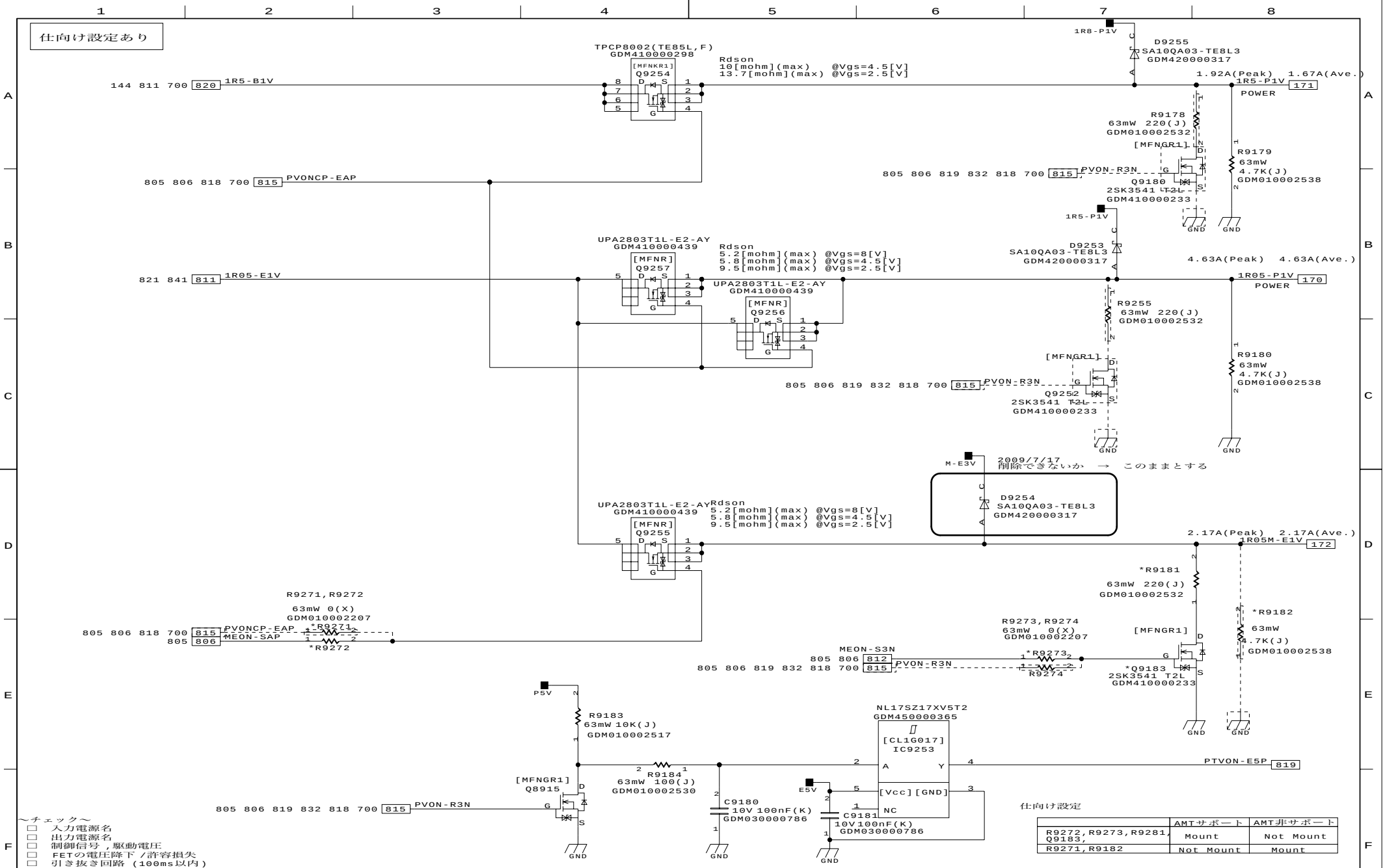
360069769

2009.10.15 17:09 G11

TOSHIBA CORPORATION



DESIGNED BY 2009/10/6	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
A. Kanou	FHNSY1	[PS]1R8-P1V	815	114	00	360069769



DESIGNED BY 2009/7/16

Y. Horie

TITLE

FHNSY1

FUNCTION

[PS]LOAD SW2

SH.NO.

818

PAGE NO.

115

REV.MARK

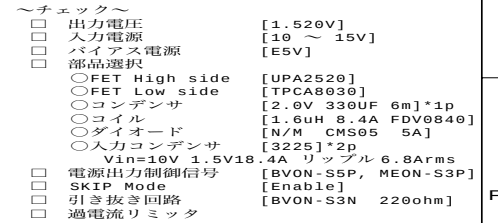
00

DRAWING.NO.

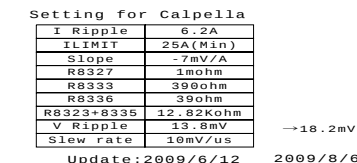
360069769

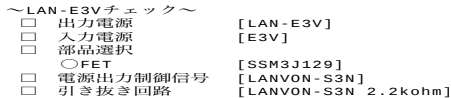
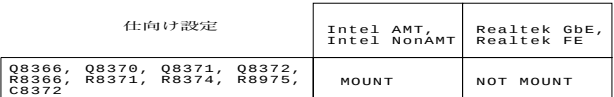
2009.10.15 17:09 G11

TOSHIBA CORPORATION



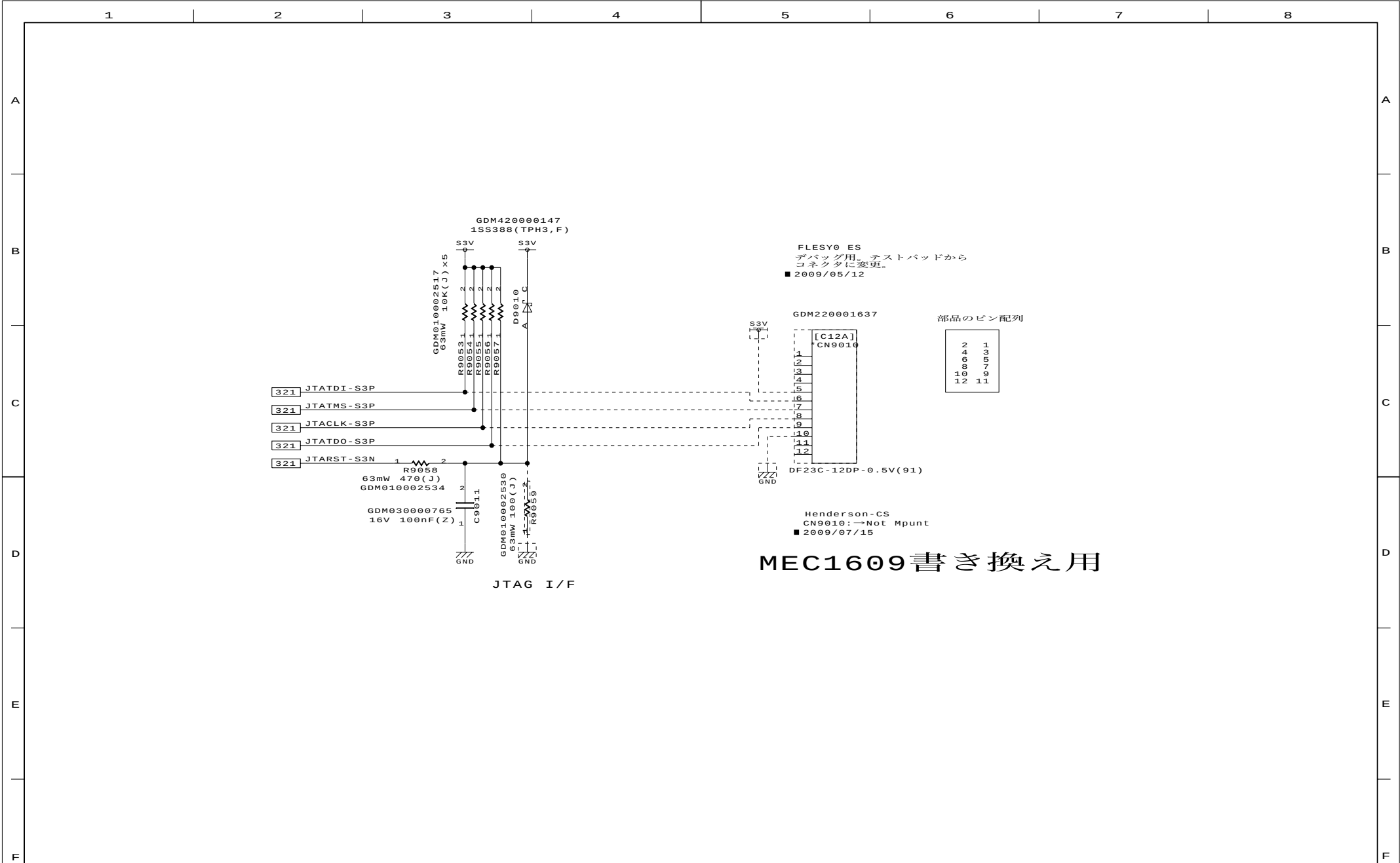




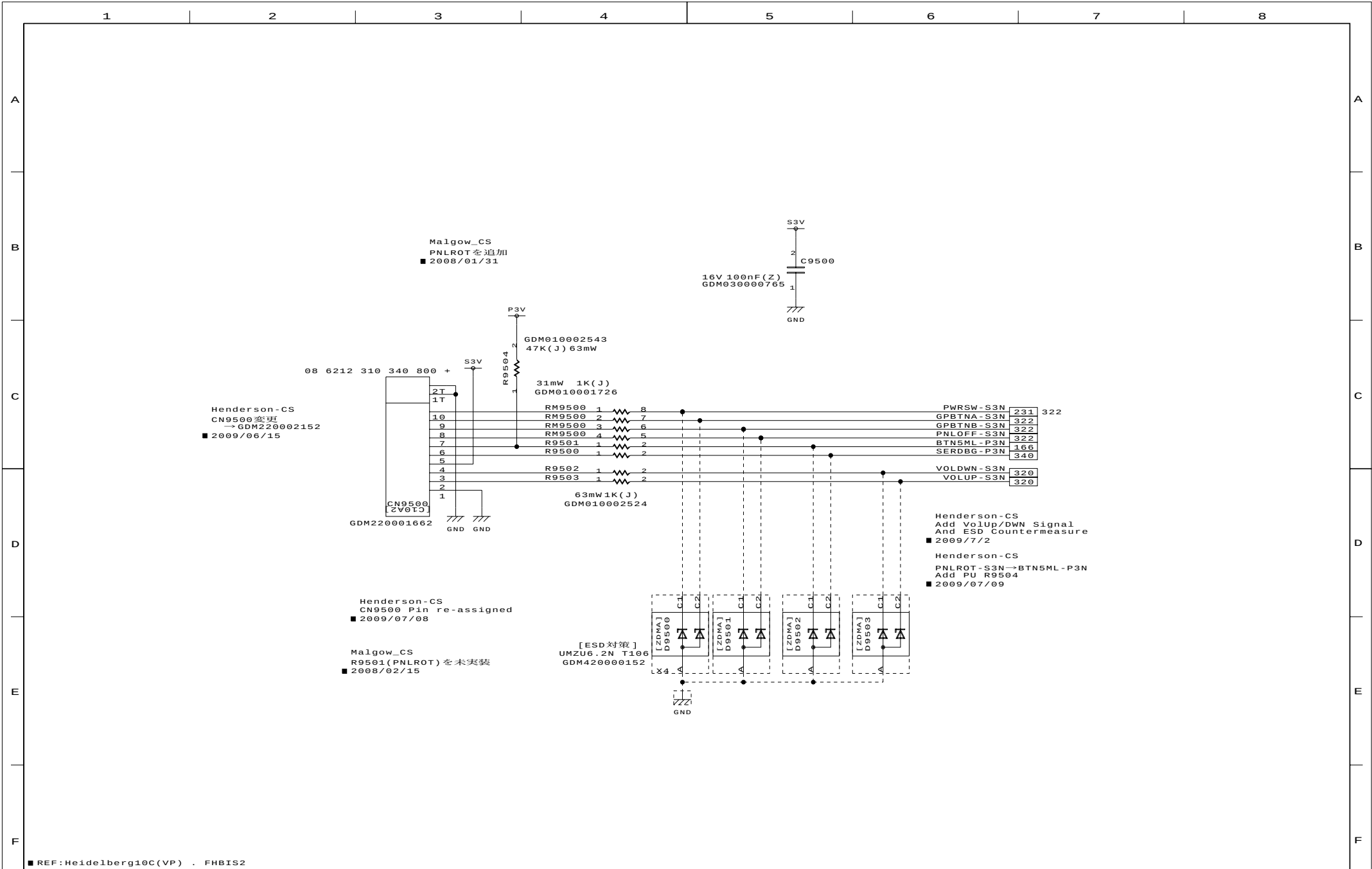


DRAWING . NO .

360069769

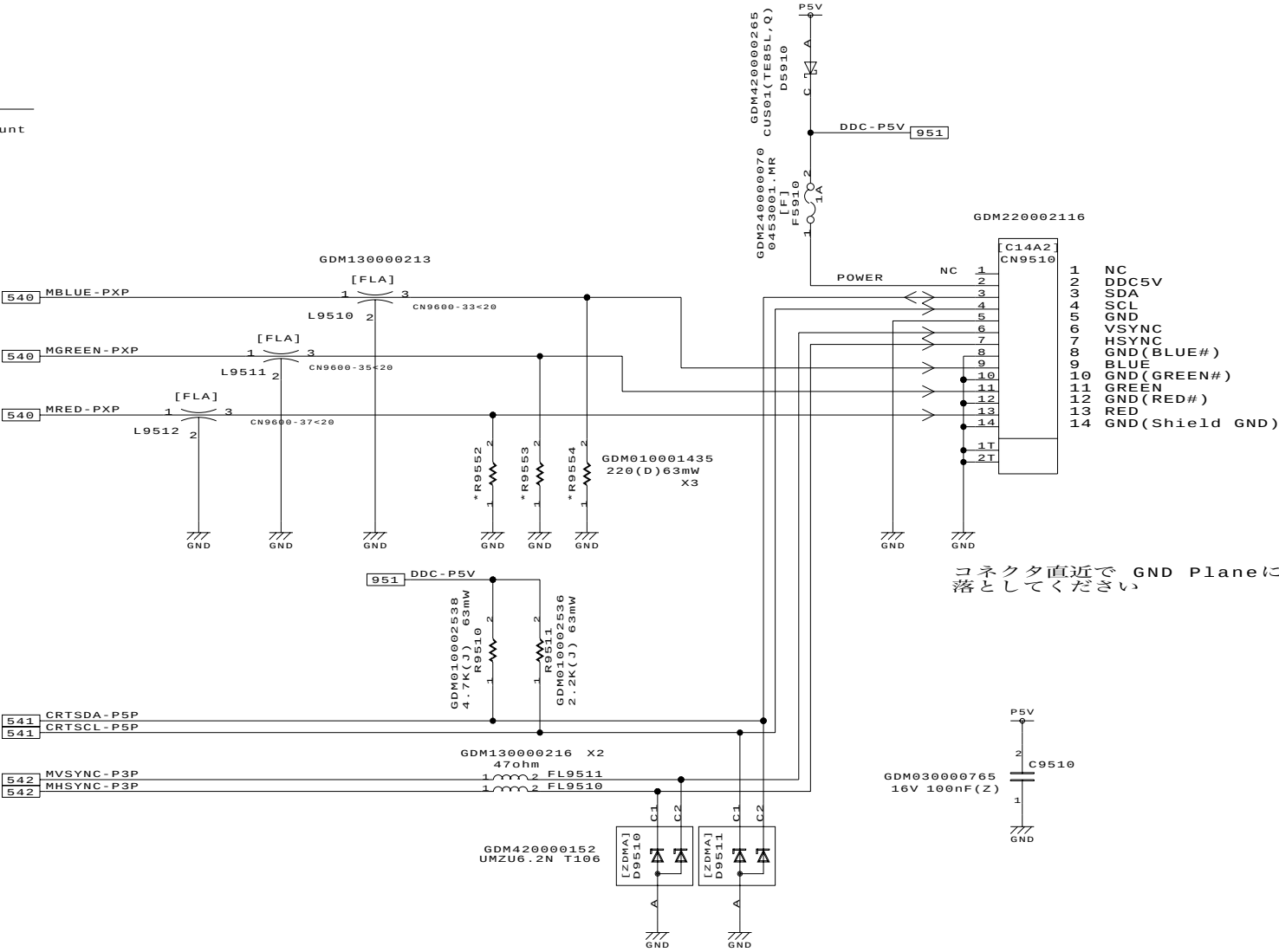


MEC1609書き換え用



仕向け設定有

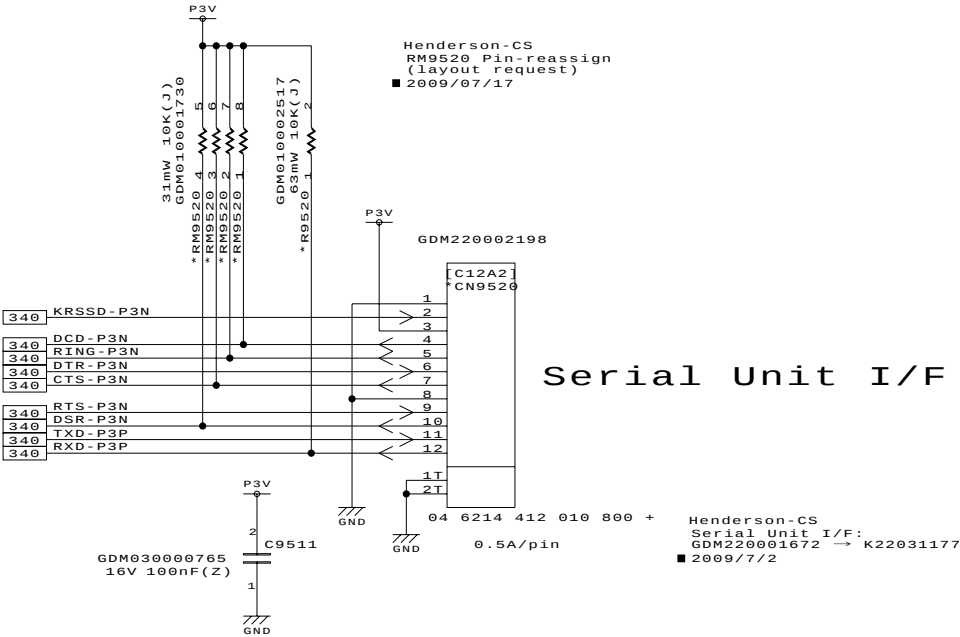
	Dock	
	Yes	No
R9552-R9554	Mount	NotMount



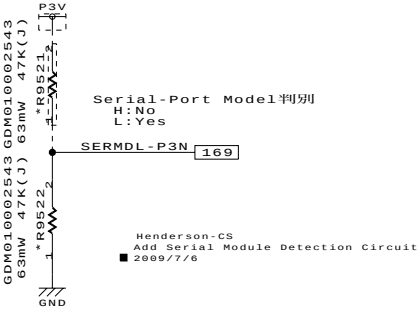
REF: Moscow10C (VP)

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Naruse/T.OCHIAI	FHNSY1	RGB Unit I/F	951	124	00	360069769

仕向け設定有

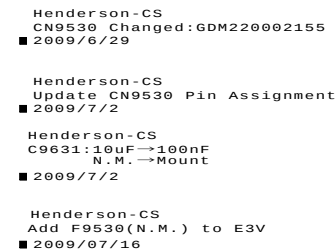


Serial Unit I/F



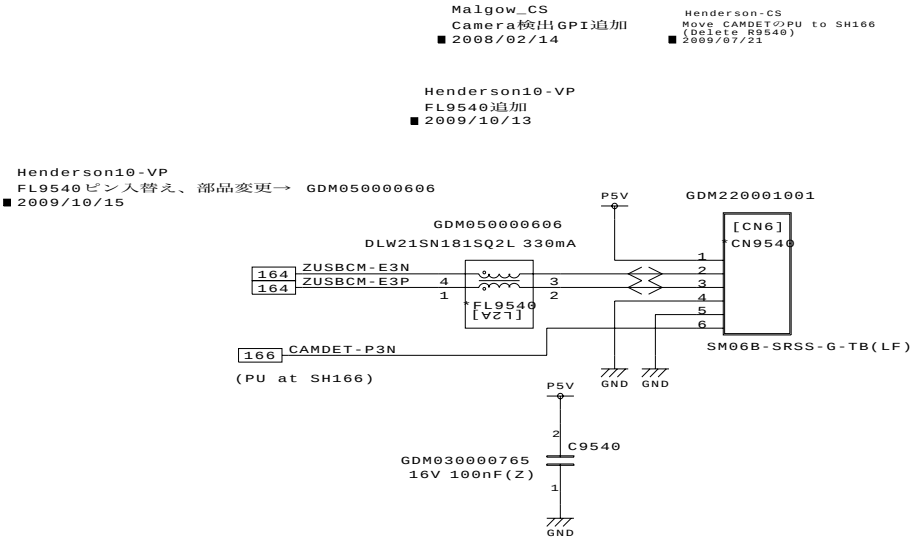
REF:Malgow(VP) . FG6IN1

DESIGNED BY	2009/10/15	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
T.Ichimura		FHNSY1	Serial Unit I/F	952	125	00	360069769



TOSHIBA CORPORATION

仕向け設定有



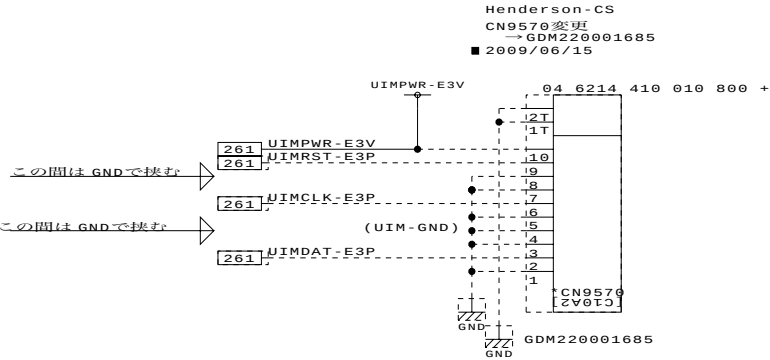
DESIGNED BY T. Ichimura		TITLE FHNSY1	FUNCTION WebCam I/F	SH.NO. 954	PAGE NO. 127	REV.MARK 00	DRAWING.NO. 360069769
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F



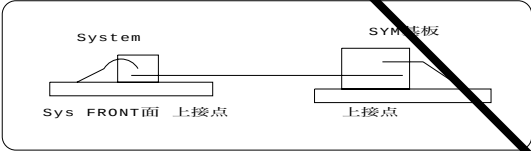
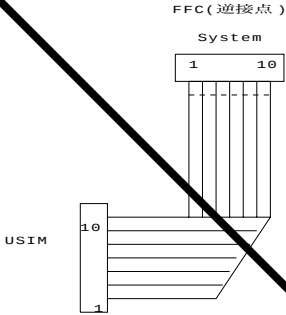
2009.10.15 17:09

仕向け設定有



Henderson-CS
CN9570 Pin Assign Reversed
■2009/07/08

pin2 pin6 pin7は不使用のため削除。
2005/09/30 五十嵐



■ REF:Maigow(VP) . FGGIN2

DESIGNED BY	TITLE	FUNCTION	SH.NO.	PAGE NO.	REV.MARK	DRAWING.NO.
S.Anwar/T.Ichimura	FHNSY1	FHNSM* I/F	957	129	00	360069769

