

Dasher-1 Block Diagram

LDB-1 91.4KH01.001

Jan. 10, 2011

PCB Layer Stackup

L1:Component
L2:GND
L3:Signal 1
L4:VCC
L5:Signal 2
L6:Signal 3
L7:GND
L8:Component

Battery Charger/Selector

BQ24742 74

INPUTS		OUTPUTS	
DOCK_PWR20		CHARGER_OUT12	

System DC/DC
TPS51220ARSN 78

VINT20	VCC5M
	VCC3M

CPU DC/DC
VT1316/VT1317 79

VCC5V_OUT	VCCCPUCORE
-----------	------------

GMCH GFX CORE
VT1317 81

VCC5V_OUT	VCCGFXCORE
-----------	------------

VCC1R5A
VT357FCX 86

VCC5V_OUT	VCC1R5A
-----------	---------

VCC0R75B
MAX1510 87

VCC1R5A	VCC0R75B
---------	----------

VCC1R8B
TPS62060 89

VCC5V_OUT	VCC1R8B
-----------	---------

VCC1R05AMT
VT356FCX 85

VCC5V_OUT	VCC1R05LAN
-----------	------------

VCC1R05B_VTT
VT357FCX 84

VCC5V_OUT	VCC1R05B_VTT
-----------	--------------

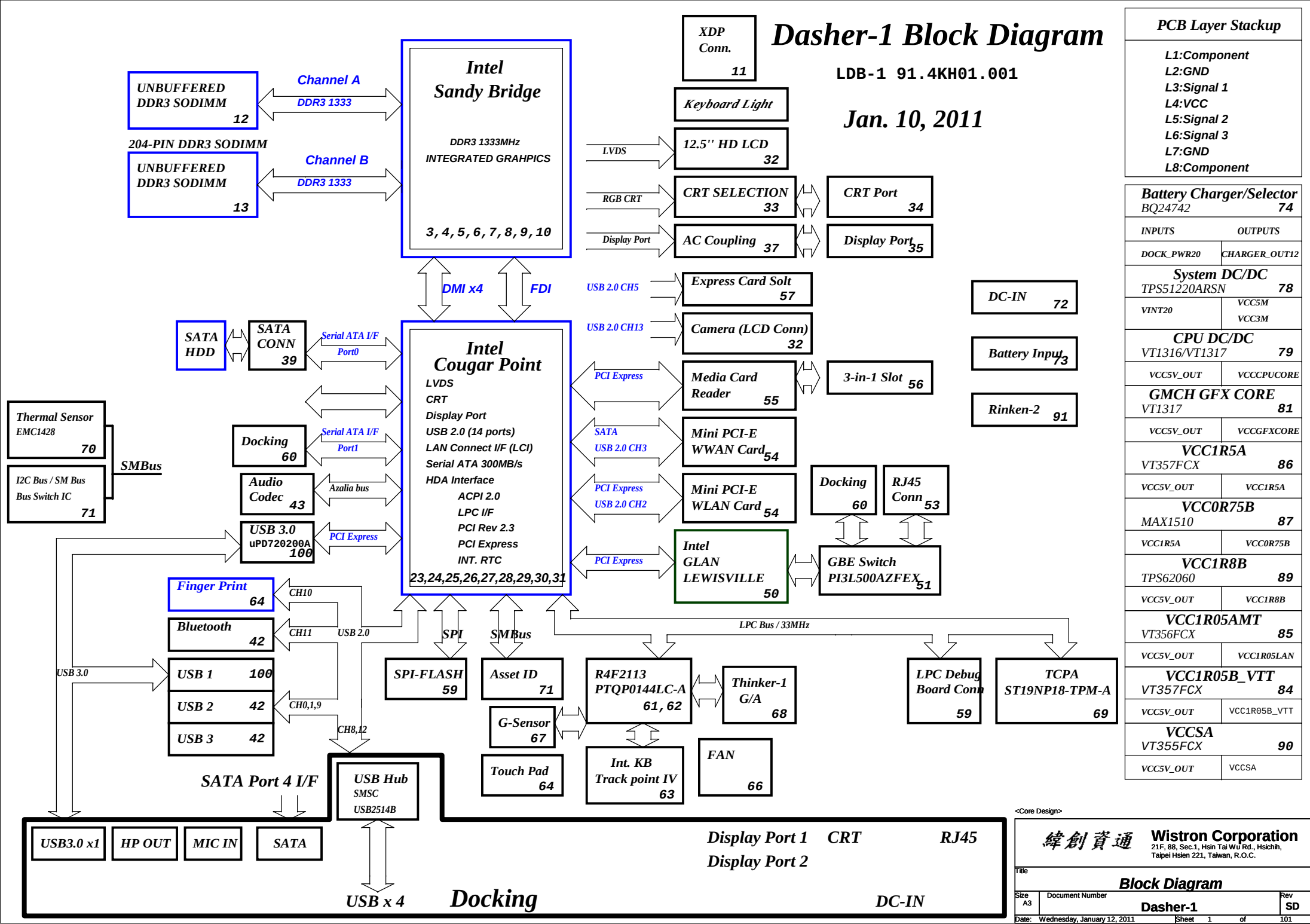
VCCSA
VT355FCX 90

VCC5V_OUT	VCCSA
-----------	-------

<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Block Diagram			
Size A3	Document Number	Rev SD	
Date: Wednesday, January 12, 2011		Sheet 1	of 101



RESISTOR

Symbol name	Value	Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	Rating 0402=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
10KR3	10K Ohm	If no letter, it means J: 5%	1/16W, 75V	0603
33D3R5	33.3 Ohm	If no letter, it means J: 5%	1/10W, 100V	0805
1KR3F	1K Ohm	F: 1%	1/16W, 75V	0603

The naming rule is value + R + size + tolerance
 For the value, it can be read by the number before R. (R means resistor)
 For the tolerance, it can be read from the last letter.
 For the rating, we don't show on the symbol name.
 For the size, R2=>0402, R3=>0603, R5=>0805,....

CAPACITOR

Symbol name	Value	Tolerance (M: +/-20, K: +/-10, Z: +80/-20)	Rating	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
SCD1U10V2MX-1	0.1uF	M/X5R	10V	0402
SC10U6D3V5MX	10uF	M/X5R	6.3V	0805
SC2D2U16V5ZY	2.2uF	Z/Y5V	16V	0805

The naming rule is
Capacitor type + value + rating + size + tolerance + material
SCD1U10V2MX-1
SC=> SMT Ceramic, TC=> POS cap or SP cap
D1U => 0.1uF
10V => the voltage rating is 10V
2=> 0402, 3=>0603, 5=>0805
M=>tolerance M, K, Z
X=> X7R/X5R, Y=> Y5V
-1 => symbol version, nonsense to EE characteristic

PLANAR_ID[3..0]

IBEXPEAK-M	39	38	48	49	Planar ID Version	Planar PCB Version
PLANAR_IDn	3	2	1	0		
	0	0	0	0		
	0	0	0	1		
	0	0	1	0	Dasher-1 Pre-DV	SA
	0	0	1	1	Dasher-1 SDV/UT	SB
	0	1	0	0	Dasher-1 MFVT	SC
	0	1	0	1	Dasher-1 FVT	SD
	0	1	1	0	Dasher-1 M-SIT	SE
	0	1	1	1	Dasher-1 SIT	SF
	1	0	0	0	Dasher-1 SVT	-1

EC HISTORY

[illegible]

<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	

Reference

Size
A3

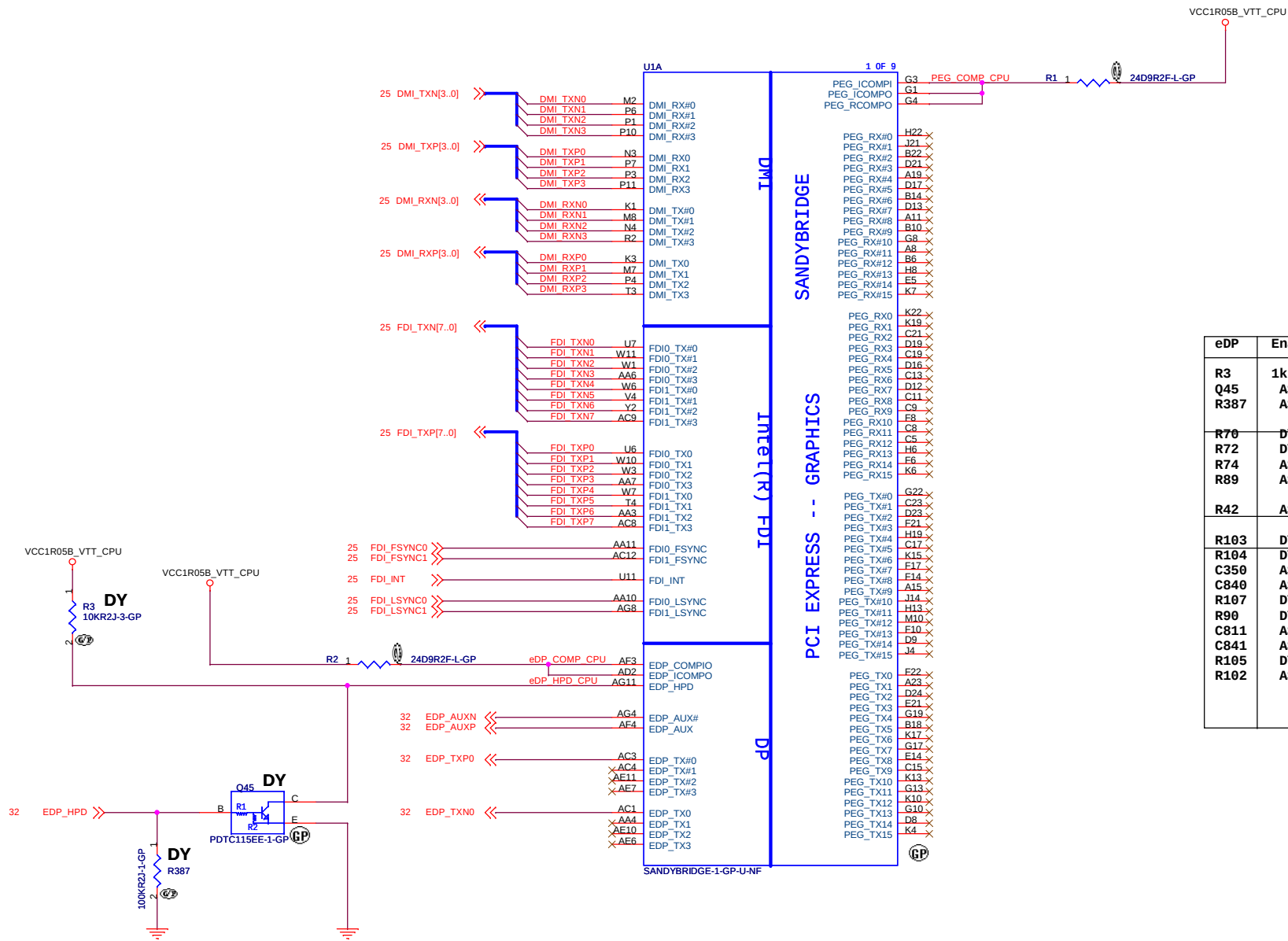
Document Number

Dasher-1

Rev	SD
-----	----

Date: Monday, January 10, 2011

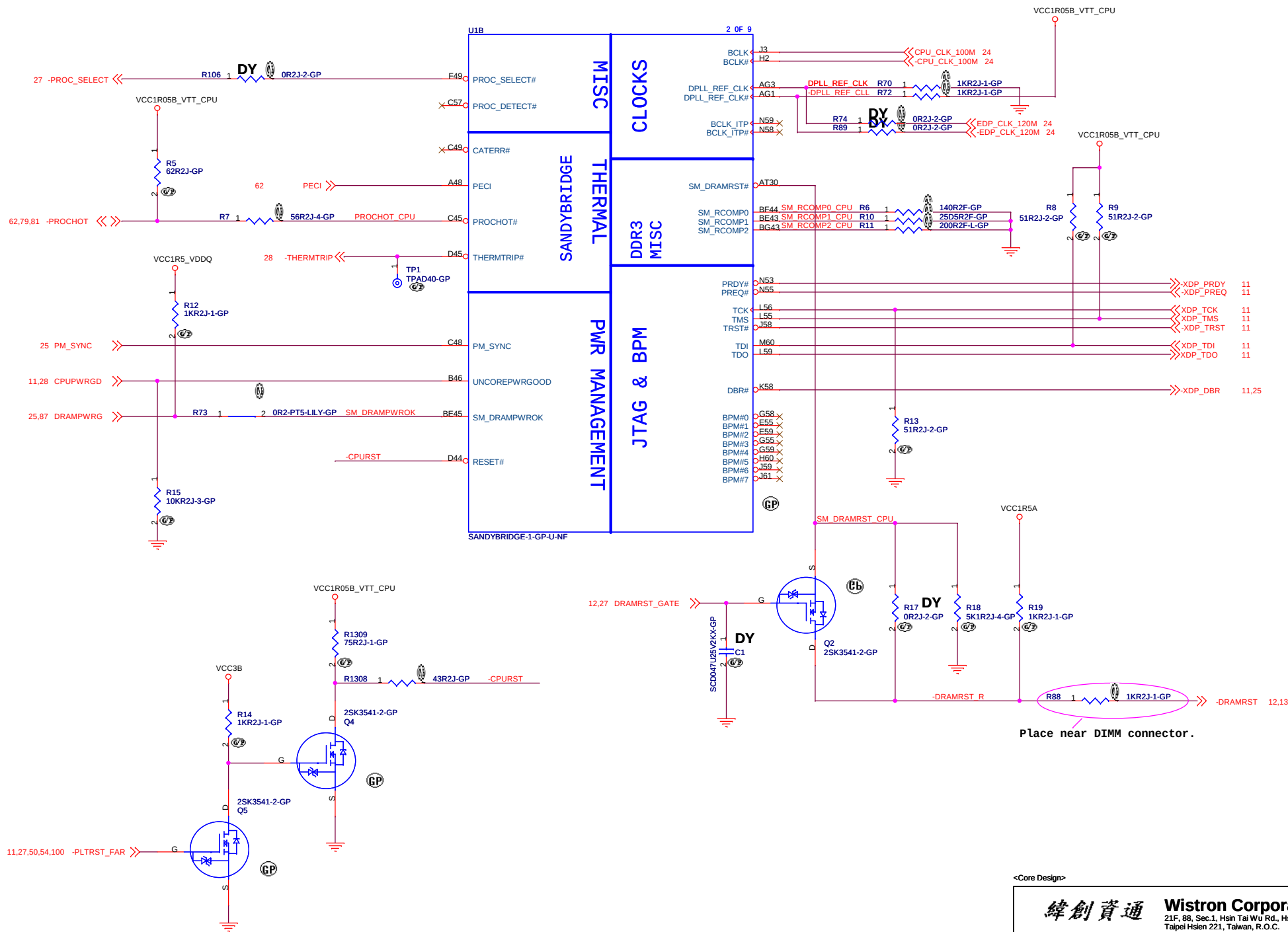
Sheet 2 of 101



Logic

eDP	Enable	Disable
R3 Q45 R387	1k-ohm ASM ASM	10k-ohm DY DY
R70 R72 R74 R89	DY DY ASM ASM	ASM ASM DY DY
R42	ASM	DY
R103 R104 C350 C840 R107 R90 C811 C841 R105 R102	DY DY ASM ASM DY DY ASM ASM DY ASM	ASM ASM DY ASM ASM DY DY ASM DY

<Core Design>



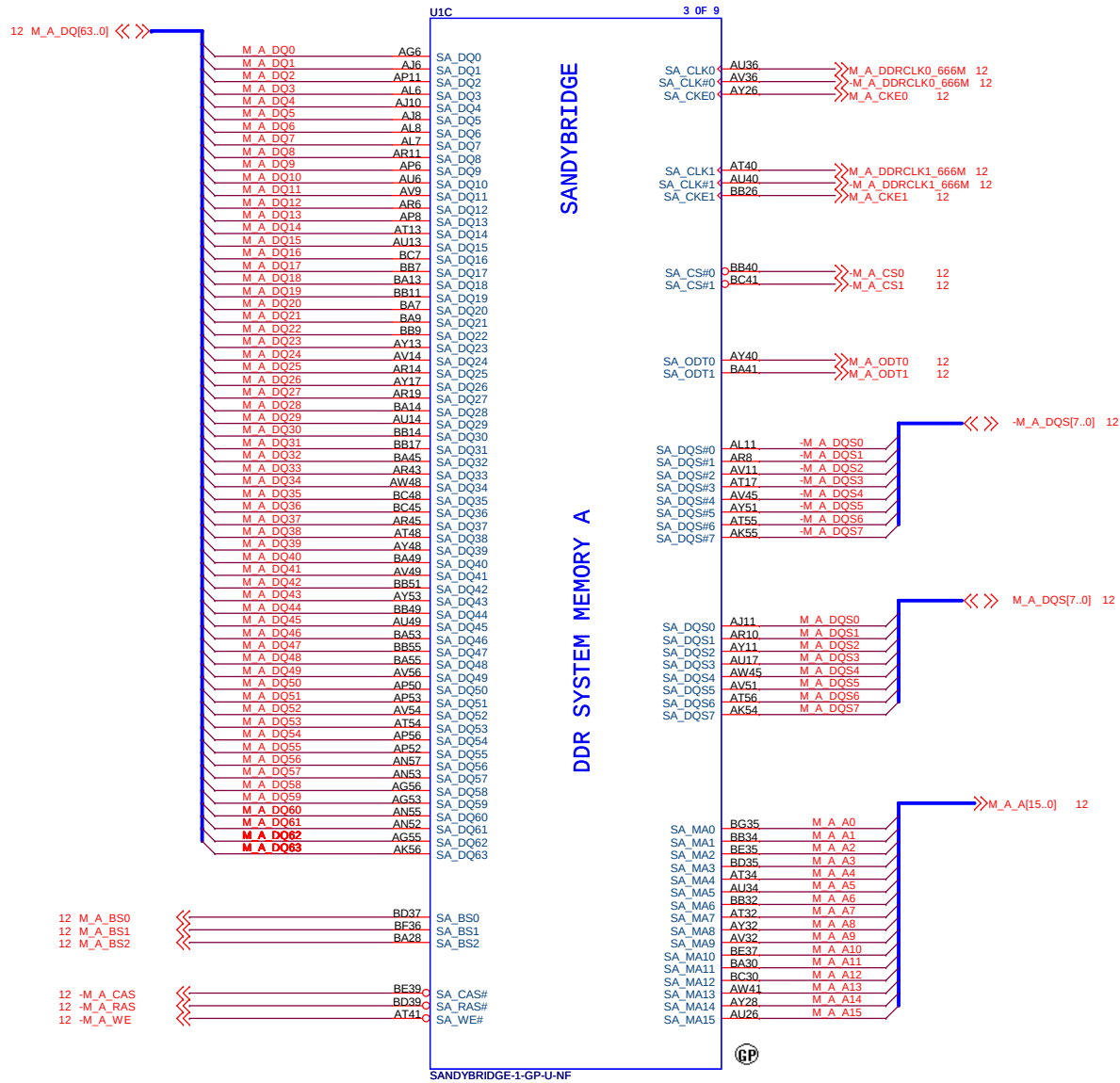
<Core Design>

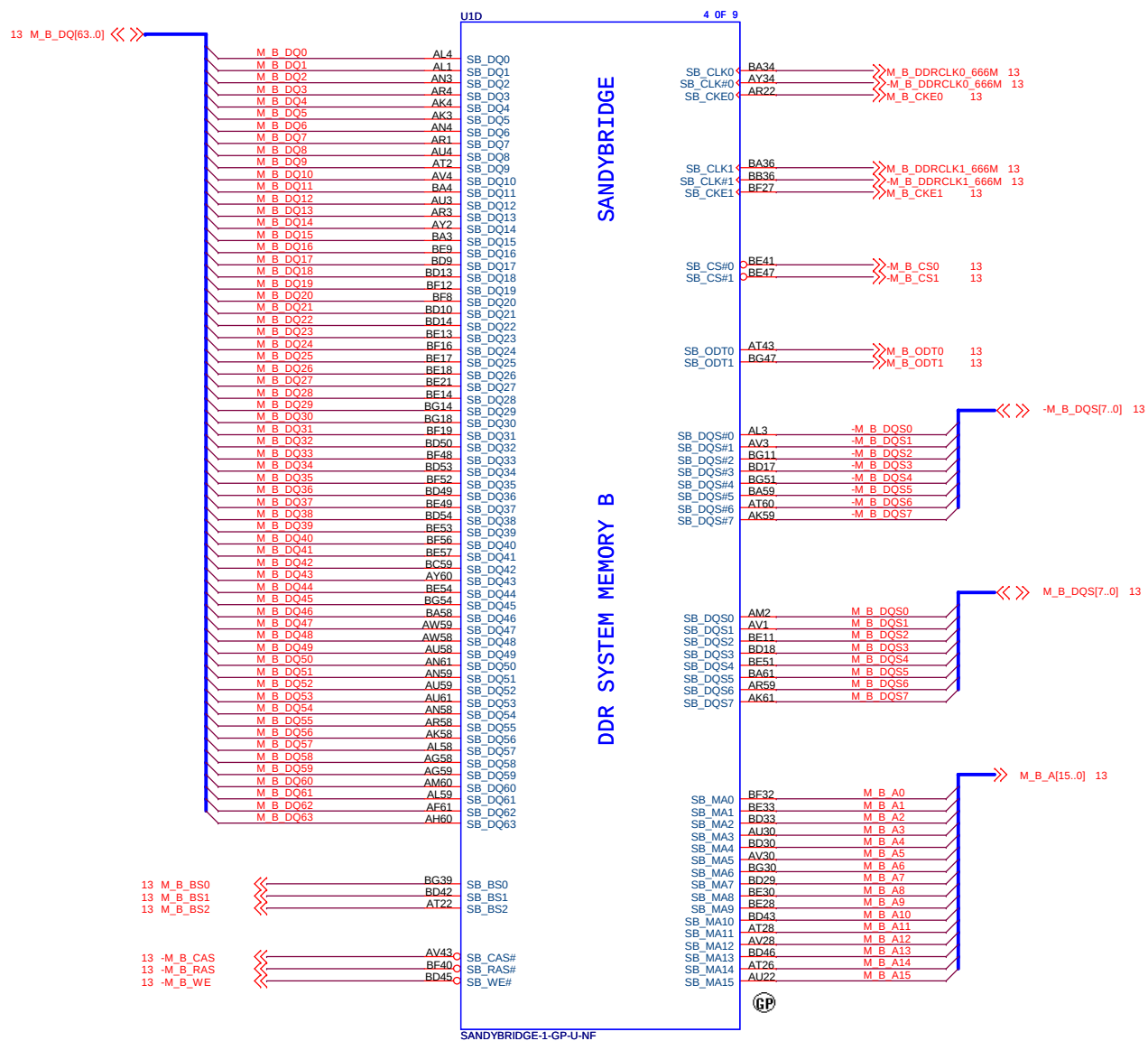
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

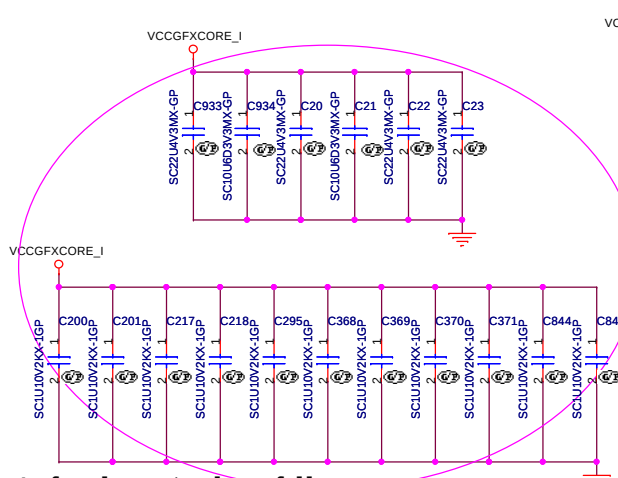
Title **CPU(2/8):CLK/MISC/JTAG**

Size A3	Document Number Dasher-1	Rev SD
------------	------------------------------------	------------------

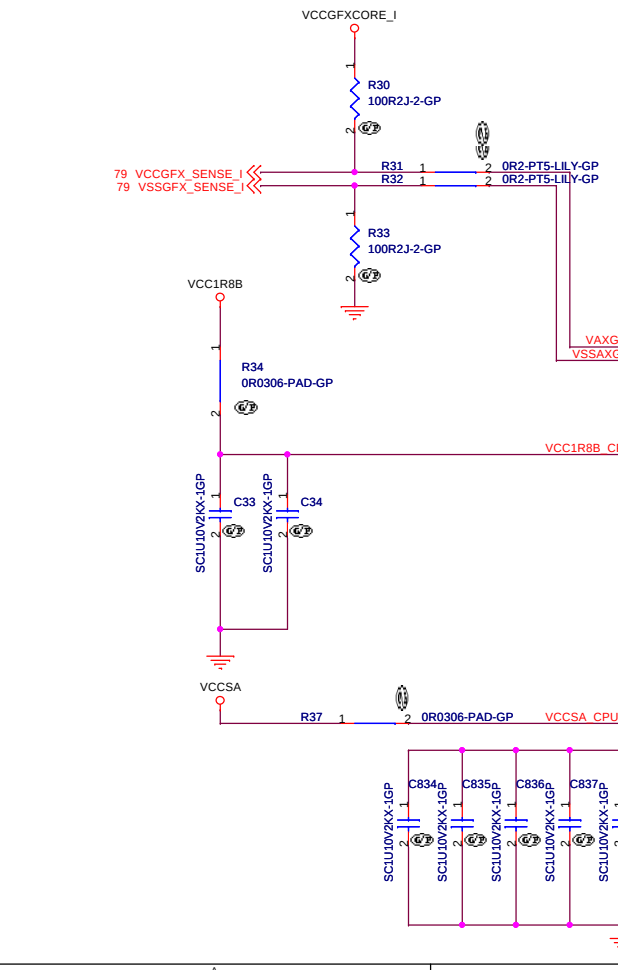
Date: Monday, January 10, 2011 Sheet 4 of 101







As for placement, please follow
"Huron River Platform Power Delivery Design Guide Rev1.0"
 (Intel DocNo.439028)



- AA46 VAXG
- AB47 VAXG
- AB50 VAXG
- AB51 VAXG
- AB52 VAXG
- AB53 VAXG
- AB55 VAXG
- AB56 VAXG
- AB58 VAXG
- AB59 VAXG
- AC61 VAXG
- AD47 VAXG
- AD48 VAXG
- AD50 VAXG
- AD51 VAXG
- AD52 VAXG
- AD53 VAXG
- AD55 VAXG
- AD56 VAXG
- AD58 VAXG
- AD59 VAXG
- AE46 VAXG
- N45 VAXG
- P47 VAXG
- P48 VAXG
- P50 VAXG
- P51 VAXG
- P52 VAXG
- P53 VAXG
- P54 VAXG
- P56 VAXG
- P61 VAXG
- T48 VAXG
- T58 VAXG
- T59 VAXG
- U61 VAXG
- V47 VAXG
- V48 VAXG
- V50 VAXG
- V51 VAXG
- V52 VAXG
- V53 VAXG
- V55 VAXG
- V56 VAXG
- V58 VAXG
- V59 VAXG
- W50 VAXG
- W51 VAXG
- W52 VAXG
- W53 VAXG
- W55 VAXG
- W56 VAXG
- W61 VAXG
- Y48 VAXG
- Y61 VAXG

- L17 VCCSA
- L21 VCCSA
- N16 VCCSA
- N20 VCCSA
- N22 VCCSA
- P17 VCCSA
- P20 VCCSA
- R16 VCCSA
- R18 VCCSA
- R21 VCCSA
- U15 VCCSA
- V16 VCCSA
- V17 VCCSA
- V18 VCCSA
- V21 VCCSA
- W20 VCCSA

POWER

GRAPHICS

SENSE LINES

1.8V RAIL

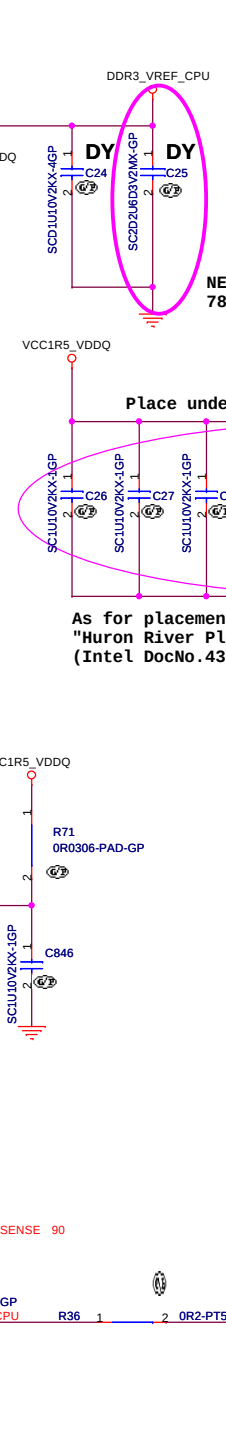
SA RAIL

QUIET RAILS

SENSE LINES

SA RAIL

- AJ28 VDDQ
- AJ33 VDDQ
- AJ36 VDDQ
- AJ40 VDDQ
- AL30 VDDQ
- AL34 VDDQ
- AL38 VDDQ
- AL42 VDDQ
- AM33 VDDQ
- AM36 VDDQ
- AM40 VDDQ
- AN30 VDDQ
- AN34 VDDQ
- AN38 VDDQ
- AR26 VDDQ
- AR28 VDDQ
- AR30 VDDQ
- AR32 VDDQ
- AR34 VDDQ
- AR36 VDDQ
- AR40 VDDQ
- AV41 VDDQ
- AW26 VDDQ
- BA40 VDDQ
- BB28 VDDQ
- BG33 VDDQ



NEED SC2D2U4V2MX-1GP
 78.22510.5FL

Place under CPU

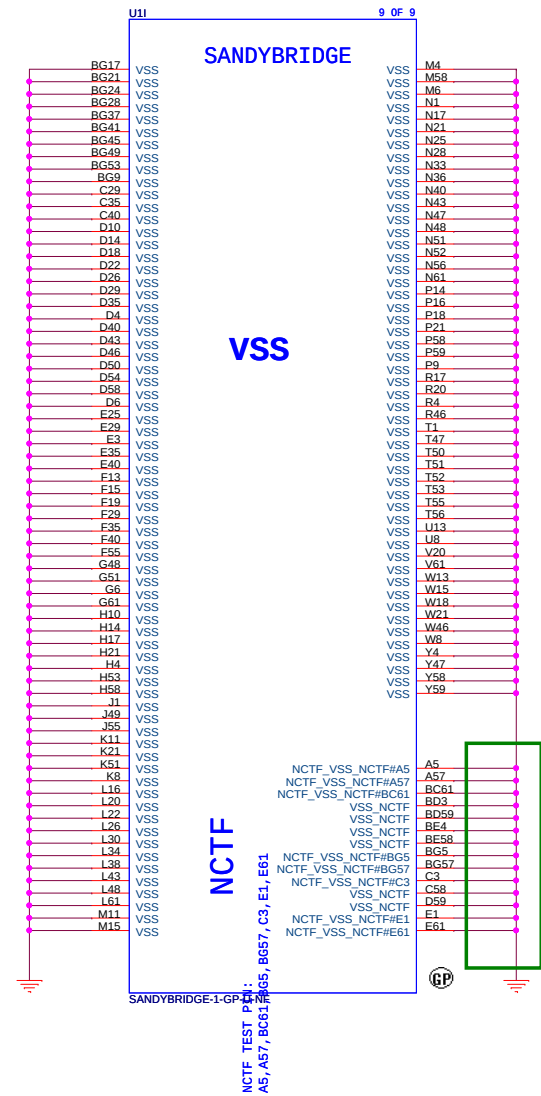
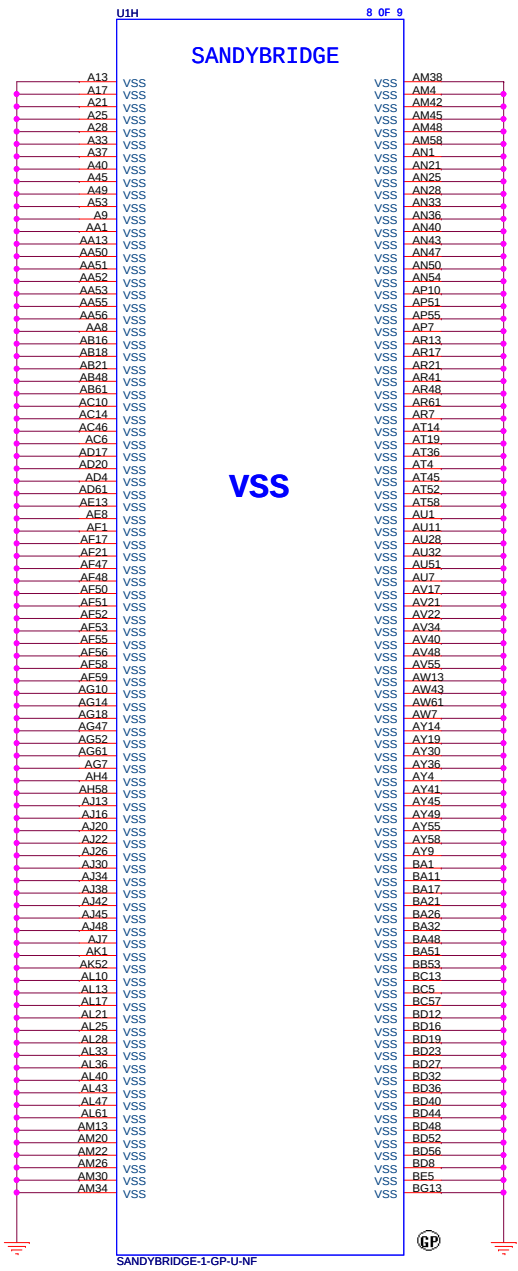
As for placement, please follow
"Huron River Platform Power Delivery Design Guide Rev1.0"
 (Intel DocNo.439028)

Wistron Corporation
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
 Taipei Hsien 221, Taiwan, R.O.C.

CPU(6/8):Graphics Power

Size A3 Document Number **Dasher-1** Rev SD

Date: Monday, January 10, 2011 Sheet 8 of 101



<Core Design>

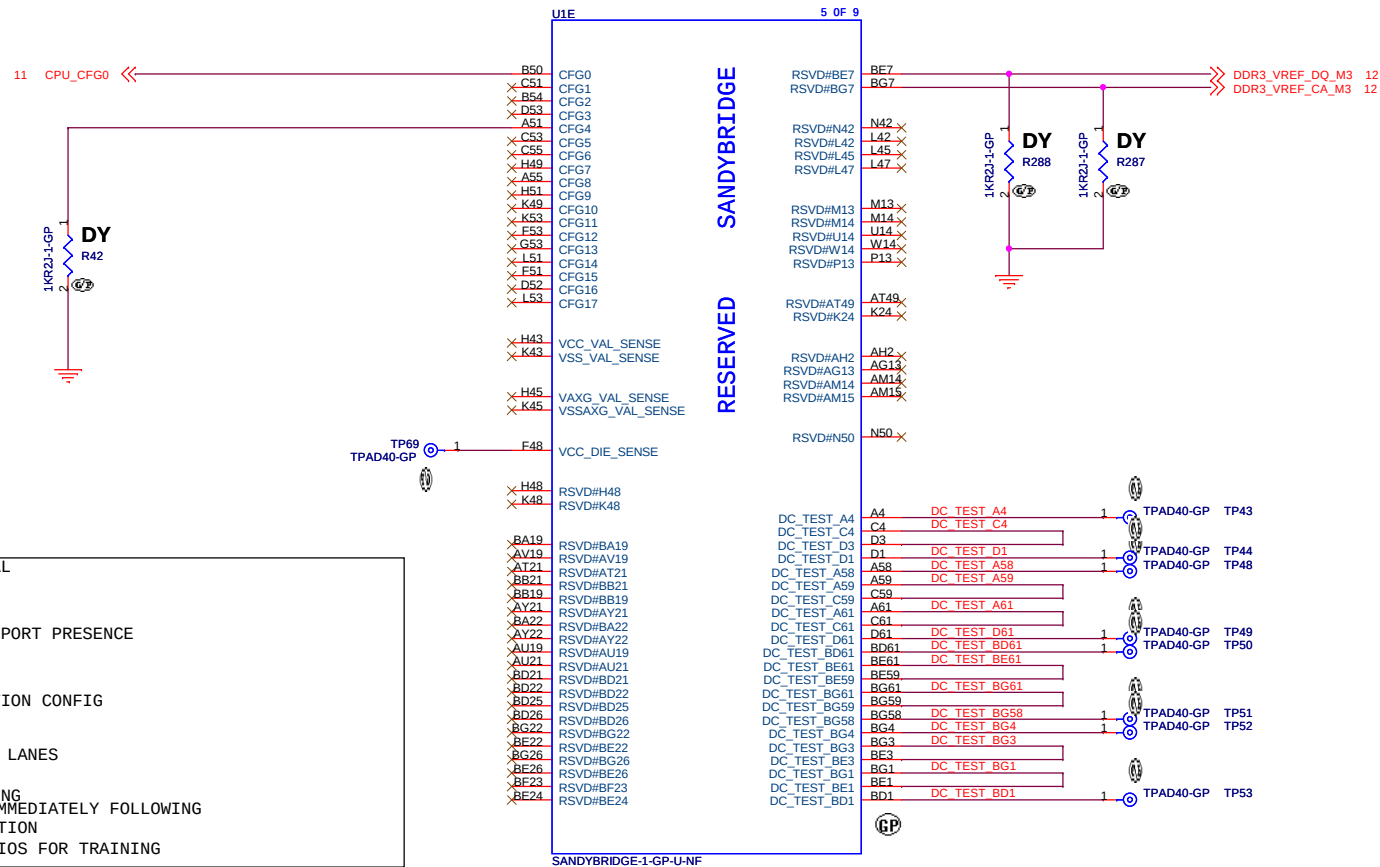
緯創資通 Wistron Corporation		
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title CPU(7/8):GND		
Size Custom	Document Number Dasher-1	Rev SD
Date: Monday, January 10, 2011	Sheet 9 of	101

CFG2 PEG LANE REVERSAL
1 NO-ASM : NORMAL
0 ASM : REVERSE

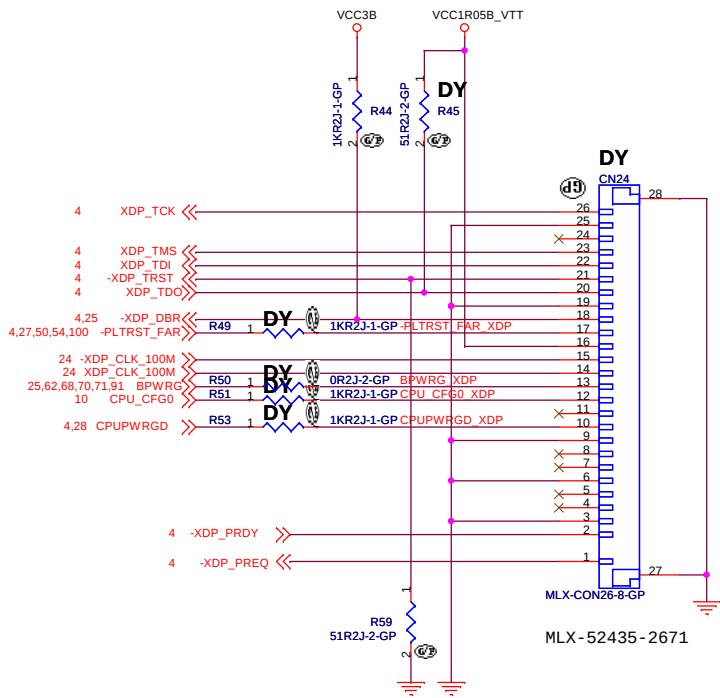
CFG4 Embedded DISPLAY PORT PRESENCE
1 NO-ASM : DISABLE
0 ASM : ENABLE

CFG[6:5] PEG BIFURCATION CONFIG
11 SINGLE 16 LANES
10 2x8 LANES
01 RESERVED
00 8 LANES AND 2x4 LANES

CFG7 PEG DEFER TRAINING
1 NO-ASM : TRAIN IMMEDIATELY FOLLOWING
XXRESETB DEASSERTION
0 ASM : WAIT FOR BIOS FOR TRAINING



<Core Design>

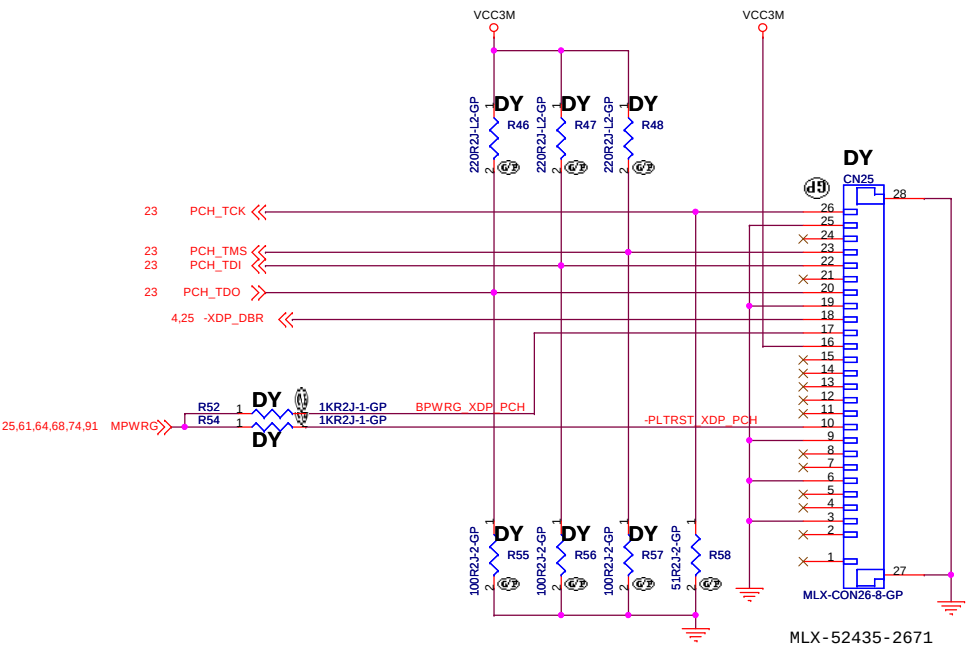


DEBUG Interface for Processor.

XDP1

		ENABLE	DISABLE
TD0	R45	ASM	DY
TRST#	R59	ASM	ASM
DBRST#	R44	ASM	ASM
RESET#	R49	ASM	DY
CFG0	R51	ASM	DY
PWRGD	R53	ASM	DY
BPWRG	R50	ASM	DY
	CN24	ASM	DY

SIT Logic



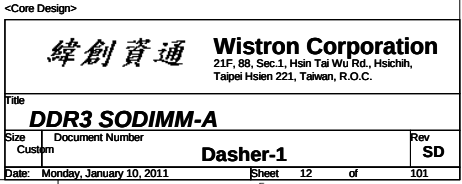
DEBUG Interface for PCH.

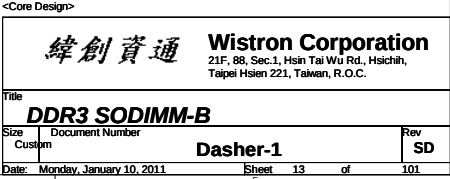
XDP2

		ENABLE	DISABLE
TD0	R46	220	DY
	R55	100	DY
TMS	R48	220	DY
	R57	100	DY
TDI	R47	220	DY
	R56	100	DY
TCK	R58	51	51
MPWRG	R52	ASM	DY
	R54	ASM	DY
	CN25	ASM	DY

SIT Logic

<Core Design>





BLANK

<Core Design>			
緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
BLANK			
Size	Document Number		Rev
A3	Dasher-1		SD
Date:	Monday, January 10, 2011	Sheet 14 of 101	1

BLANK

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size

Document Number

Rev

A3

Dasher-1

SD

Date: Monday, January 10, 2011

Sheet 15 of 101

BLANK

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size

Document Number

Rev

A3

Dasher-1

SD

Date: Monday, January 10, 2011

Sheet 16 of 101

BLANK

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size
A3

Document Number
Dasher-1

Rev
SD

Date: Monday, January 10, 2011

Sheet 17 of 101

BLANK

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size

Document Number

Rev

A3

Dasher-1

SD

Date: Monday, January 10, 2011

Sheet 18 of 101

BLANK

<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size
A3

Document Number

Dasher-1

Rev
SD

Date: Monday, January 10, 2011

Sheet 19 of 101

1

BLANK

<Core Design>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
BLANK			
Size	Document Number		Rev
A3	Dasher-1		SD
Date:	Monday, January 10, 2011	Sheet 20 of	101

BLANK

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size

A3

Document Number

Dasher-1

Rev

SD

Date: Monday, January 10, 2011

Sheet 1

21 of 101

BLANK

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size

A3

Document Number

Dasher-1

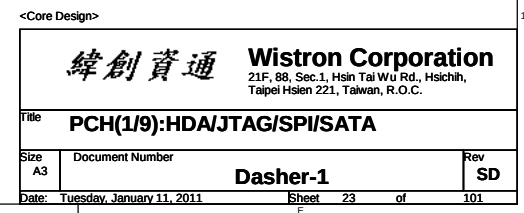
Rev

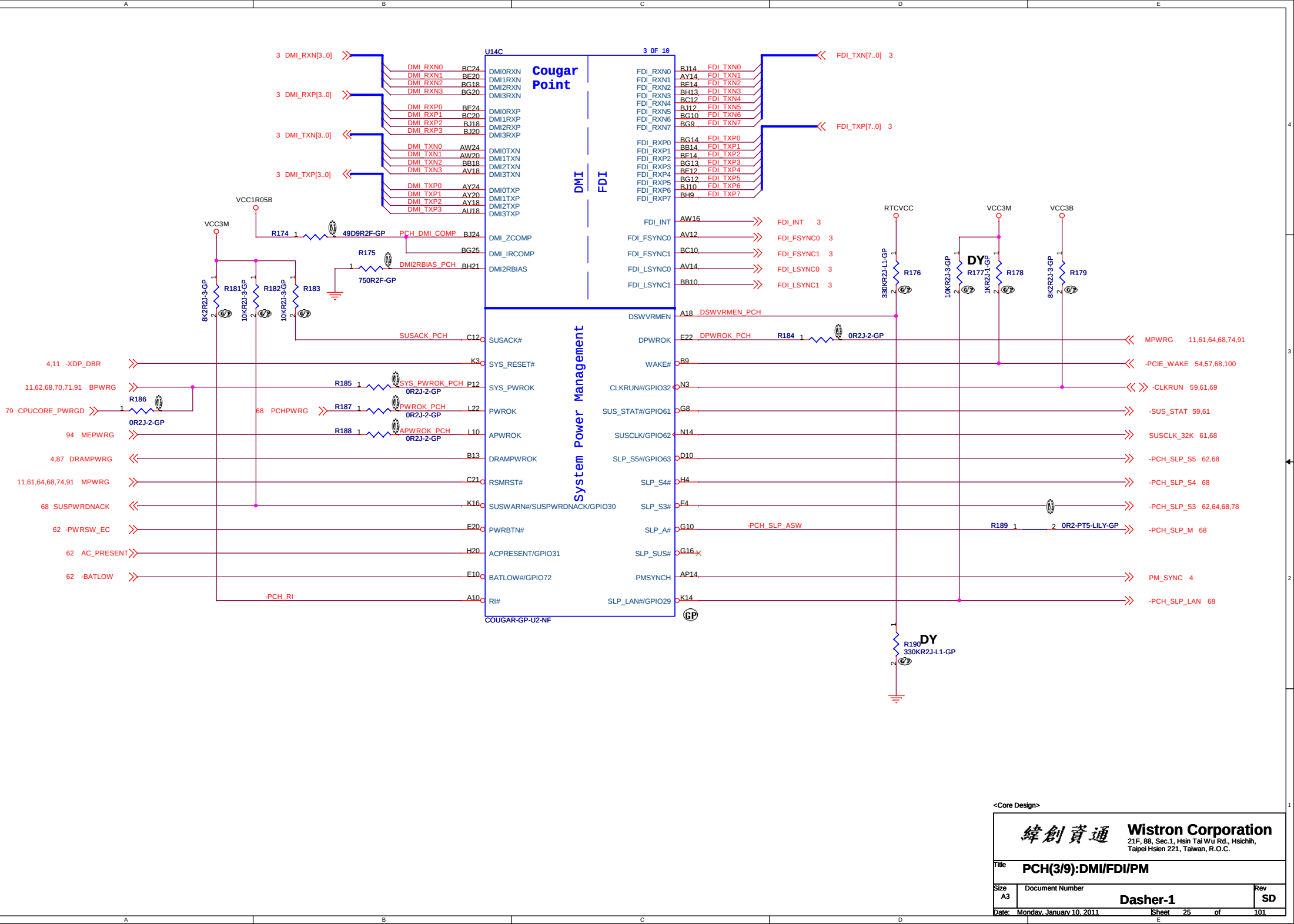
SD

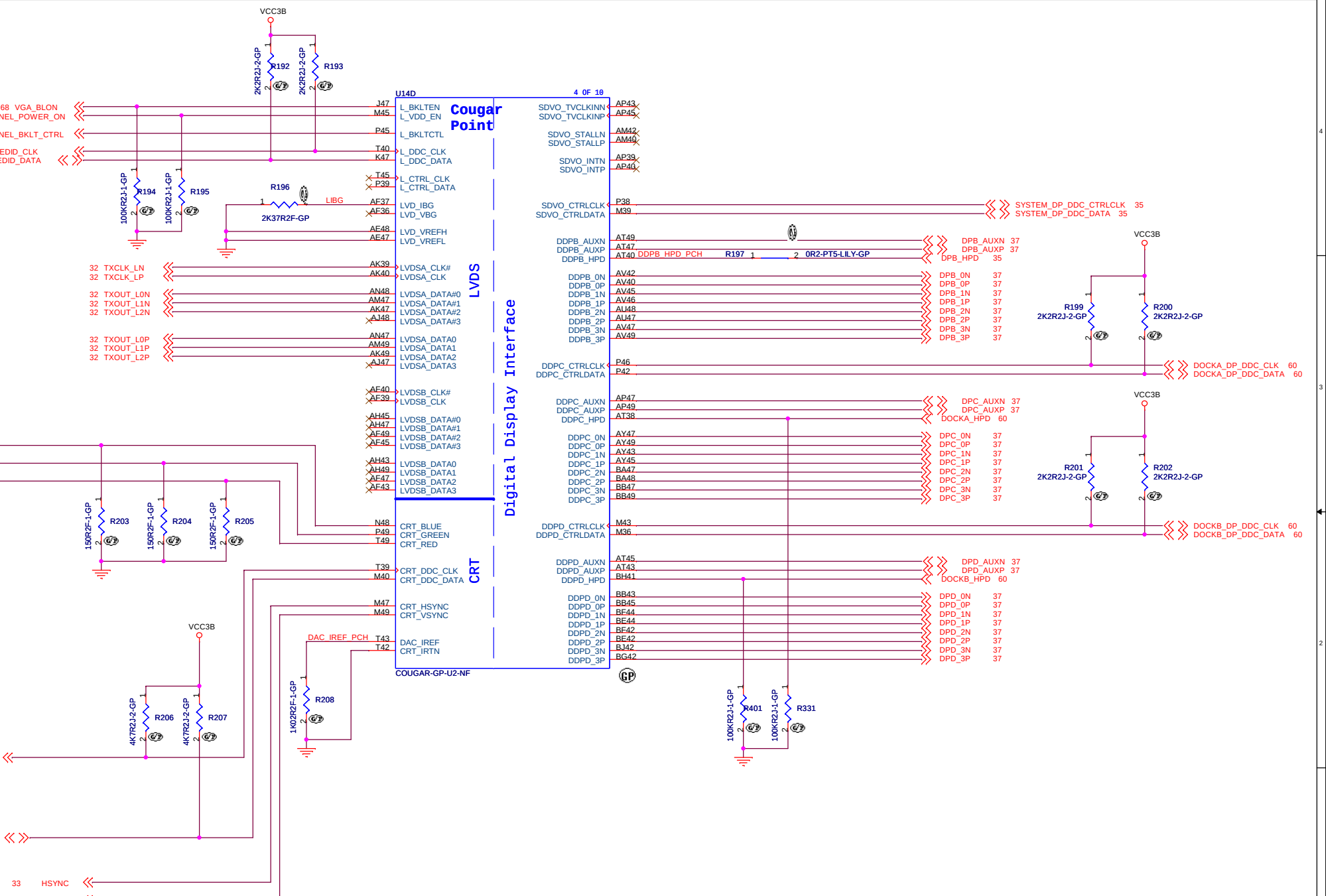
Date: Monday, January 10, 2011

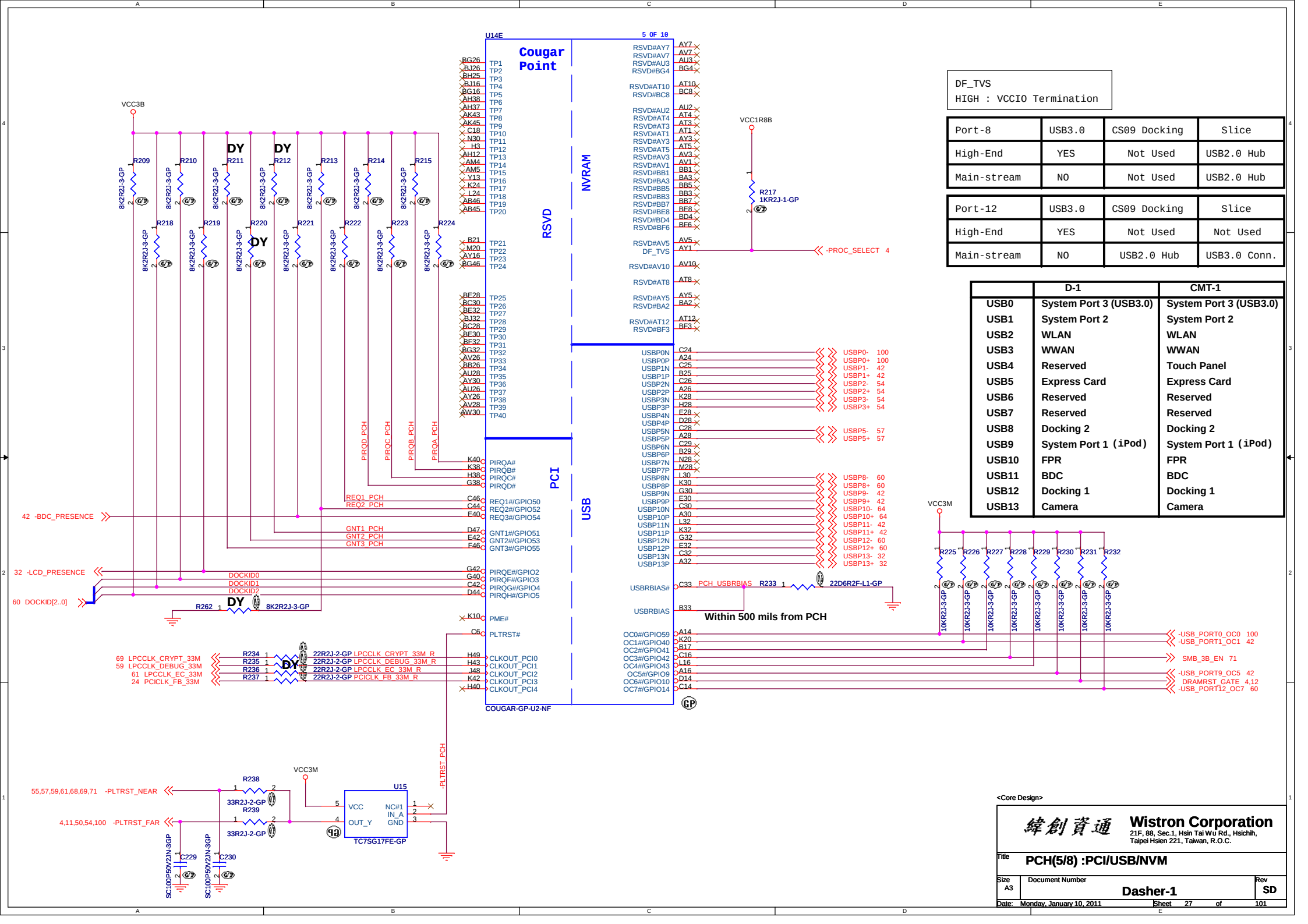
Sheet 22 of 101

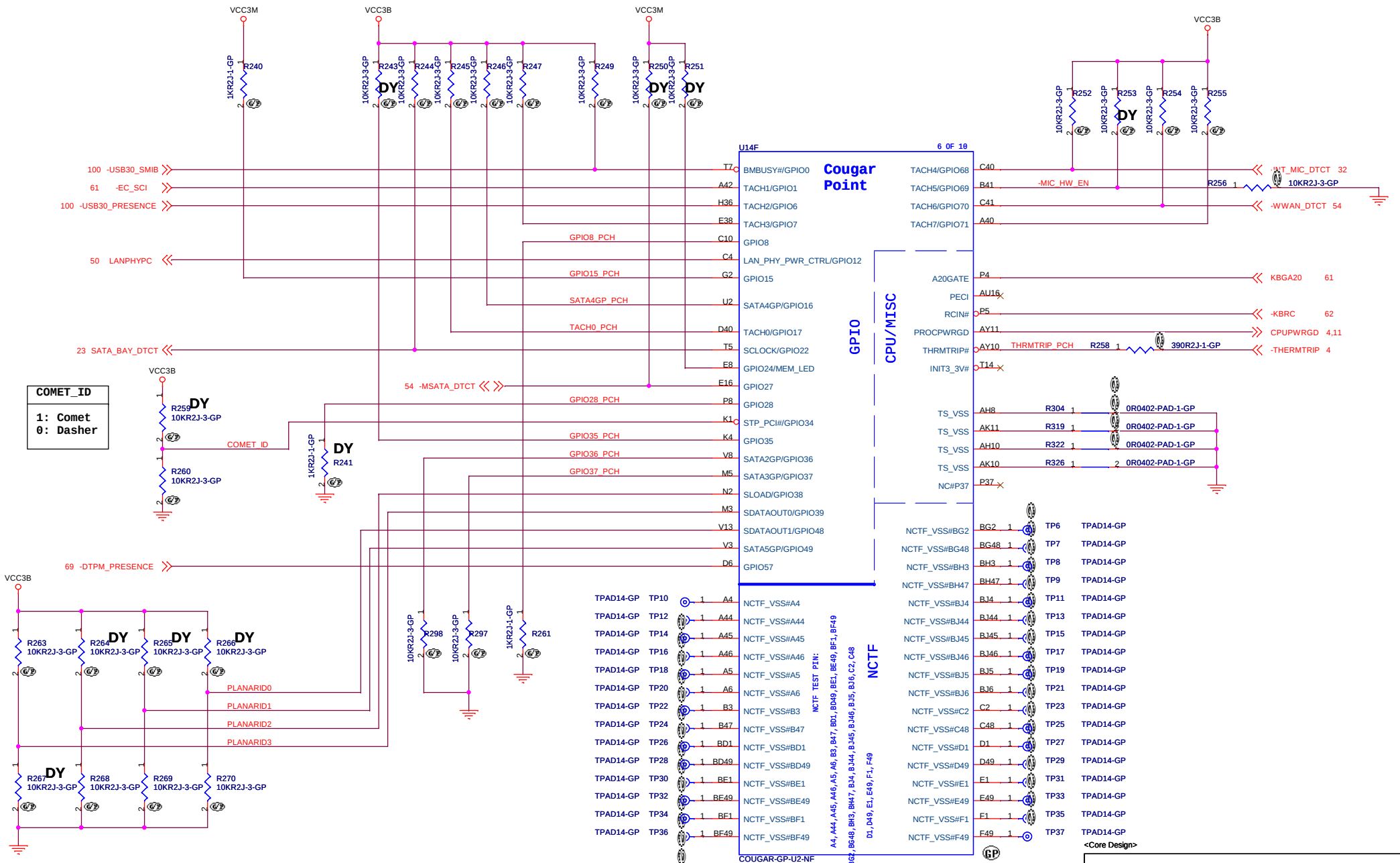
1











COMET_ID
1: Comet
0: Dasher

PLANAR ID: 1000 for SVT

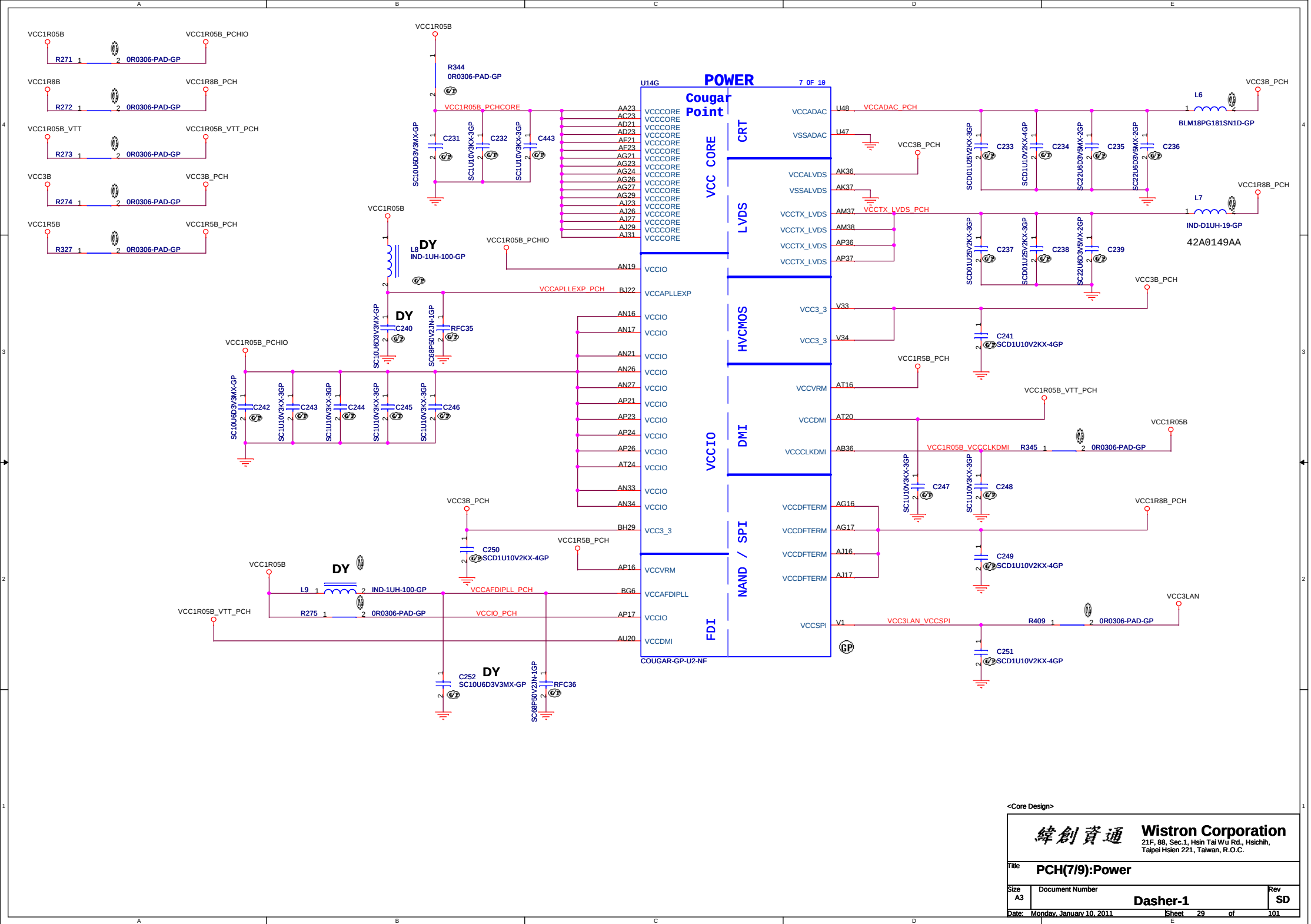
NCTF TEST PIN:
A4, A44, A45, A46, A5, A6, B3, B47, BD1, BD49, BE1, BE49, BF1, BF49
BG2, BG48, BH3, BH7, BJ4, BJ44, BJ45, BJ46, BJ5, BJ6, C2, C48
D1, D49, E1, E49, F1, F49

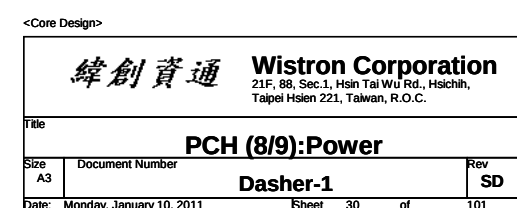
緯創資通

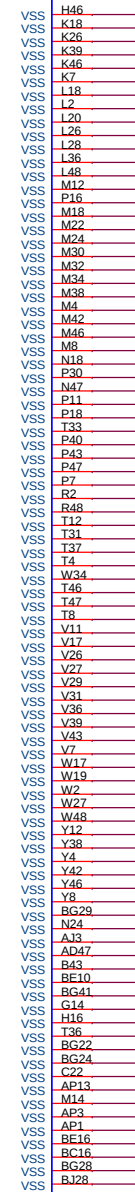
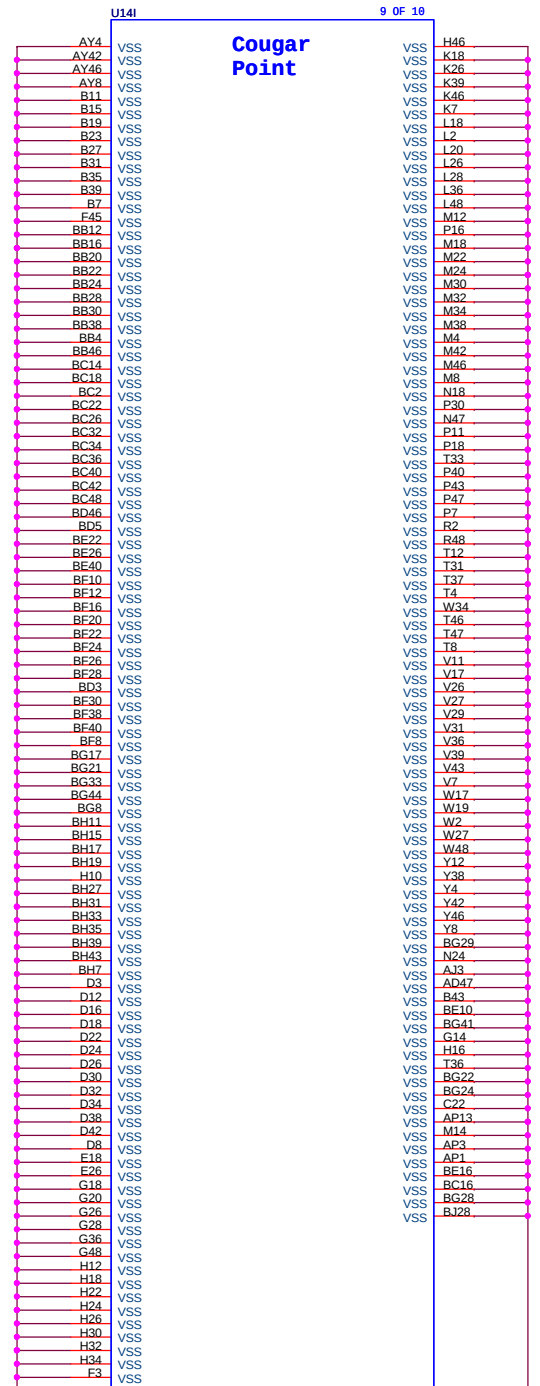
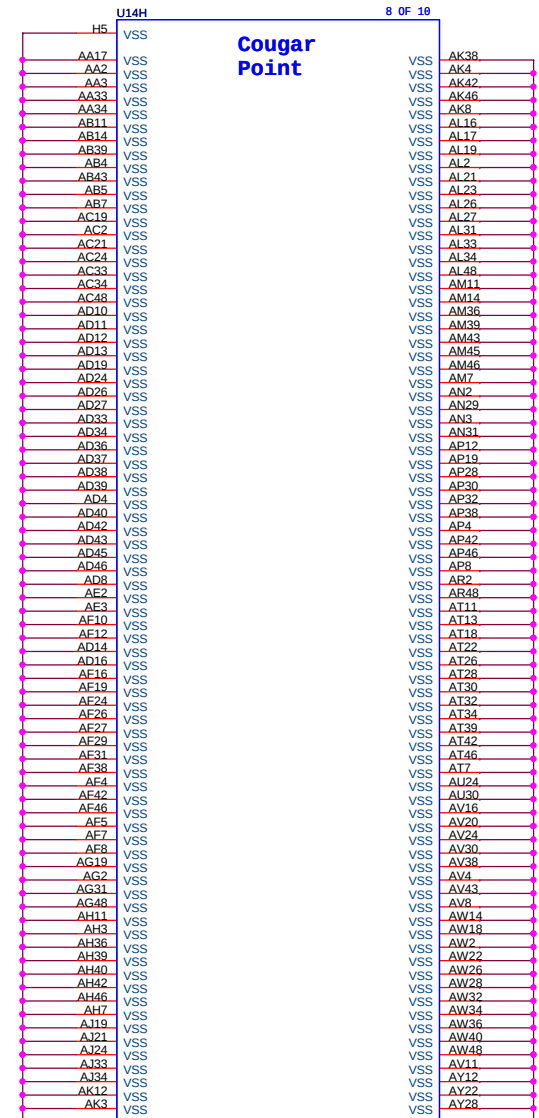
Wistron Corporation

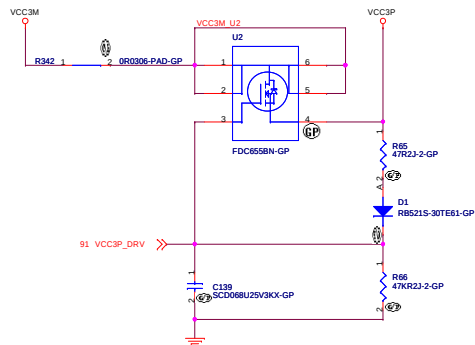
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title PCH(6/9):GPIO/NCTF/RSVD		
Size A3	Document Number Darsher-1	Rev SD
Date: Monday, January 10, 2011	Sheet 28 of 101	

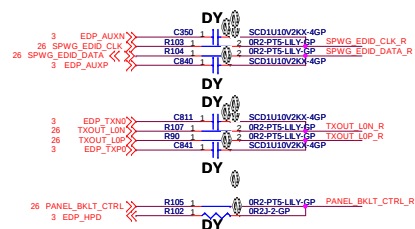
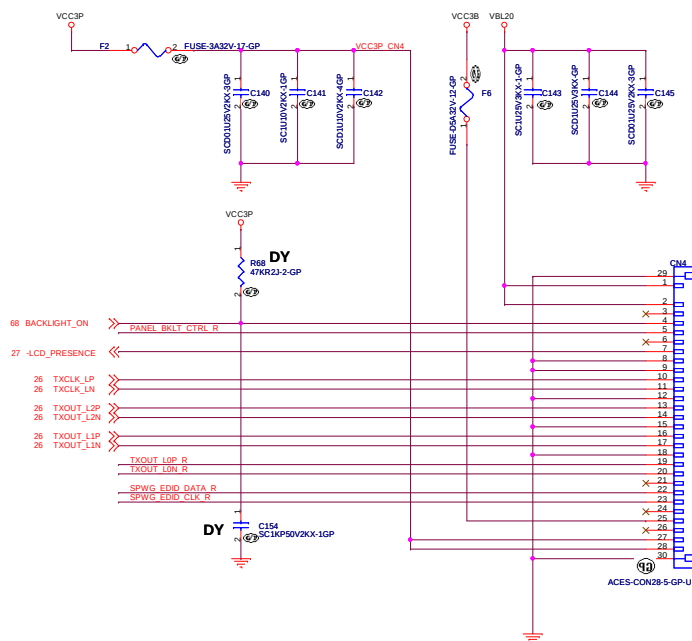




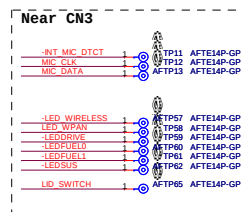
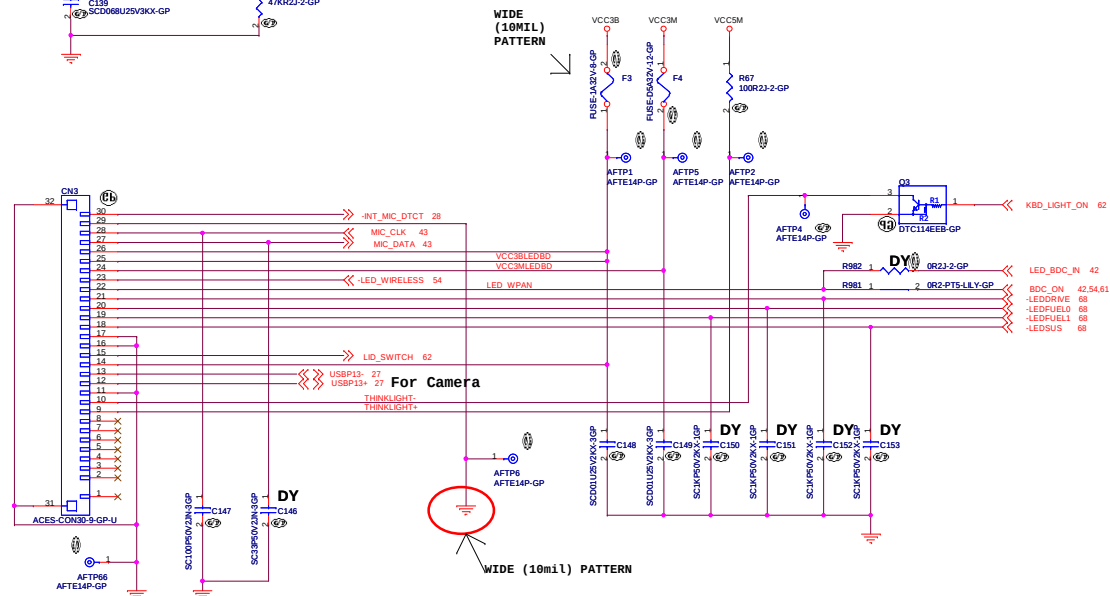


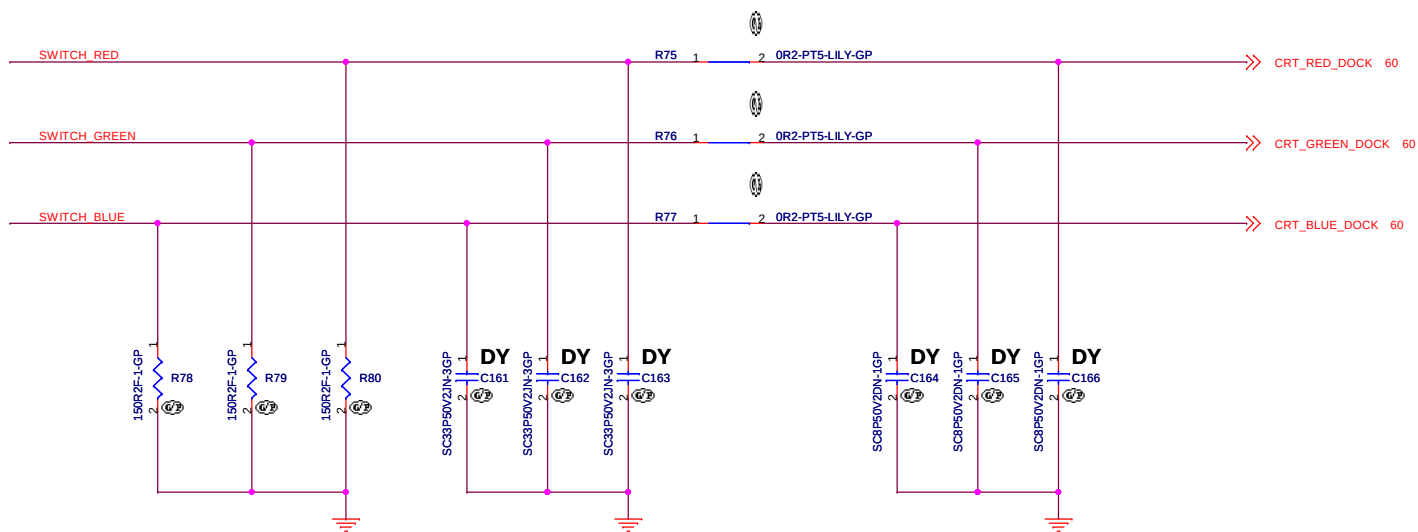


LCD Connector



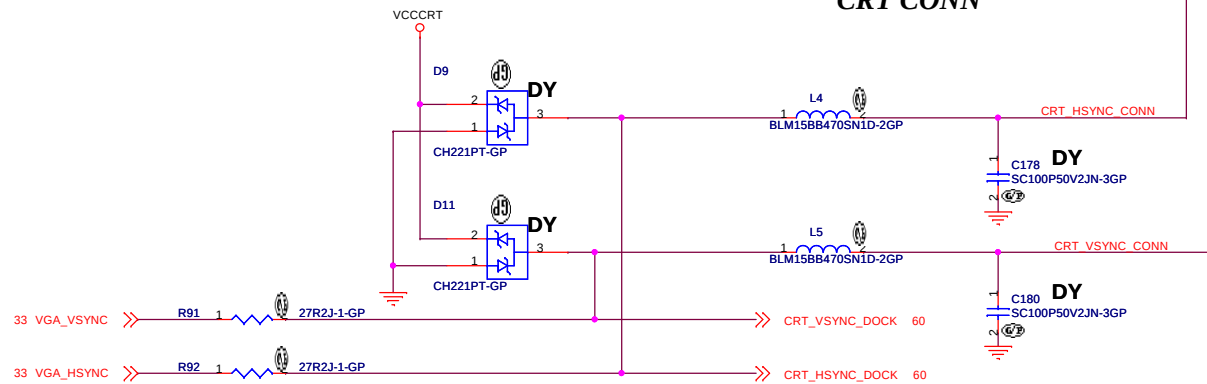
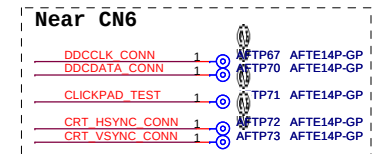
PolySW





Title			
CRT SELECTOR			
Size A3	Document Number Dasher-1		Rev SD
Date: Monday, January 10, 2011	Sheet 33	of	101

SPACING=20MIL



BLANK

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size

A3

Document Number

Dasher-1

Rev

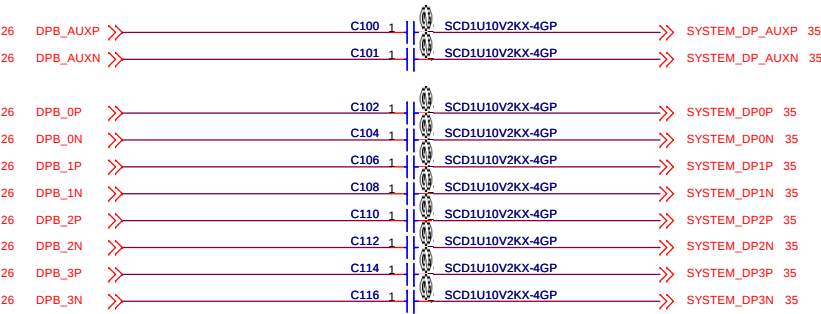
SD

Date: Monday, January 10, 2011

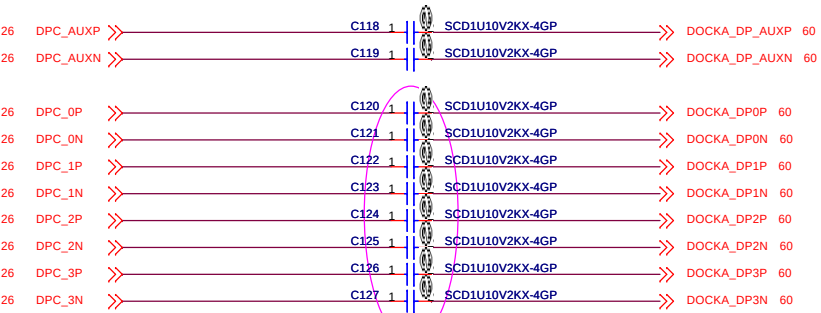
Sheet 36 of 101

1

System DP Connector

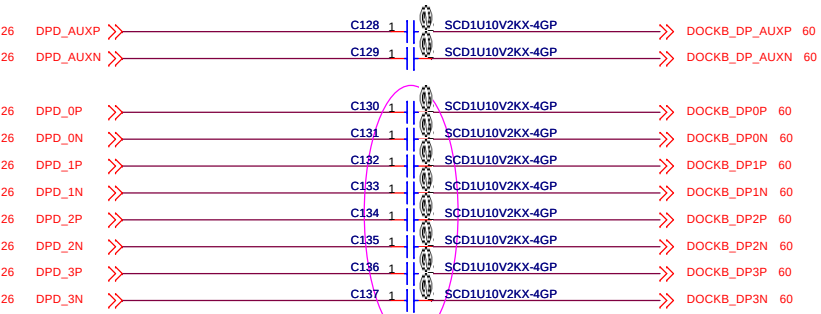


Docking DP Connector A



Place Near Docking Connector

Docking DP Connector B



Place Near Docking Connector

BLANK

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

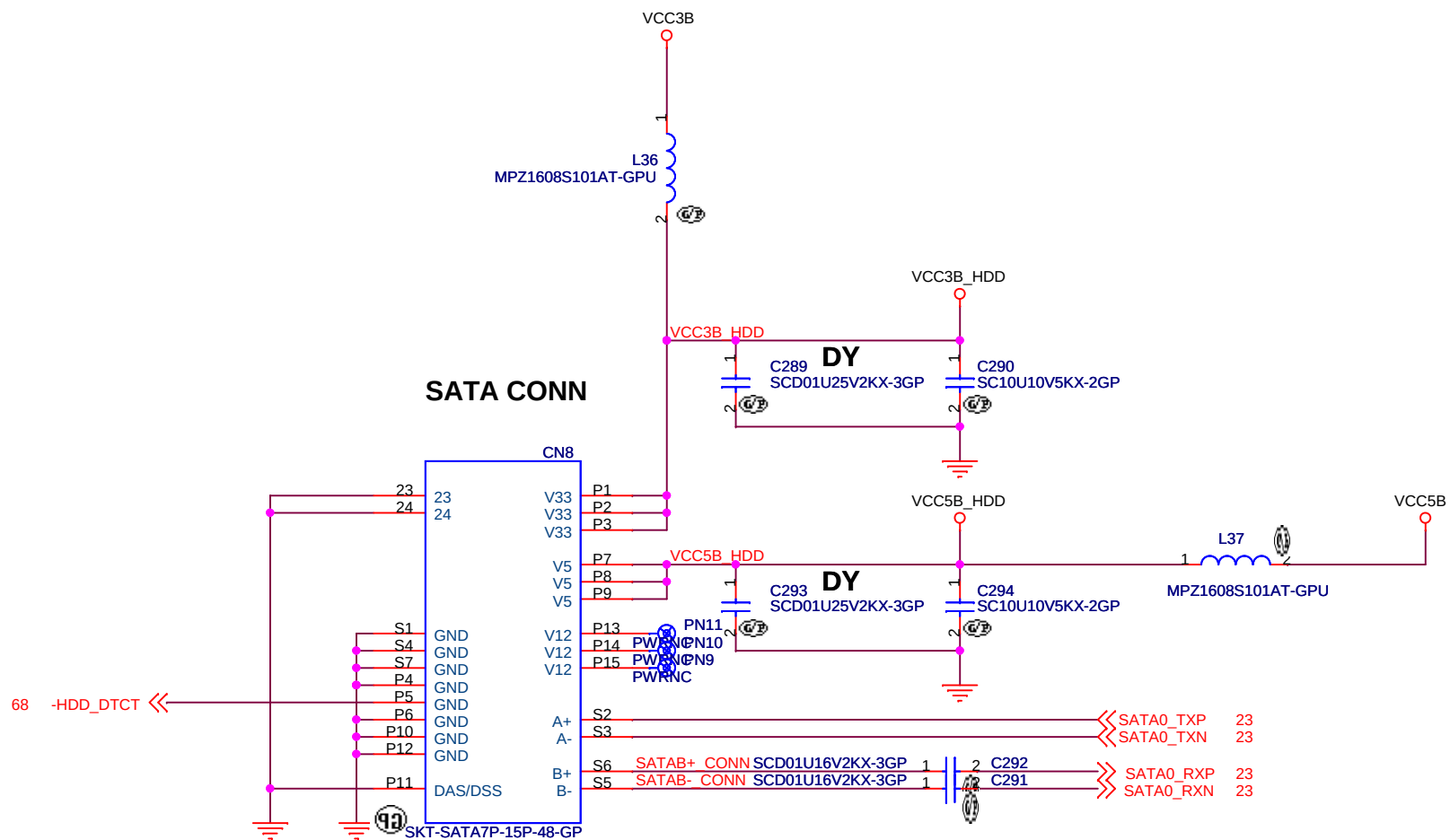
BLANK

Size
A3

Document Number
Dasher-1

Rev
SD

Date: Monday, January 10, 2011Sheet 38 of 101



<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

SATA Connector

Size
A4

Document Number

Dasher-1

Rev
SD

Date: Monday, January 10, 2011

Sheet 39 of

101

BLANK

<Core Design>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
BLANK			
Size	Document Number		Rev
A3	Dasher-1		SD
Date:	Monday, January 10, 2011	Sheet 40 of	101

BLANK

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

SizeA3

Document Number

RevSD

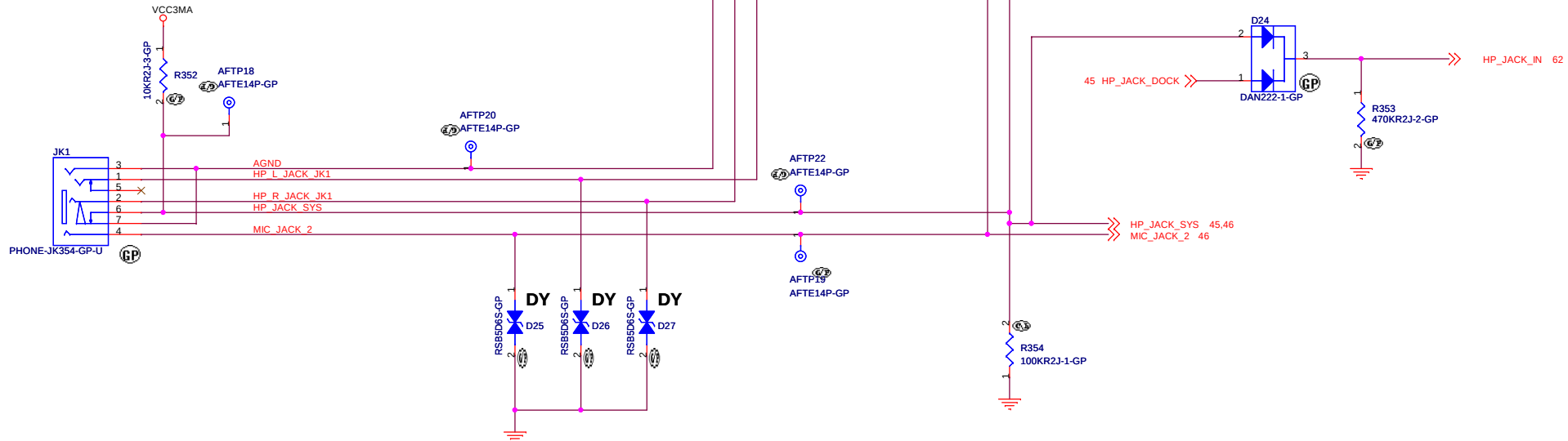
Date: Monday, January 10, 2011

Sheet 41 of 101

NEAR HEADPHONE CONN

WIDE PATTERN

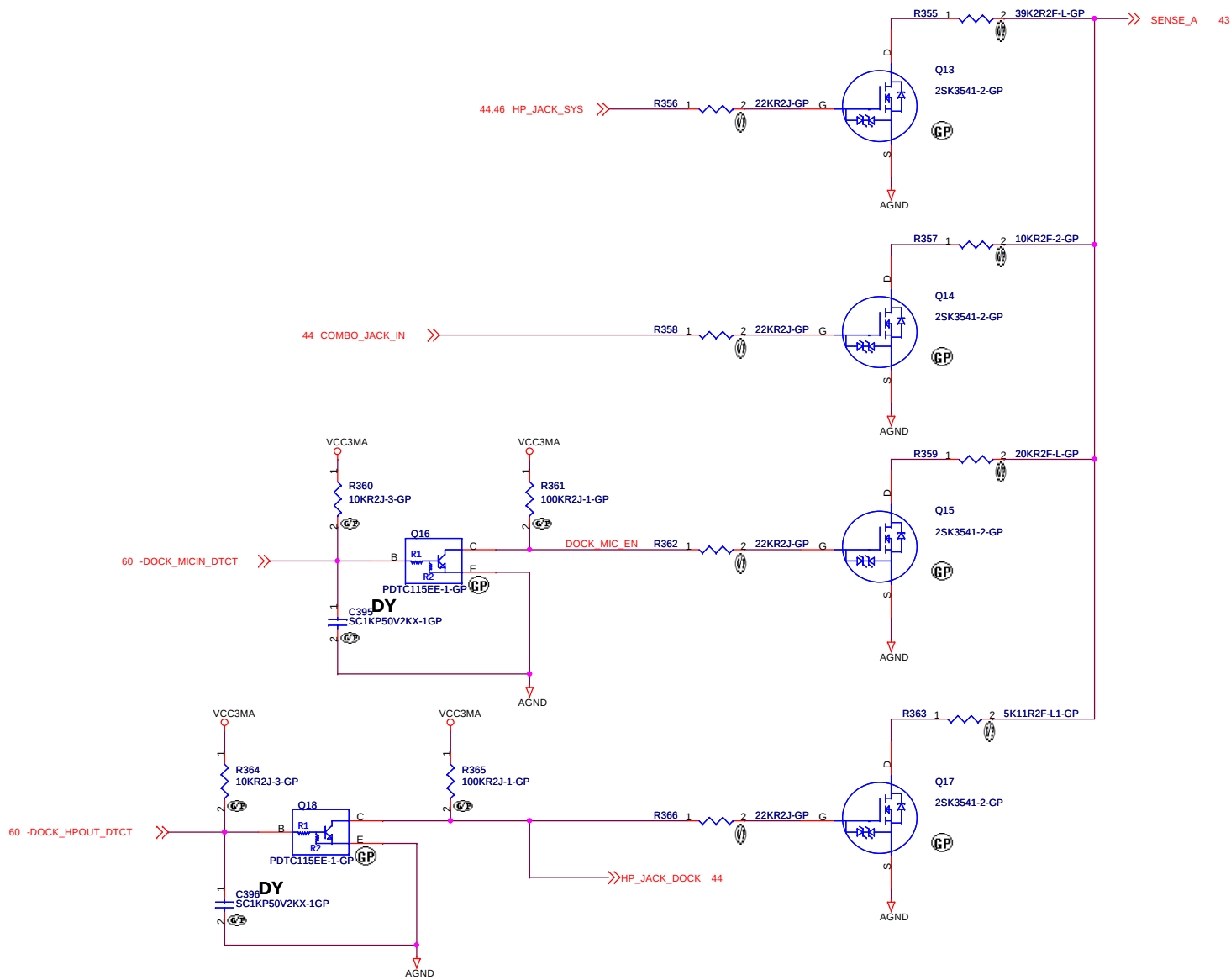
WIDE AND SHORT PATTERN



<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

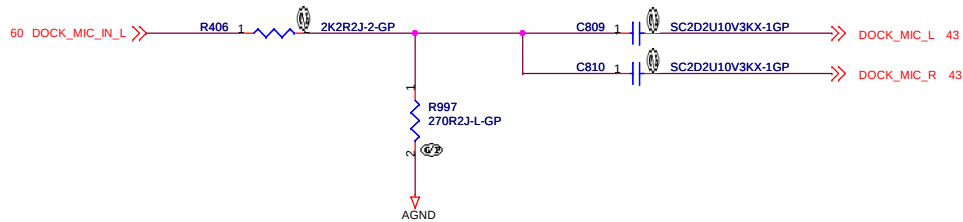
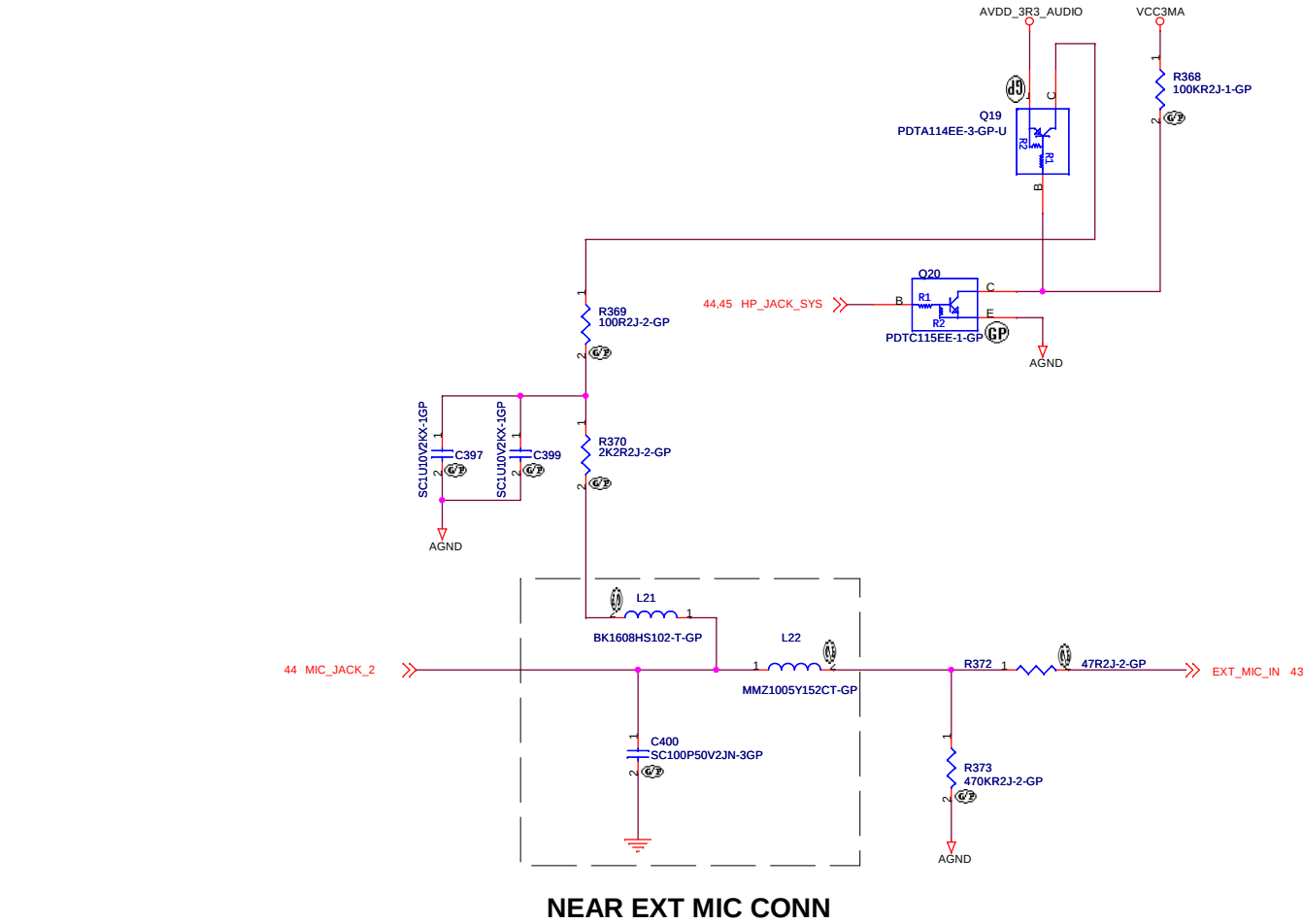
Title		
Audio Connector		
Size	Document Number	Rev
A3	Dasher-1	SD
Date: Monday, January 10, 2011		Sheet 44 of 101



<Core Design>

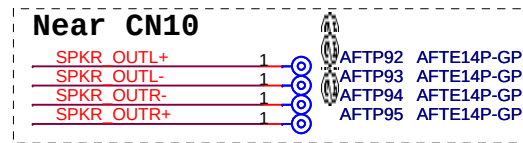
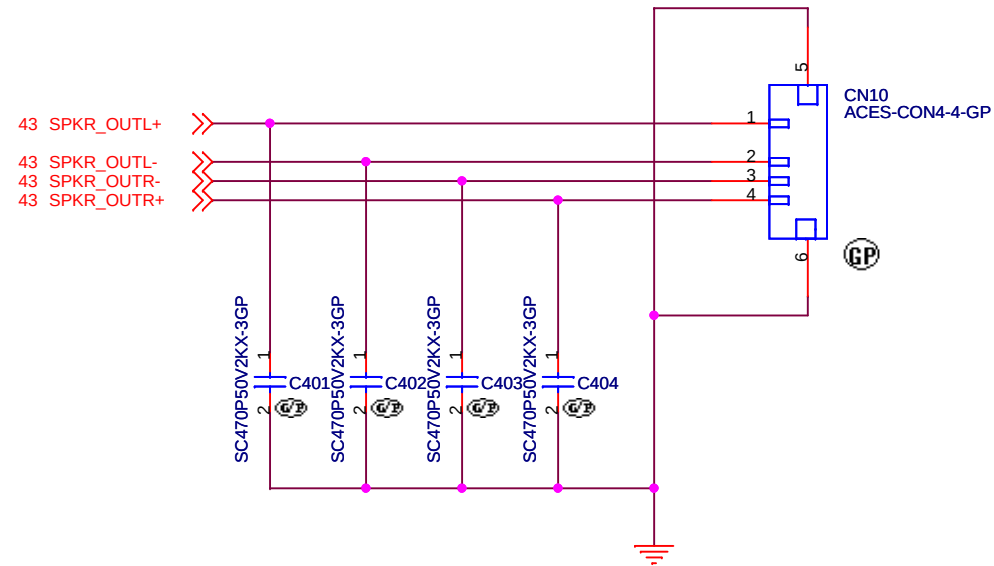
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			Audio Jack Sense	
Size	Document Number	Dasher-1		Rev
A3				SD
Date:	Monday, January 10, 2011	Sheet	45	of 101



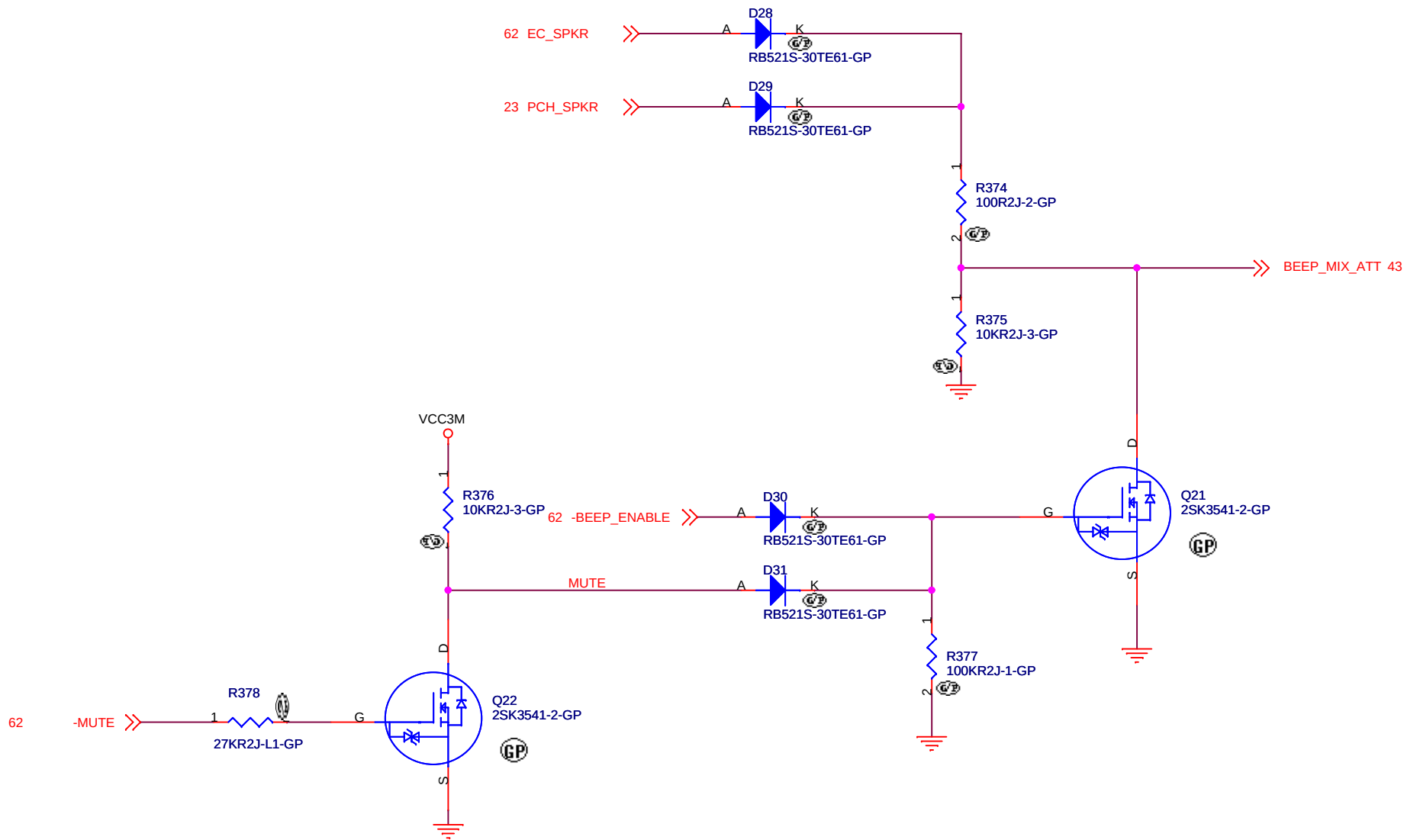
<Core Design>

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title		Audio Ext MIC I/F	
Size A3	Document Number	Dasher-1	Rev SD
Date: Monday, January 10, 2011		Sheet 46 of	101



<Core Design>

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
AUDIO SPEAKER			
Size A4	Document Number Dasher-1		Rev SD
Date:	Monday, January 10, 2011	Sheet	47 of 101



<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

Audio BEEP

Size
A4

Document Number

Dasher-1

Rev
SD

Date: Monday, January 10, 2011

Sheet 48 of 101

BLANK

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size

A3

Document Number

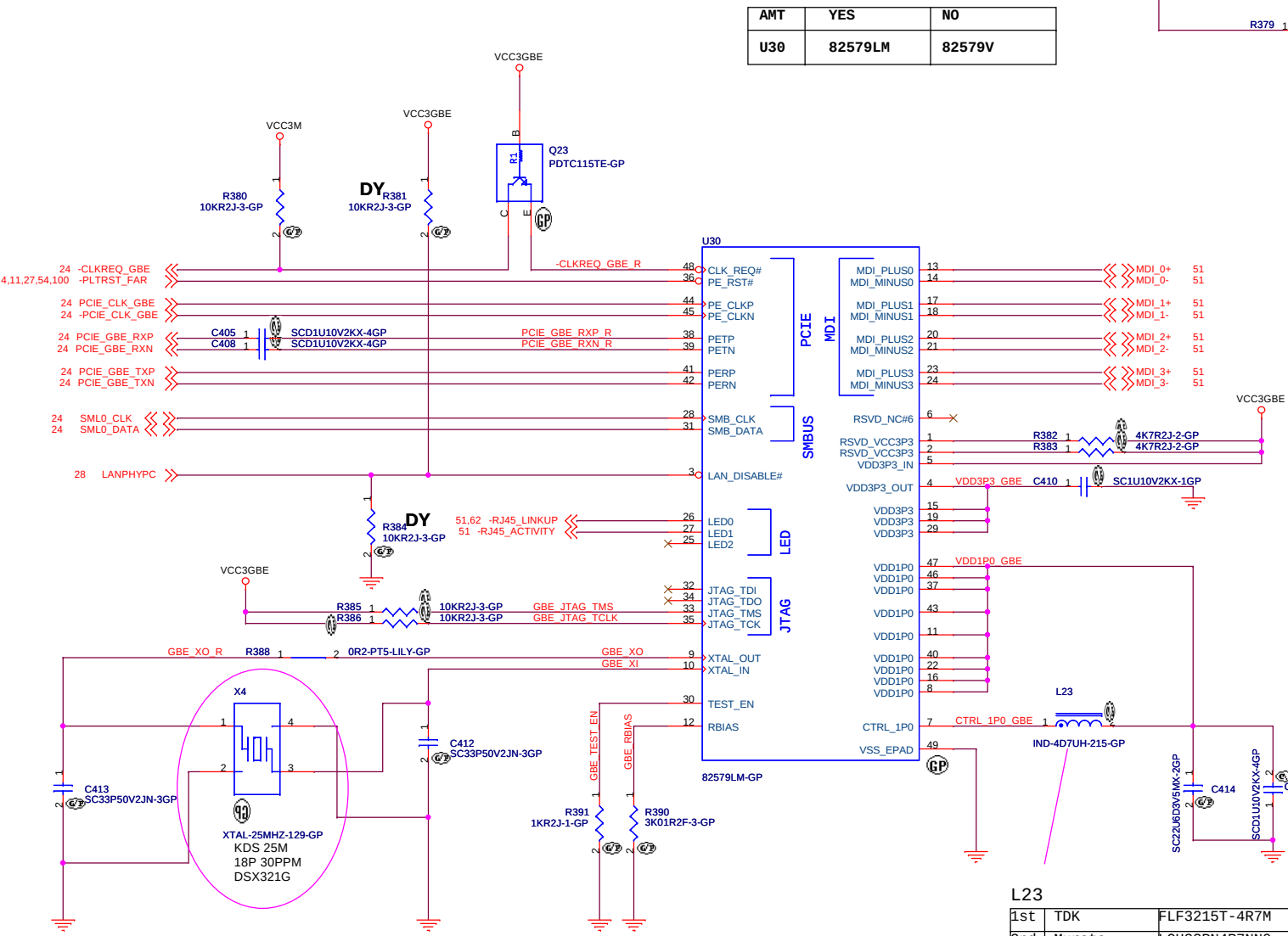
Dasher-1

Rev

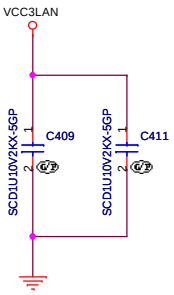
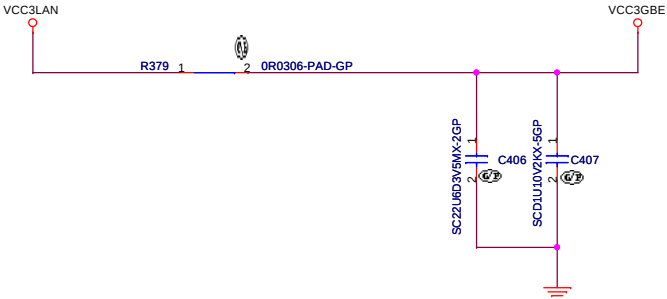
SD

Date: Monday, January 10, 2011

Sheet 49 of 101



AMT	YES	NO
U30	82579LM	82579V



X4			
1st	KDS	DSX321G 25M 18P 30PPM	82.30020.B11
2nd	H.ELE	HSX321S 25M 18P 30PPM	82.30020.B21
3rd	TXC	7V25020001 25M 18P 30PPM	41U6141AA

L23			
1st	TDK	FLF3215T-4R7M	68.4R71D.10Q
2nd	Murata	LQH32PN4R7NN0	68.4R750.20H
3rd	Taiyo Yuden	BRL3225T4R7M	68.4R71E.10I

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title

GBE Lewisville

Size

A3

Document Number

Dasher-1

Rev

SD

Date

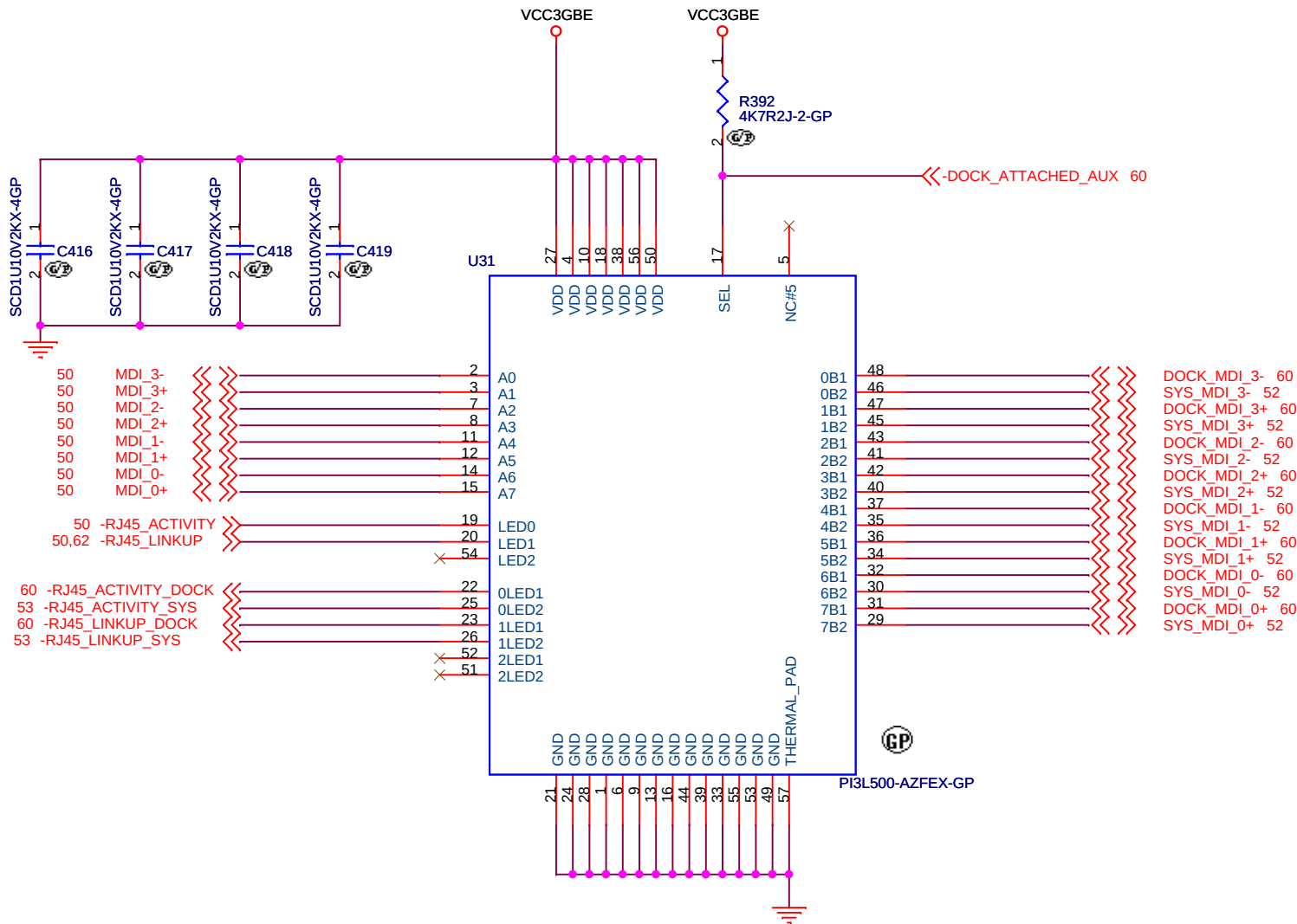
Monday, January 10, 2011

Sheet

50

of

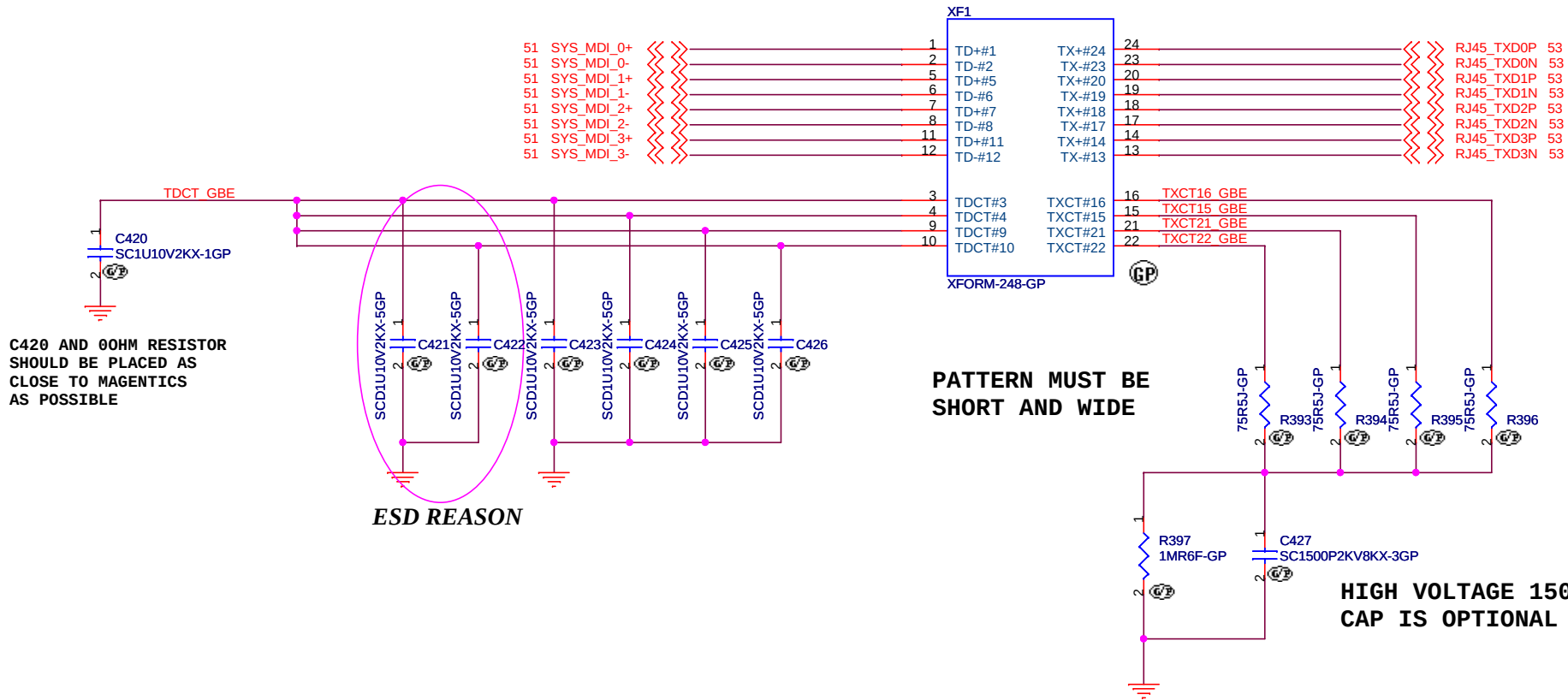
101



		Vendor P/N	Wistron P/N
1st	Pericom	PI3L500AZFEX	73.3L500.003
2nd	TI	TS3L500AERHUR	73.3L500.A0V
3rd	OnSemi	NS3L500	73.03500.003

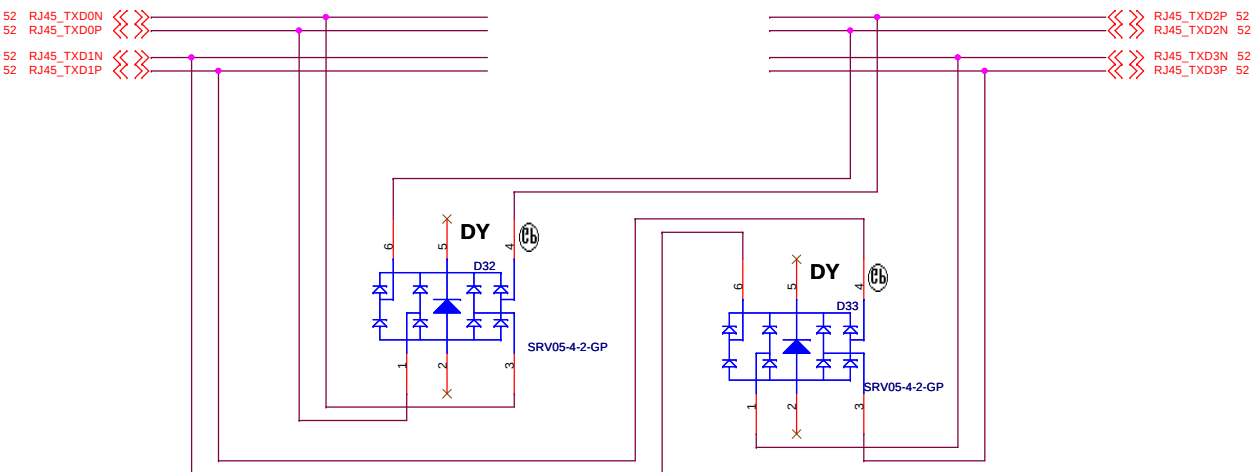
<Core Design>

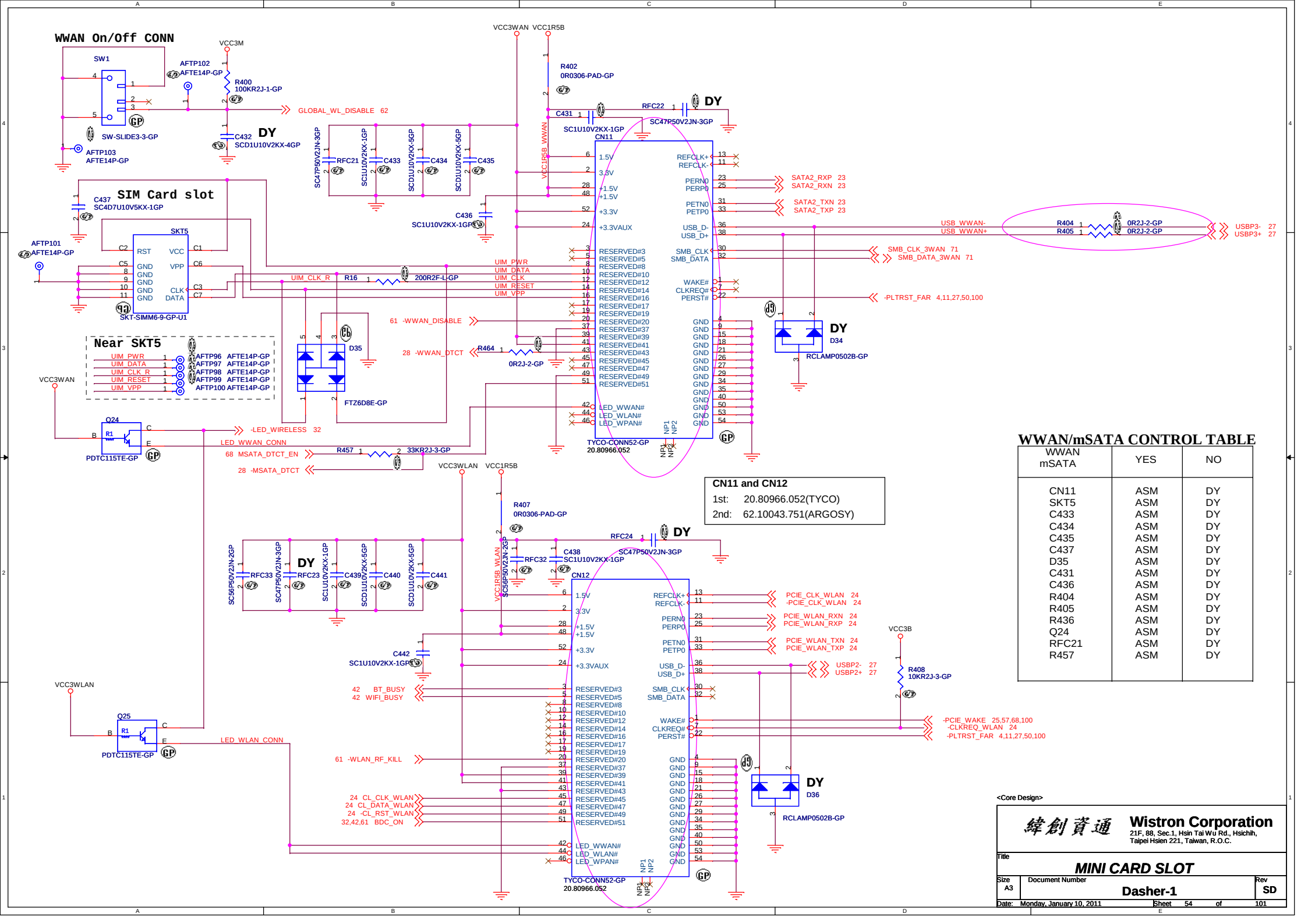
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title		
GBE LAN Switch		
Size A4	Document Number Dasher-1	Rev SD
Date: Monday, January 10, 2011	Sheet 51 of	101



<Core Design>

緯創資通		Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title			
GBE MAGNETICS			
Size	Document Number	Rev	
Custom	Dasher-1	SD	
Date:	Monday, January 10, 2011	Sheet	52 of 101





WWAN/mSATA CONTROL TABLE

WWAN	mSATA	YES	NO
CN11	ASM	DY	
SKT5	ASM	DY	
C433	ASM	DY	
C434	ASM	DY	
C435	ASM	DY	
C437	ASM	DY	
D35	ASM	DY	
C431	ASM	DY	
C436	ASM	DY	
R404	ASM	DY	
R405	ASM	DY	
R436	ASM	DY	
Q24	ASM	DY	
RFC21	ASM	DY	
R457	ASM	DY	

<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

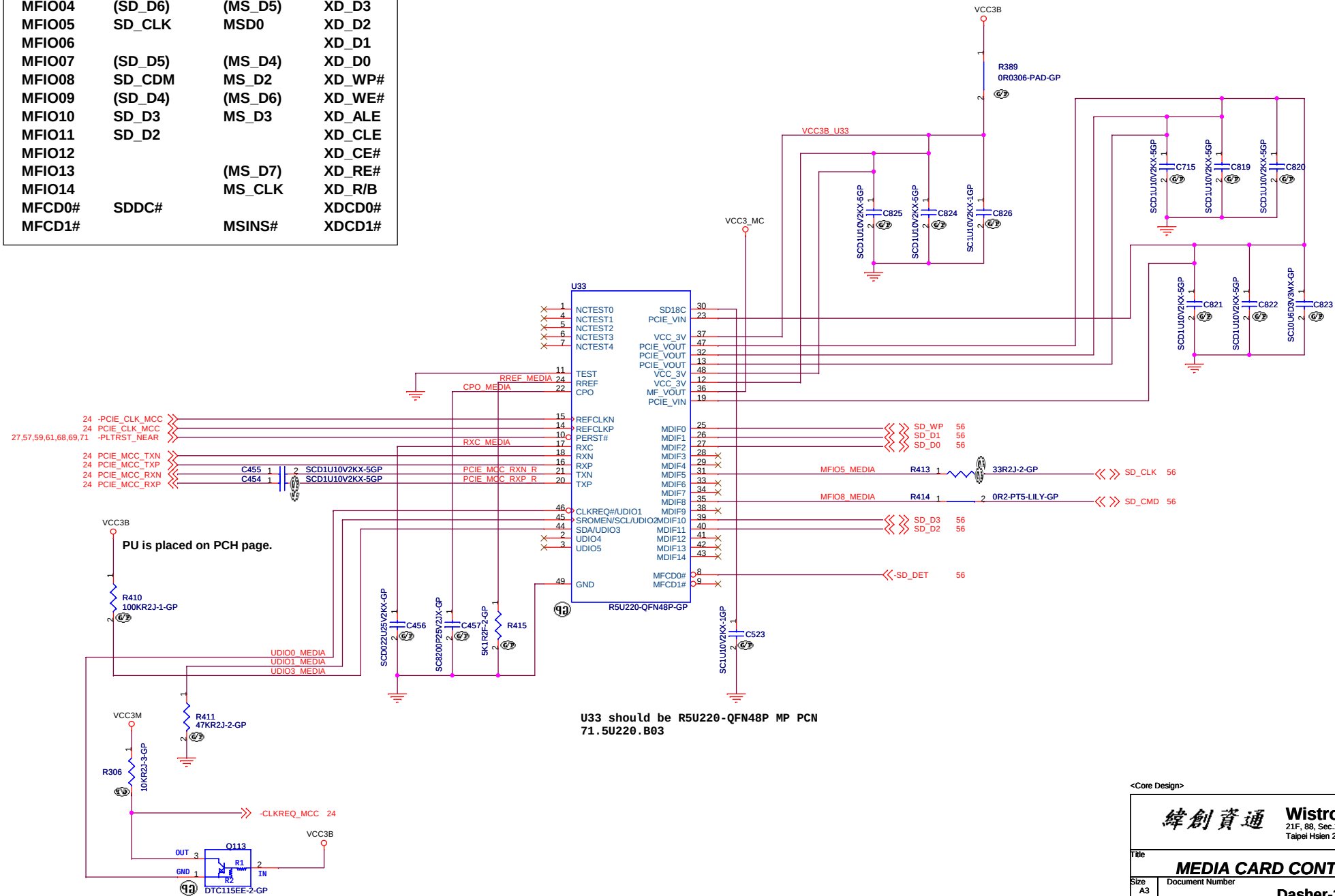
Title

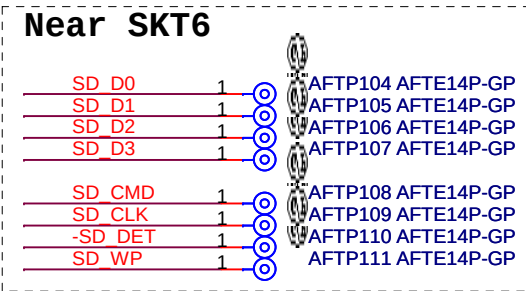
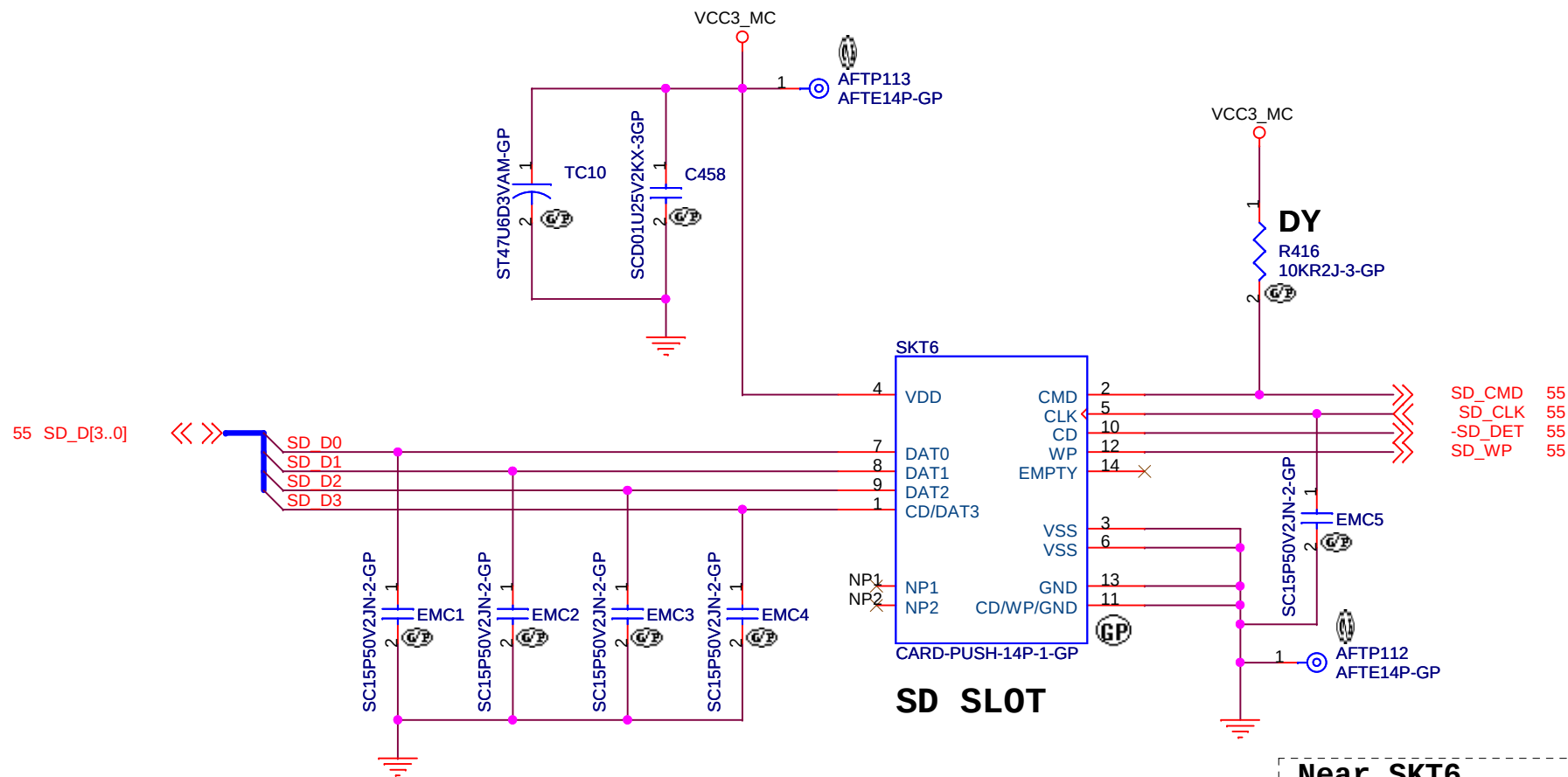
MINI CARD SLOT

Size	Document Number	Rev
A3	Dasher-1	SD

Date: Monday, January 10, 2011 Sheet 54 of 101

MEDIA I/F	SD/MMC	MEMORYSTICK	XD
MFIO00	SDWP#	MSBS	XD_D7
MFIO01	SD_D1		XD_D6
MFIO02	SD_D0	MS_D1	XD_D5
MFIO03	(SD_D7)		XD_D4
MFIO04	(SD_D6)	(MS_D5)	XD_D3
MFIO05	SD_CLK	MSD0	XD_D2
MFIO06			XD_D1
MFIO07	(SD_D5)	(MS_D4)	XD_D0
MFIO08	SD_CDM	MS_D2	XD_WP#
MFIO09	(SD_D4)	(MS_D6)	XD_WE#
MFIO10	SD_D3	MS_D3	XD_ALE
MFIO11	SD_D2		XD_CLE
MFIO12			XD_CE#
MFIO13		(MS_D7)	XD_RE#
MFIO14		MS_CLK	XD_R/B
MFCD0#	SDDC#		XDCD0#
MFCD1#		MSINS#	XDCD1#





<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

Media Card Slot

Size
A

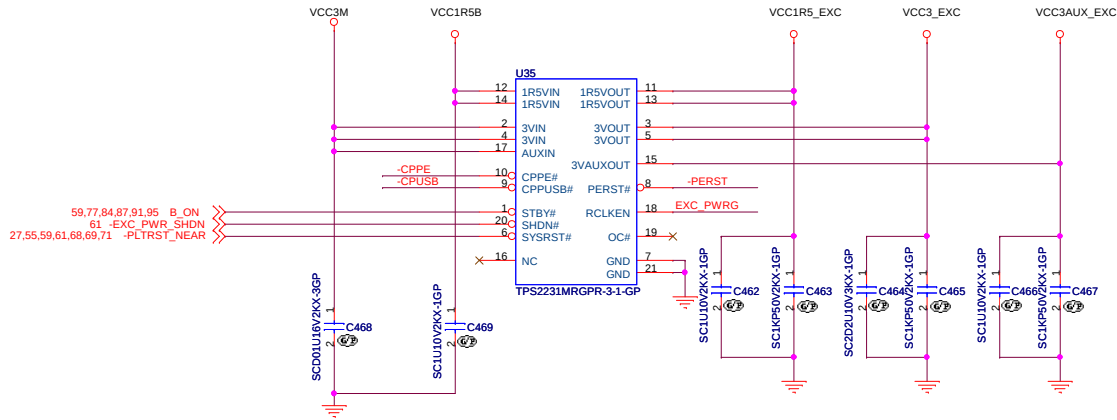
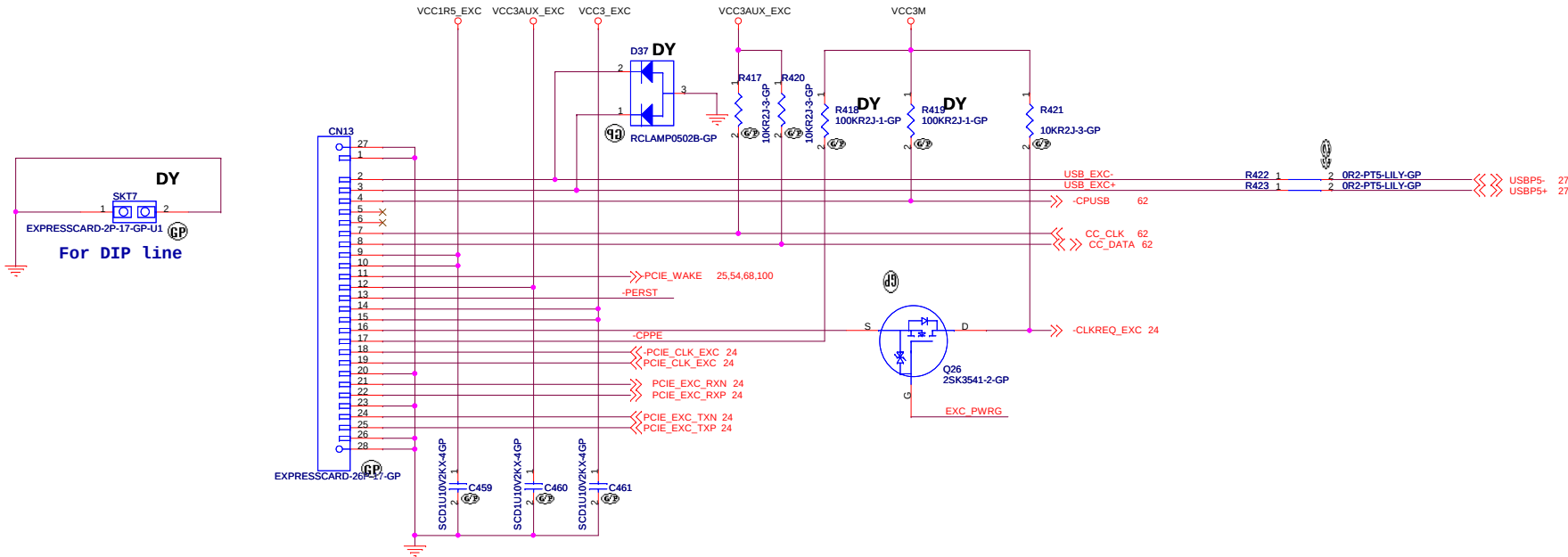
Document Number

Dasher-1

Rev
SD

Date: Monday, January 10, 2011

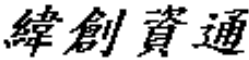
Sheet 56 of 101



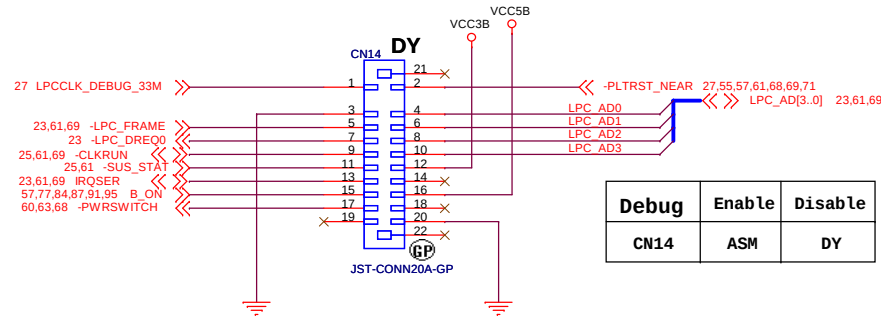
U35	TI	TPS2231MRGP-3	45K0234BA
	Rohm	BD4156MUV-E2	74.04156.07T
	Rohm	BD4157MUV-E2	74.04157.073

BLANK

<Core Design>

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
BLANK			
Size A	Document Number Dasher-1		Rev SD
Date:	Monday, January 10, 2011	Sheet 58 of	101

Debug card connector

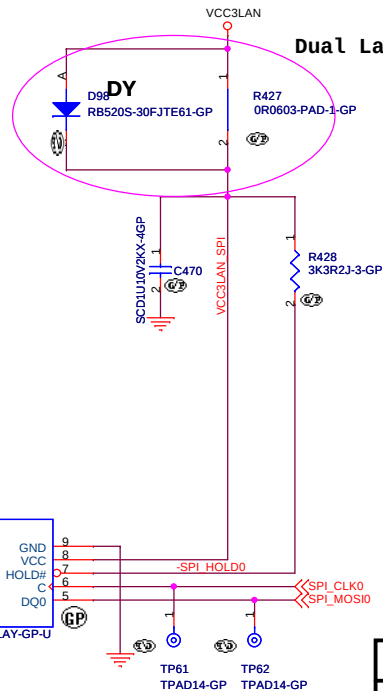


Put "easy-to -access" place

64Mbit SPI FLASH (for vPRO model)

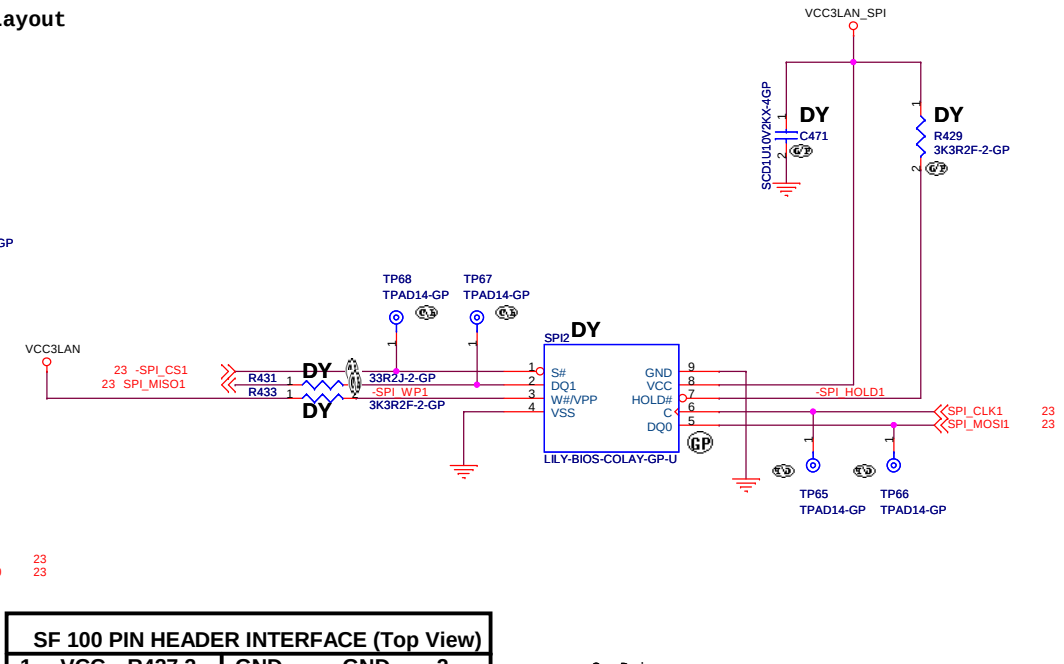
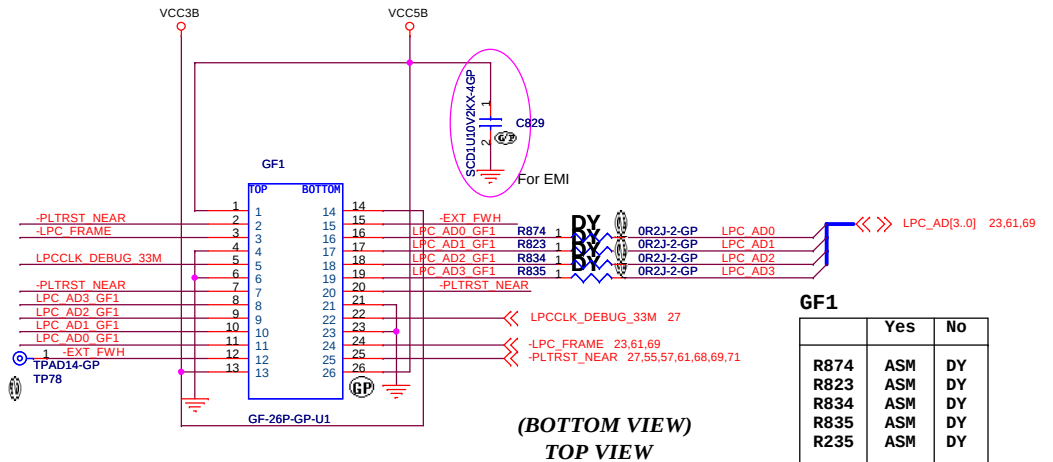
S08		
Marconix	MX25L6436EM2I-10G	72.25643.001
Marconix	MX25L6406EM2I-10G	
Winbond	W25Q64CVSSIG	72.25Q64.B01
Atmel	AT25DF641 or 641A	72.25641.003
MLP8		
Numonyx	M25PX64-VMD6TG	72.25P64.E01

Part Number wait confirm



SO8 and WSON8 are both supported!

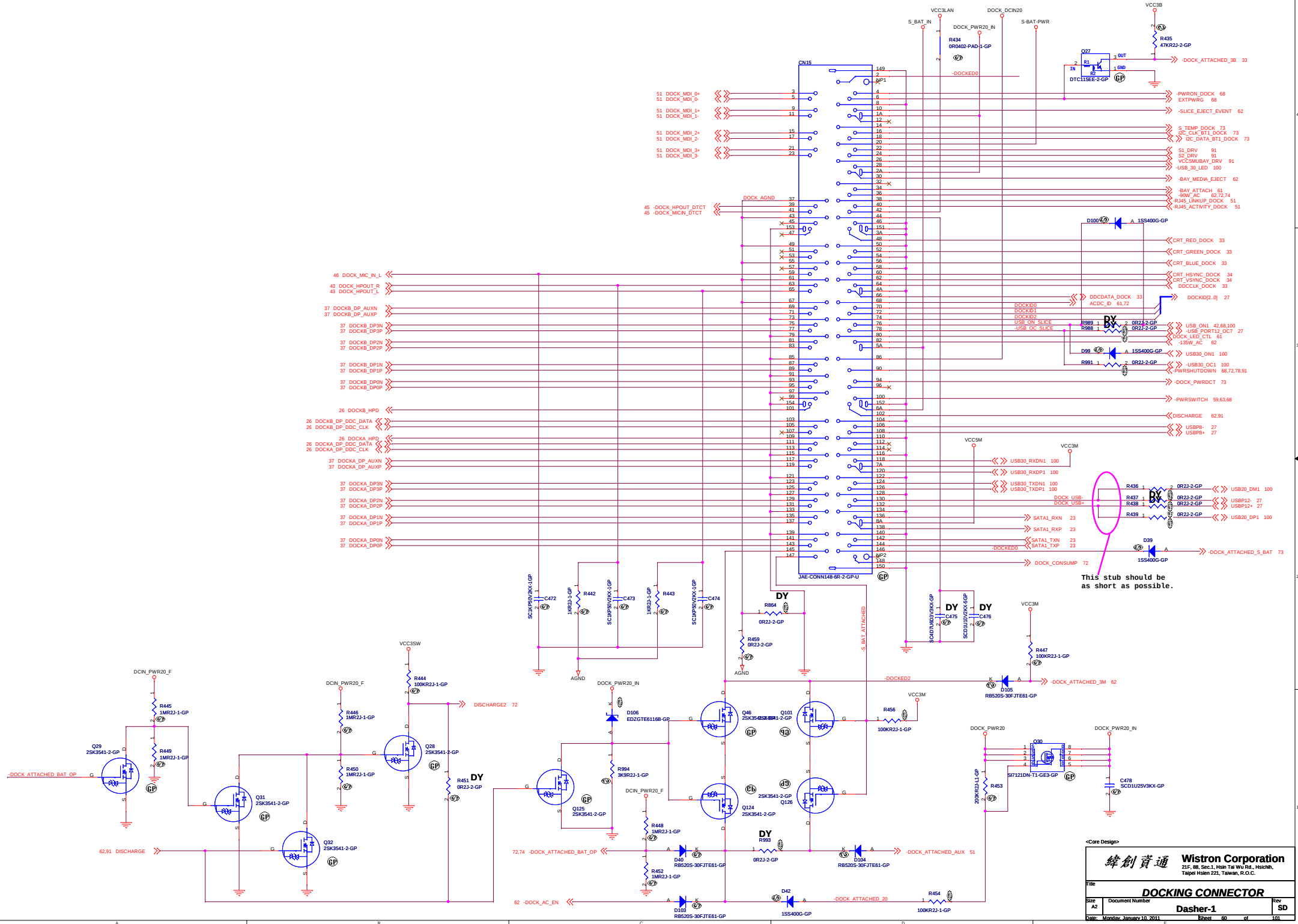
Golden Finger for Debug Board

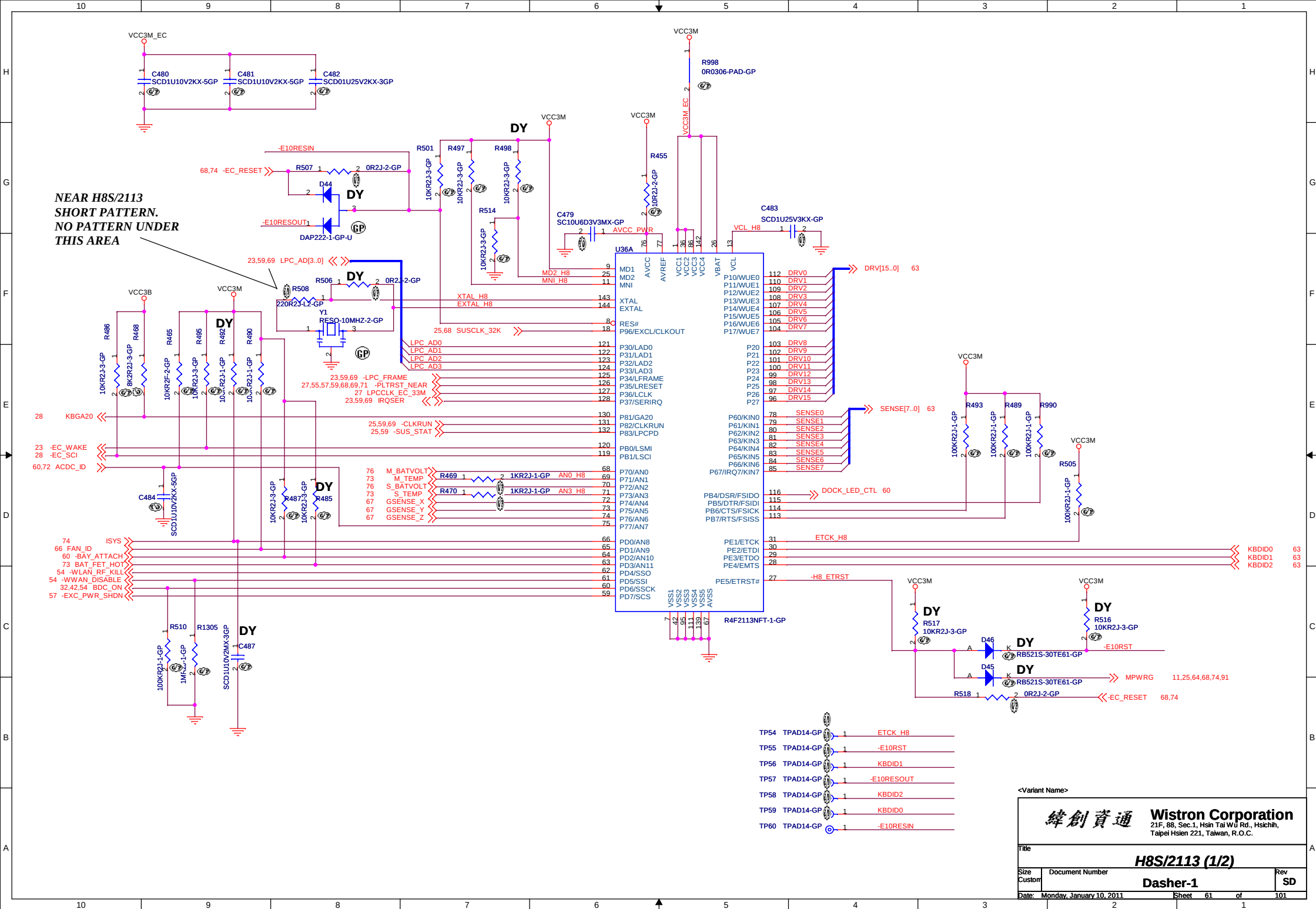


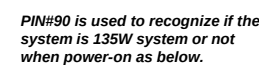
SF 100 PIN HEADER INTERFACE (Top View)							
1	VCC	R427.2	GND	GND	2		
3	CS#	R148.1	R145.1	CLK	4		
5	MISO	R430.2	R151.1	MOSI	6		
7	(KEY)	N/A	N/A	(RESET)	8		

<Core Design>

緯創資通 Wistron Corporation			
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title			
SPI&LPC debug card			
Size	Document Number	Rev	SD
A3			
Date: Monday, January 10, 2011	Sheet 59	of	101

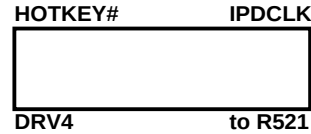
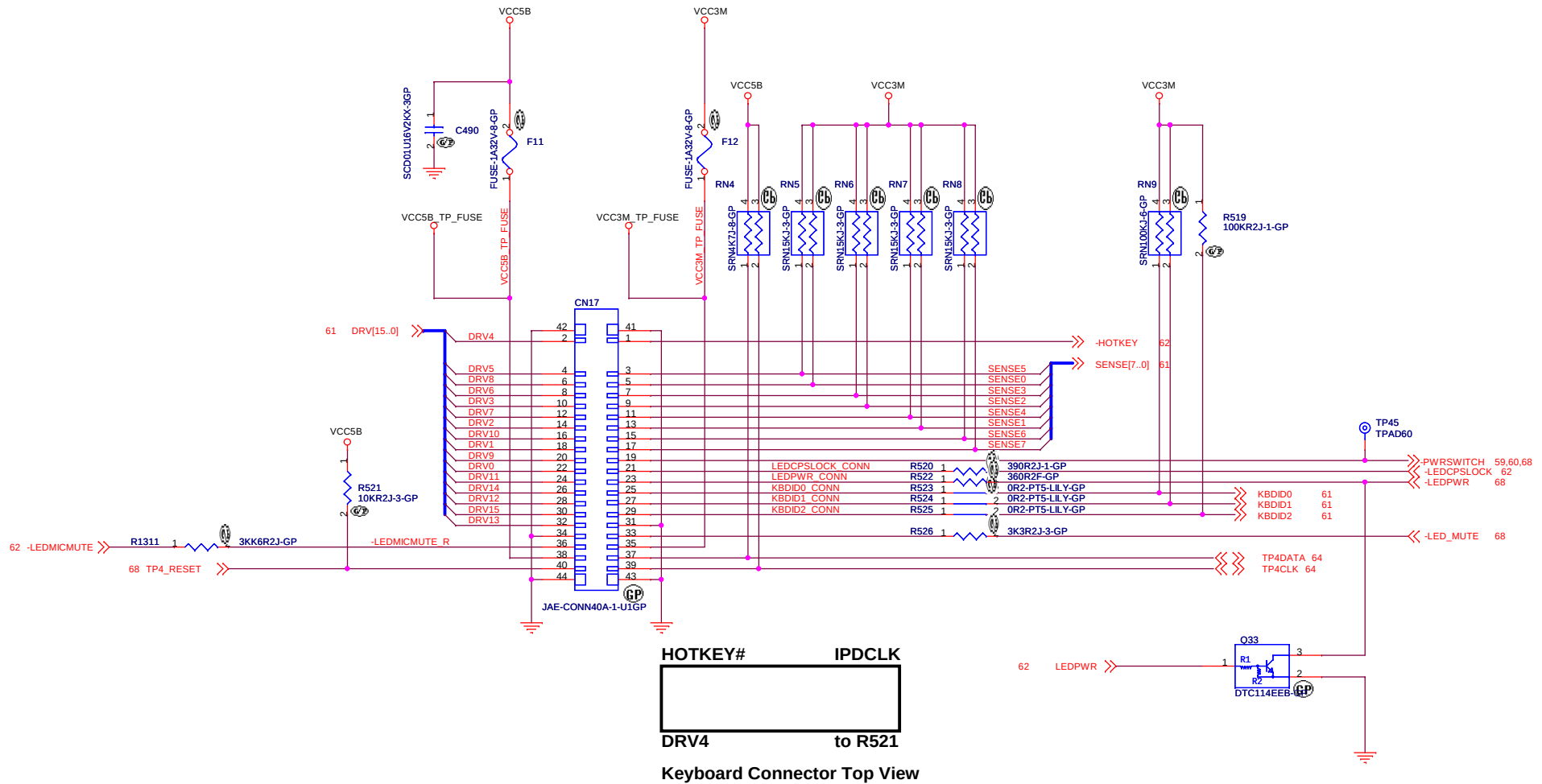






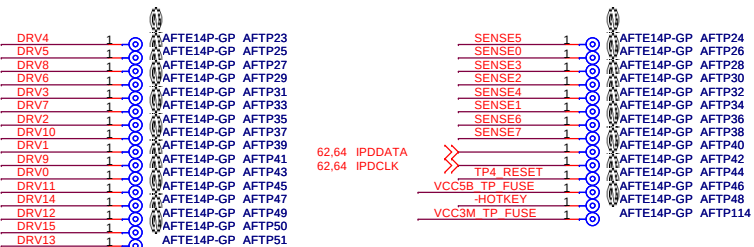
1

Keyboard Connector



Keyboard Connector Top View

Near CN17

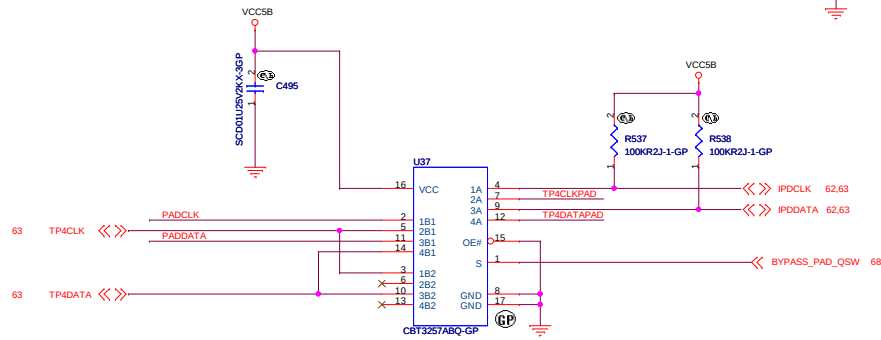
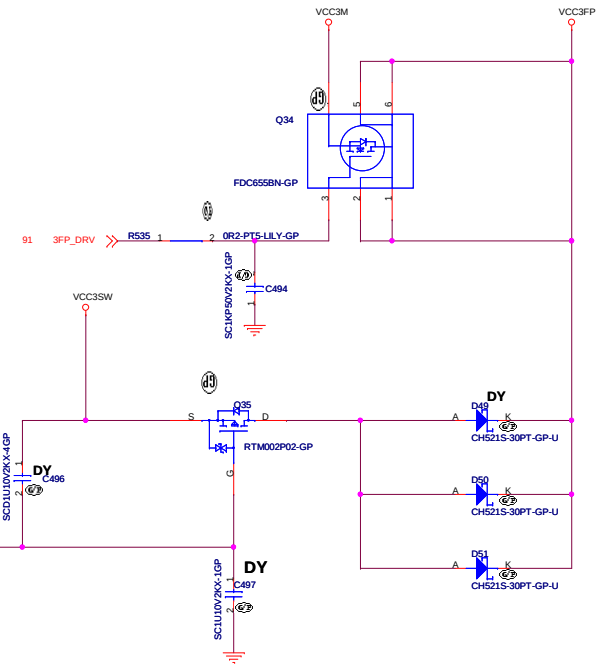
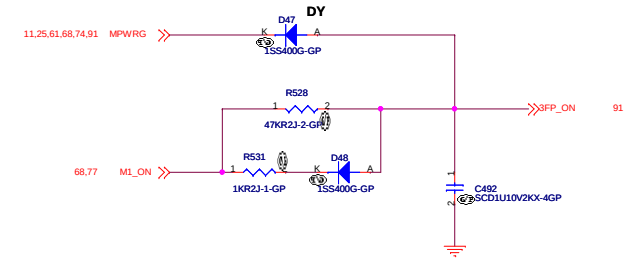
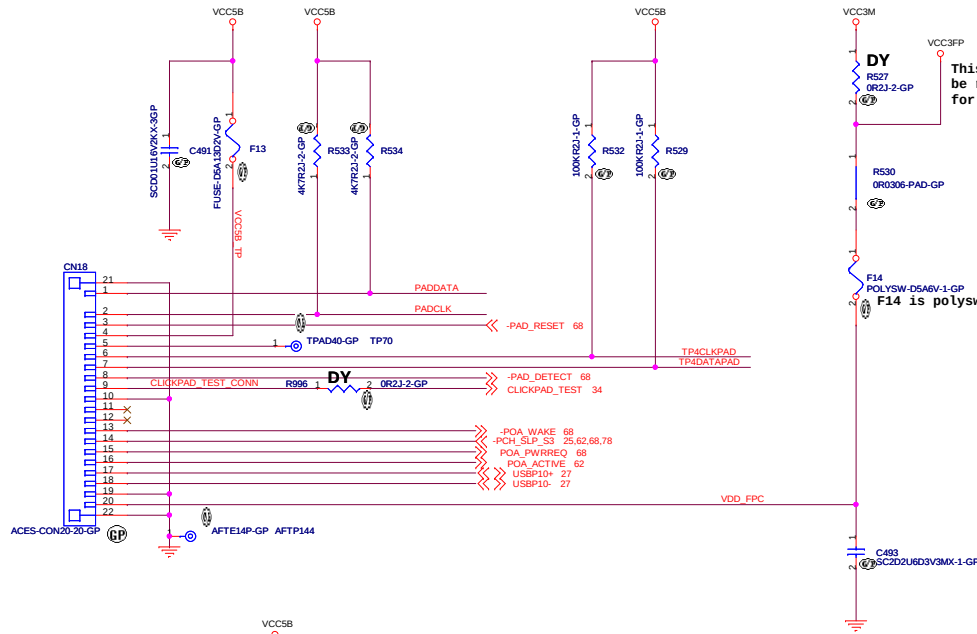


<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			
KEYBOARD CONNECTOR			
Size A3	Document Number	Rev SD	
Date: Monday, January 10, 2011		Sheet 63 of 101	

Fingerprint Reader / Touch PAD



Near CN18

PADDATA	1	AFT14P-GP	AFTP124
PADCLK	1	AFT14P-GP	AFTP125
-PAD_RESET	1	AFT14P-GP	AFTP126
VCC5B_TP	1	AFT14P-GP	AFTP127
TP4CLKPAD	1	AFT14P-GP	AFTP128
TP4DATAPAD	1	AFT14P-GP	AFTP129
-PAD_DETECT	1	AFT14P-GP	AFTP130
CLICKPAD_TEST_CONN	1	AFT14P-GP	AFTP131
-POA_WAKE	1	AFT14P-GP	AFTP132
-POH_SLP_S3	1	AFT14P-GP	AFTP133
POA_PWRREQ	1	AFT14P-GP	AFTP134
POA_ACTIVE	1	AFT14P-GP	AFTP135
VDD_FPC	1	AFT14P-GP	AFTP136

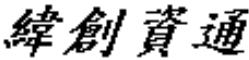
<Core Design>

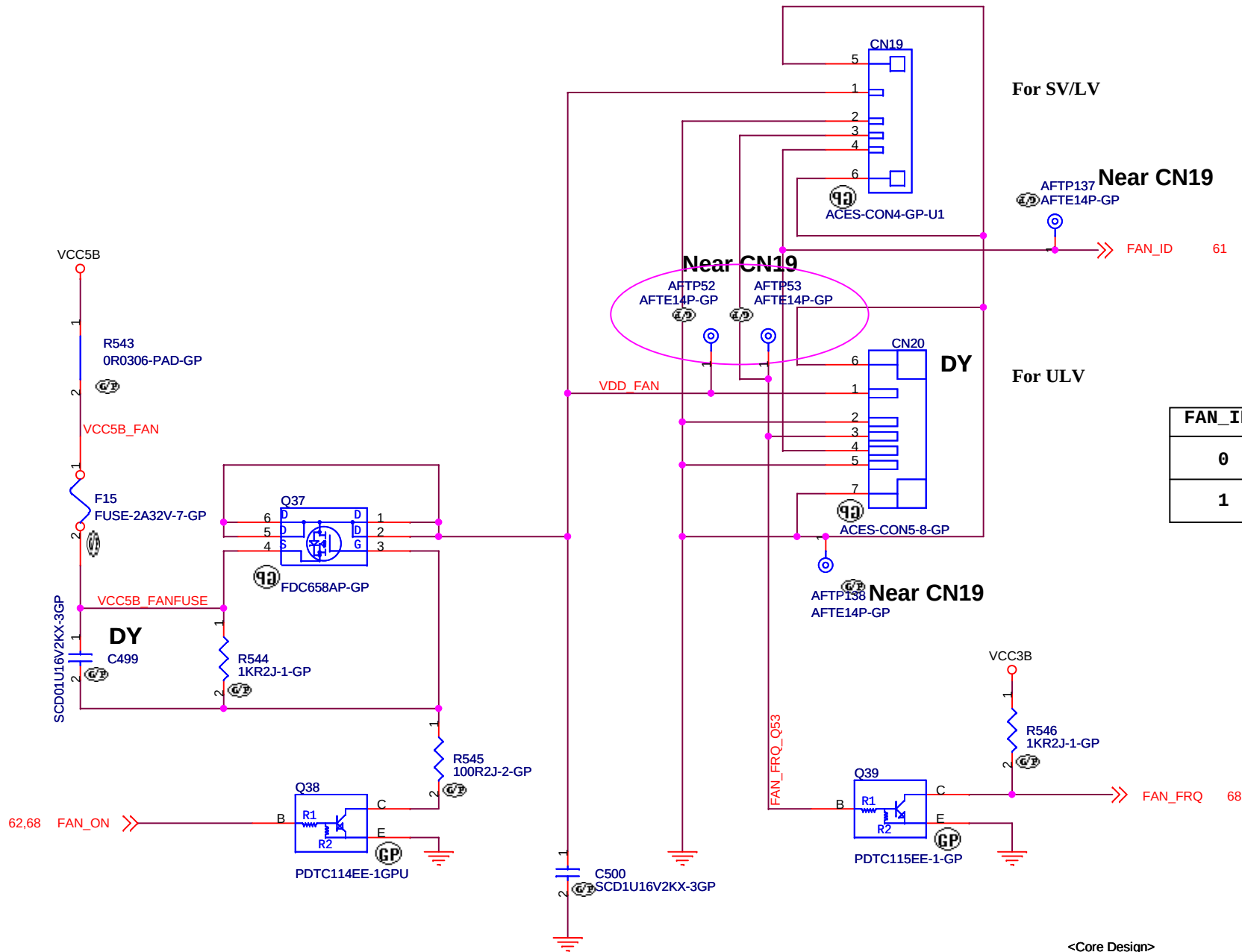
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	TOUCH PAD CONNECTOR		
Size	Document Number	Dasher-1	Rev SD
Custom			
Date:	Monday, January 10, 2011	Sheet 64	of 101

BLANK

<Core Design>

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title BLANK			
Size A	Document Number Dasher-1		Rev SD
Date: Monday, January 10, 2011		Sheet 65 of	101



FAN_ID	Thermal module
0	THT
1	AVC or DELTA

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

FAN Control

Size
A4

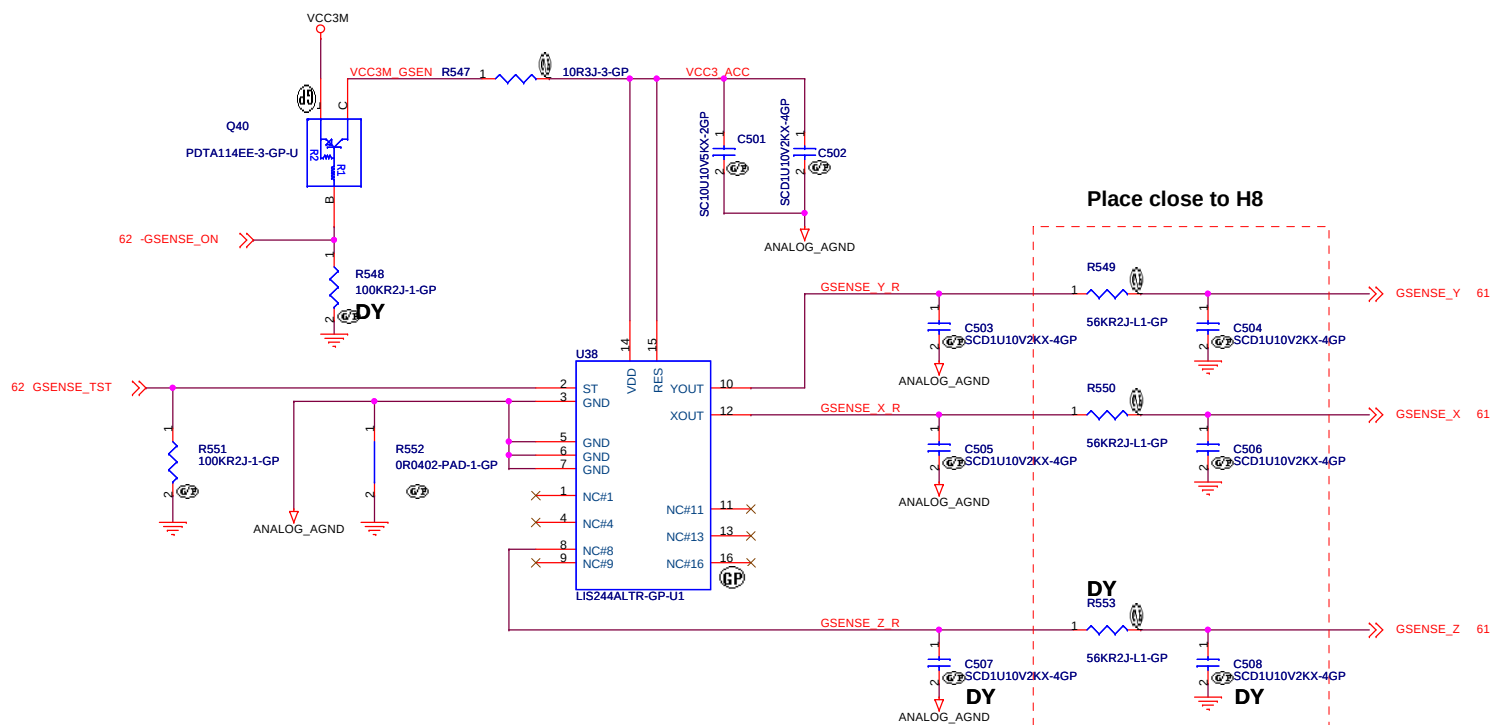
Document Number

Dasher-1

Rev
SD

Date: Monday, January 10, 2011

Sheet 66 of 101



	LIS244AL LIS34AL	No Accel
R548	DY	ASM
R551	ASM	ASM
U38	ASM	DY
Q40	ASM	DY
R547	DY	DY
C501	ASM	DY
C502	ASM	DY
R552	ASM	DY
C503	ASM	DY
R549	ASM	DY
C504	ASM	DY
C505	ASM	DY
R550	ASM	DY
C506	ASM	DY
C507	DY	DY
R553		
C508		

U38

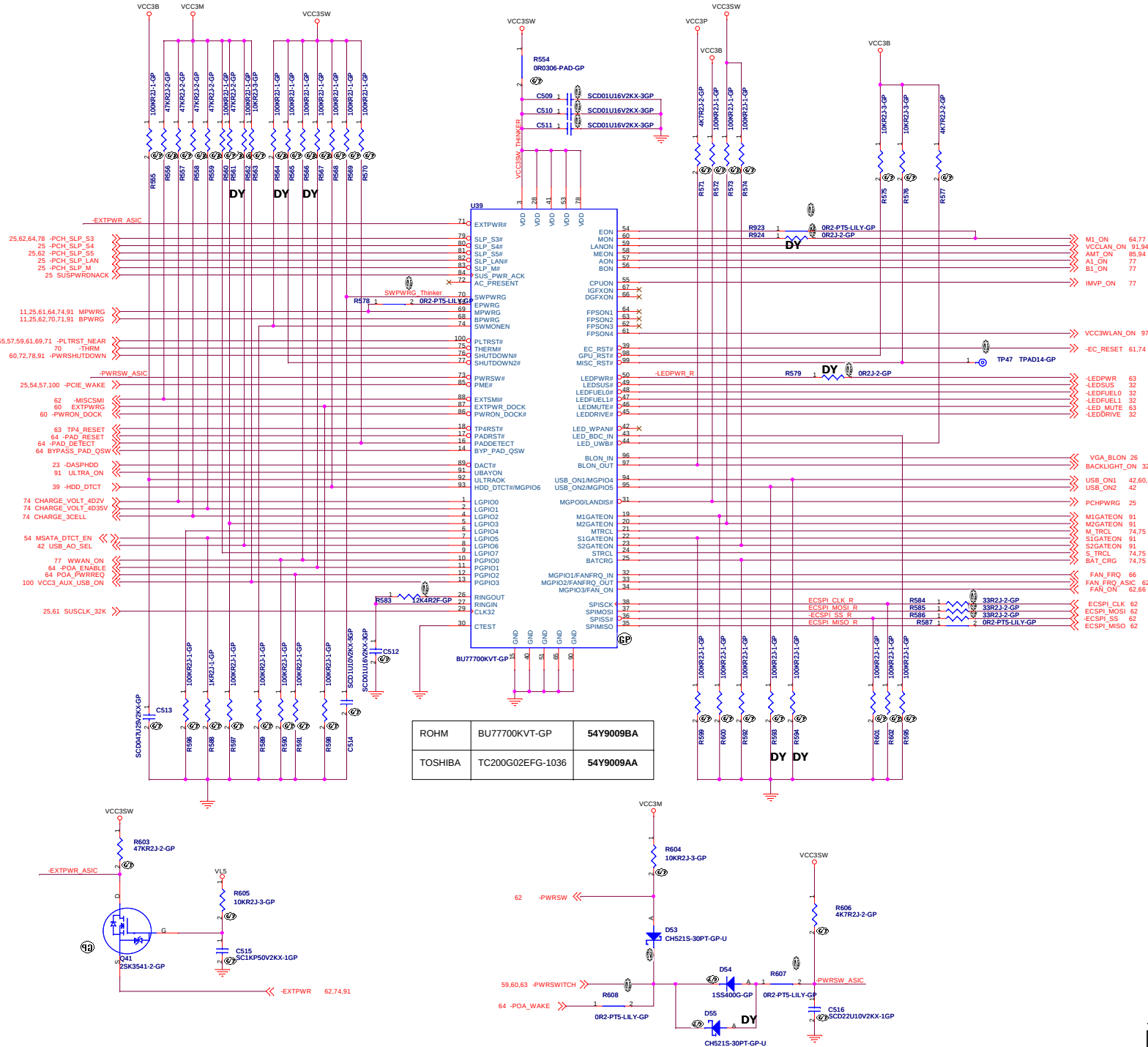
Primary	STMicro LIS244AL	74.00244.0BZ
Second	STMicro LIS34AL	74.00034.0BZ
Third	Kionix KXTC8-2850	74.KXTC8.0BZ

Layout Comment :

- (1) Place C475,C476,Q64,R572,R580 C464,C472,R564,R569 close to U26
- (2) Avoid routing under DCDC switching area.

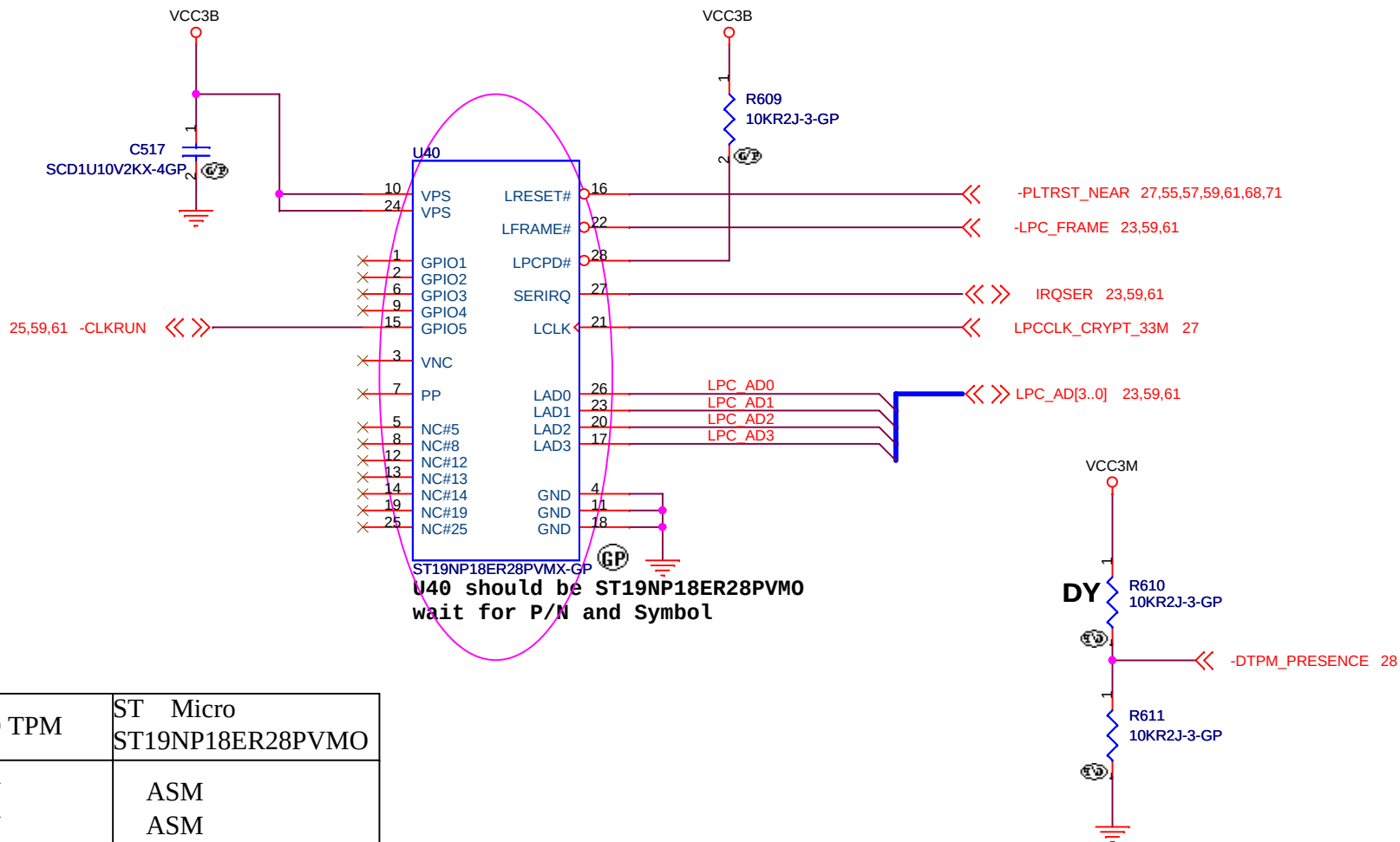
<Core Design>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
G-SENSOR			
Size A3	Document Number		Rev SD
Dasher-1			
Date: Monday, January 10, 2011	Sheet 67	of	101



ROHM	BU77700KT-GP	54Y9009BA
TOSHIBA	TC200G02EFG-1036	54Y9009AA

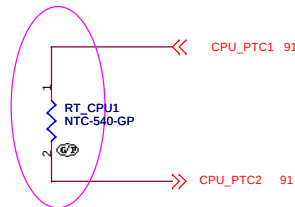




	NO TPM	ST Micro ST19NP18ER28PVM0
U40	DY	ASM
C517	DY	ASM
R609	DY	ASM
R610	ASM	DY
R611	DY	ASM

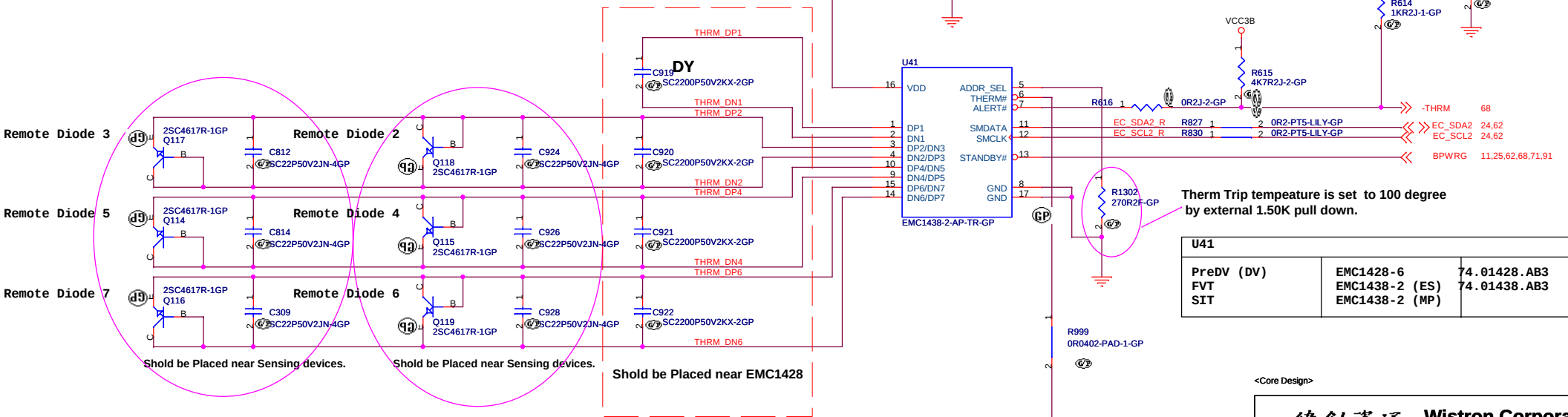
<Core Design>

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
TPM			
Size A4	Document Number		Rev SD
Dasher-1			
Date: Monday, January 10, 2011	Sheet 69 of 101		

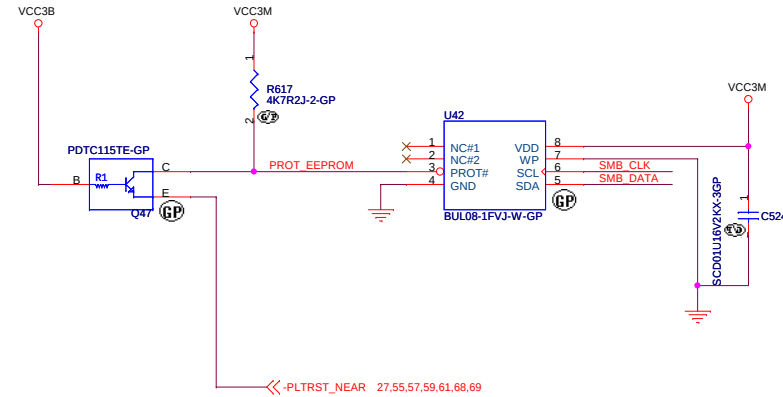


It should be arranged near CPU instead of CPU thermal sensor

Channel	Location
Local	MEMORY
Remote Diode 2	CPUCORE DC/DC
Remote Diode 3	Rear Cover (1R05B_VTT DC/DC)
Remote Diode 4	Base Cover (1R5A DC/DC)
Remote Diode 5	GFXCORE
Remote Diode 6	WWAN
Remote Diode 7	WLAN



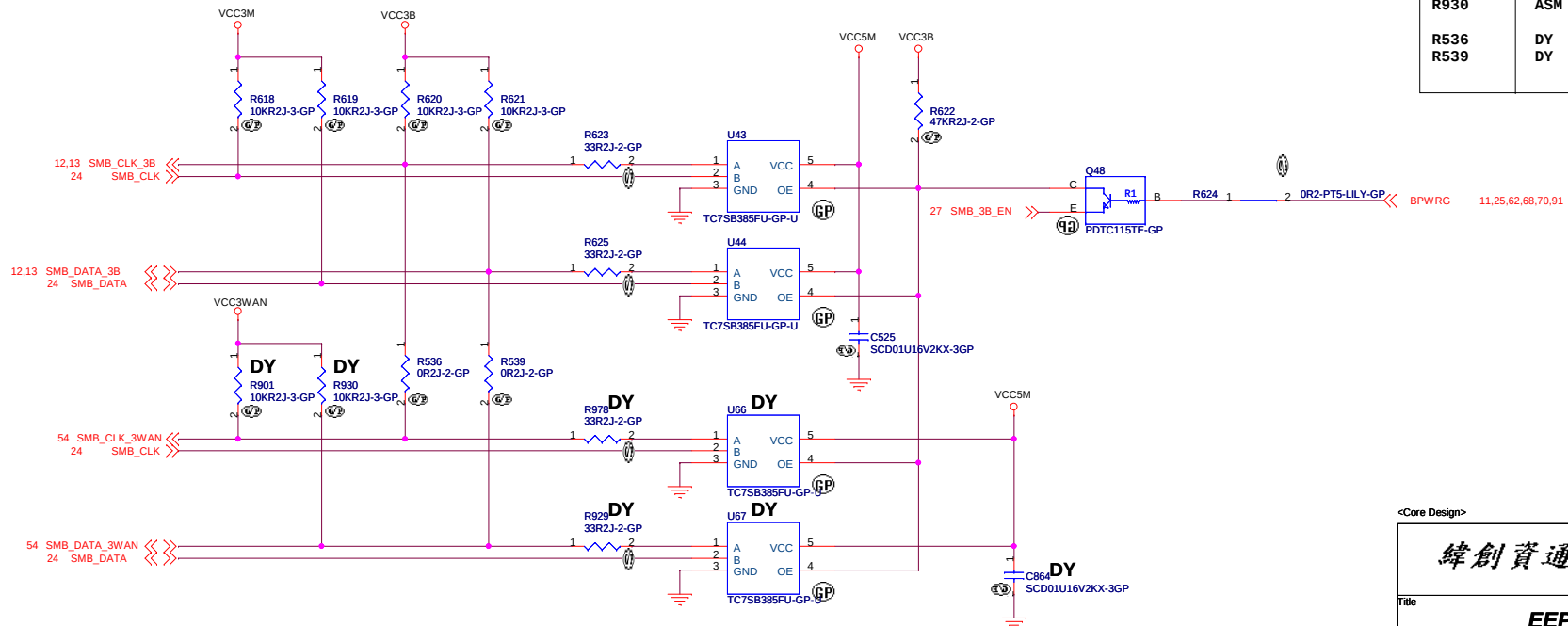
EEPROM



	Vendor	U42	Part Number
1st	ROHM	BUL08-1FVJ-W	72.BUL08.00Q
2nd	PHILIPS	PCA24S08ADP	72.24S08.A0Q
3rd	Sanyo	LE26CAP08TT	72.26C08.00R

CONSTANT SECURE	YES	NO
U66	ASM	DY
U67	ASM	DY
C864	ASM	DY
R978	ASM	DY
R929	ASM	DY
R901	ASM	DY
R930	ASM	DY
R536	DY	ASM
R539	DY	ASM

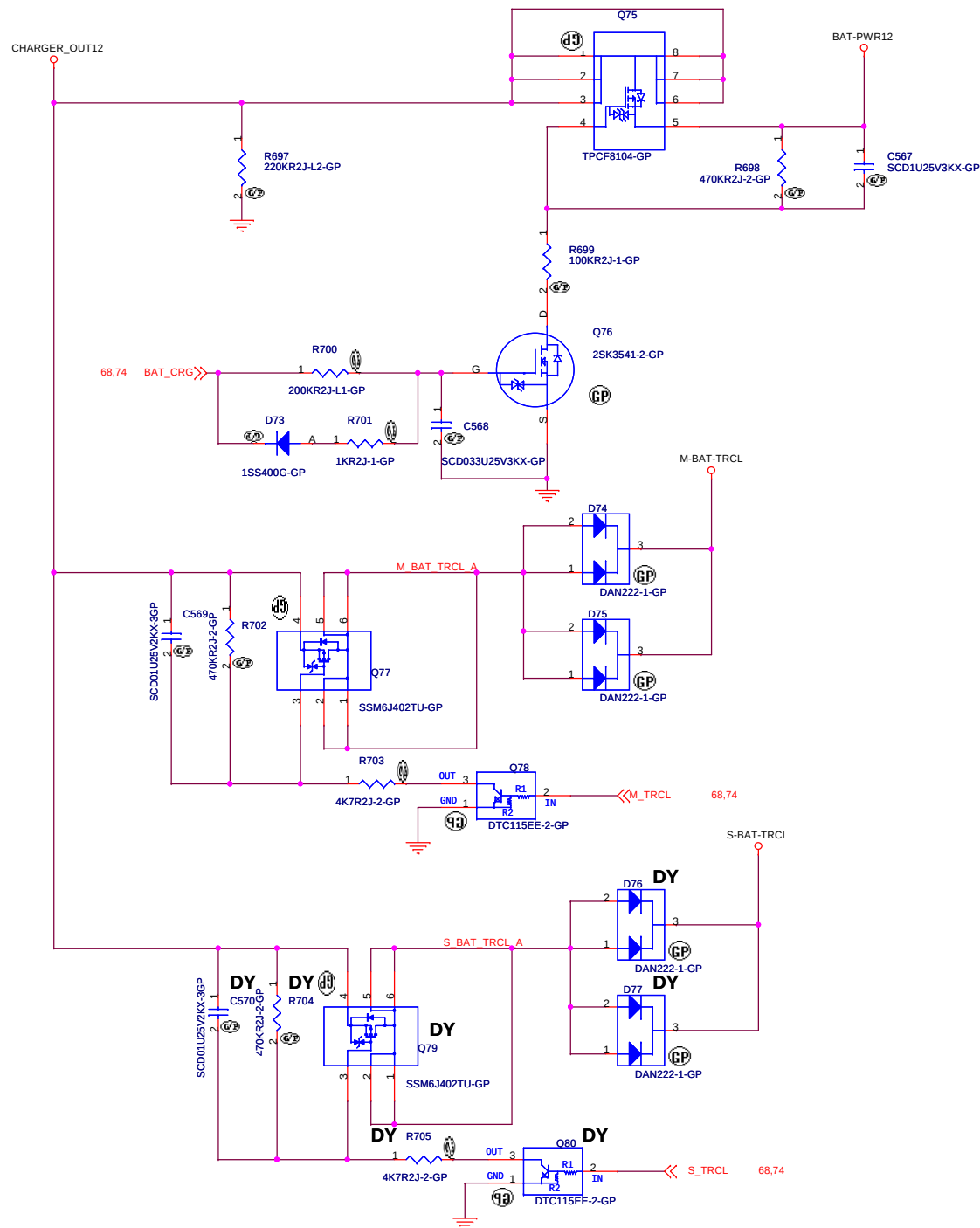
Logic



<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	Document Number	Rev
EEPROM/SMBUS SW	Dasher-1	SD
Date: Monday, January 10, 2011	Sheet 71 of 101	

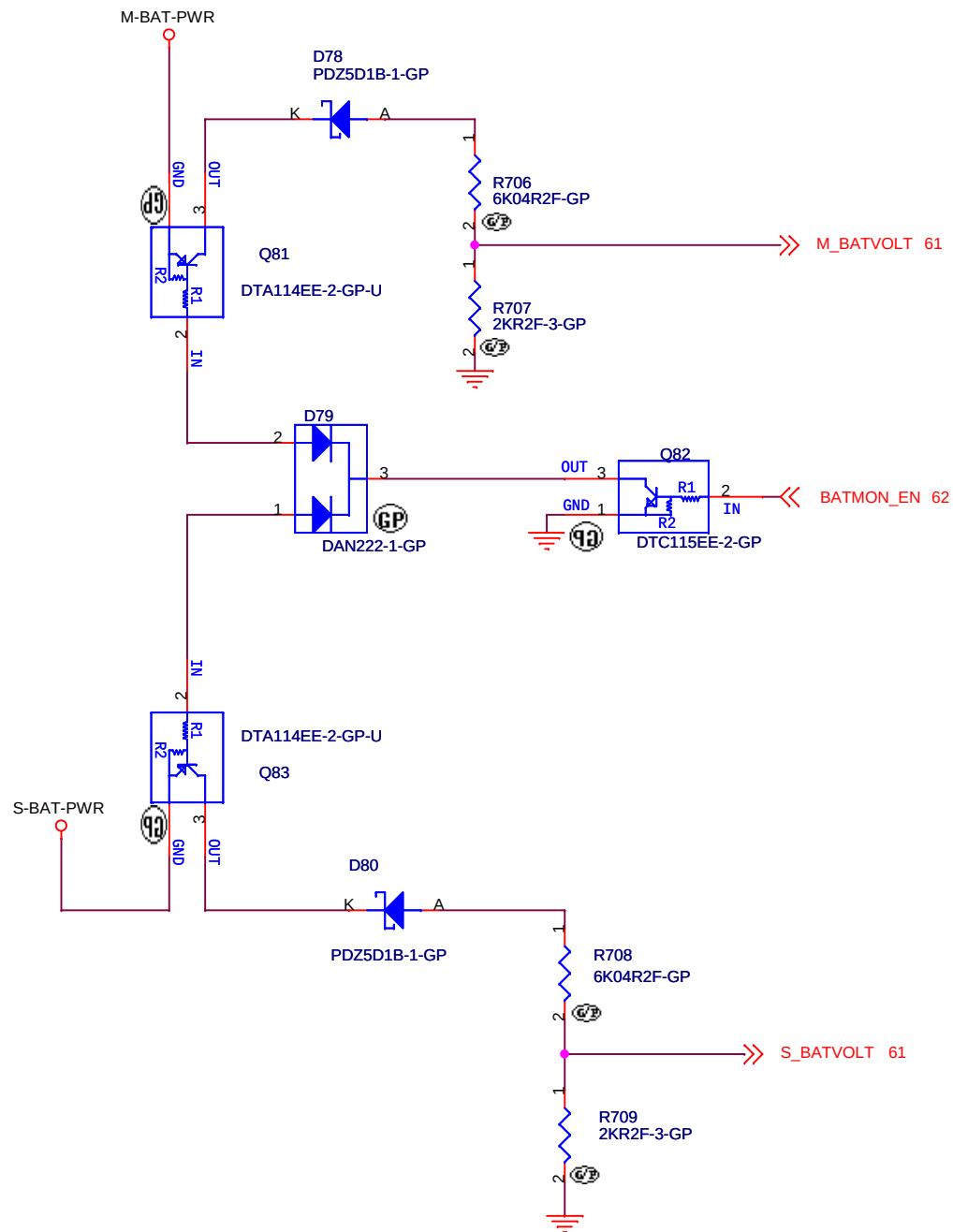


<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title		
CHARGER SELECT		
Size A3	Document Number Dasher-1	Rev SD
Date: Monday, January 10, 2011	Sheet 75 of 101	

$V_{OUT} = 0.249 (V_{BAT} - 5)$



<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BATTERY MONITOR

Size
A4

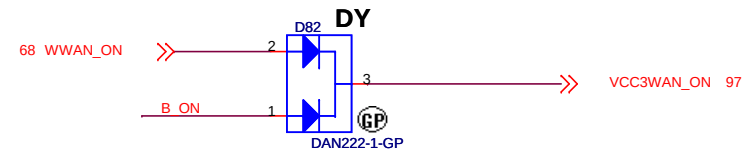
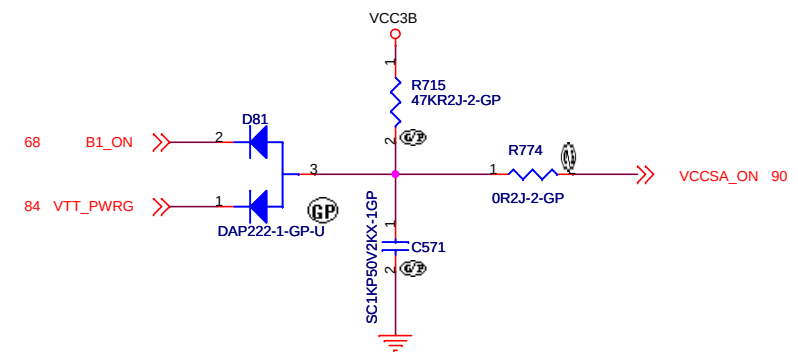
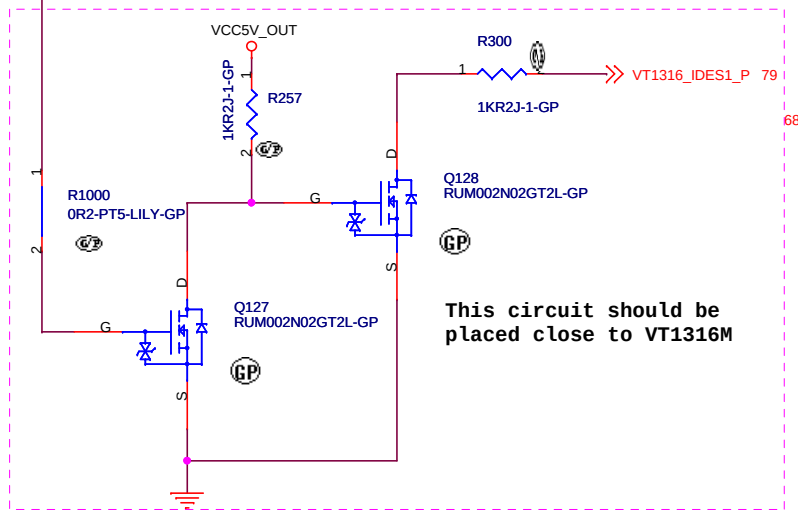
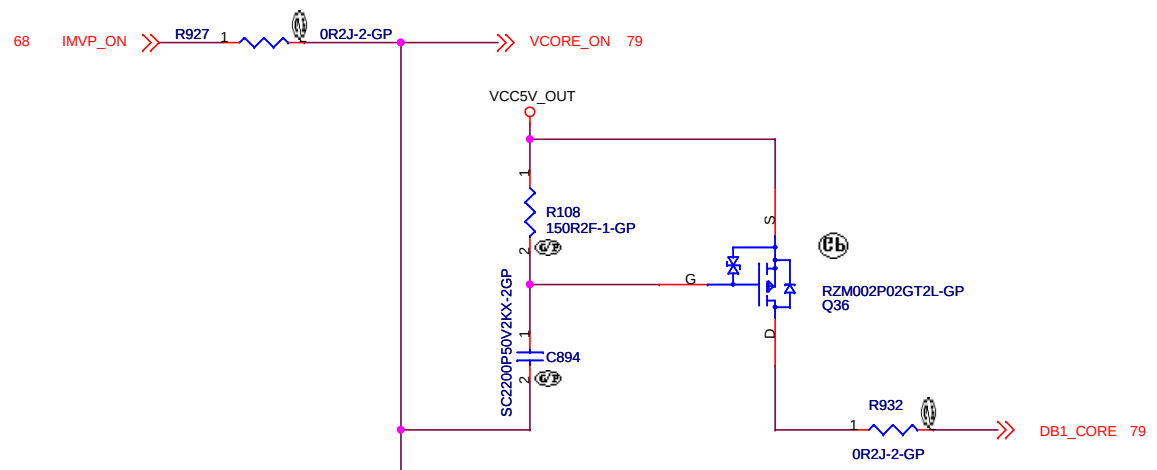
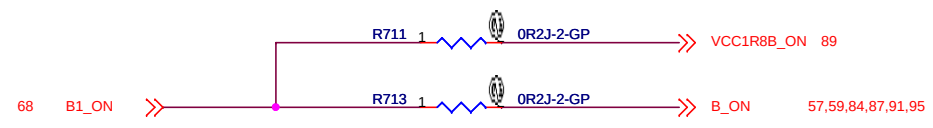
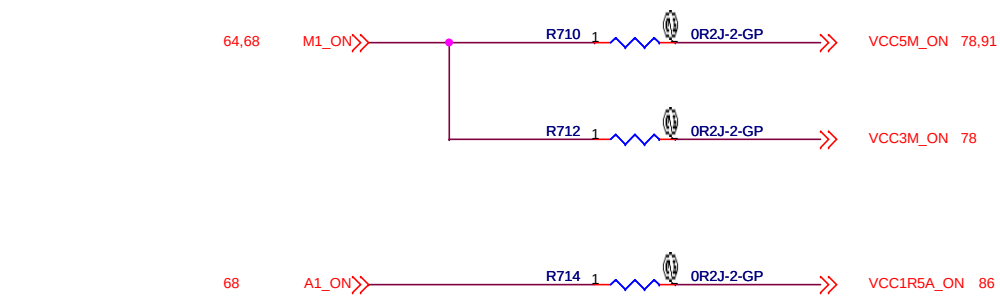
Document Number

Dasher-1

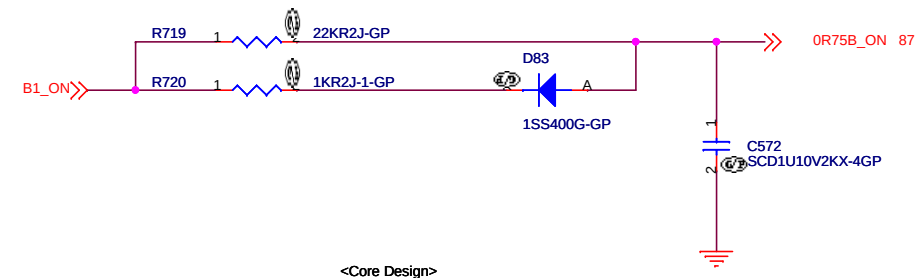
Rev
SD

Date: Monday, January 10, 2011

Sheet 76 of 101



CONSTANT CONTECT	YES	NO
D82	ASM	NO-ASM



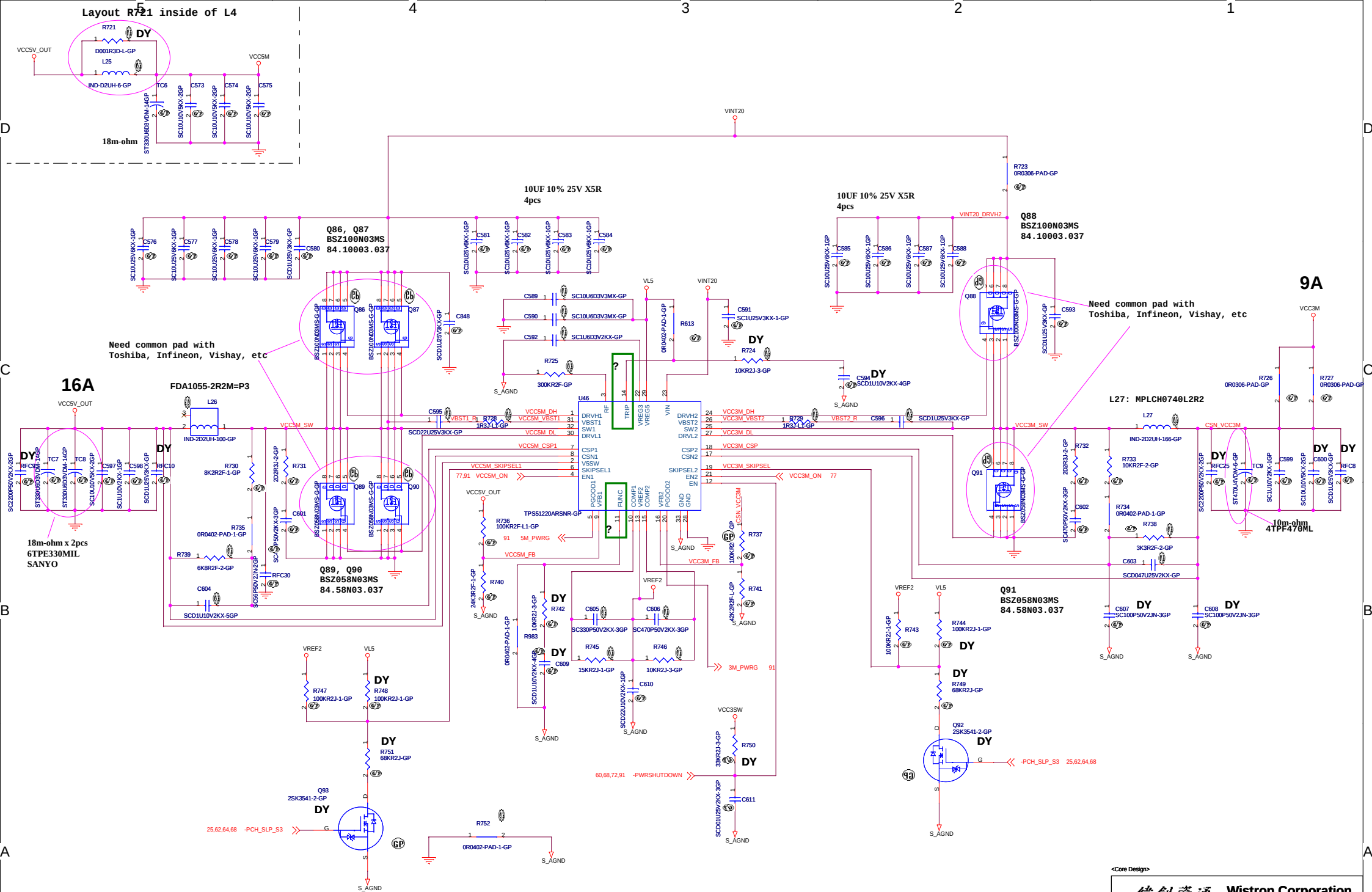
<Core Design>

緯創資通

Wistron Corporation

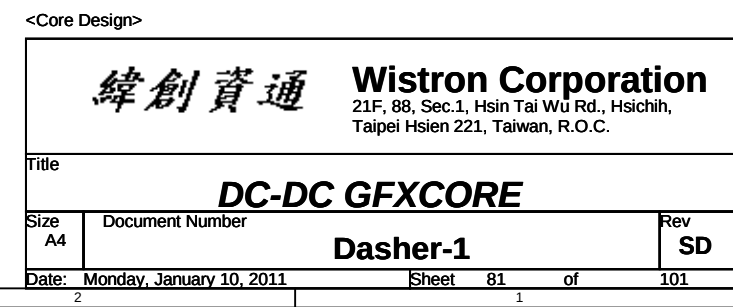
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichin,
Taipei Hsien 221, Taiwan, R.O.C.

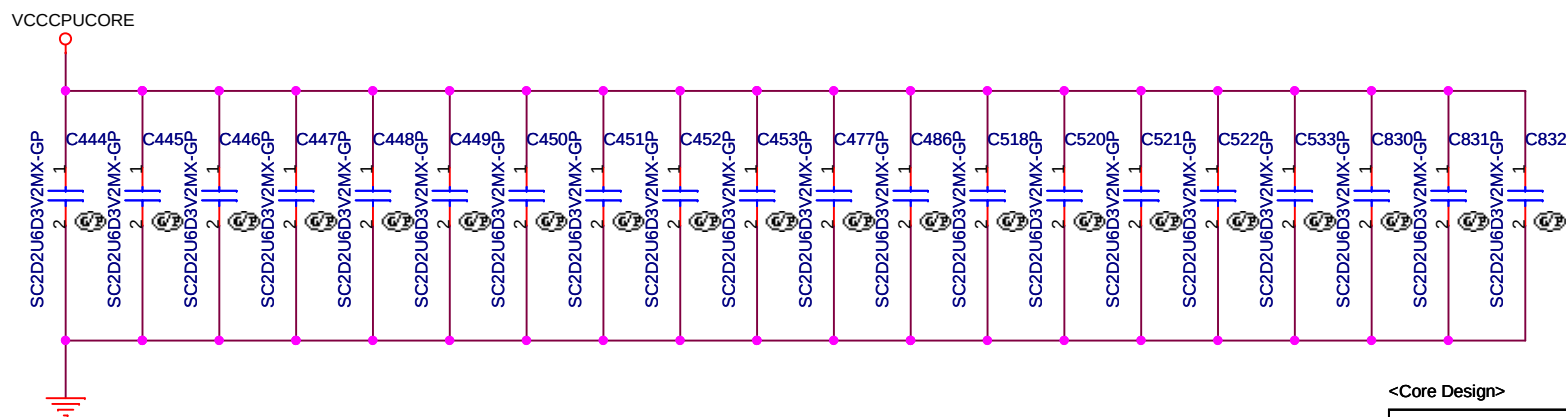
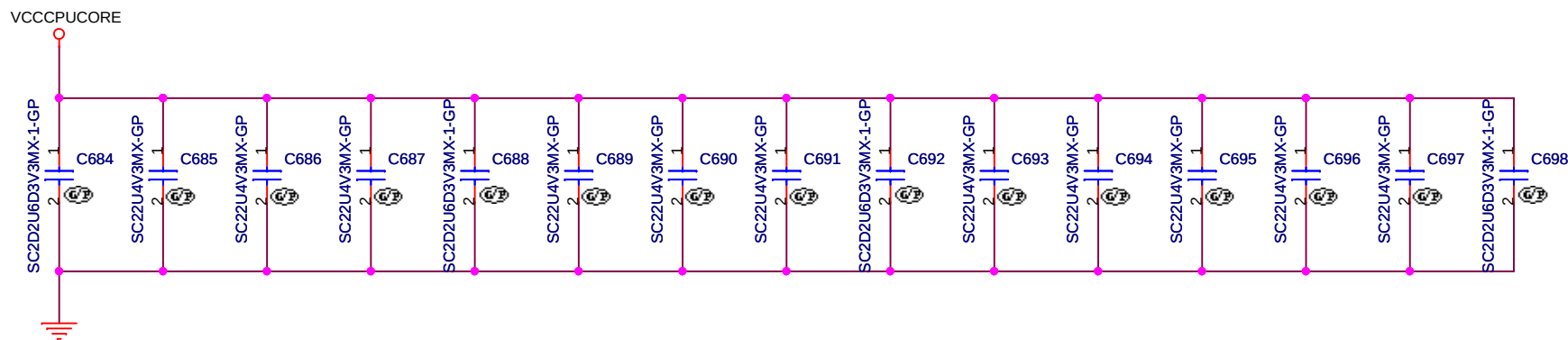
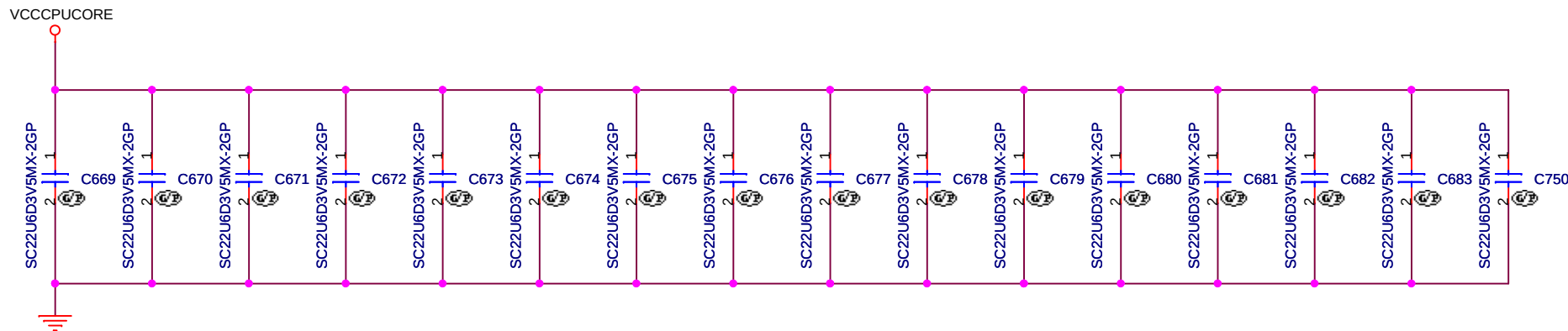
Title		
POWER SEQUENCE		
Size	Document Number	Rev
Custom	Dasher-1	SD
Date:	Monday, January 10, 2011	Sheet 77 of 101



CPU/GFXCORE Table					
CPU	SV 2Phase(53A)		LV 2Phase(43A)	ULV 1Phase(33A)	Remark
GFX	GT1(24A)	GT2(33A)	GT2(33A)	GT2(33A)	
U47 U49	VT1317S VT1317S	VT1317S VT1317S	VT1314S VT1314S	VT1317S NO_ASM	Slave IC
R866 R967	NO_ASM 0	NO_ASM 0	NO_ASM 0	0 NO_ASM	Phase
L38 L28	NO_ASM ASM	NO_ASM ASM	NO_ASM ASM	ASM NO_ASM	Inductor
R783	27.4k	27.4k	39.2k	20.8k	R_SEL0
R784	27.4k	27.4k	6.57k	27.4k	R_SEL1
R785	39.2k	39.2k	39.2k	39.2k	R_SEL2
R788	27.4k	32.4k	32.4k	32.4k	R_SEL3
R789	11.4k(0.5%)	23.7k(1%)	23.7k(1%)	23.7k(1%)	R_SEL4
R798	27.4k	27.4k	27.4k	27.4k	R_SEL5
R792	8.66k(0.5%)	8.66k(0.5%)	8.66k(0.5%)	8.66k(0.5%)	R_SEL6
R779	7.87k	7.87k	7.87k	NO_ASM	RDES #2
C627	1000pF	1000pF	1000pF	NO_ASM	
R781	7.68k	7.68k	7.68k	NO_ASM	
R801	7.87k	7.87k	7.87k	NO_ASM	
C638	1000pF	1000pF	1000pF	NO_ASM	
R802	7.68k	7.68k	7.68k	NO_ASM	
C628	4700pF	4700pF	4700pF	NO_ASM	
R795	3.09k	3.09k	3.09k	NO_ASM	
R772	8.25k	8.25k	10.5k	8.06k	IMON1
R775	191	191	165	383	
R773	8.87k	8.66k	8.66k	8.66k	IMON2
R776	178	294	294	294	
R798	140k	140k	255k	221k	DCMDR1
R803	1.69k	1.69k	3.57k	3.16k	
R797	52.3k	52.3k	110k	97.6k	
R800	316k	274k	274k	274k	DCMDR1(prs)
R804	4.99k	4.12k	4.12k	(drp)	
R799	154k	127k	127k	(ofs)	
C631	10uF	10uF	10uF	NO_ASM	GIN
C636	10uF	1uF	1uF	NO_ASM	
C633	1uF	1uF	1uF	NO_ASM	
C634	1uF	1uF	1uF	NO_ASM	
C635	0.1uF	0.1uF	0.1uF	NO_ASM	
C632	10uF	10uF	10uF	NO_ASM	
C637	10uF	10uF	10uF	NO_ASM	
R796	10	10	10	NO_ASM	AVDD
C641	0.1uF	0.1uF	0.1uF	NO_ASM	
R805	0	0	0	NO_ASM	
C684	2.2uF	2.2uF	10uF	10uF	Coutput(1603)
C685	22uF	22uF	10uF	22uF	
C686	22uF	22uF	NO_ASM	22uF	
C687	22uF	22uF	NO_ASM	22uF	
C688	2.2uF	2.2uF	10uF	10uF	
C689	22uF	22uF	NO_ASM	22uF	
C690	22uF	22uF	10uF	22uF	
C691	22uF	22uF	NO_ASM	22uF	
C692	2.2uF	2.2uF	10uF	10uF	
C693	22uF	22uF	10uF	10uF	
C694	22uF	22uF	10uF	22uF	
C695	22uF	22uF	NO_ASM	22uF	
C696	22uF	22uF	10uF	22uF	
C697	22uF	22uF	10uF	22uF	
C698	2.2uF	2.2uF	10uF	10uF	
U50	VT1314S	VT1317S	VT1317S	VT1317S	Slave IC
R806	3.4k	3.16k	3.16k	3.16k	RDES G
C653	2700pF	2700pF	2700pF	2700pF	
R807	12.4k	11.5k	11.5k	11.5k	
R810	3.4k	3.16k	3.16k	3.16k	
C662	2700pF	2700pF	2700pF	2700pF	
R811	12.4k	11.5k	11.5k	11.5k	
C654	4700pF	4700pF	4700pF	4700pF	
R808	3.09k	3.09k	3.09k	3.09k	
C642	22uF	22uF	22uF	22uF	Coutput
C643	22uF	22uF	22uF	22uF	
C644	22uF	22uF	22uF	22uF	
C645	22uF	22uF	22uF	22uF	
C646	22uF	22uF	22uF	22uF	
C647	22uF	22uF	22uF	22uF	
C648	22uF	22uF	22uF	22uF	
C649	22uF	22uF	22uF	22uF	
C650	22uF	22uF	22uF	22uF	
C651	22uF	22uF	22uF	22uF	
C652	22uF	22uF	22uF	22uF	
C663	22uF	22uF	22uF	22uF	
C664	22uF	22uF	22uF	22uF	
C665	22uF	22uF	22uF	22uF	
C666	22uF	22uF	22uF	22uF	
C667	22uF	22uF	22uF	22uF	
C20	10uF	22uF	22uF	22uF	Coutput
C21	10uF	10uF	10uF	10uF	CPU side
C22	10uF	22uF	22uF	22uF	
C23	10uF	22uF	22uF	22uF	
C933	10uF	22uF	22uF	22uF	
C934	10uF	10uF	10uF	10uF	

CPU	SV 2Phase(53A)		LV 2Phase(43A)	ULV 1Phase(33A)	Remark
R754	7.5k	7.5k	7.5k	6.98k	IDES (CPU)
R765	7.5k	7.5k	7.5k	6.98k	
R781	7.5k	7.5k	7.5k	NO_ASM	
R802	7.5k	7.5k	7.5k	NO_ASM	
R806	3.16k	3.16k	3.16k	6.2k	IDES (GFX)
R810	3.16k	3.16k	3.16k	6.2k	
R807	11.3k	11.3k	11.3k	8.06k	
R811	11.3k	11.3k	11.3k	8.06k	
C654	4700pF	4700pF	4700pF	NO_ASM	
R808	3.09k	3.09k	3.09k	NO_ASM	





<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title		
DC-DC VCCCPUCORE		
Size A4	Document Number Dasher-1	Rev SD
Date: Monday, January 10, 2011	Sheet 82 of 101	

BLANK

<Core Design>

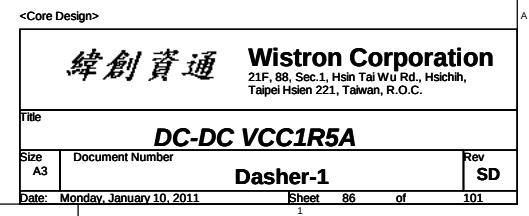
緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title **BLANK**

Size A4	Document Number Dasher-1	Rev SD
------------	------------------------------------	------------------

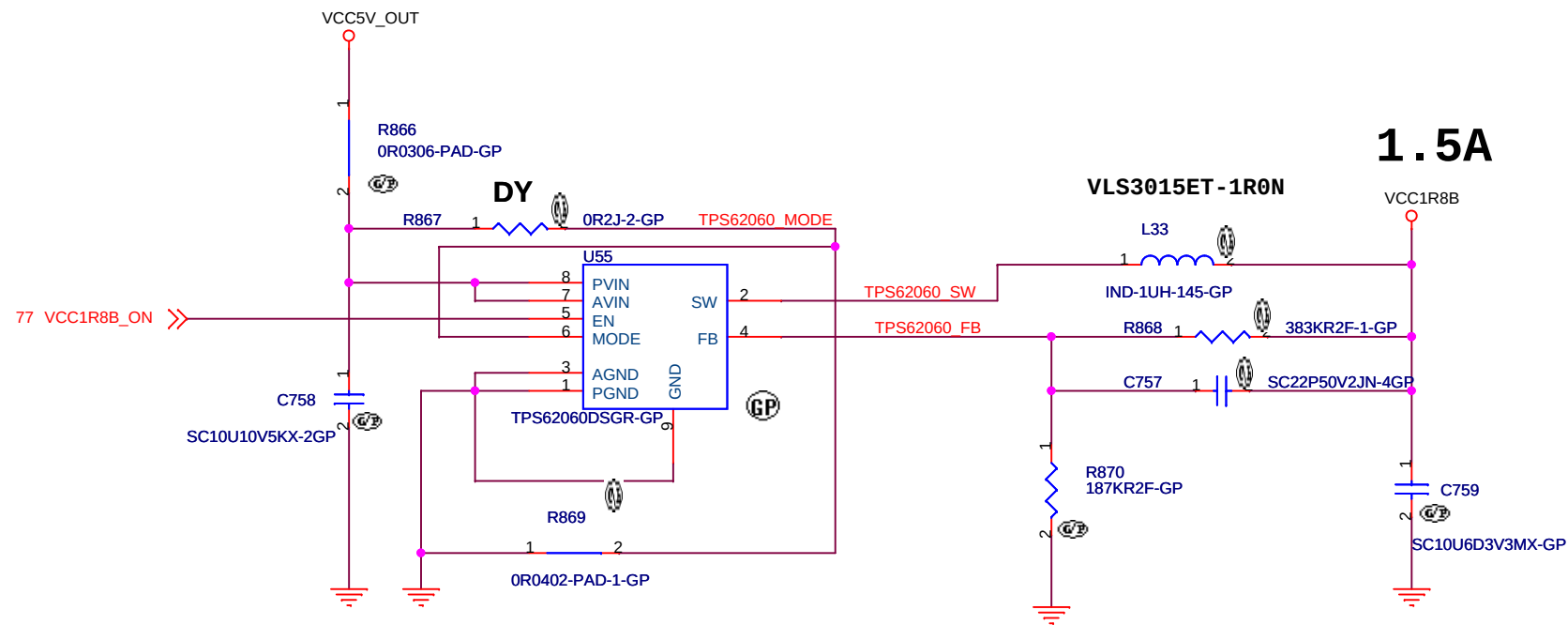
Date: Monday, January 10, 2011 Sheet 83 of 101



BLANK

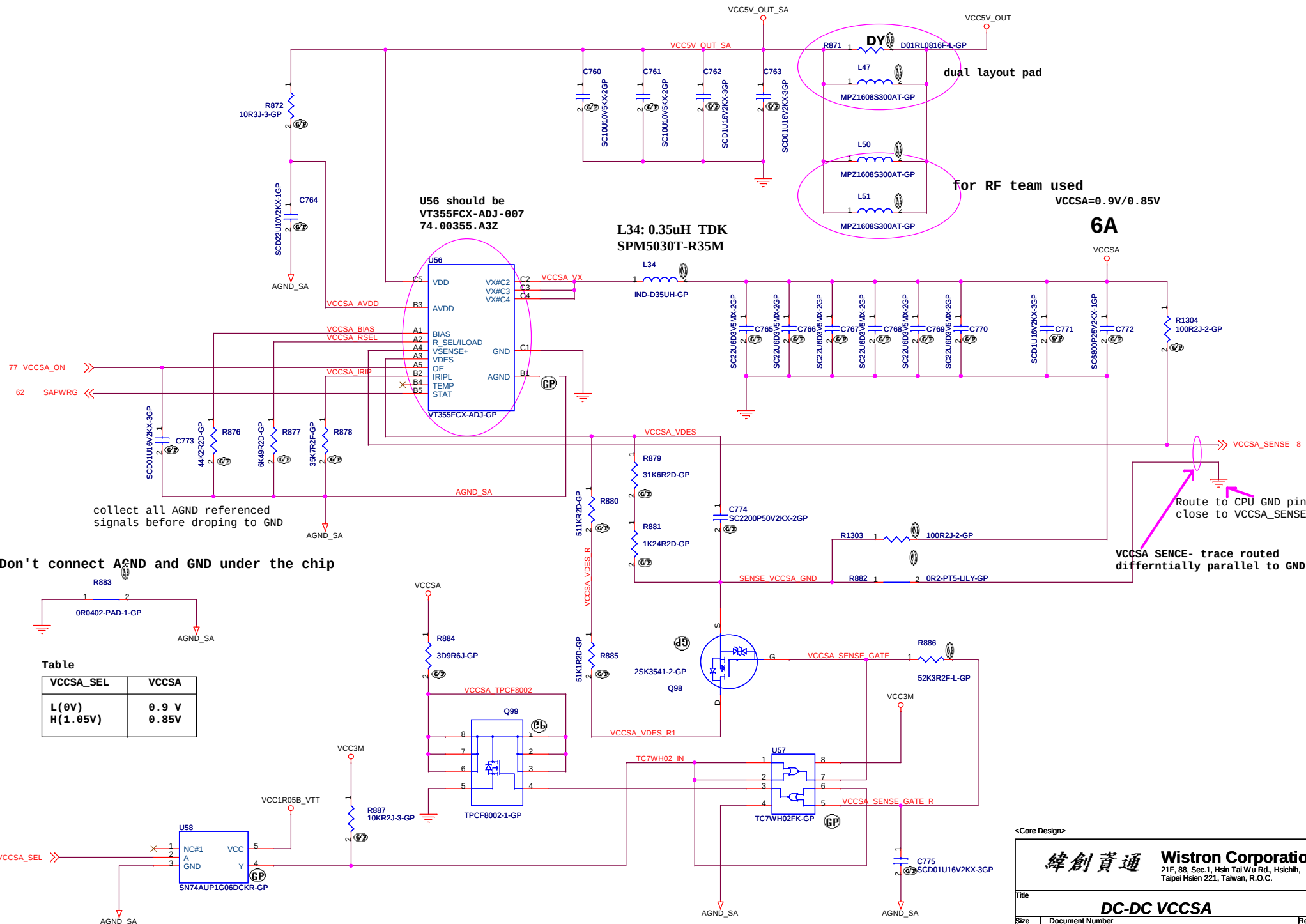
<Core Design>

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
BLANK			
Size A4	Document Number Dasher-1		Rev SD
Date: Monday, January 10, 2011		Sheet 88 of	101



<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		Title DC-DC VCC1R8B	
Size A4	Document Number Dasher-1	Rev SD	
Date: Monday, January 10, 2011	Sheet 89	of	101

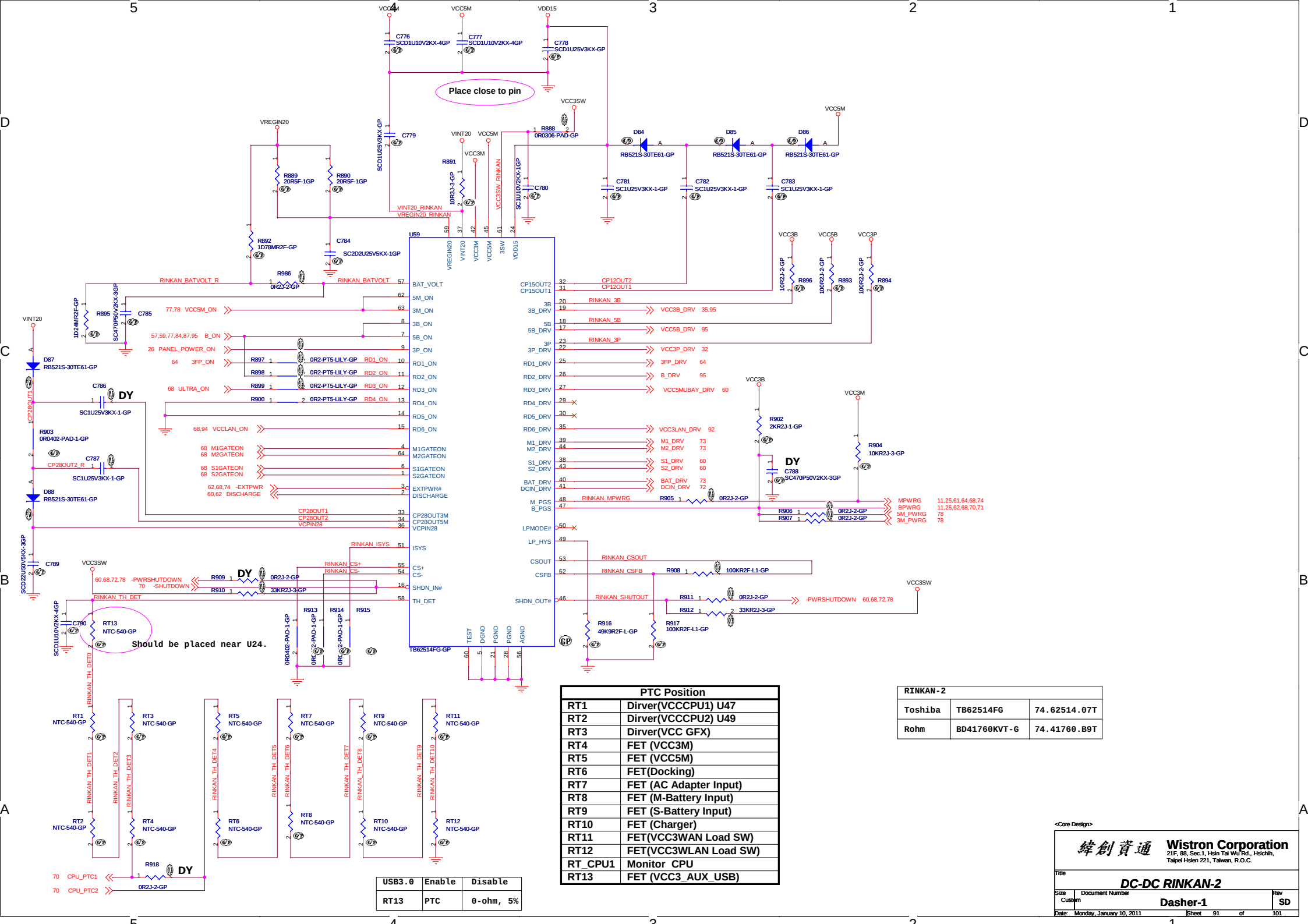


Don't connect AGND and GND under the chip

collect all AGND referenced signals before dropping to GND

Table

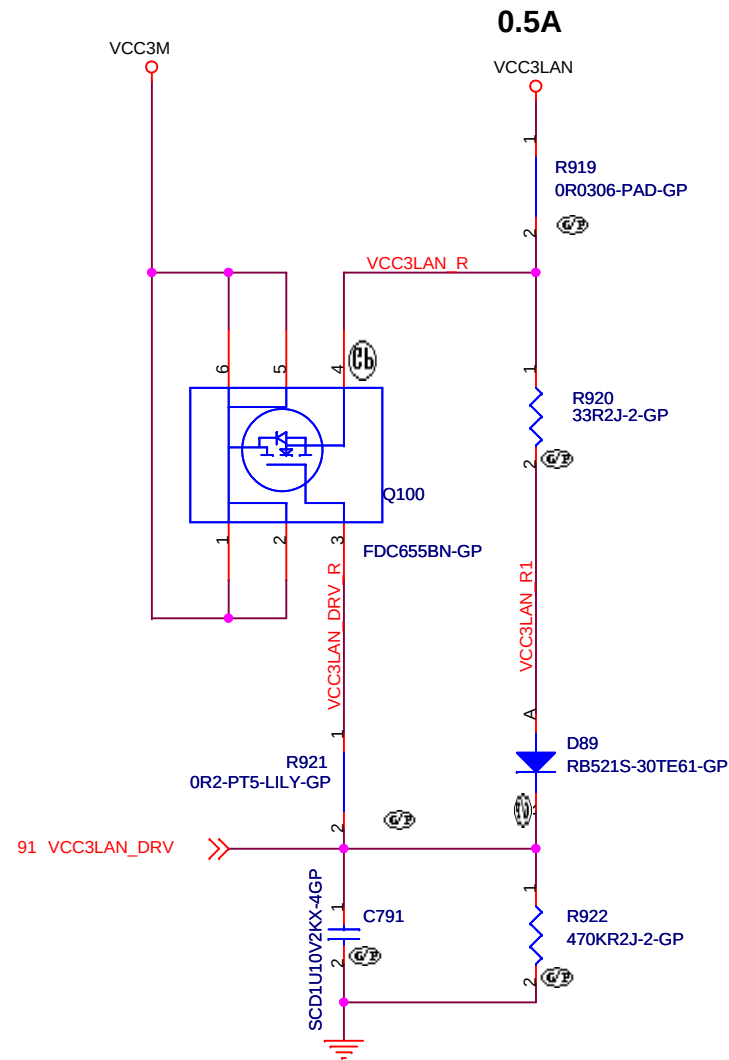
VCCSA_SEL	VCCSA
L(0V)	0.9 V
H(1.05V)	0.85V



PTC Position	
RT1	Dirver(VCCCPU1) U47
RT2	Dirver(VCCCPU2) U49
RT3	Dirver(VCC GFX)
RT4	FET (VCC3M)
RT5	FET (VCC5M)
RT6	FET(Docking)
RT7	FET (AC Adapter Input)
RT8	FET (M-Battery Input)
RT9	FET (S-Battery Input)
RT10	FET (Charger)
RT11	FET(VCC3WAN Load SW)
RT12	FET(VCC3WLAN Load SW)
RT_CPU1	Monitor CPU
RT13	FET (VCC3_AUX_USB)

RINKAN-2		
Toshiba	TB62514FG	74.62514.07T
Rohm	BD41760KVT -G	74.41760.89T

USB3.0		Enable	Disable
RT13	PTC	0-ohm	5%



<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

LOAD SW 1

Size
A4

Document Number

Dasher-1

Rev
SD

Date: Monday, January 10, 2011

Sheet 92 of 101

BLANK

<Core Design>

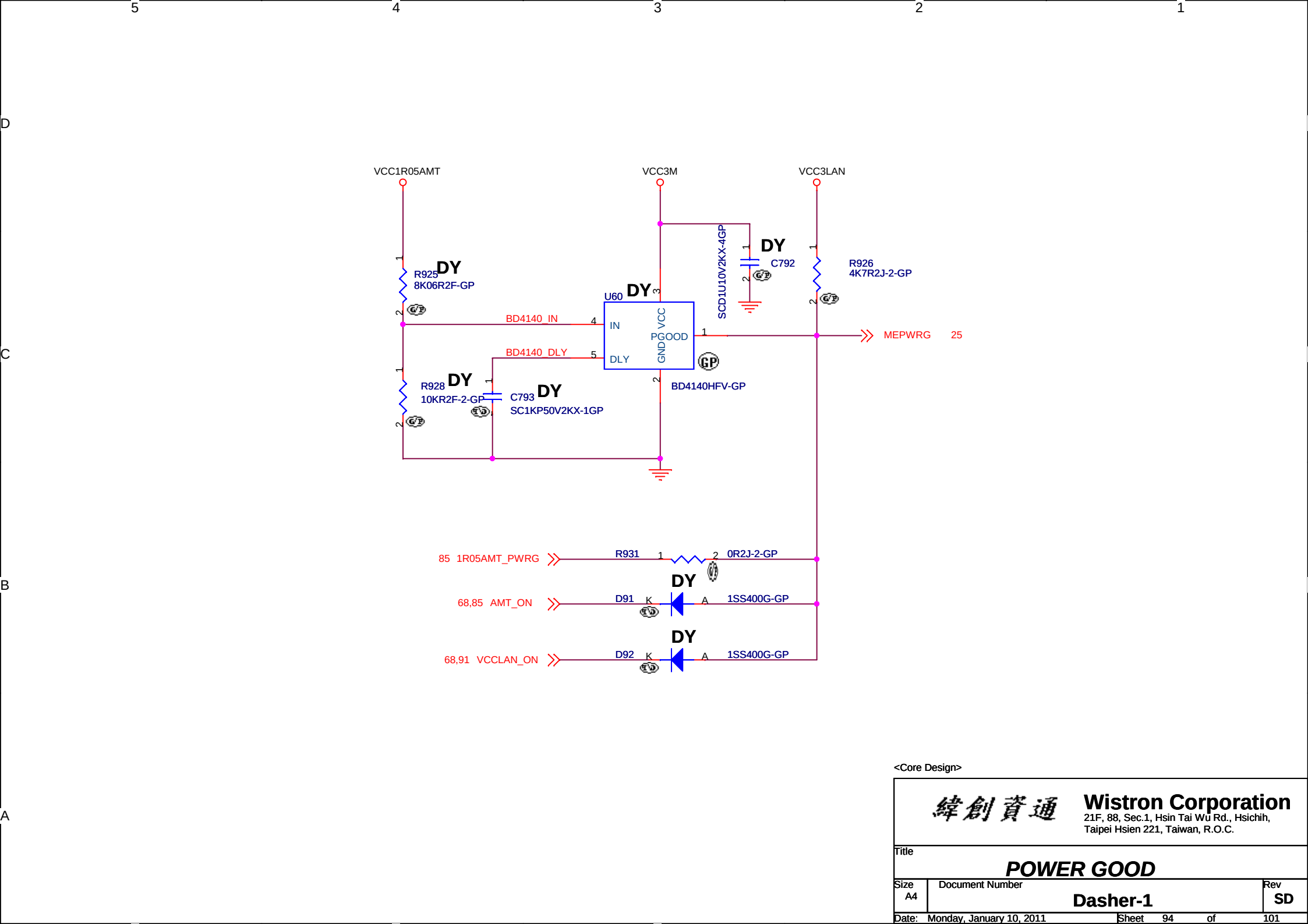
緯創資通

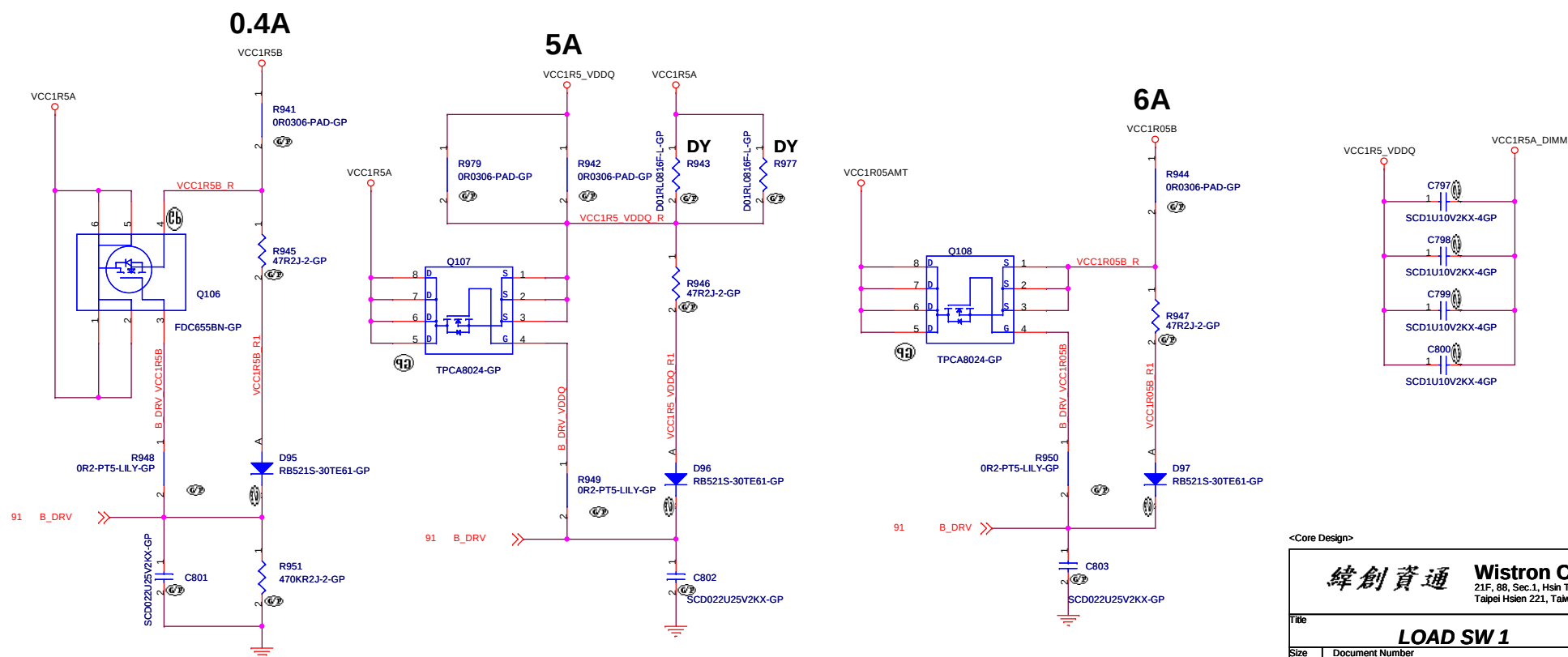
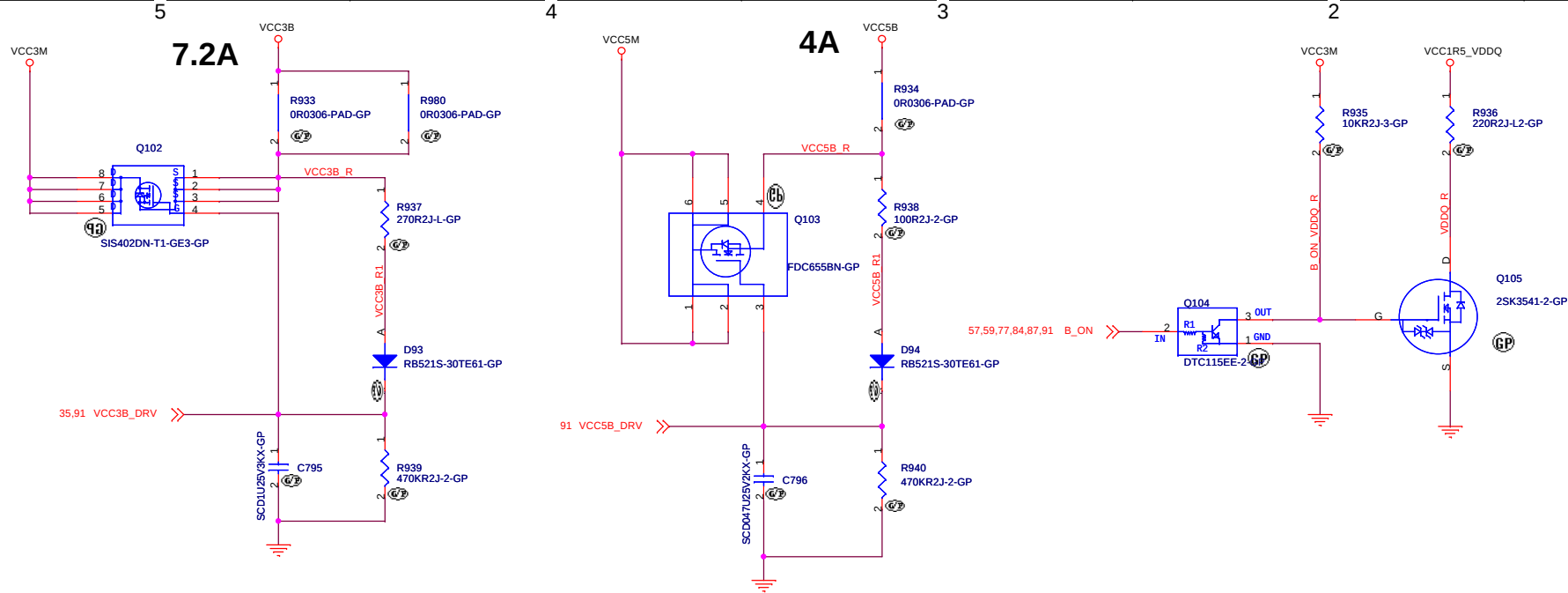
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title **BLANK**

Size A4	Document Number Dasher-1	Rev SD
------------	------------------------------------	------------------

Date: Monday, January 10, 2011 Sheet 93 of 101





BLANK

<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size
A4

Document Number

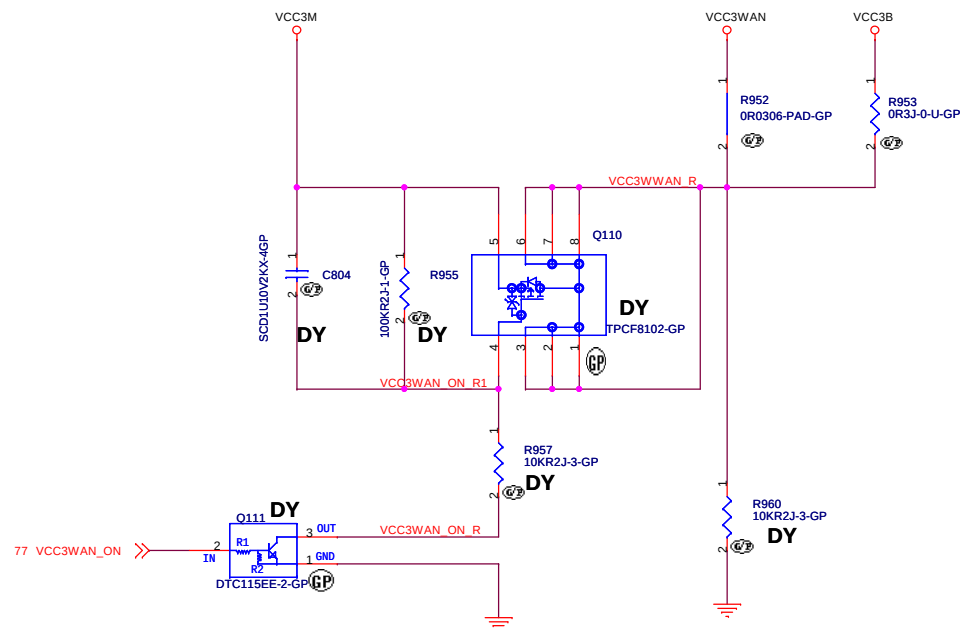
Dasher-1

Rev
SD

Date: Monday, January 10, 2011

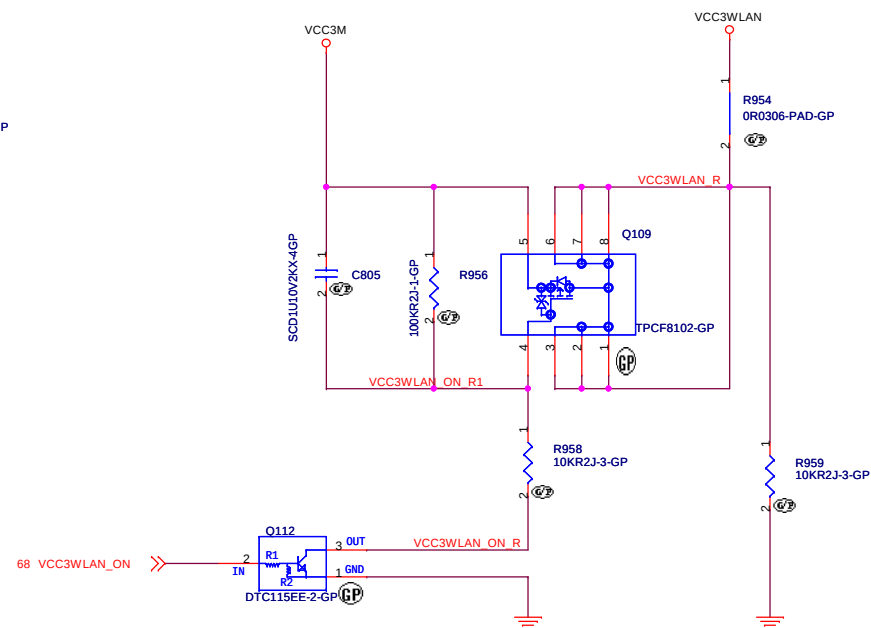
Sheet 96 of 101

2.7A

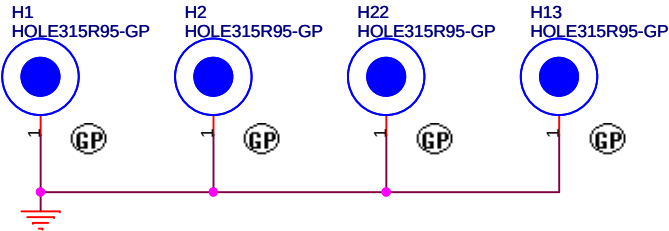


Constant Secure	YES	NO
R953	DY	ASM
C804 R955 Q110 R957 Q111 R960	ASM	DY
R952	ASM	ASM

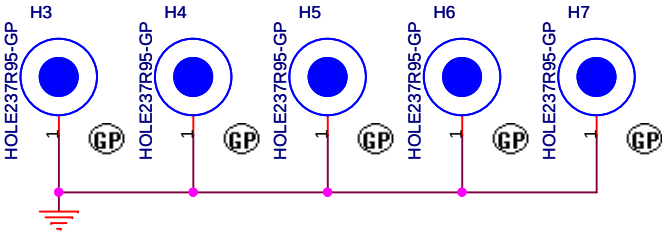
2.7A



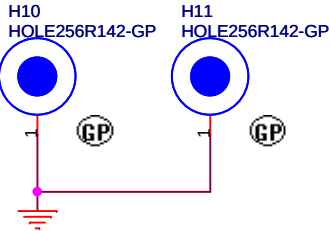
ZZ.00PAD.911



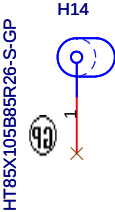
ZZ.00PAD.921



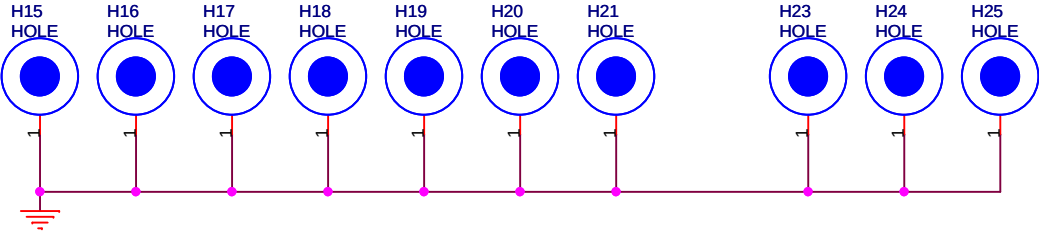
ZZ.00PAD.801



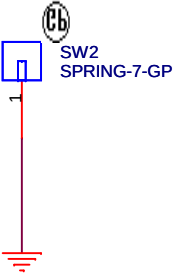
ZZ.SCREW.191



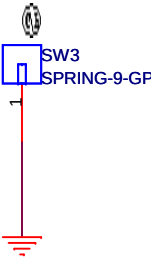
ZZ.0HOLE.XXX



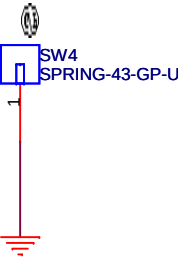
34.49U26.001
GND pad for docking connector



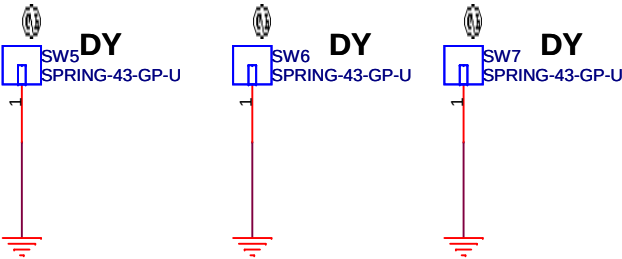
34.49U23.001
GND pad for docking connector



34.15J03.001
spring for EMI



34.15J03.001
spring for RF (SW5~SW7)



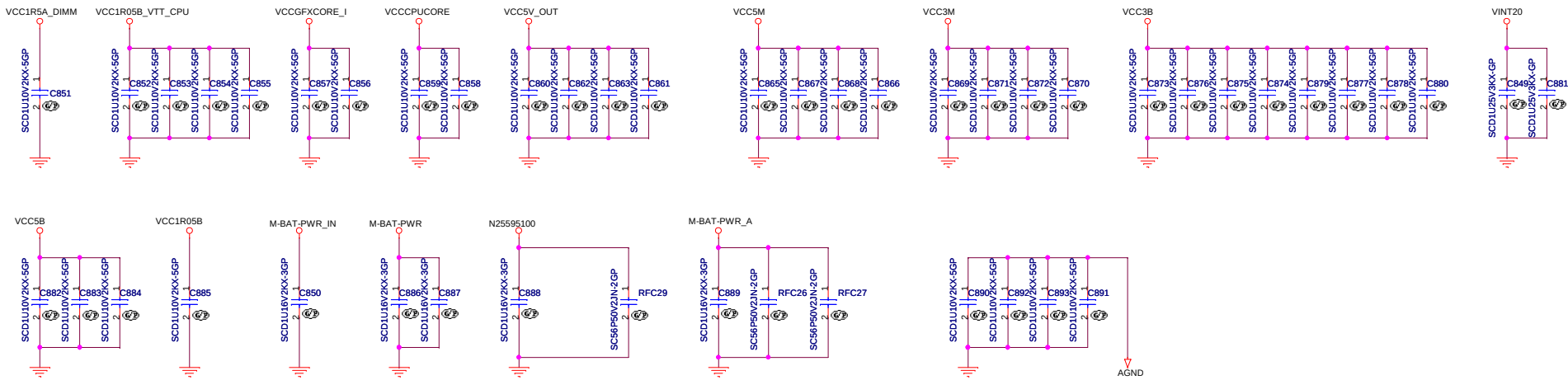
<Core Design>

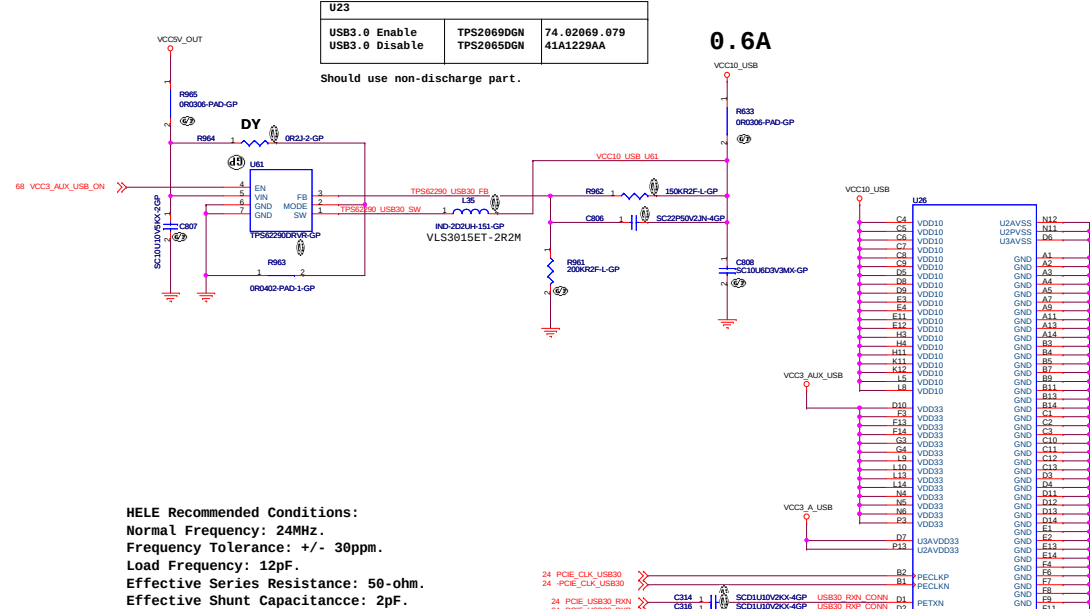
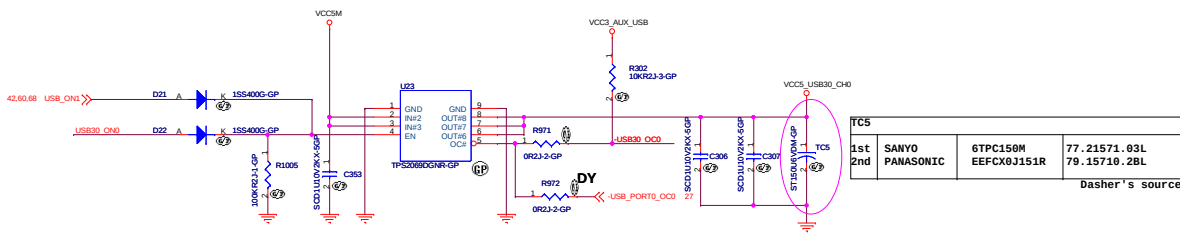
		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
HOLES/GND/PADS			
Size A4	Document Number Dasher-1		Rev SD
Date:	Monday, January 10, 2011	Sheet 98 of	101

RF decoupling caps

named as RFCxxx

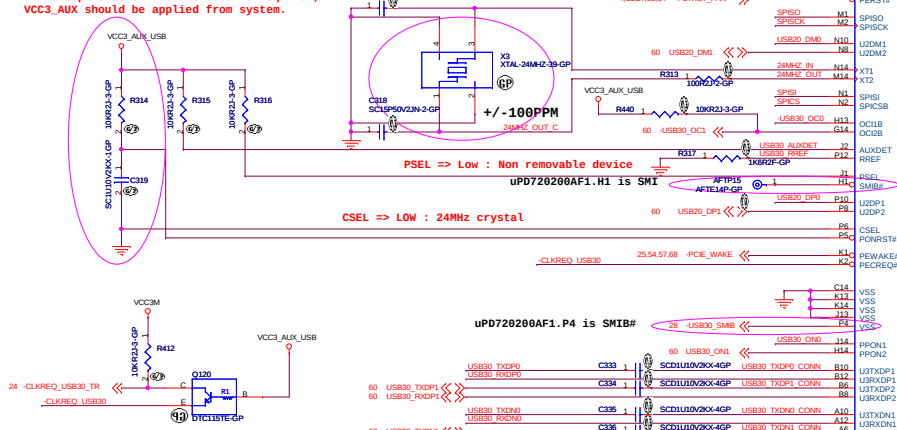
Long power trace EMI decoupling caps



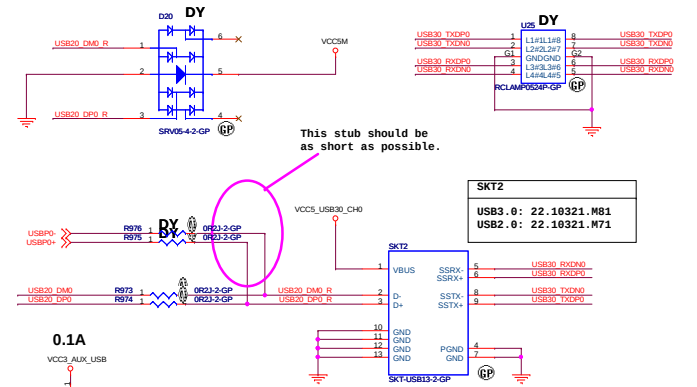


HELE Recommended Conditions:
 Normal Frequency: 24MHz.
 Frequency Tolerance: +/- 30ppm.
 Load Frequency: 12pF.
 Effective Series Resistance: 50-ohm.
 Effective Shunt Capacitance: 2pF.

If Wakeup function from D3 cold is required,
 VCC3_AUX should be applied from system.

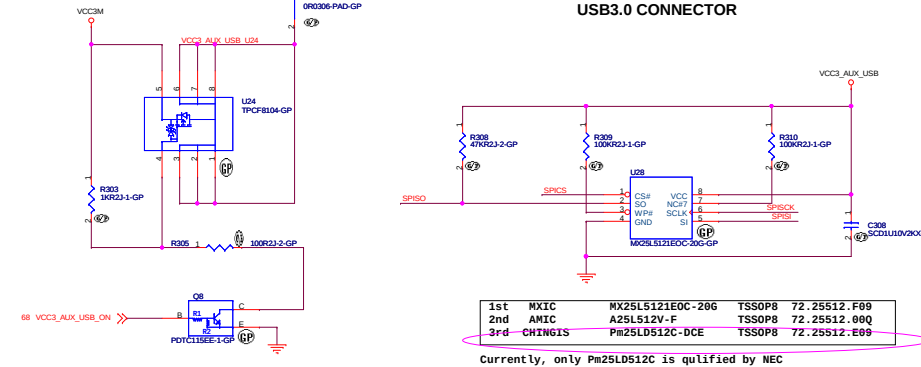


U26 should be uPD720200AF1
 71.72020.000



This stub should be
 as short as possible.

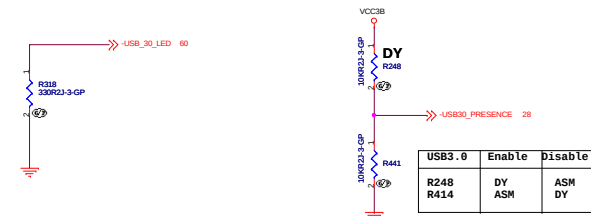
USB3.0 CONNECTOR



1st MXIC MX25L5121E0C-20G TSSOP8 72.25512.F09
 2nd AMIC A25L512V-F TSSOP8 72.25512.00Q
 3rd CHINGIS PM25LD512C-DCE TSSOP8 72.25512.E09

Currently, only Pm25LD512C is qualified by NEC

NOT MANDATORY TO PLACE.



USB3.0	Enable	Disable
R248	DY	ASH
R414	ASH	DY

<Core Design>

緯創資通 Wistron Corporation
 2/F, 88, Sec.1, Hsin-Tsuei Rd., Hsinchu, Taiwan, R.O.C.

File	USB 3.0
Size	Document Number
Customer	Dasher-1
Date	Monday, January 30, 2012
Printed	100 of 101

5		
USB3.0 Enable USB3.0 Disable		
U23	TPS2069DGN	TPS2065DGN
D21	ASM	ASM
D22	ASM	DY
C353	ASM	ASM
R302	ASM	DY
R971	ASM	DY
R972	DY	ASM
C306	ASM	ASM
C307	ASM	ASM
TC5	ASM	ASM
U61	ASM	DY
R965	ASM	DY
R964	DY	DY
R963	ASM	DY
C807	ASM	DY
L35	ASM	DY
R961	ASM	DY
R962	ASM	DY
C806	ASM	DY
C808	ASM	DY
R633	ASM	DY
U26	ASM	DY
C314	ASM	DY
C316	ASM	DY
R313	ASM	DY
X3	ASM	DY
C317	ASM	DY
C318	ASM	DY
C319	ASM	DY
R440	ASM	DY
R314	ASM	DY
R315	ASM	DY
R316	ASM	DY
R317	ASM	DY
C310	ASM	DY
R412	ASM	ASM
Q120	ASM	DY
C333	ASM	DY
C334	ASM	DY
C335	ASM	DY
C336	ASM	DY
U28	ASM	DY
R308	ASM	DY
R309	ASM	DY
R310	ASM	DY
C308	ASM	DY
U24	ASM	DY
R638	ASM	DY
R303	ASM	DY
R305	ASM	DY
Q8	ASM	DY
SKT2	USB3.0 Conn.	USB2.0 Conn.
R973	ASM	DY
R974	ASM	DY
R975	DY	ASM
R976	DY	ASM
D20	DY	DY
D25	DY	DY
C310	ASM	DY
C311	ASM	DY
C312	ASM	DY
C313	ASM	DY
C315	ASM	DY
C320	ASM	DY
C321	ASM	DY
C322	ASM	DY
C323	ASM	DY
C324	ASM	DY
C325	ASM	DY
C326	ASM	DY
C327	ASM	DY
L14	ASM	DY
C329	ASM	DY
C330	ASM	DY
C331	ASM	DY
C332	ASM	DY
R318	ASM	DY
R248	DY	ASM
R441	ASM	DY

4		
USB3.0 Enable USB3.0 Disable		
R437	DY	ASM
R438	DY	ASM
R436	ASM	DY
R439	ASM	DY
D99	ASM	DY
D100	ASM	DY
R989	DY	ASM
R991	ASM	DY
R988	DY	ASM