

NOZOMI-3 SWG SDV2 LOGIC SCHEMATICS

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NZM3H-2
VER 2.10
JUNE/08/2010

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- 78.DC/DC VCC5M/VCC3M(TPS51220ARSNR)
- 79.DC/DC VCCCPUCORE(VT1316/VT1317)
- 80.BLANK
- 81.DC/DC VCCGFXCORE_I(VT1317)
- 82.VCCCPUCORE DECOUPLING
- 83.DC/DC VCCGFXCORE_D(ADP3211)
- 84.DC/DC VCC1R05B_VTT(VT357)
- 85.DC/DC VCC1R05LAN(VT356)
- 86.DC/DC VCC1R5A(VT357)
- 87.DC/DC VCC0R75B(MAX1510)
- 88.DC/DC VCC1R5VIDEO(BD9139)
- 89.DC/DC VCC1R8B(TPS62290)
- 90.DC/DC VCCSA(VT356)
- 91.DC/DC RINKAN-2
- 92.LOAD SW LAN
- 93.LOAD SW VIDEO
- 94.LOAD SW AMT/ MEPWRG
- 95.LOAD SW B
- 96.LOAD SW VCC5MUBAY
- 97.LOAD SW WAN & WLAN
- 98.PTH FOR SCREW HOLES
- 99.BLANK

BASE LOGIC :NZM3 SWG SDV
VER 1.22
APR/26/2010

EC HISTORY

NOZOMI-3 PRE-DV :BASE LOGIC NZM1 EXT 512MB SIT VER.7.03 12/01/2009

VER.0.01 12/10/2009 APPLIED PDV_EC001
VER.0.02 12/14/2009 APPLIED PDV_EC002-012
VER.0.03 12/15/2009 APPLIED PDV_EC013-025
VER.0.04 12/16/2009 APPLIED PDV_EC026-033
VER.0.05 12/17/2009 APPLIED PDV_EC034-038
VER.0.06 12/18/2009 APPLIED PDV_EC039-041,043-45,047-054
VER.0.07 12/21/2009 APPLIED PDV_EC042,046,055-066
VER.0.08 12/22/2009 APPLIED PDV_EC067-080
VER.0.09 12/24/2009 APPLIED PDV_EC081-085
VER.0.10 12/25/2009 APPLIED PDV_EC086-089
VER.0.11 12/28/2009 APPLIED PDV_EC090-095
VER.0.12 01/06/2010 APPLIED PDV_EC096-112
VER.0.13 01/07/2010 APPLIED PDV_EC113-120
VER.0.14 01/08/2010 APPLIED PDV_EC121
VER.0.15 01/12/2010 APPLIED PDV_EC122
VER.0.16 01/13/2010 APPLIED PDV_EC123-125
VER.0.17 01/14/2010 APPLIED PDV_EC126-128,130
VER.0.18 01/15/2010 APPLIED PDV_EC131-133
VER.0.19 01/18/2010 APPLIED PDV_EC134-137

VER.0.20 01/19/2010 APPLIED PDV_EC138
VER.0.21 01/20/2010 APPLIED PDV_EC139,140
VER.0.22 01/21/2010 APPLIED PDV_EC141-146
VER.0.23 01/22/2010 APPLIED PDV_EC147,148
VER.0.24 01/25/2010 APPLIED PDV_EC149-160
VER.0.25 01/26/2010 APPLIED PDV_EC162-168
VER.0.26 01/27/2010 APPLIED PDV_EC169-176
VER.0.27 01/28/2010 APPLIED PDV_EC177-179
VER.0.28 01/29/2010 APPLIED PDV_EC180
VER.0.29 02/01/2010 APPLIED PDV_EC181-189
VER.0.30 02/02/2010 APPLIED PDV_EC190-193
VER.0.31 02/03/2010 APPLIED PDV_EC194-197
VER.0.32 02/04/2010 APPLIED PDV_EC198-202

NOZOMI-3 SDV :BASE LOGIC NZM3 PRE-DV VER.0.32 02/04/2010

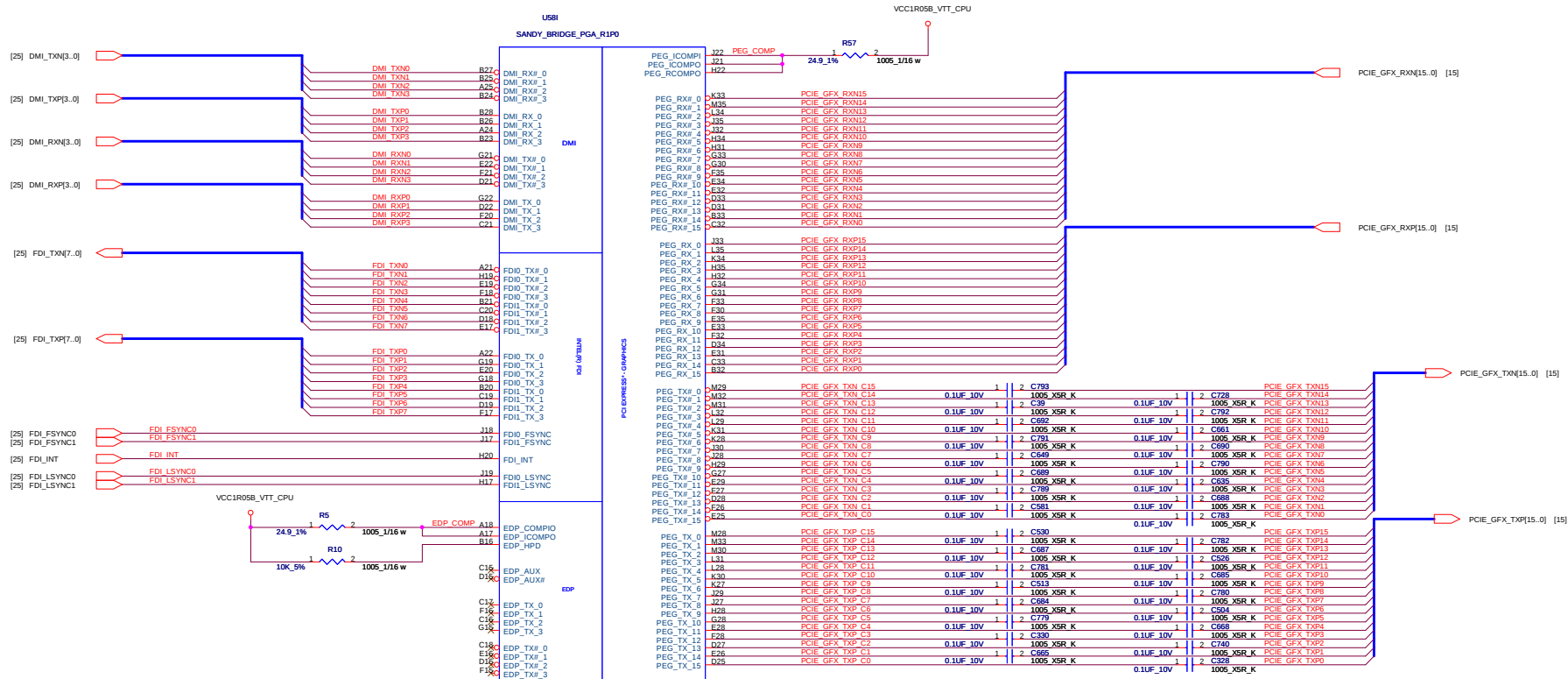
VER.1.00 02/05/2010 APPLIED SDV_EC001-004
VER.1.01 02/08/2010 APPLIED SDV_EC005-007
VER.1.02 02/09/2010 APPLIED SDV_EC010
VER.1.03 02/10/2010 APPLIED SDV_EC011-013
VER.1.04 02/15/2010 APPLIED SDV_EC014
VER.1.05 02/16/2010 APPLIED SDV_EC015-019
VER.1.06 02/17/2010 APPLIED SDV_EC020-022
VER.1.07 02/18/2010 APPLIED SDV_EC024,025,027
VER.1.08 02/19/2010 APPLIED SDV_EC028,030-032
VER.1.09 02/22/2010 APPLIED SDV_EC033-035
VER.1.10 02/23/2010 APPLIED SDV_EC036-038

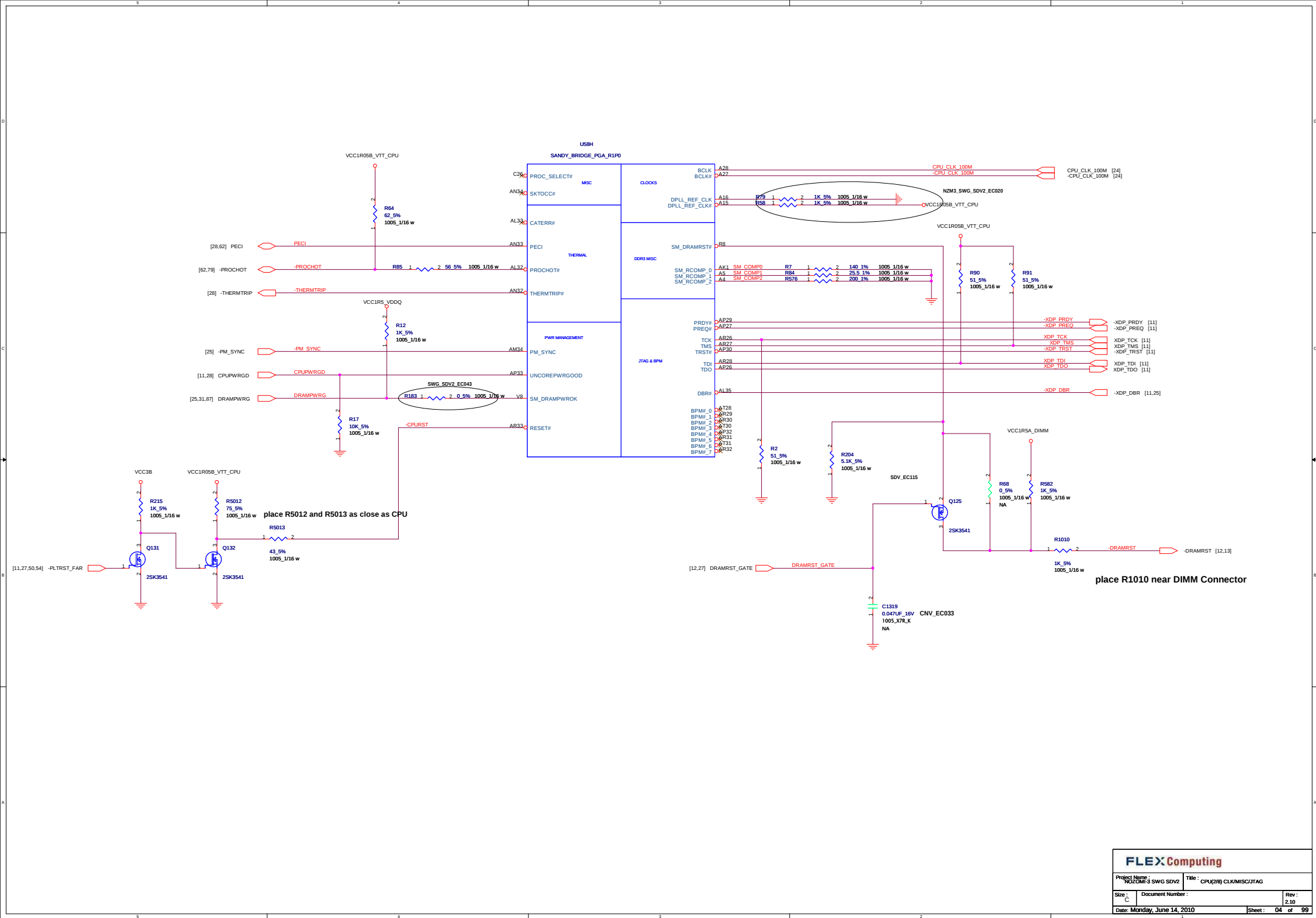
VER.1.11 03/18/2010 APPLIED SDV_EC045,046
VER.1.12 03/19/2010 APPLIED SDV_EC040-043,CNV_EC061-066
VER.1.13 03/22/2010 APPLIED SDV_EC048-052
VER.1.14 03/24/2010 APPLIED SDV_EC053-059, ECR001-003
VER.1.15 03/26/2010 APPLIED SDV_EC061
VER.1.16 04/1/2010 APPLIED SDV_EC065-075, except SDV_EC069, SDV_EC076-079
VER.1.17 04/6/2010 APPLIED SDV_EC080-084, ECR004, ECR008 and ECR009
VER.1.18 04/8/2010 APPLIED SDV_EC085-091, SDV_ECR010-013
VER.1.19 04/13/2010 APPLIED SDV_EC092-094, change PCB footprint of all resistors from xxx to xxx-R, SDV_EC103 and EC047
VER.1.20 04/16/2010 APPLIED SDV_EC105-110 and ECR023, ECR026 and ECR027
VER.1.21 04/21/2010 APPLIED ECR028

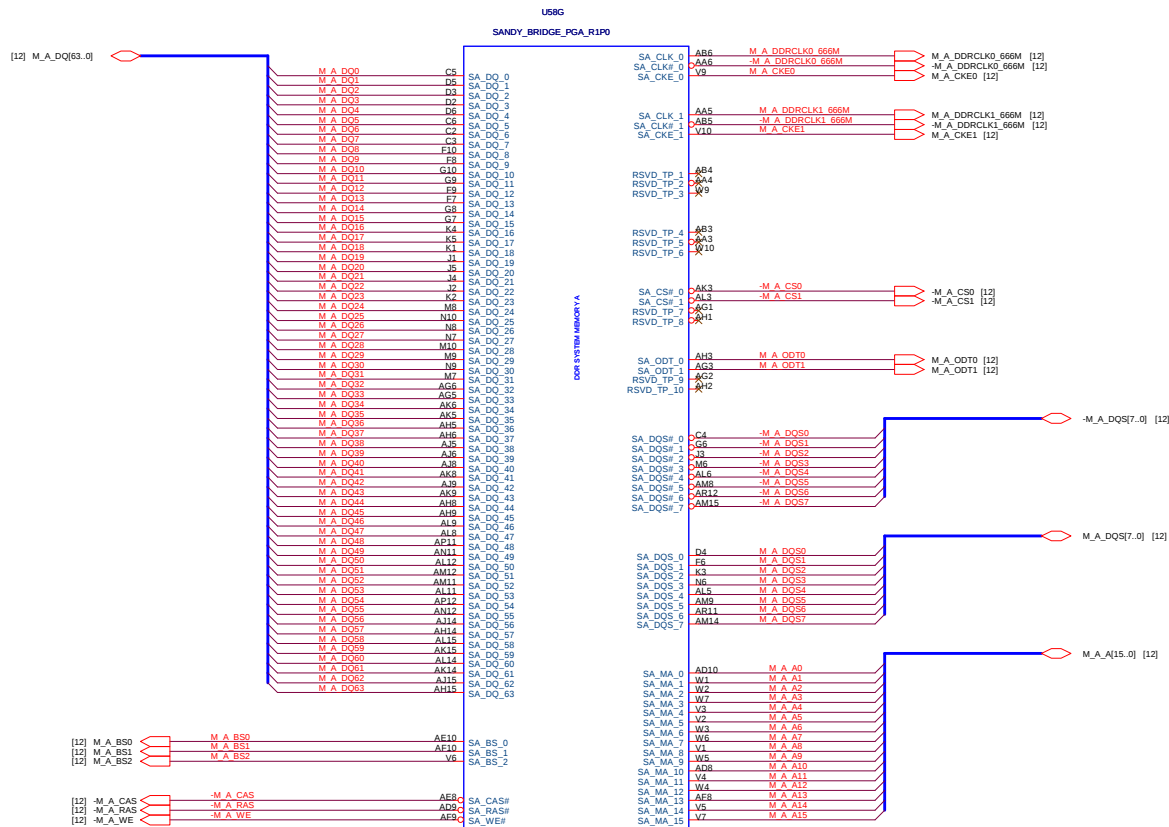
~~VER.1.22 04/26/2010 APPLIED EC111-113, EC115-117~~ NZM3_SWG_SDV2_EC077

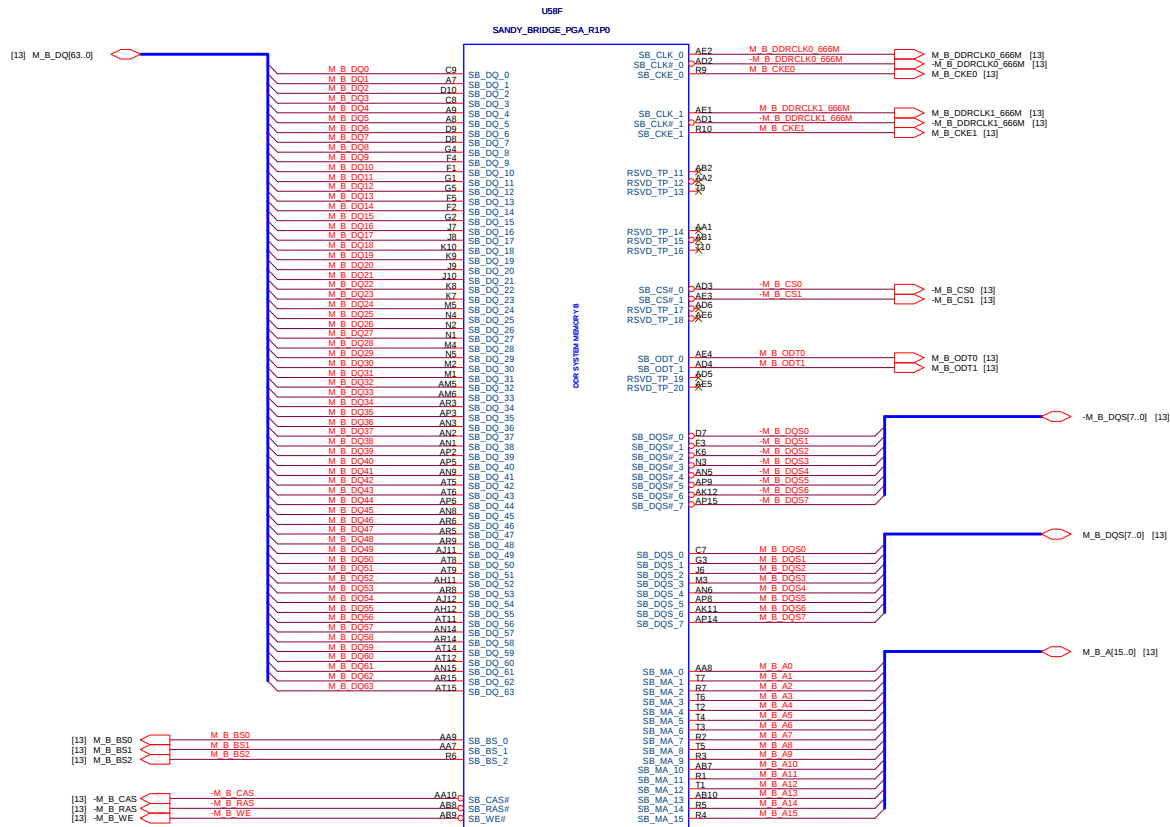
~~NOZOMI-3 SWG SDV2 :BASE LOGIC NZM3 SWG SDV VER.1.22 04/26/2010~~ NZM3_SWG_SDV2_EC077

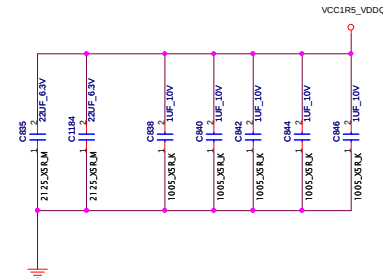
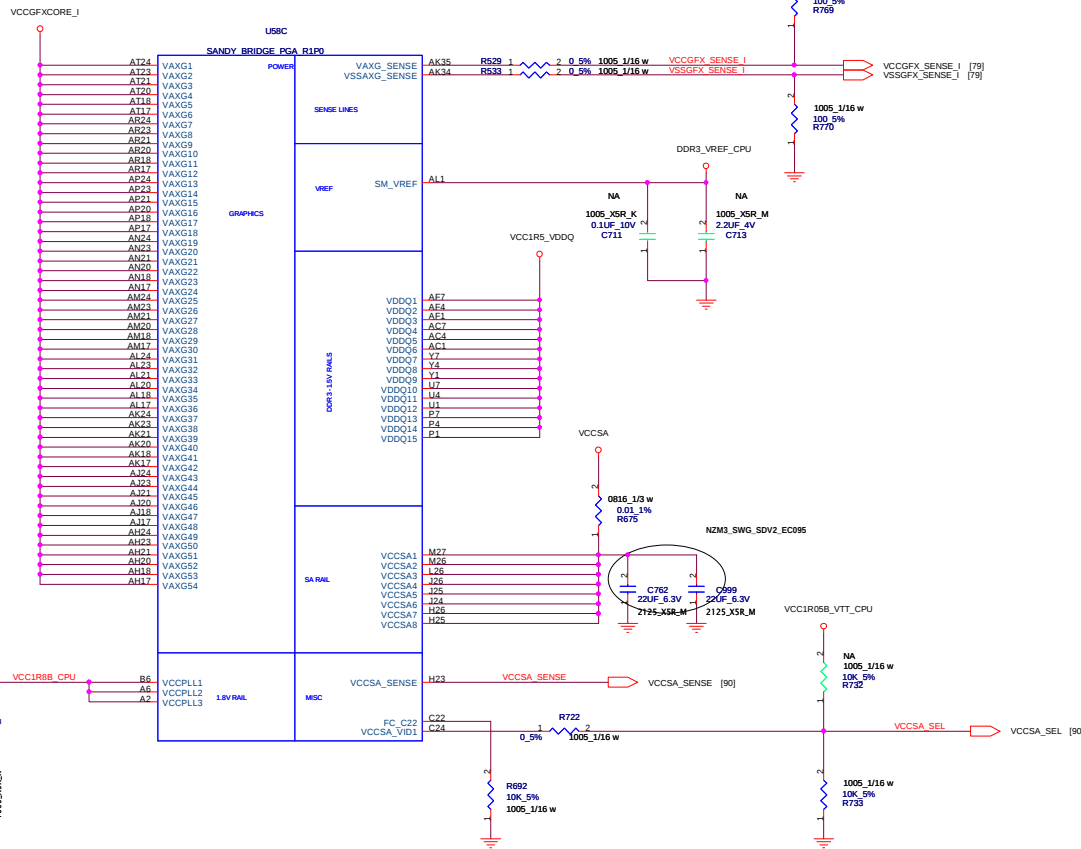
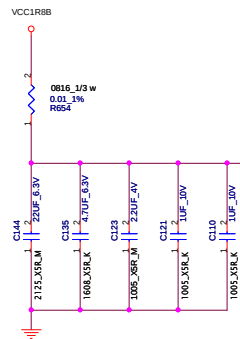
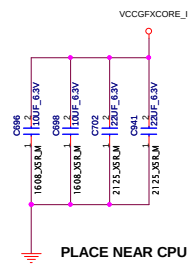
VER.2.00 04/28/2010 APPLIED NZM3_SWG_SDV2_EC003-021
VER.2.01 04/30/2010 APPLIED NZM3_SWG_SDV2_EC022-033
VER.2.02 05/04/2010 APPLIED NZM3_SWG_SDV2_EC035-061
VER.2.03 05/07/2010 APPLIED NZM3_SWG_SDV2_EC062-065
VER.2.04 05/12/2010 APPLIED NZM3_SWG_SDV2_EC066-071
VER.2.05 05/13/2010 APPLIED NZM3_SWG_SDV2_EC072
VER.2.06 05/21/2010 APPLIED NZM3_SWG_SDV2_EC073, EC076 and NZM3_SWG_SDV2_ECR001-003
VER.2.07 05/25/2010 APPLIED NZM3_SWG_SDV2_EC077,EC078, EC079, EC082, EC083, EC084, EC086, EC087, EC088, EC089 and EC095
VER.2.08 06/02/2010 APPLIED NZM3_SWG_SDV2_EC096-EC100, EC104, EC106 and EC107
VER.2.09 06/03/2010 APPLIED NZM3_SWG_SDV2_EC108-EC110
VER.2.10 06/08/2010 APPLIED NZM3_SWG_SDV2_EC105

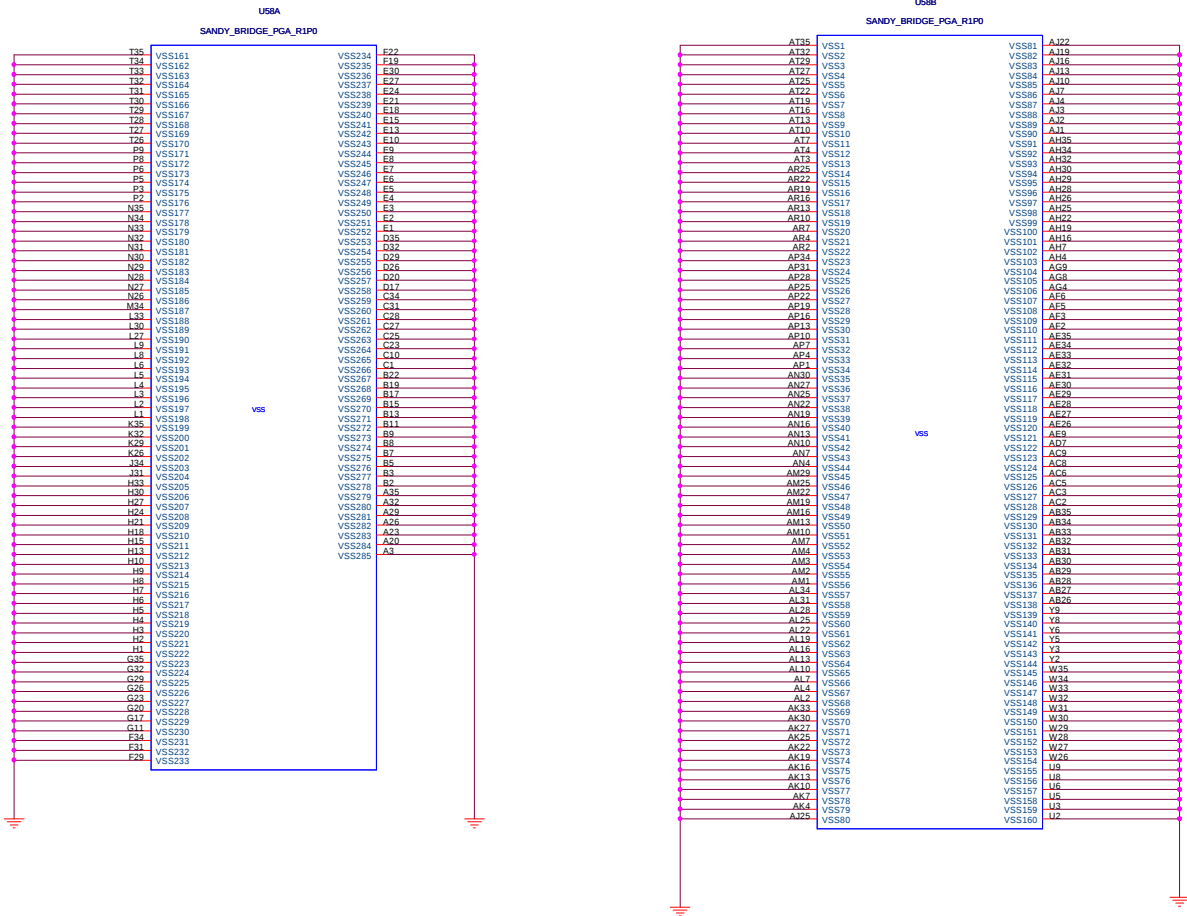


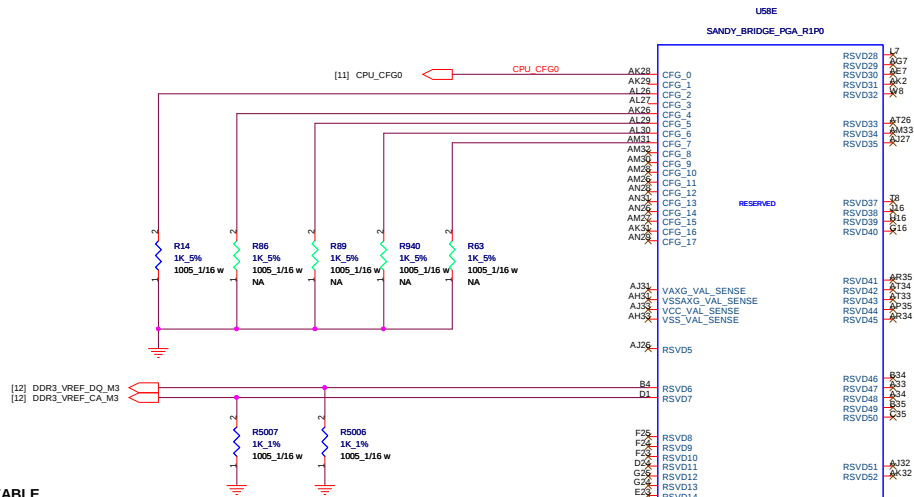












TABLE

CFG2 PEG LAN REVERSAL

- 1 -NO ASM : NORMAL
- 0 -ASM : RESVERSE

CFG4 DISPLAY PORT PRESENCE

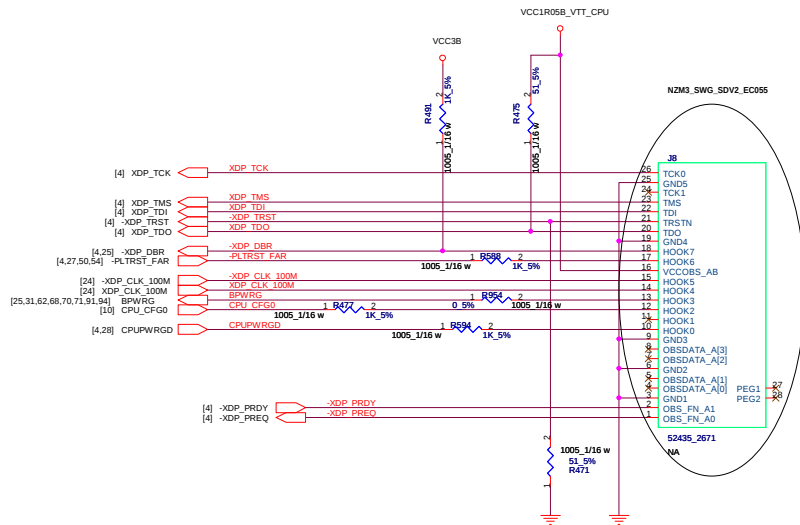
- 1 -NO ASM : DISABLE
- 0 -ASM : ENABLE

CFG[6 : 5] PEG BIFURCATION CONFIG

- 00 = 1 x 8, 2 x 4 PCI Express
- 01 = reserved
- 10 = 2 x 8 PCI Express
- 11 = 1 x 16 PCI Express

CFG7 PEG DEFER TRAINING

- 1 -NO ASM :PEG TRAIN IMMEDIATELY FOLLOWING XXRESETB DEASSERTION
- 0 -ASM : PEG WAIT FOR BIOS FOR TRAINING



CNV_EC062

TABLE NOTE: J8 "ASM" FOR SDV1 ONLY.

SIGNAL	REF DES	ENABLE	DISABLE
TDO	R475	ASM	NO ASM
TRST#	R471	ASM	ASM
DBRST#	R491	ASM	ASM
RESET#	R588	ASM	NO ASM
CFG0	R477	ASM	NO ASM
PWRGD	R594	ASM	NO ASM
BPWRG	R954	ASM	NO ASM
	J8	ASM	NO ASM

↑
LOGIC

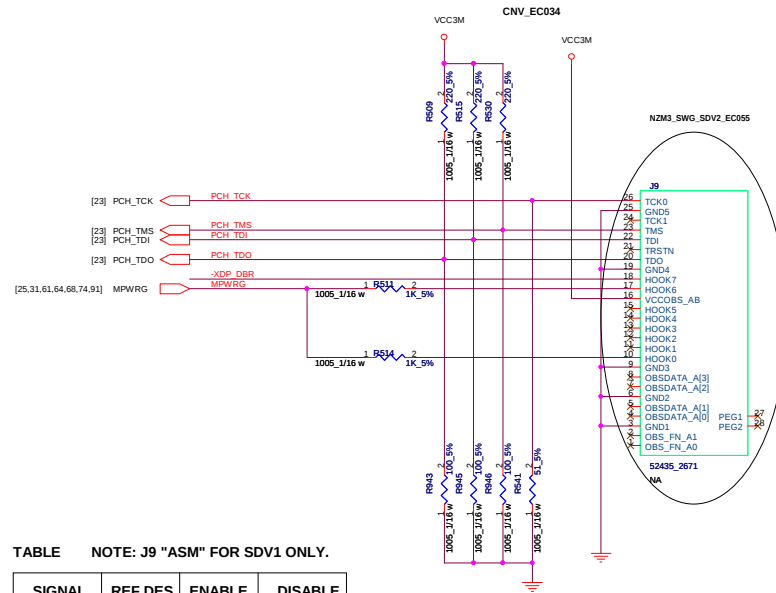
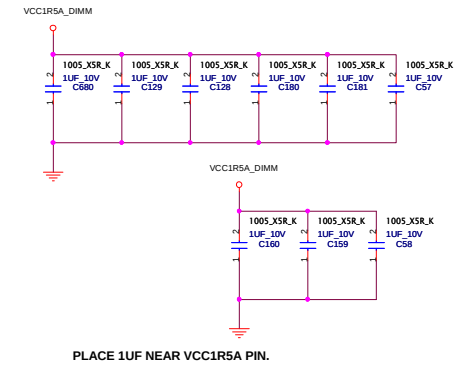


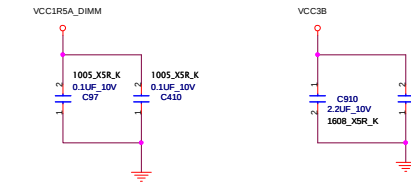
TABLE NOTE: J9 "ASM" FOR SDV1 ONLY.

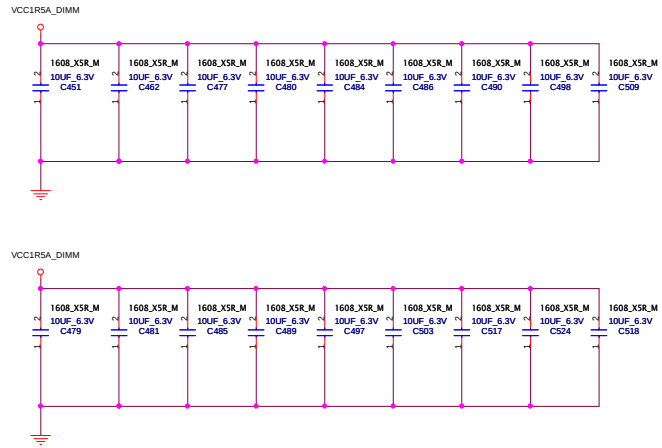
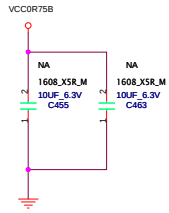
SIGNAL	REF DES	ENABLE	DISABLE
TDO	R509 R943	200 100	NO ASM NO ASM
TMS	R530 R946	220 100	NO ASM NO ASM
TDI	R515 R945	220 100	NO ASM NO ASM
TCK	R541	51	51
MPWRG	R511 R514 J9	ASM ASM ASM	NO ASM NO ASM NO ASM

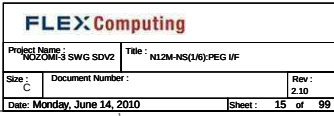
↑
LOGIC

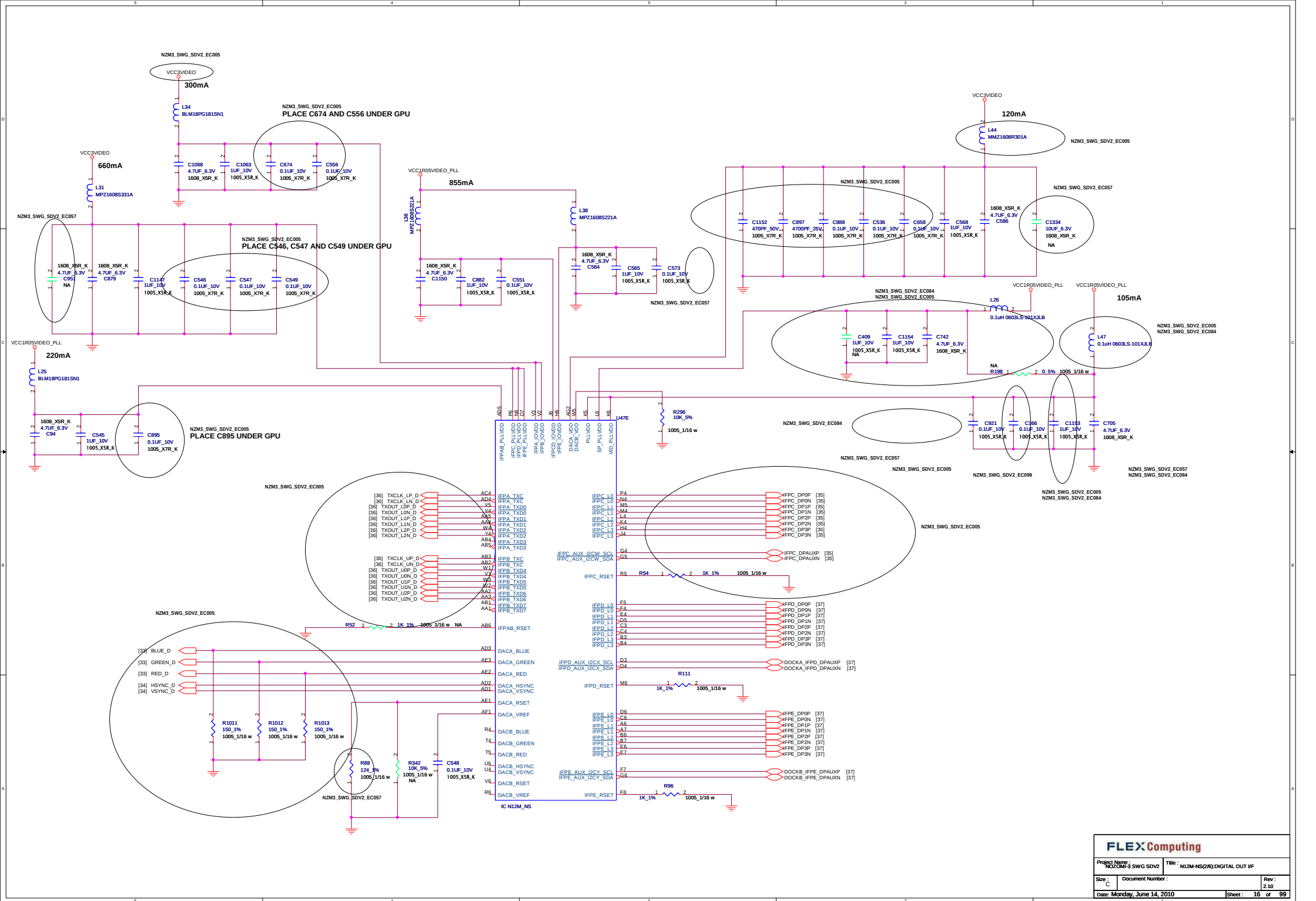


PLACE 1UF NEAR VCC1R5A PIN.









[21] FBA_D[63:0]

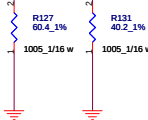
FBA_D0 D22
FBA_D1 E24
FBA_D2 E22
FBA_D3 D24
FBA_D4 D26
FBA_D5 D27
FBA_D6 C27
FBA_D7 B27
FBA_D8 A21
FBA_D9 B31
FBA_D10 C21
FBA_D11 C19
FBA_D12 C18
FBA_D13 D18
FBA_D14 E18
FBA_D15 C16
FBA_D16 E21
FBA_D17 F21
FBA_D18 D20
FBA_D19 D17
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FBA_D21 F18
FBA_D22 D16
FBA_D23 E16
FBA_D24 A22
FBA_D25 C24
FBA_D26 D21
FBA_D27 B22
FBA_D28 C22
FBA_D29 A25
FBA_D30 B25
FBA_D31 A26
FBA_D32 U24
FBA_D33 V24
FBA_D34 V23
FBA_D35 R24
FBA_D36 T23
FBA_D37 R23
FBA_D38 P24
FBA_D39 P22
FBA_D40 A24
FBA_D41 AB23
FBA_D42 AB24
FBA_D43 W24
FBA_D44 AA22
FBA_D45 W23
FBA_D46 W22
FBA_D47 V22
FBA_D48 AA25
FBA_D49 W27
FBA_D50 W26
FBA_D51 W25
FBA_D52 AB25
FBA_D53 AB26
FBA_D54 AD26
FBA_D55 AD27
FBA_D56 V25
FBA_D57 R25
FBA_D58 V26
FBA_D59 V27
FBA_D60 R26
FBA_D61 T25
FBA_D62 N25
FBA_D63 N26

U470
FBA_D0 D22
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FBA_D2 E22
FBA_D3 D24
FBA_D4 D26
FBA_D5 D27
FBA_D6 C27
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FBA_D8 A21
FBA_D9 B31
FBA_D10 C21
FBA_D11 C19
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FBA_D27 B22
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FBA_D30 B25
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FBA_D53 AB26
FBA_D54 AD26
FBA_D55 AD27
FBA_D56 V25
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FBA_D58 V26
FBA_D59 V27
FBA_D60 R26
FBA_D61 T25
FBA_D62 N25
FBA_D63 N26

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FBA_CLK1 N24
FBA_CLK1 N23
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FBA_DQM1 D19
FBA_DQM2 D19
FBA_DQM3 T24
FBA_DQM4 AA23
FBA_DQM5 AB27
FBA_DQM6 T26
FBA_DQM7 T26
FBA_CMD0 F26
FBA_CMD1 J24
FBA_CMD2 F25
FBA_CMD3 M23
FBA_CMD4 N27
FBA_CMD5 M27
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FBA_CMD7 J25
FBA_CMD8 J27
FBA_CMD9 G23
FBA_CMD10 G26
FBA_CMD11 J23
FBA_CMD12 M25
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FBA_CMD26 M24
FBA_CMD27 K22
FBA_CMD28 J22
FBA_CMD29 L22
FBA_CMD30 L22
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FBA_DQS_WP1 A19
FBA_DQS_WP2 E10
FBA_DQS_WP3 A24
FBA_DQS_WP4 T22
FBA_DQS_WP5 AA24
FBA_DQS_WP6 AA26
FBA_DQS_WP7 T27
FBA_DQS_RN0 D05
FBA_DQS_RN1 A18
FBA_DQS_RN2 E18
FBA_DQS_RN3 B04
FBA_DQS_RN4 R22
FBA_DQS_RN5 AA27
FBA_DQS_RN6 T24
FBA_DQS_RN7 R27

FB_CAL_PD_VDDQ B15
FB_VREF A16
FBA_DEBUGFBB_CAS A15
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IC N12M_NS



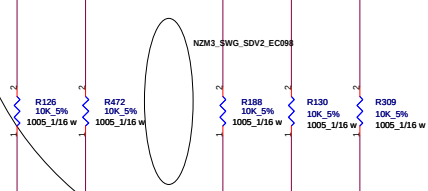
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FBA_CLK1 [21]
-FBA_CLK1 [21]

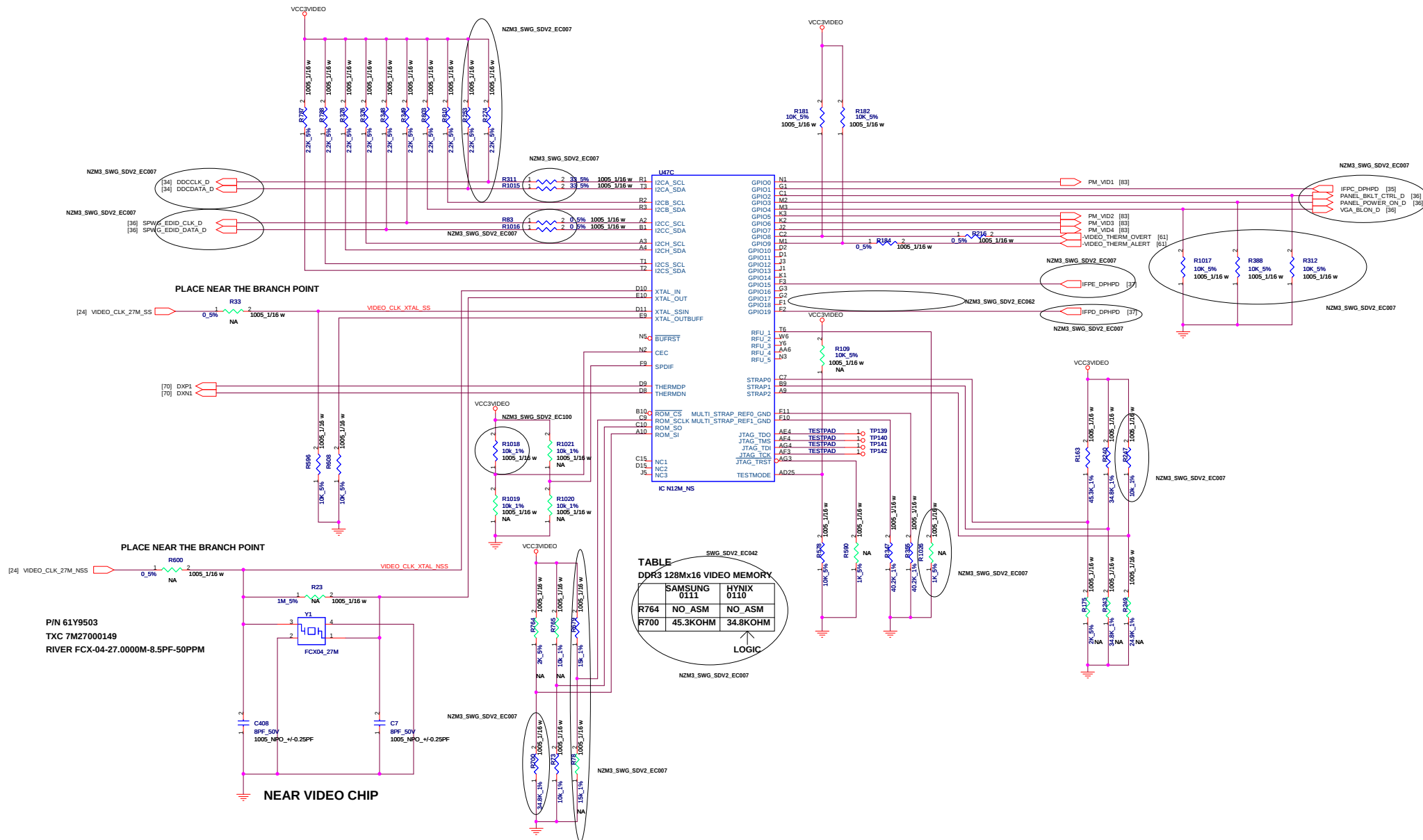
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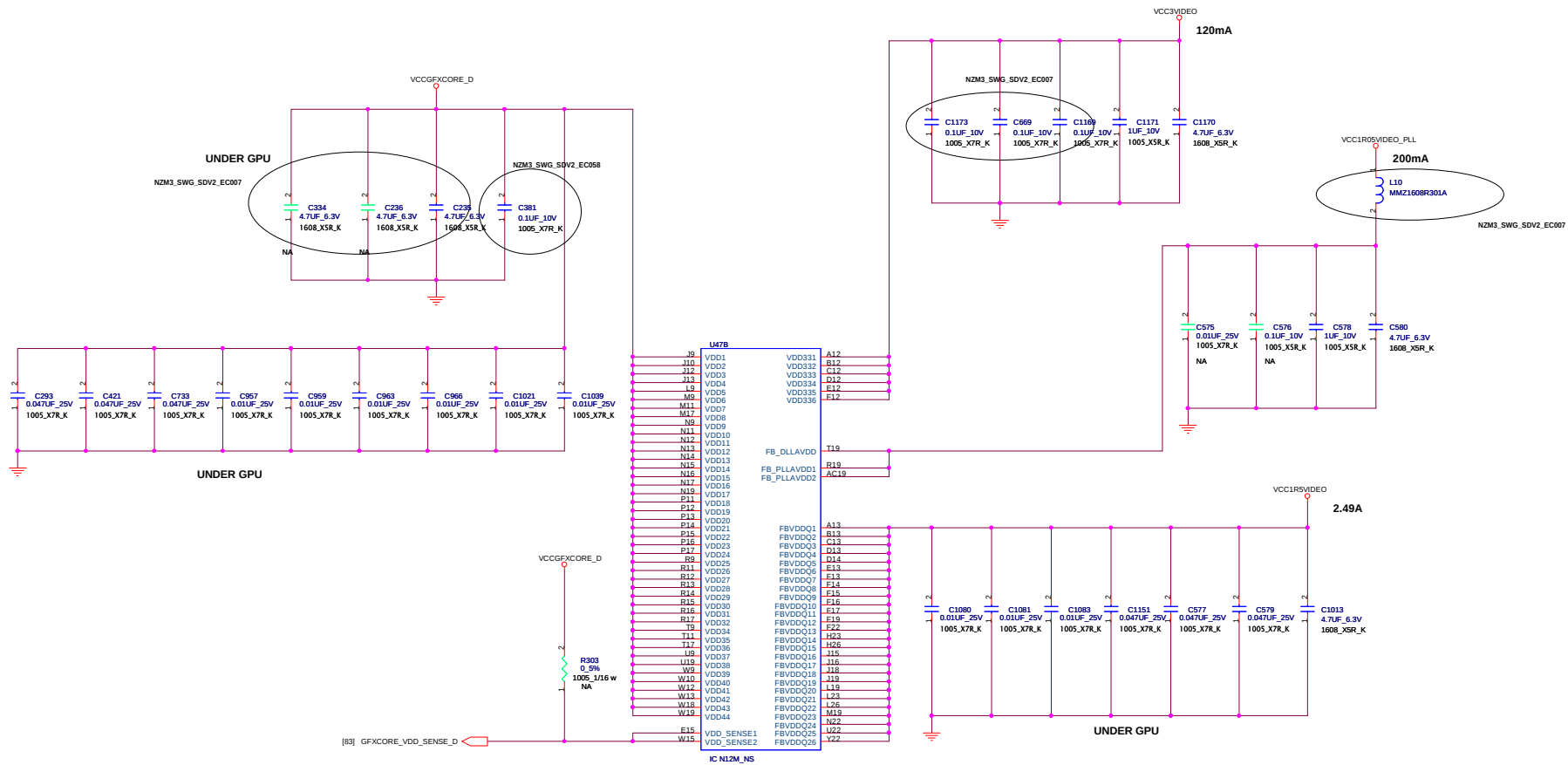
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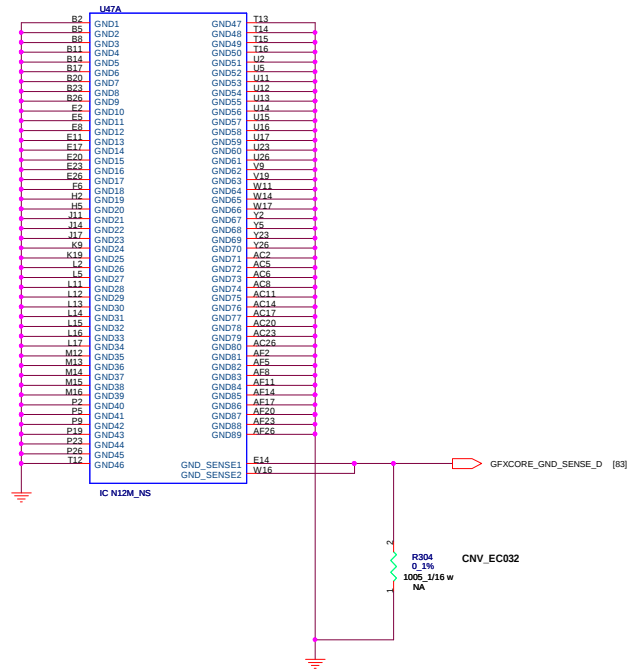
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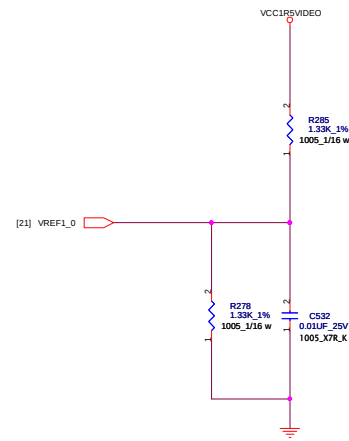
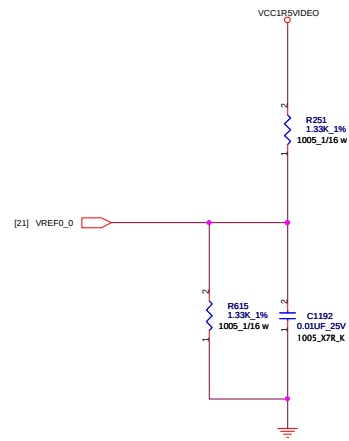
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-FBA_RST [21]

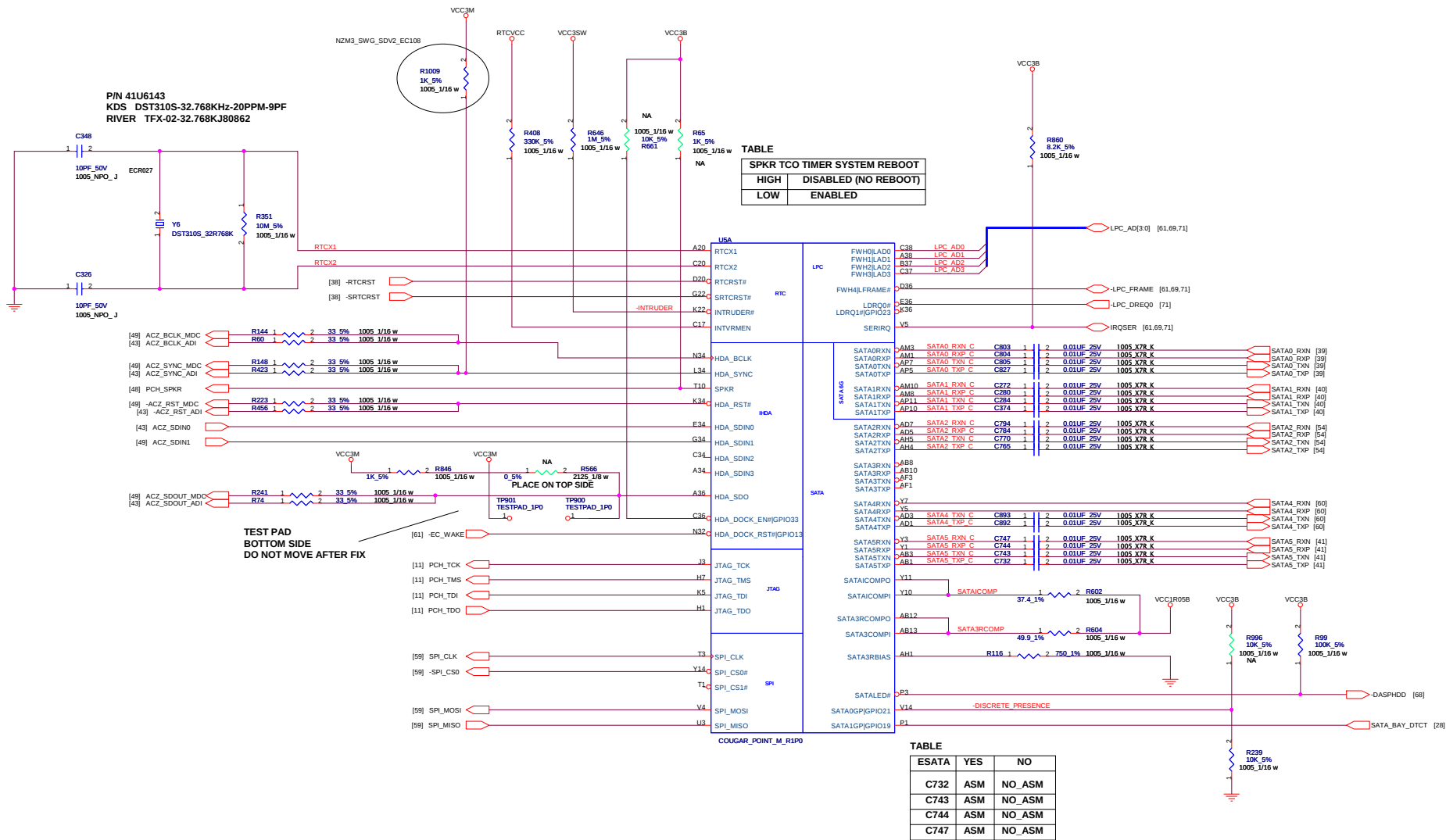


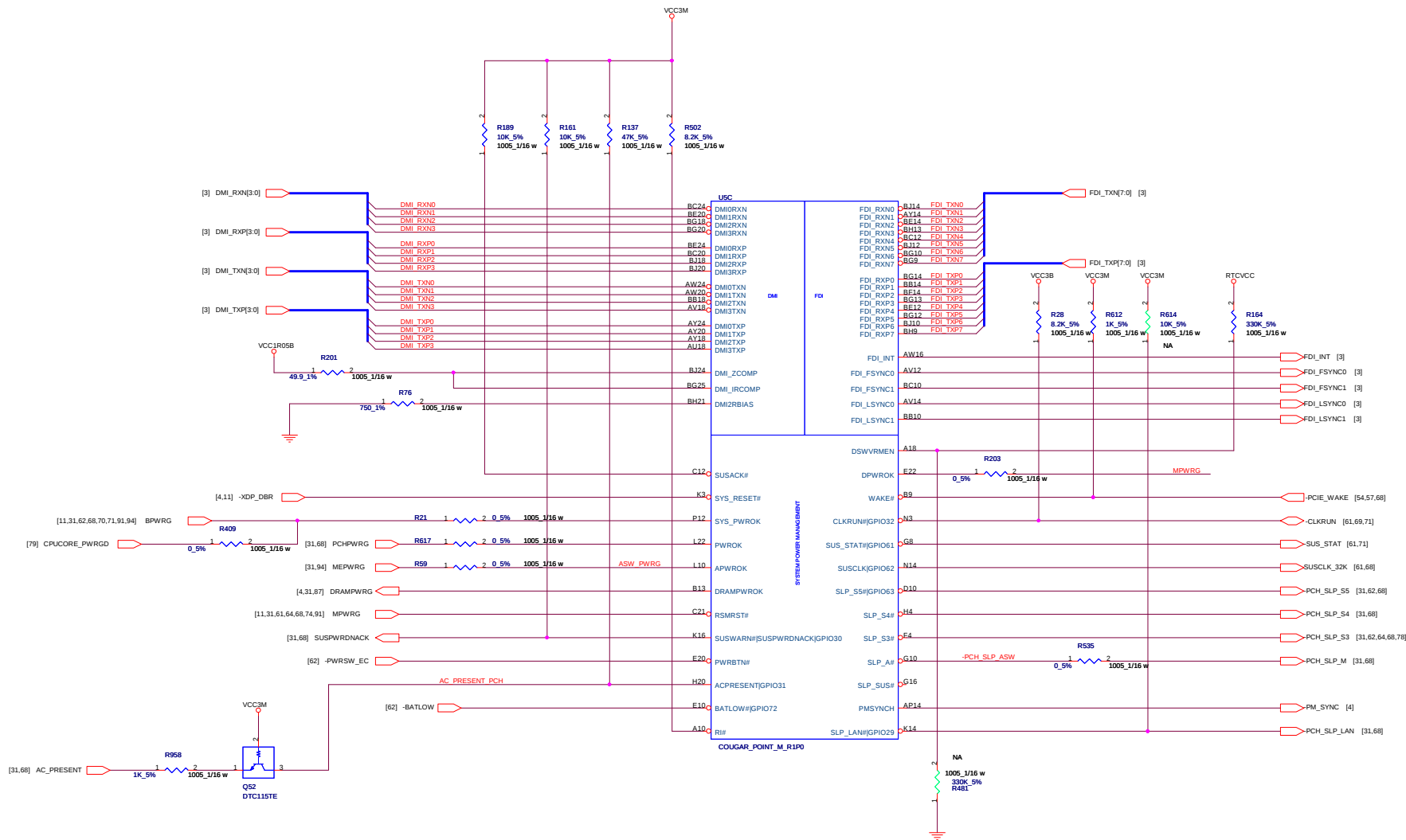












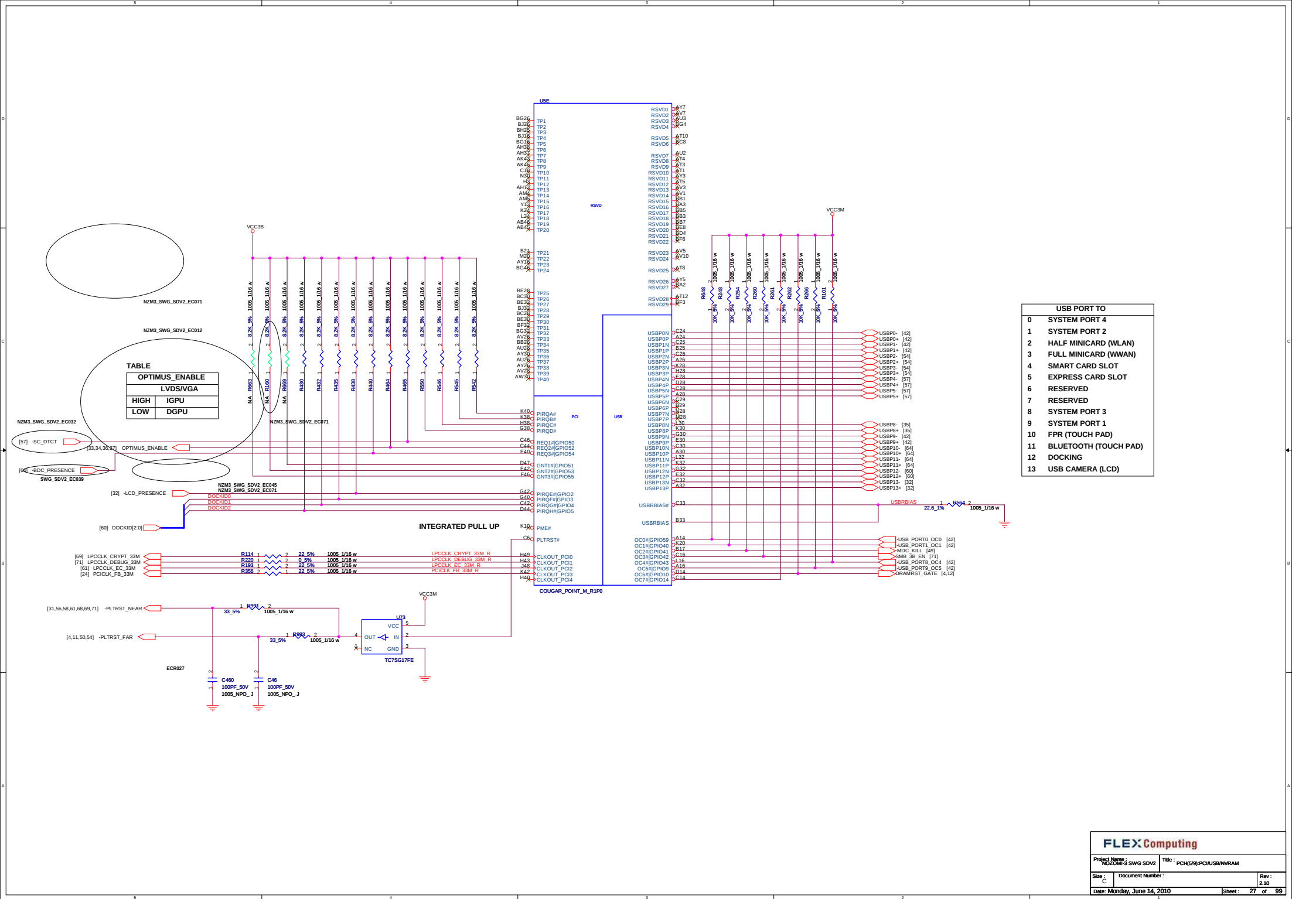


TABLE	
GPIO15 HIGH LOW	ME CRYPTO STRAP WITH CONFIDENTIALITY NO CONFIDENTIALITY

TABLE	
GPIO8 HIGH LOW	INTEGRATED CLOCKING DISABLED(BTM) ENABLED(FCIM)

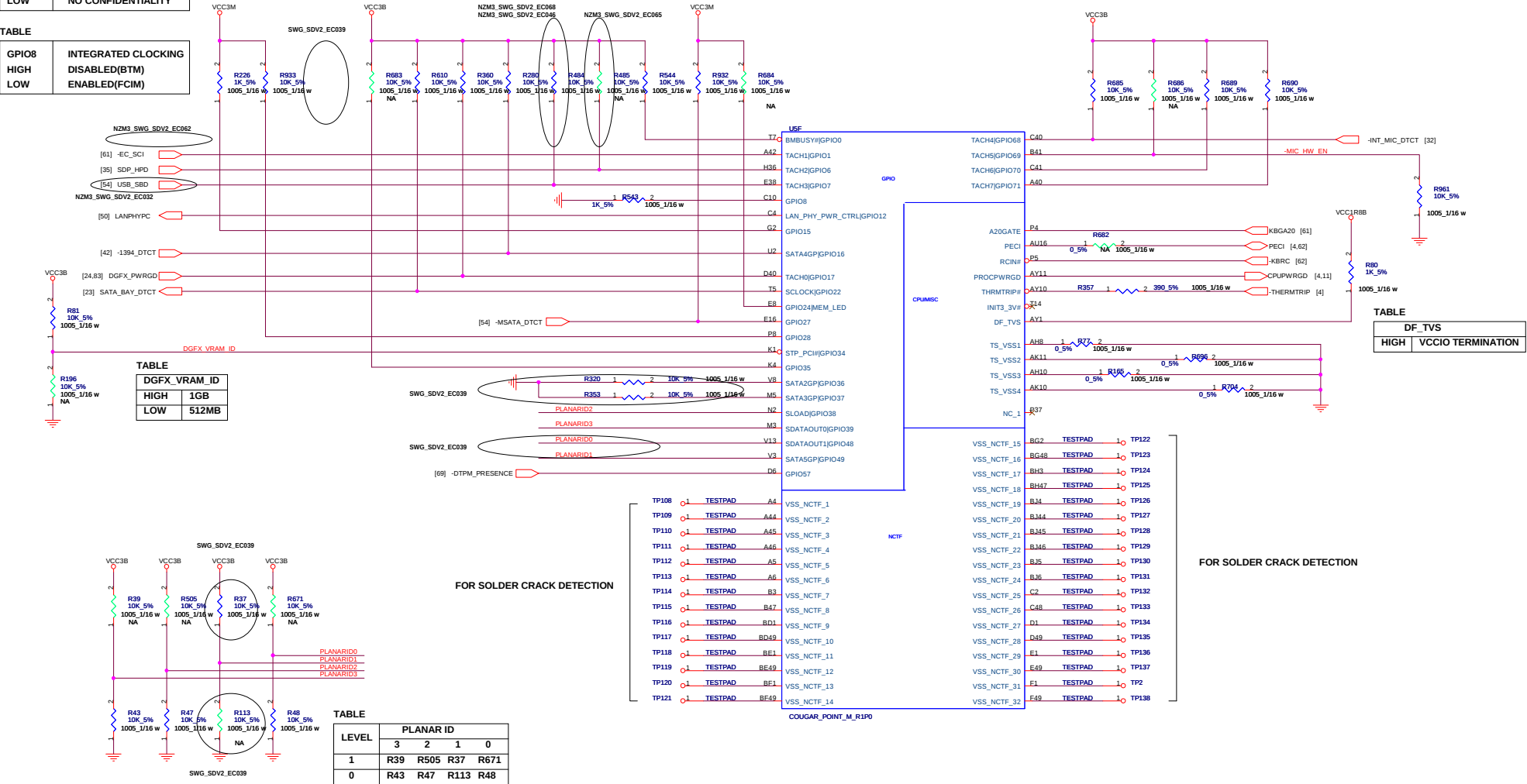


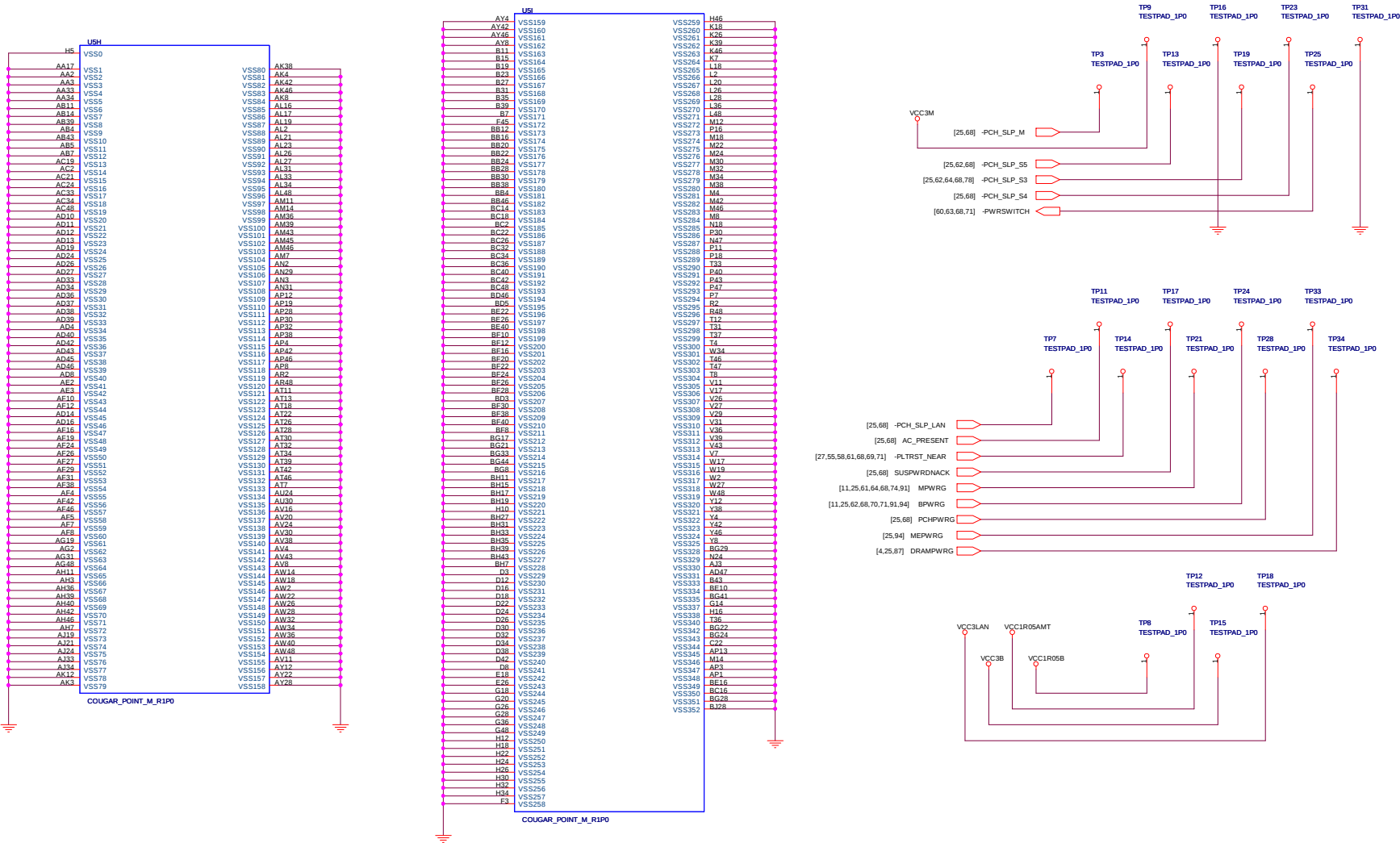
TABLE	
DGFX VRAM_ID	
HIGH	1GB
LOW	512MB

TABLE	
DF_TVS	
HIGH	VCCIO TERMINATION

TABLE	
LEVEL	PLANAR ID
1	R39 R505 R37 R671
0	R43 R47 R113 R48

TABLE	
LEVEL	PLANAR ID[3..0]
PDV	0000B
SDV	0000B/0001B
SDV2	0010B

TEST PAD FOR METS/APS



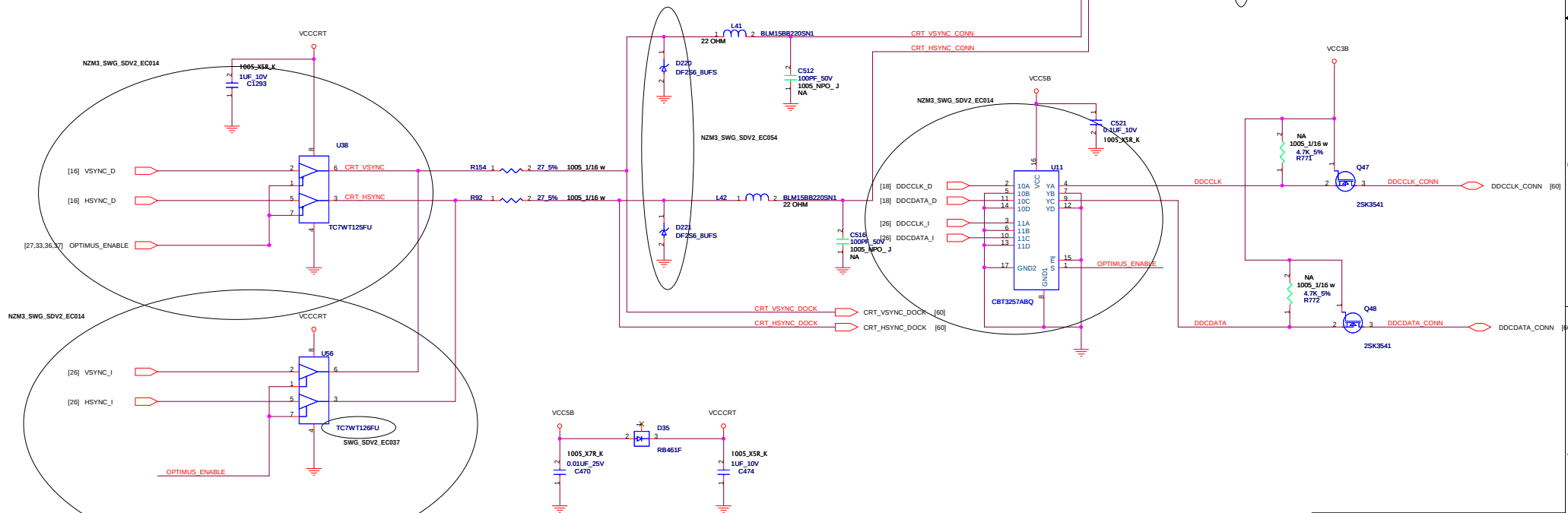
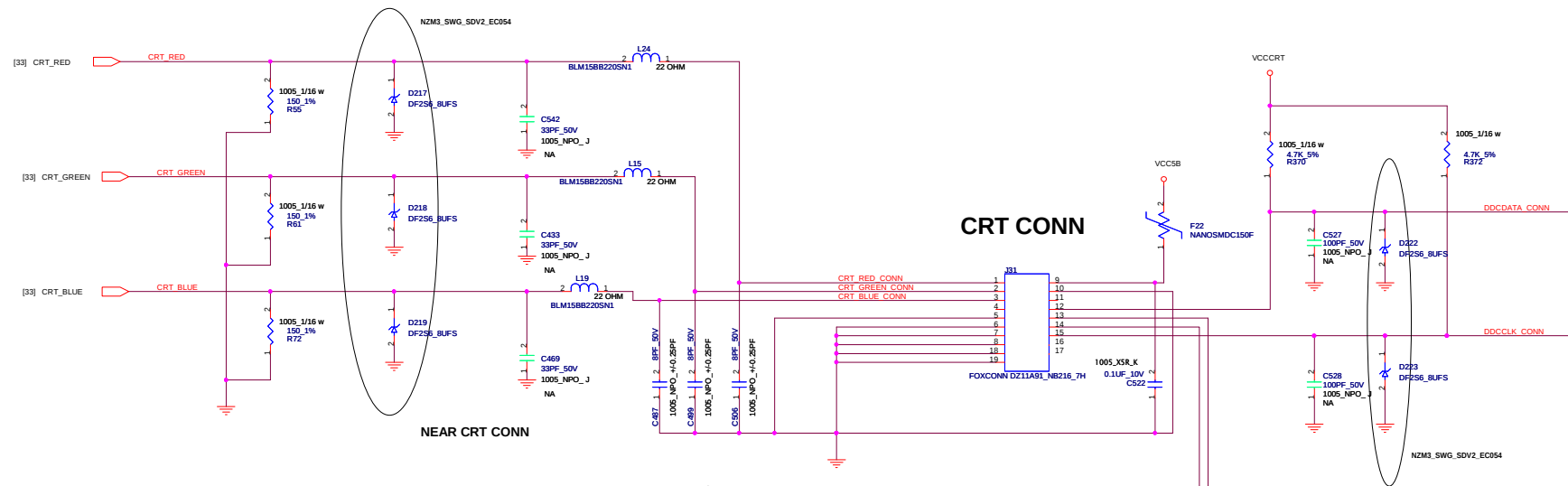
FLEX Computing

Project Name: VCC3M-S SWG SDV2 Title: PCH3M-GND

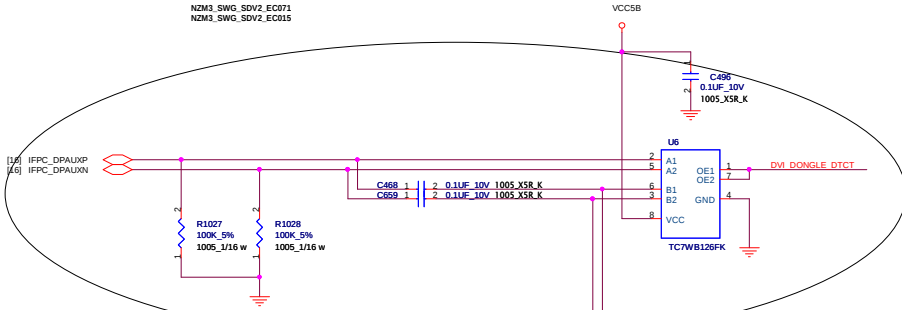
Size: C Document Number: Rev: 2.10

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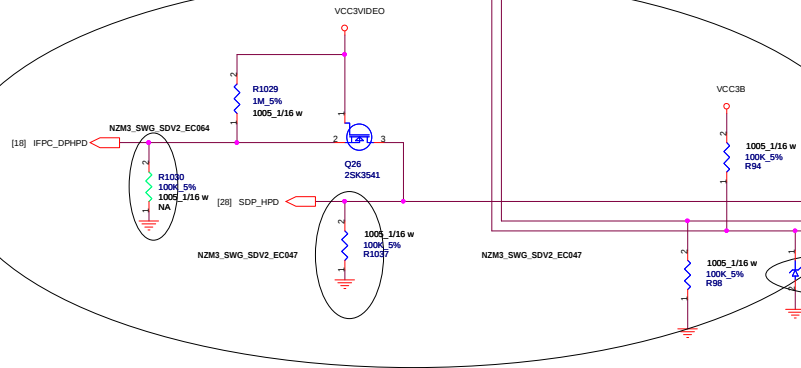




NZM3_SWG_SDV2_EC071
NZM3_SWG_SDV2_EC015



NZM3_SWG_SDV2_EC071



NZM3_SWG_SDV2_EC047

VCC5B

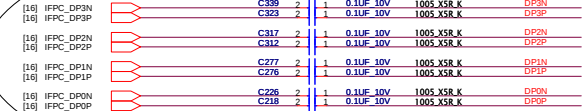
C468 1 2 0.1uF_10V 1005_X5R_K
C659 1 2 0.1uF_10V 1005_X5R_K

U6
A1
A2
OE1
OE2
B1
B2
GND
VCC
TC7WB126FK

DVI DONGLE DTCT

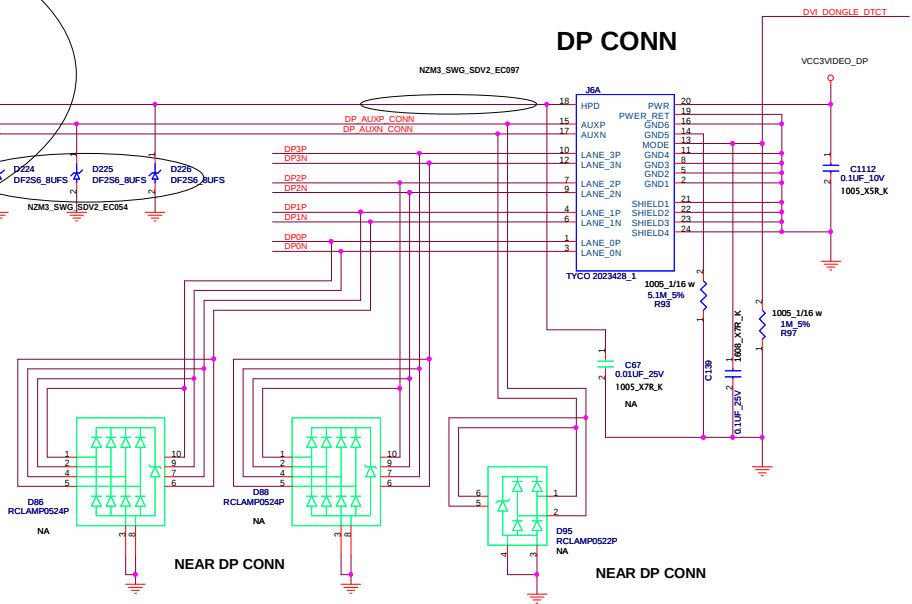
NZM3_SWG_SDV2_EC015
NZM3_SWG_SDV2_EC047
NZM3_SWG_SDV2_EC071

FOR SYSTEM DP NEAR DP CONN



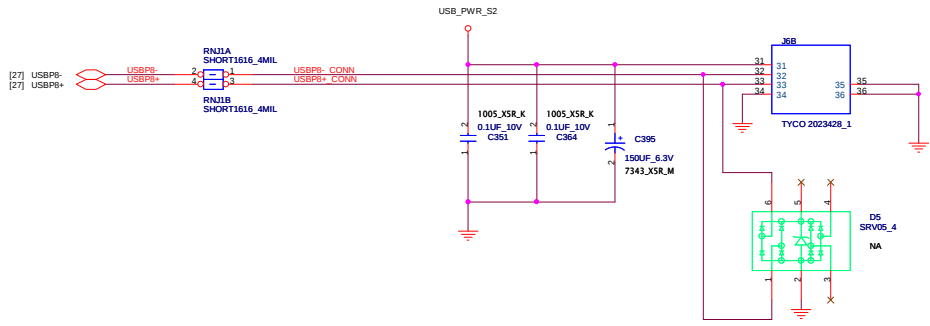
DP CONN

NZM3_SWG_SDV2_EC097



NEAR DP CONN

NEAR DP CONN



[27] USBP8-
[27] USBP8+

RN1A
SHORT1616_4MIL
USBP8- CONN
USBP8+ CONN
RN1B
SHORT1616_4MIL

USB_PWR_S2

U6
31
32
33
34
35
36
TYCO 2023428_1

D5
SRV05_4
NA

VCC3B

F18

NANOSMDC150F

Q68
TPCF8002

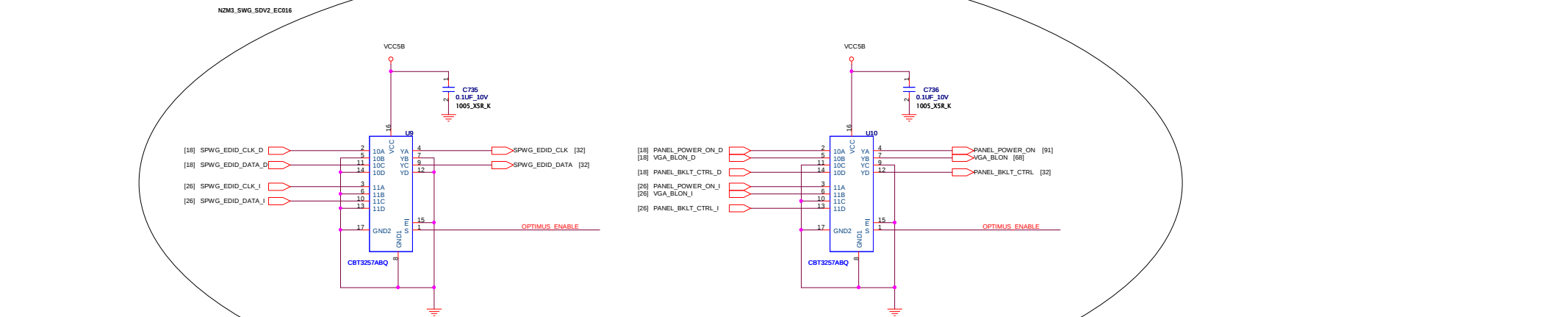
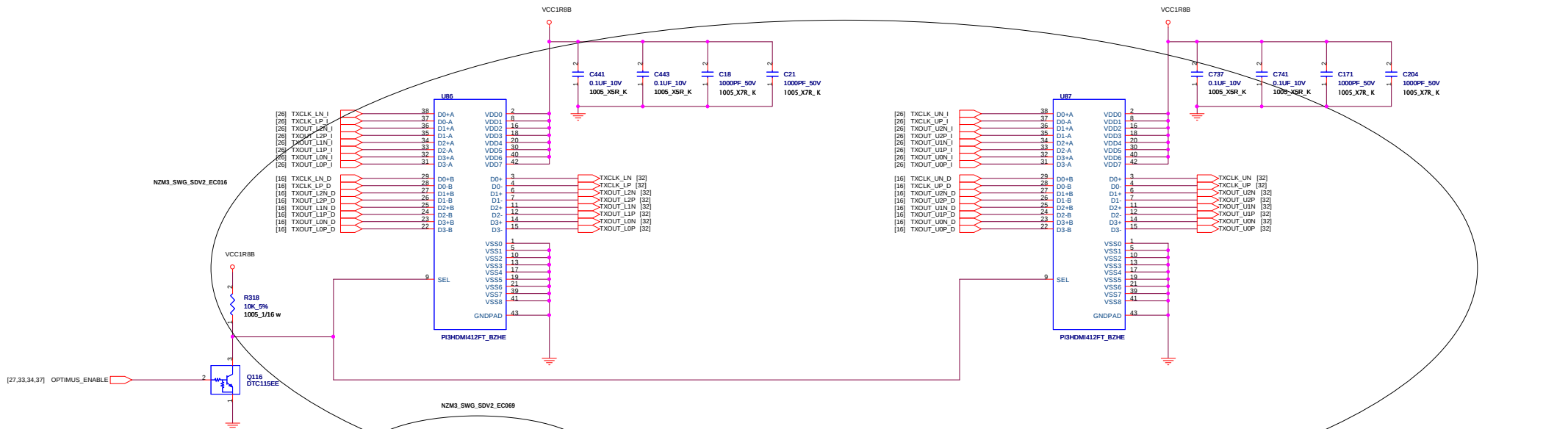
13.1

Typ. 500mA
Max 3A

C1113
4.7uF_6.3V
1608_X5R_K

C714
100uF_6.3V
3216_X5R_M

NA



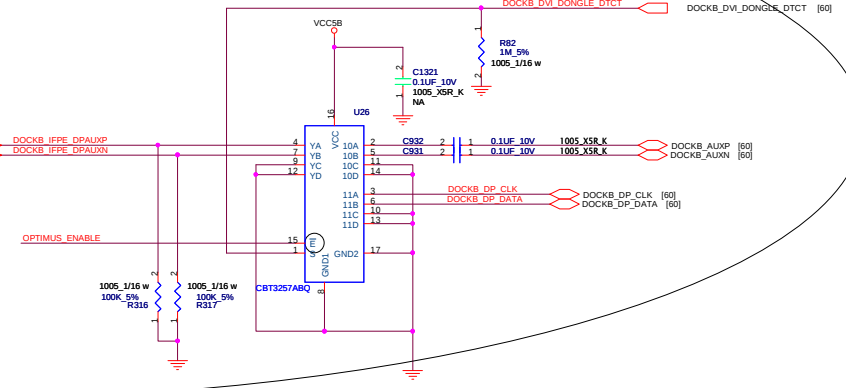
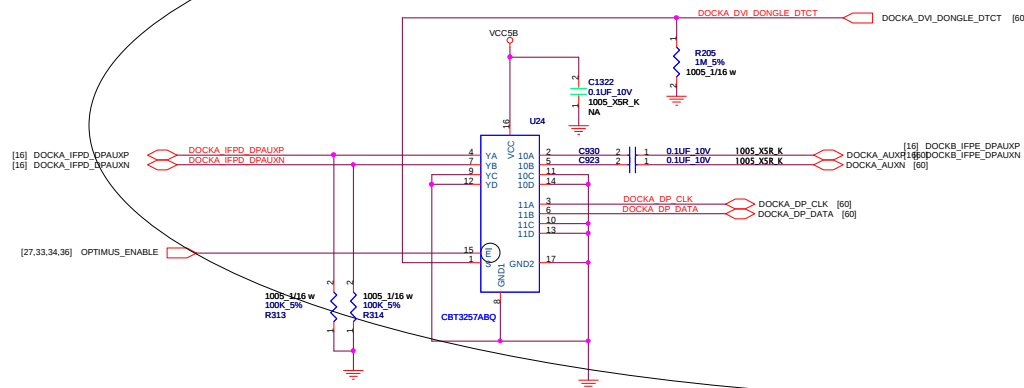
NZM3_SWG_SDV2_EC017

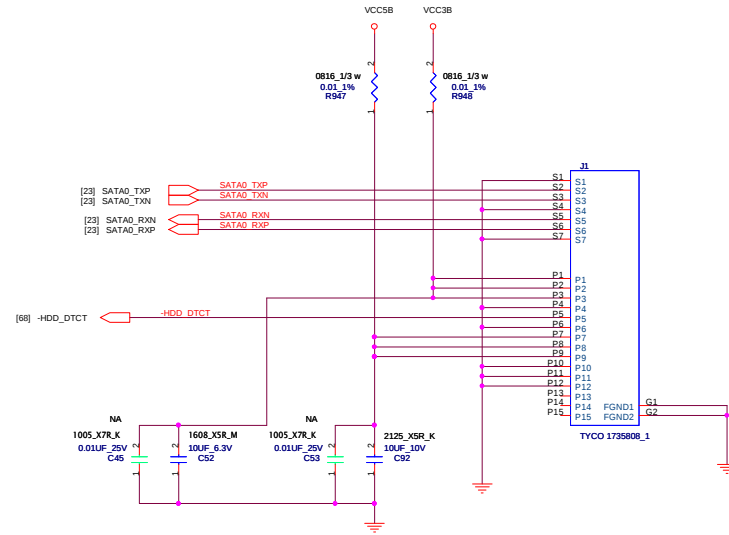


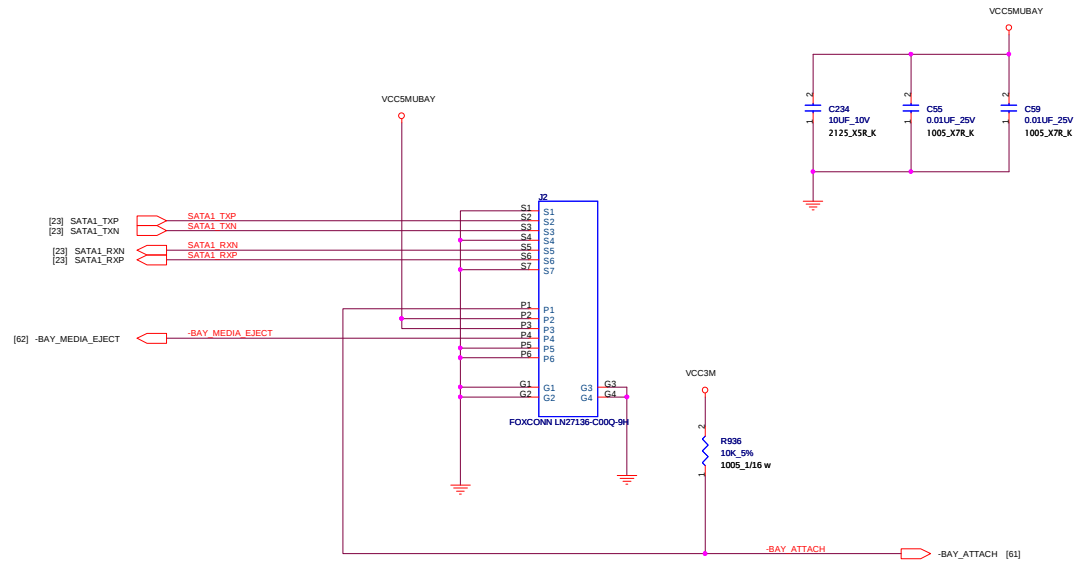
NEAR DOCK CONN									
[16] IFPD_DP3N	C841	2	1	0.1UF_10V	1005_XSR_K	DOCKA_DP3N	[60]		
[16] IFPD_DP3P	C843	2	1	0.1UF_10V	1005_XSR_K	DOCKA_DP3P	[60]		
[16] IFPD_DP2N	C883	2	1	0.1UF_10V	1005_XSR_K	DOCKA_DP2N	[60]		
[16] IFPD_DP2P	C887	2	1	0.1UF_10V	1005_XSR_K	DOCKA_DP2P	[60]		
[16] IFPD_DP1N	C901	2	1	0.1UF_10V	1005_XSR_K	DOCKA_DP1N	[60]		
[16] IFPD_DP1P	C900	2	1	0.1UF_10V	1005_XSR_K	DOCKA_DP1P	[60]		
[16] IFPD_DP0N	C903	2	1	0.1UF_10V	1005_XSR_K	DOCKA_DP0N	[60]		
[16] IFPD_DP0P	C902	2	1	0.1UF_10V	1005_XSR_K	DOCKA_DP0P	[60]		

NEAR DOCK CONN									
[16] IFPE_DP3N	C904	2	1	0.1UF_10V	1005_XSR_K	DOCKB_DP3N	[60]		
[16] IFPE_DP3P	C907	2	1	0.1UF_10V	1005_XSR_K	DOCKB_DP3P	[60]		
[16] IFPE_DP2N	C909	2	1	0.1UF_10V	1005_XSR_K	DOCKB_DP2N	[60]		
[16] IFPE_DP2P	C906	2	1	0.1UF_10V	1005_XSR_K	DOCKB_DP2P	[60]		
[16] IFPE_DP1N	C922	2	1	0.1UF_10V	1005_XSR_K	DOCKB_DP1N	[60]		
[16] IFPE_DP1P	C920	2	1	0.1UF_10V	1005_XSR_K	DOCKB_DP1P	[60]		
[16] IFPE_DP0N	C919	2	1	0.1UF_10V	1005_XSR_K	DOCKB_DP0N	[60]		
[16] IFPE_DP0P	C918	2	1	0.1UF_10V	1005_XSR_K	DOCKB_DP0P	[60]		

NZM3_SWG_SDV2_EC017







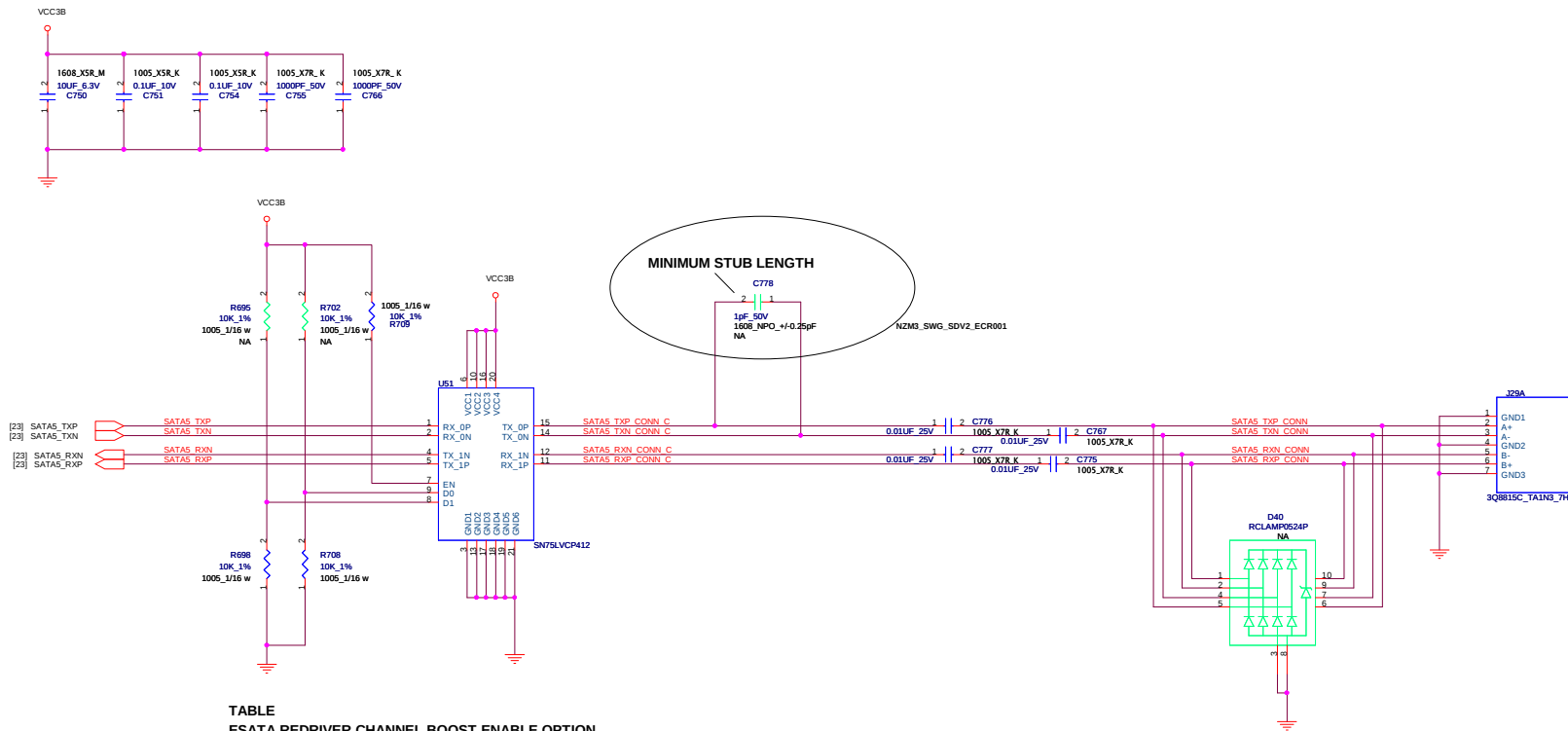
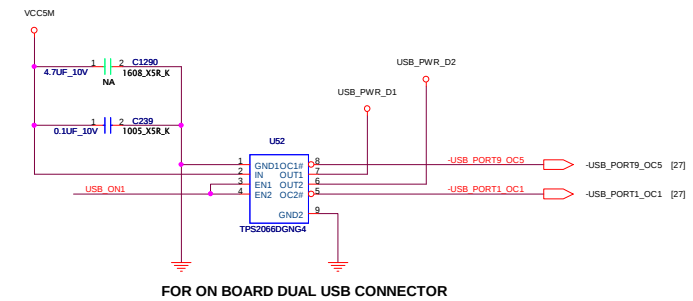
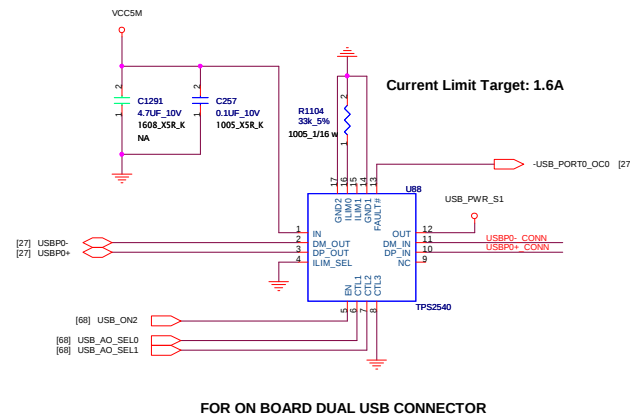
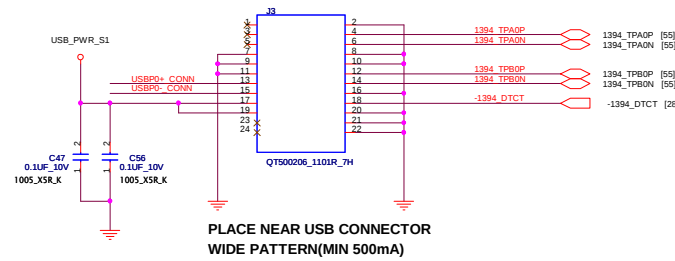
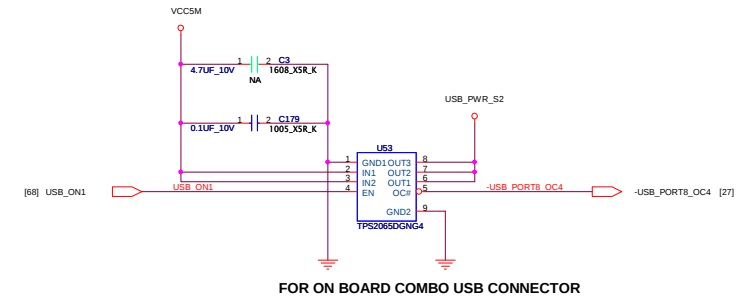
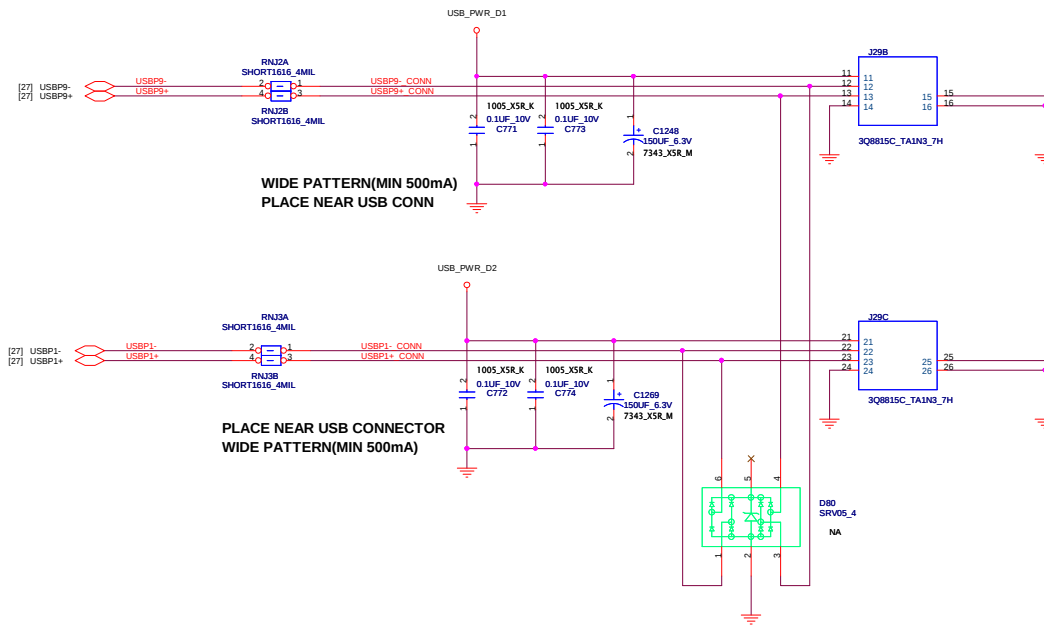
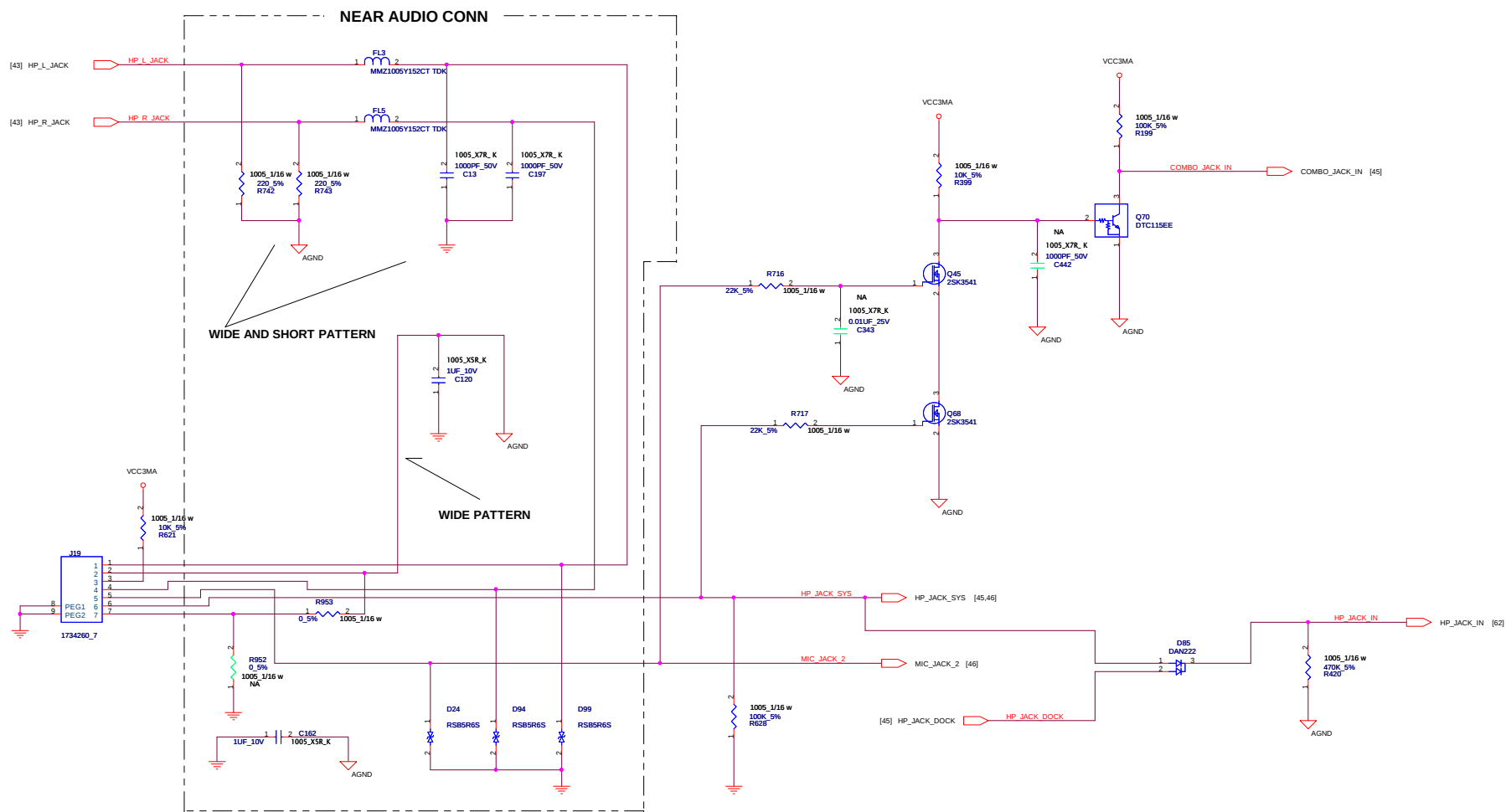


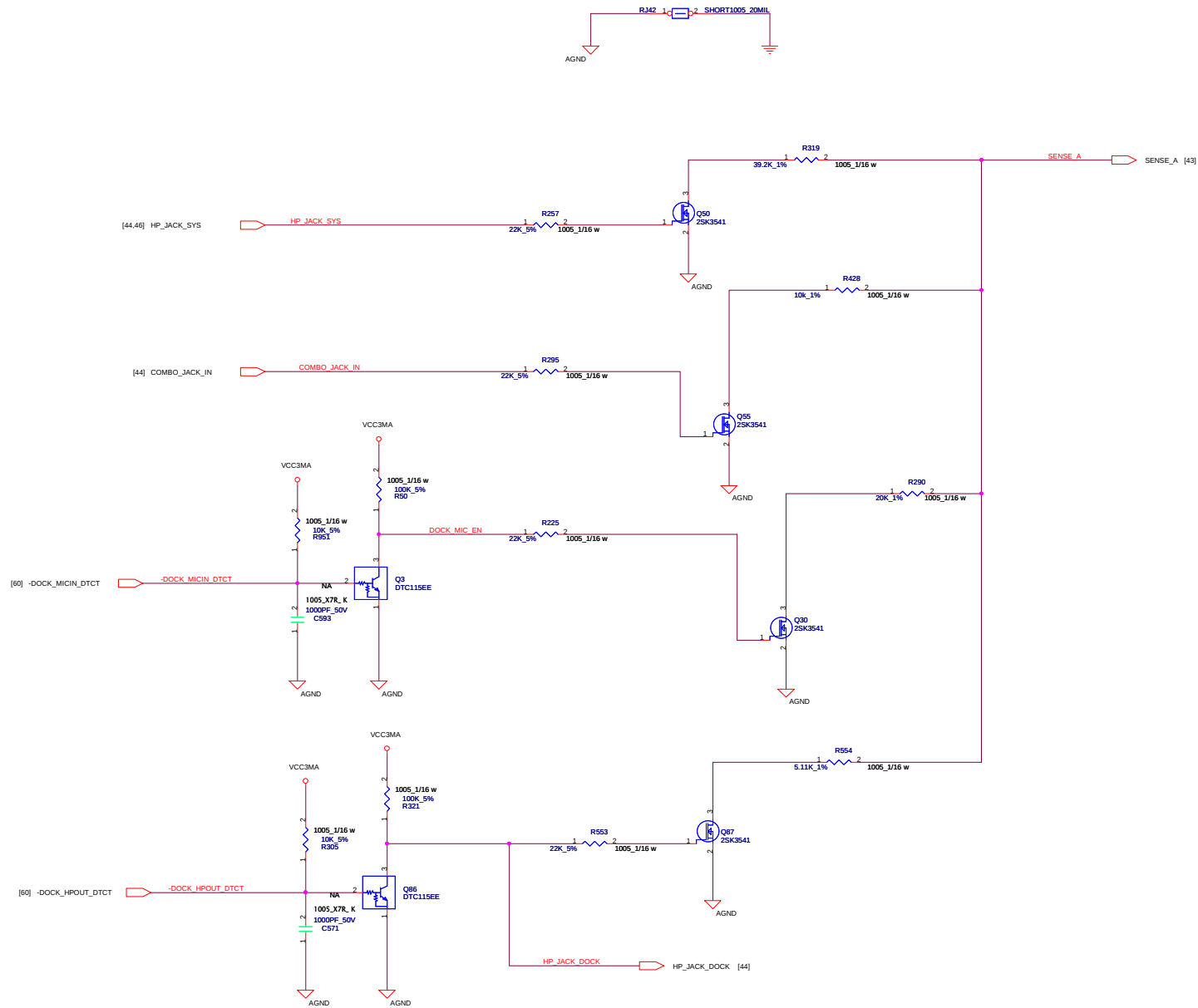
TABLE
ESATA REDRIVER CHANNEL BOOST ENABLE OPTION

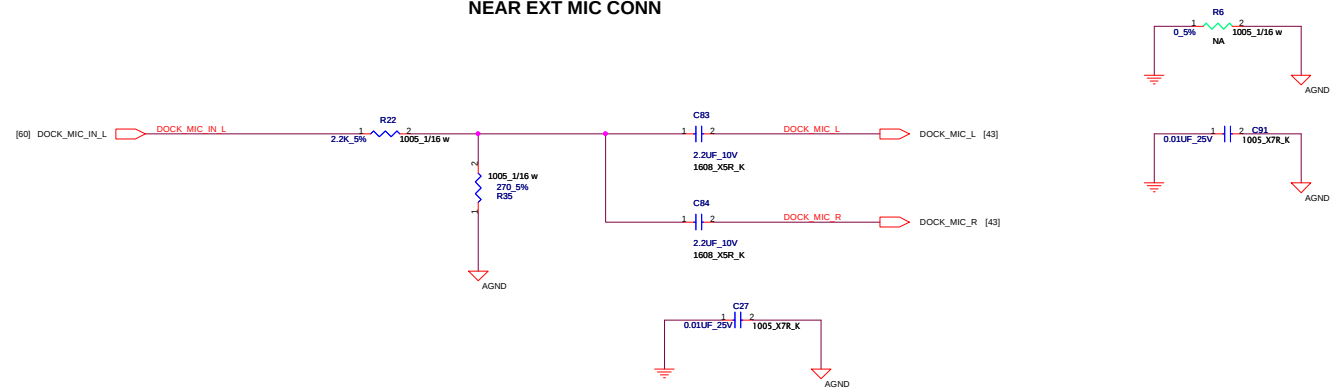
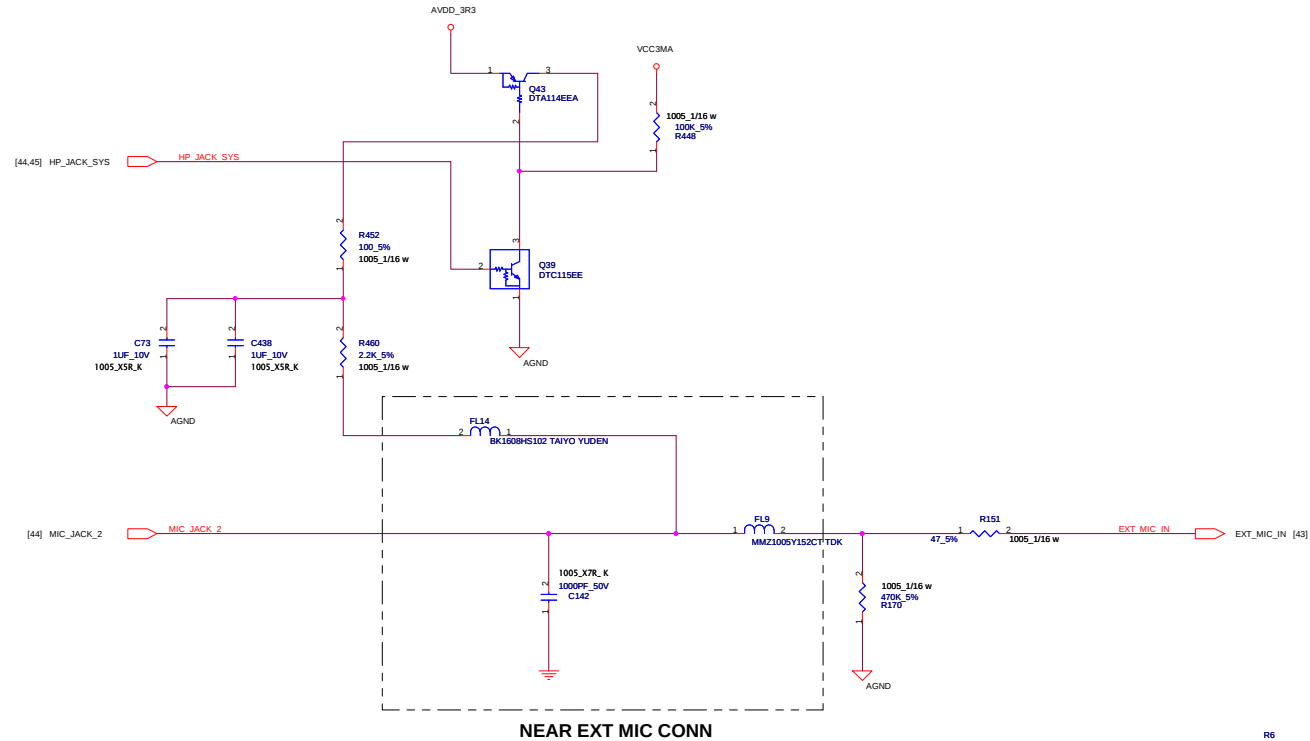
EN	D0	D1	CH-0	CH-1
0	X	X	STANDBY	STANDBY
1	0	0	STANDARD	STANDARD
1	1	0	BOOST	STANDARD
1	0	1	STANDARD	BOOST
1	1	1	BOOST	BOOST

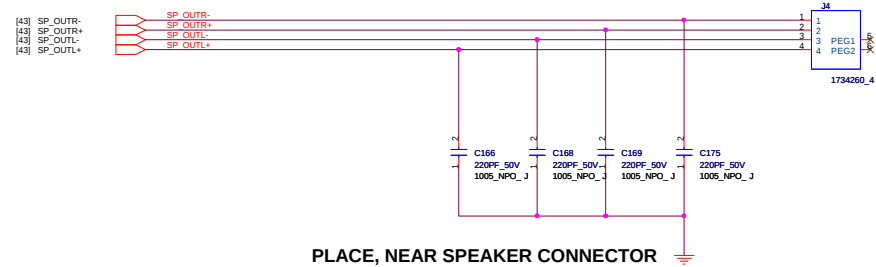
← LOGIC

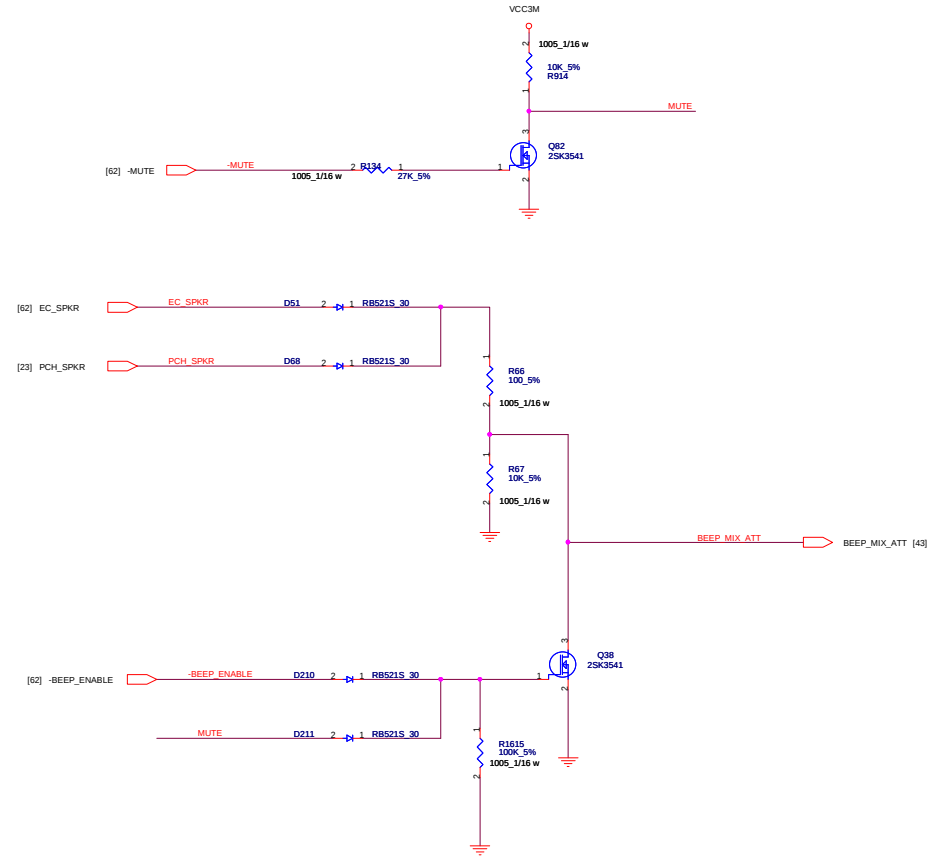


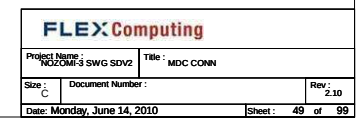


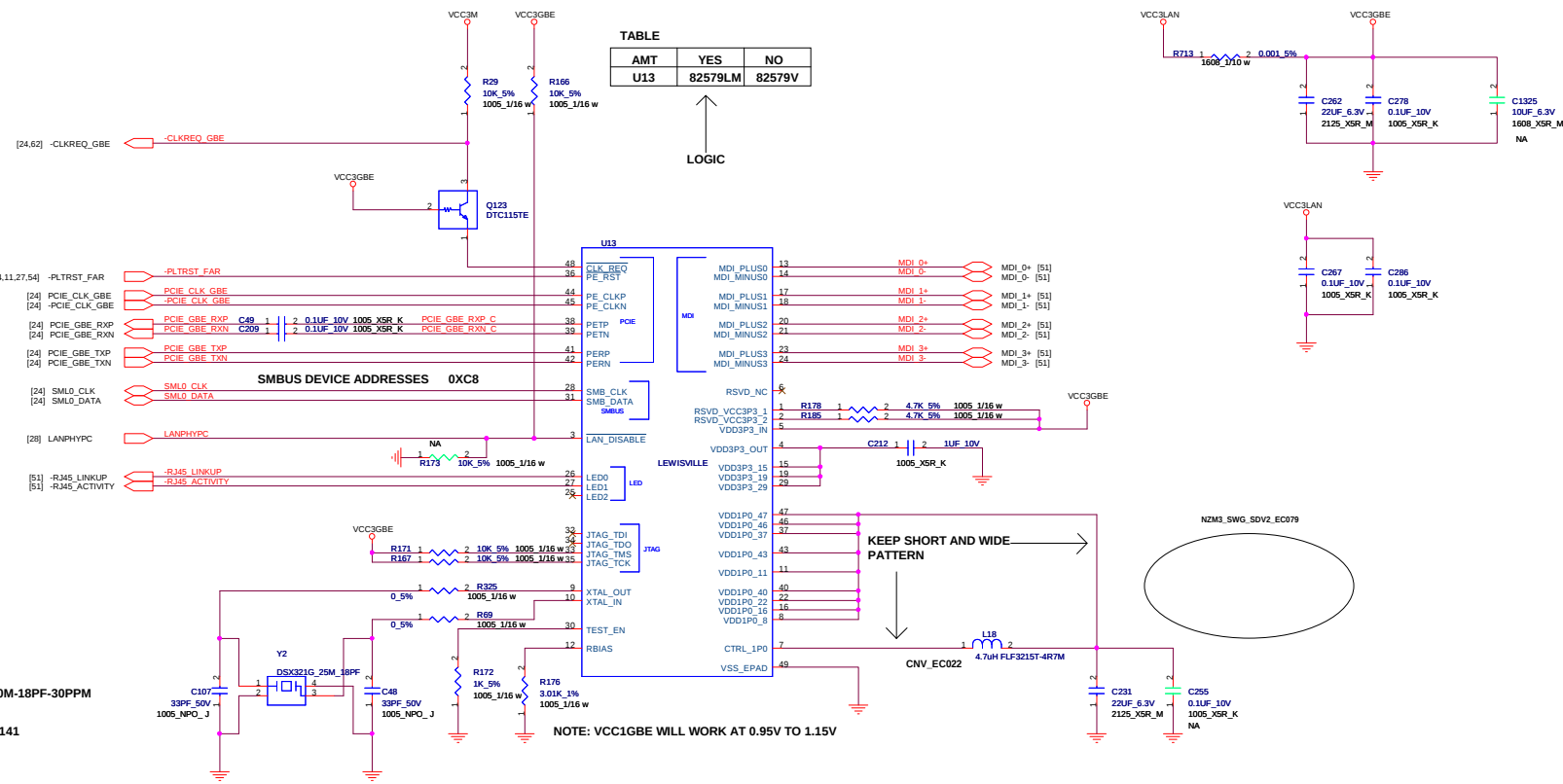










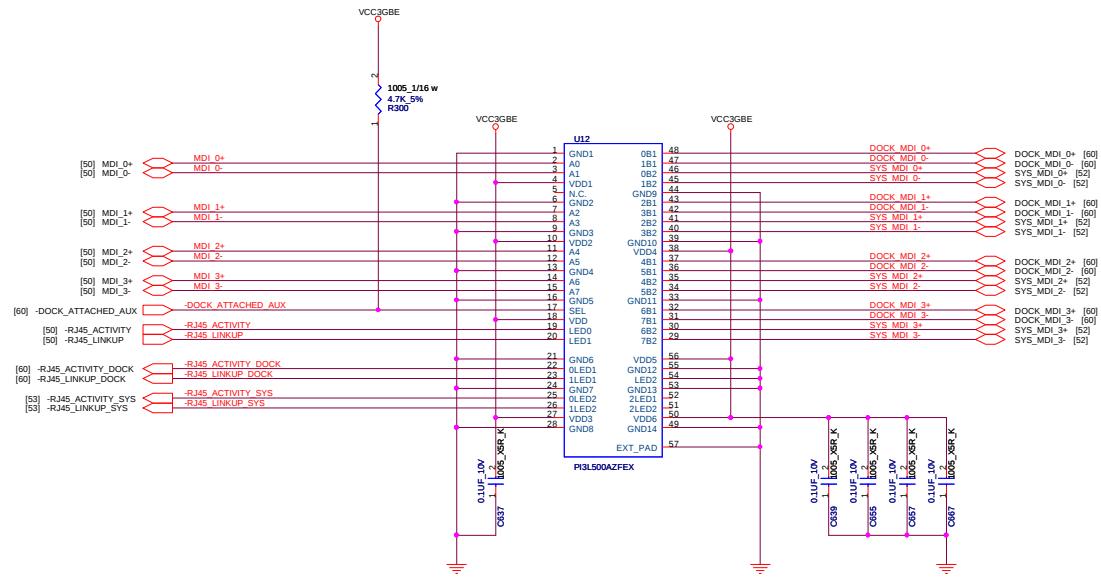


AMT	YES	NO
U13	82579LM	82579V

LOGIC

P/N 41U6141
KDS DSX321G-25.000M-18PF-30PPM
TXC 7V25020001
RIVER FCX-04-25MJ90141

NOTE: VCC1GBE WILL WORK AT 0.95V TO 1.15V



THE WIDTH OF THESE TRACE SHOULD
BE WIDER THAN 35MIL TO PREVENT
VOLTAGE DROP.

C303
SHOULD BE PLACED AS CLOSE
TO MAGNETICS AS POSSIBLE.

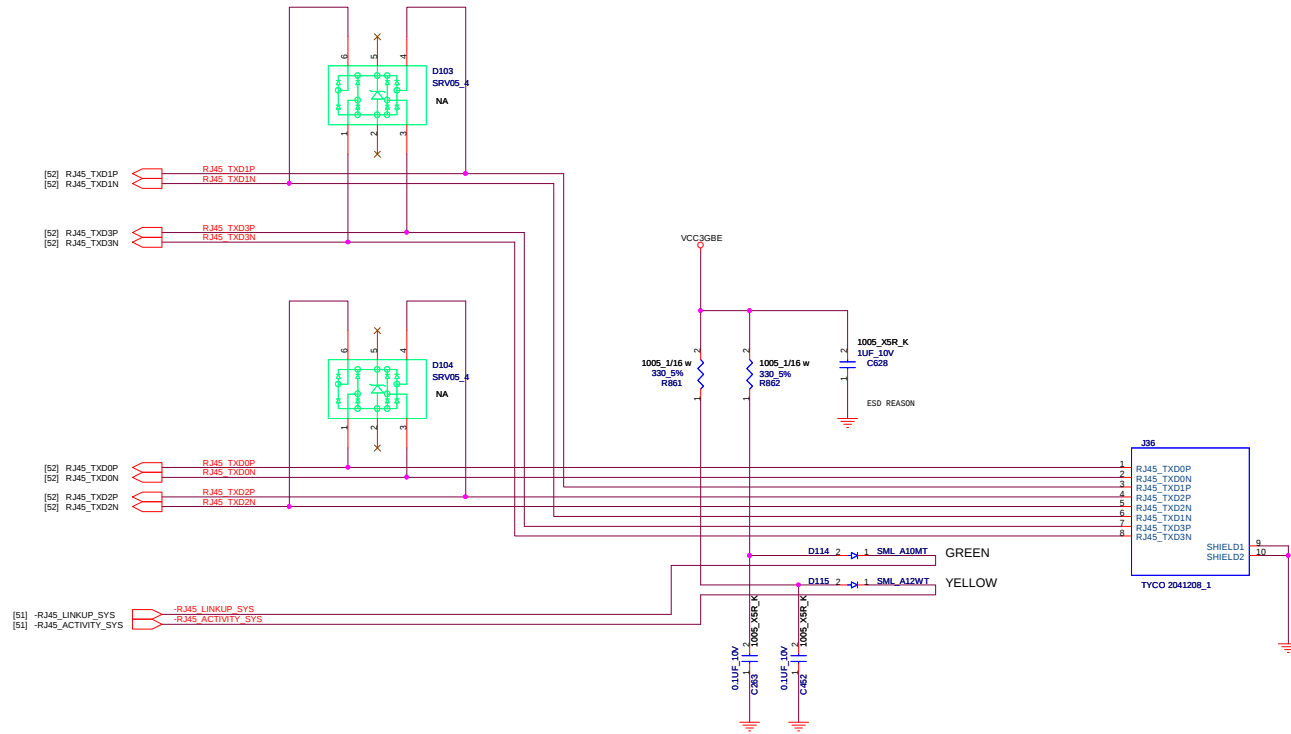
ESD REASON

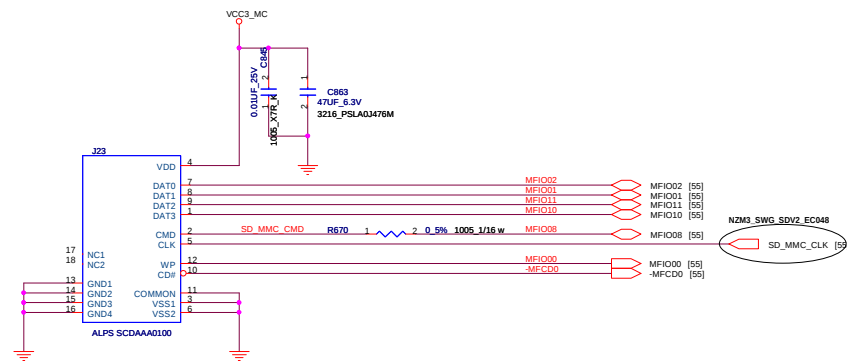
PATTERN MUST BE
SHORT AND WIDE.

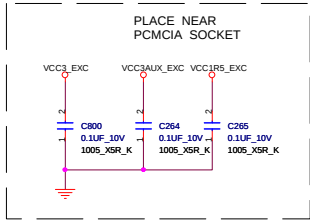
HIGH VOLTAGE
1500PF CAP
IS OPTIONAL

FLEX Computing

Project Name : WZCOM-3 SWG SDV2		Title : GBE MAGNETICS	
Size : C	Document Number :		Rev : 2.10
Date : Monday, June 14, 2010		Sheet : 52 of 99	



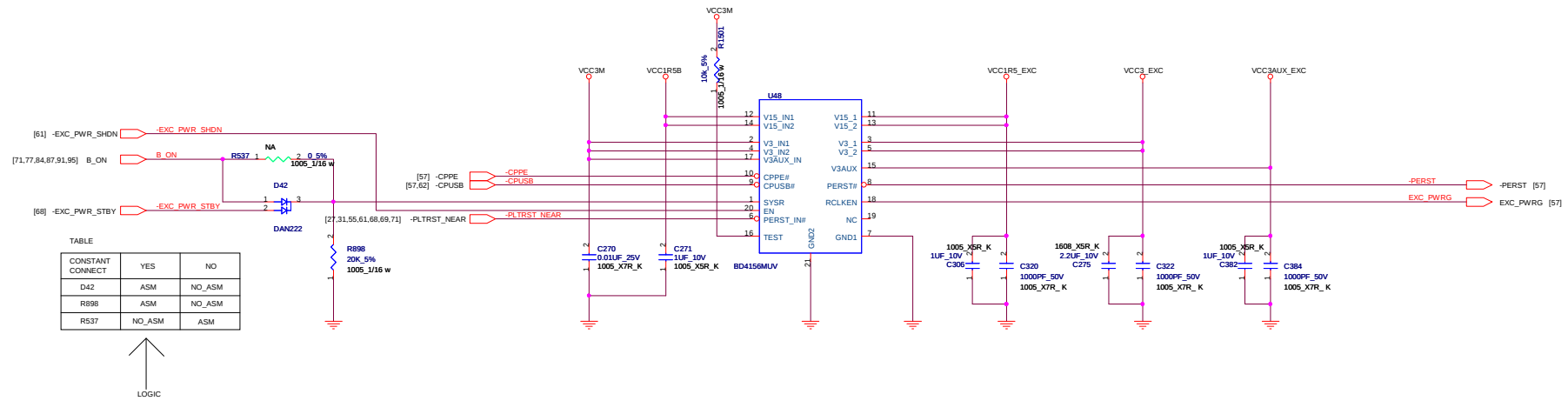


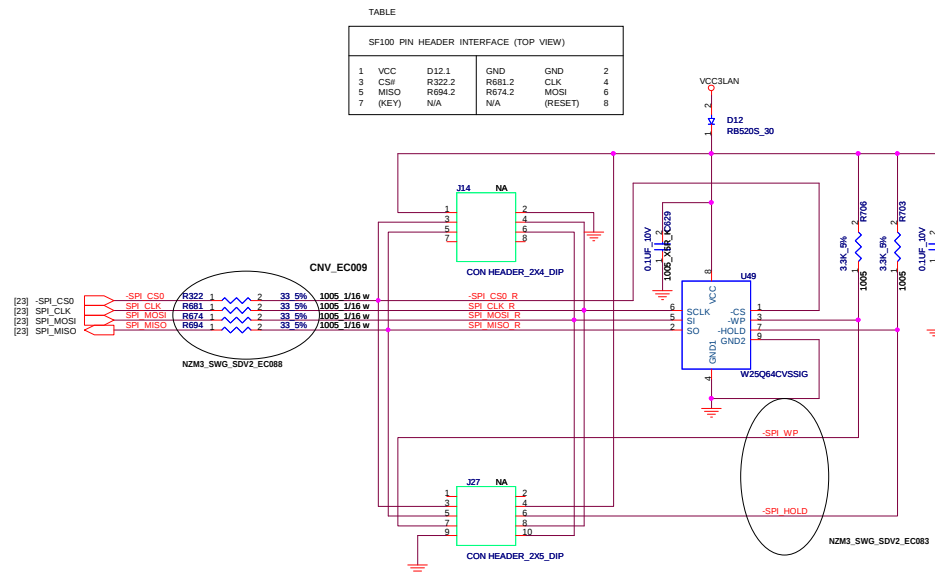


TABLE

CONSTANT CONNECT	VER.2/VER.1	NO
U48	BD4156MUV TPS2231MRGPR-3	TPS2231MRGPR BD4156MUV W83L351YG
R1501	ASM	NO_ASM

LOGIC





TABLE

SF100 PIN HEADER INTERFACE (TOP VIEW)							
1	VCC	D12.1	GND	GND	2		
3	CS#	R322.2	R681.2	CLK	4		
5	MISO	R694.2	R674.2	MOSI	6		
7	(KEY)	N/A	N/A	(RESET)	8		

TABLE

AMT	YES	NO	NO
RPAT	YES	YES	NO
U49	8MB	8MB	4MB

LOGIC

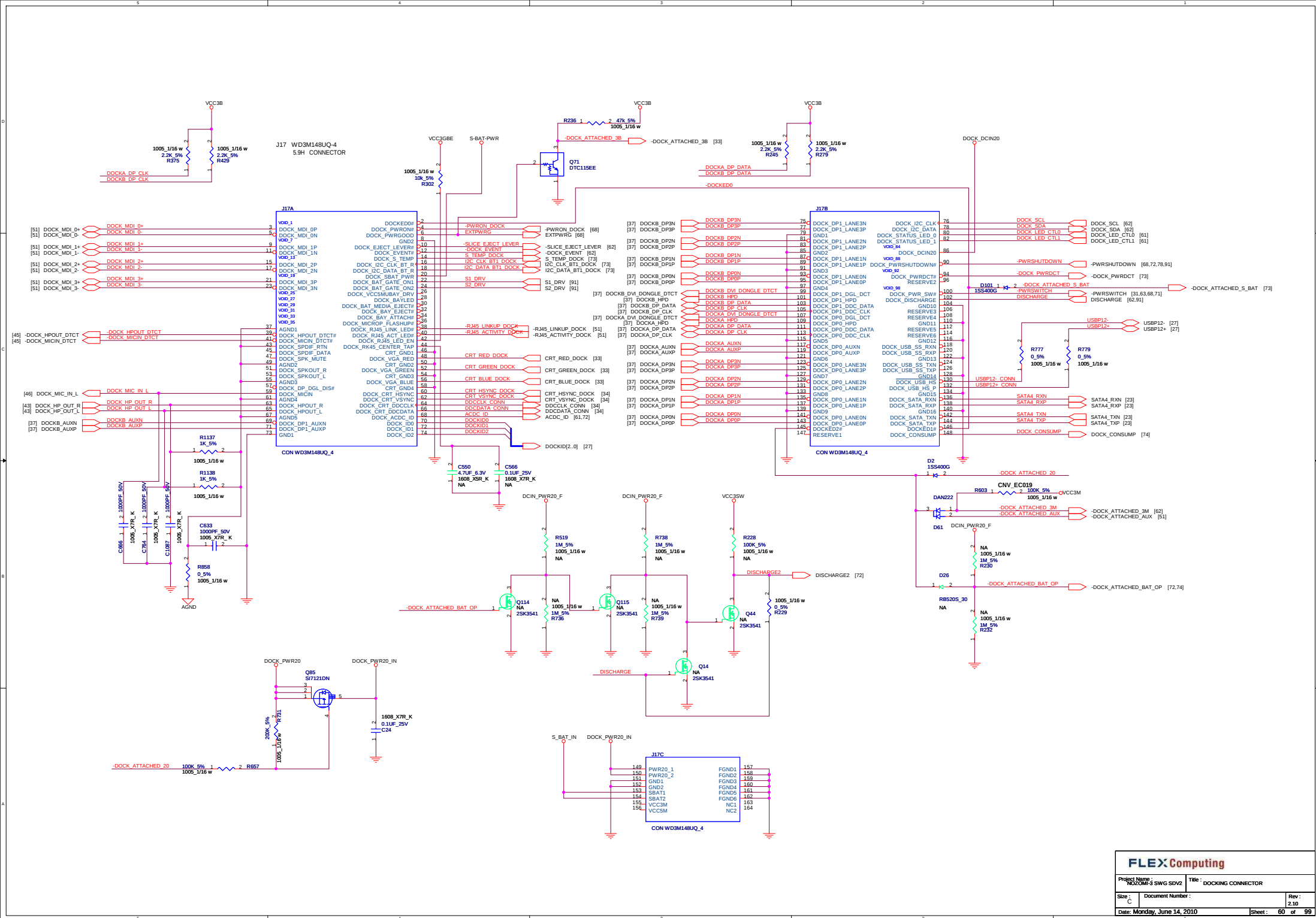
8MB SOIC8
MACRONIX MX25L6436EM2I-10G
WINBOND W25Q64BVSSIG
WINBOND W25Q64CVSSIG

8MB WSON8
NUMONYX M25PX64-VMD6TG

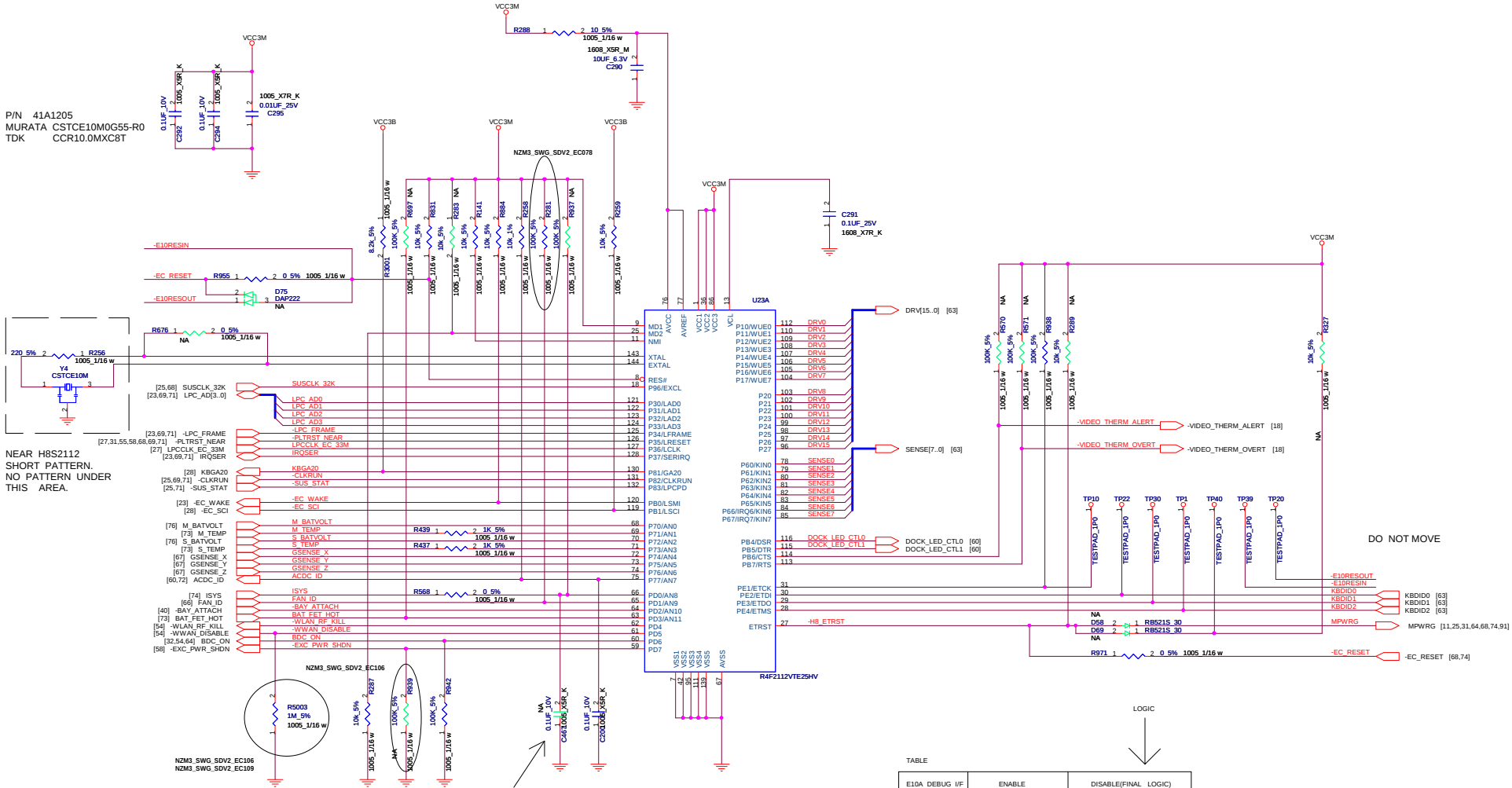
4MB SOIC8
MACRONIX MX25L3236DM2I-10G
WINBOND W25Q32BVSSIG
NUMONYX M25PX32-VMW6F

TABLE

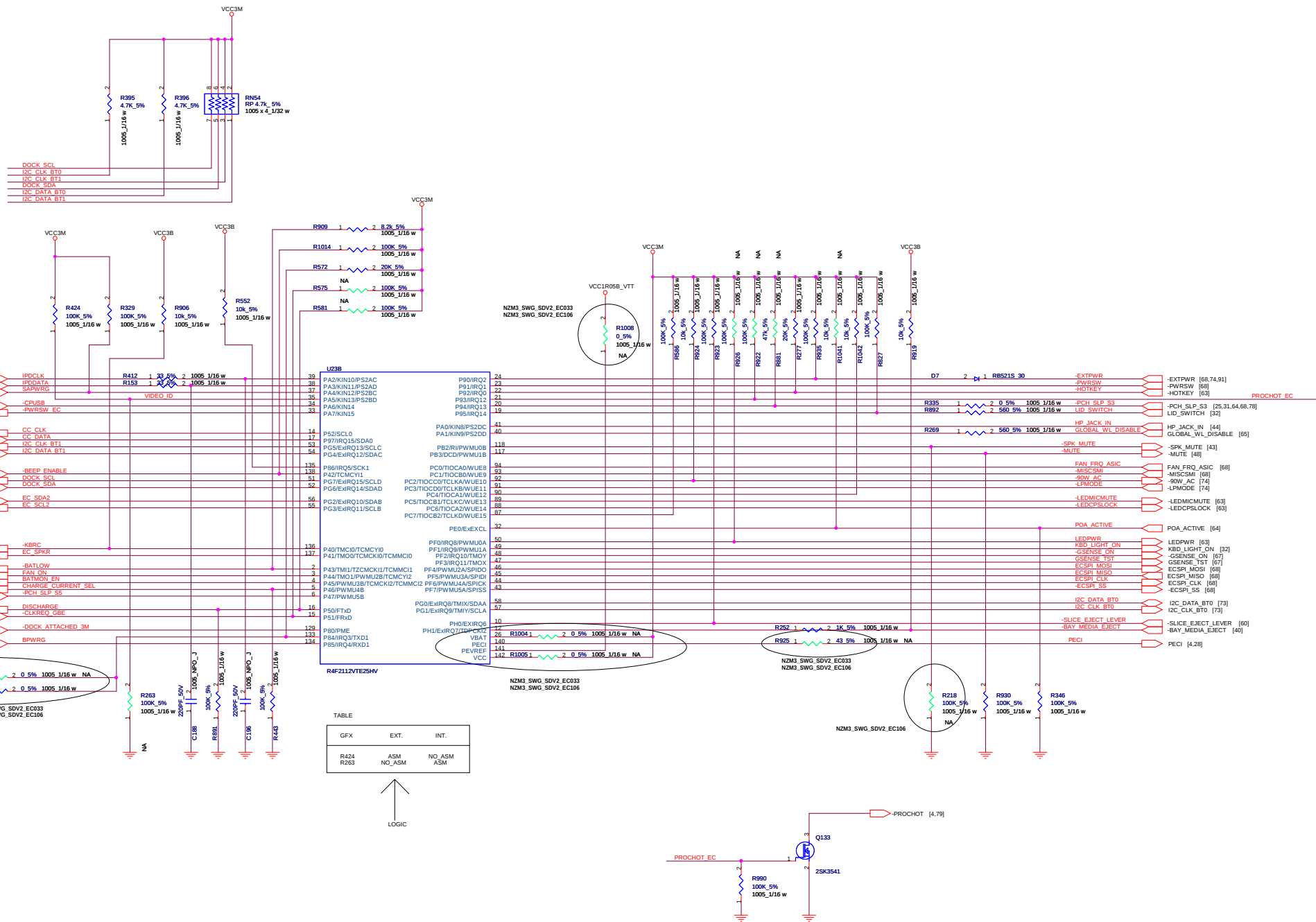
EM100 PIN HEADER INTERFACE (TOP VIEW)									
1	(HOLD1#)	(CS1#)	2						
3	CS0#	VCC	4						
5	MISO	HOLD0#	6						
7	WP0#	CLK	8						
9	GND	MOSI	10						

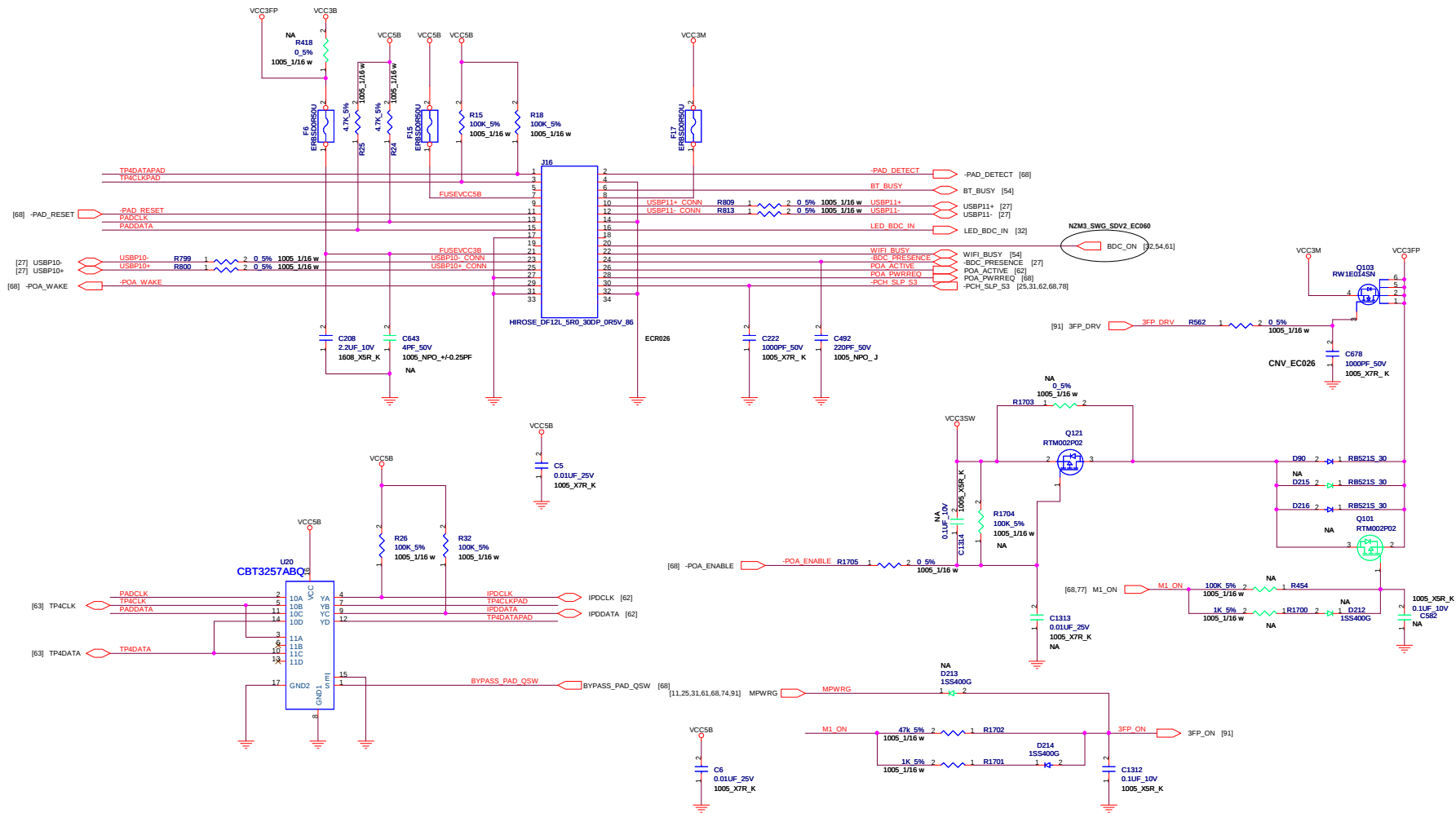


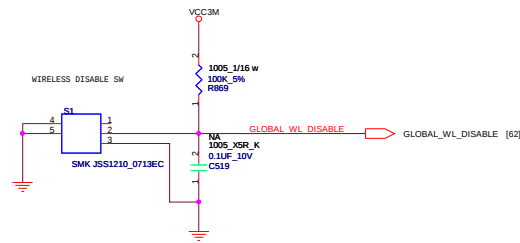
P/N 41A1205
MURATA CSTCE10M0G55-R0
TDK CCR10.0MXC8T

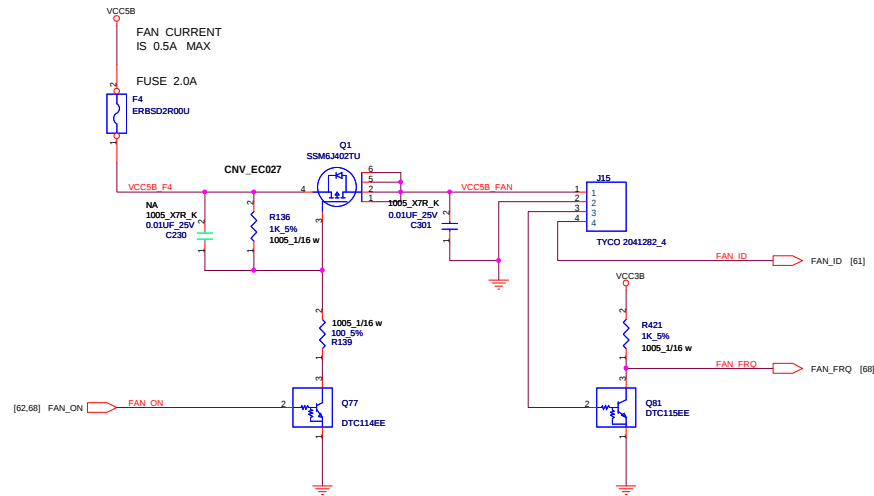


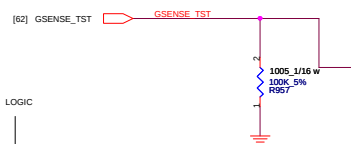
E10A DEBUG I/F	ENABLE	DISABLE(FINAL LOGIC)
R955 D75 R631	NO_ASM ASM ASM	ASM NO_ASM
R971 D58 D69 R295 R327	NO_ASM ASM ASM ASM	ASM NO_ASM NO_ASM NO_ASM
R283 R287	ASM NO_ASM	NO_ASM ASM
R881 R218	ASM NO_ASM	NO_ASM ASM
RN25	NO_ASM	ASM





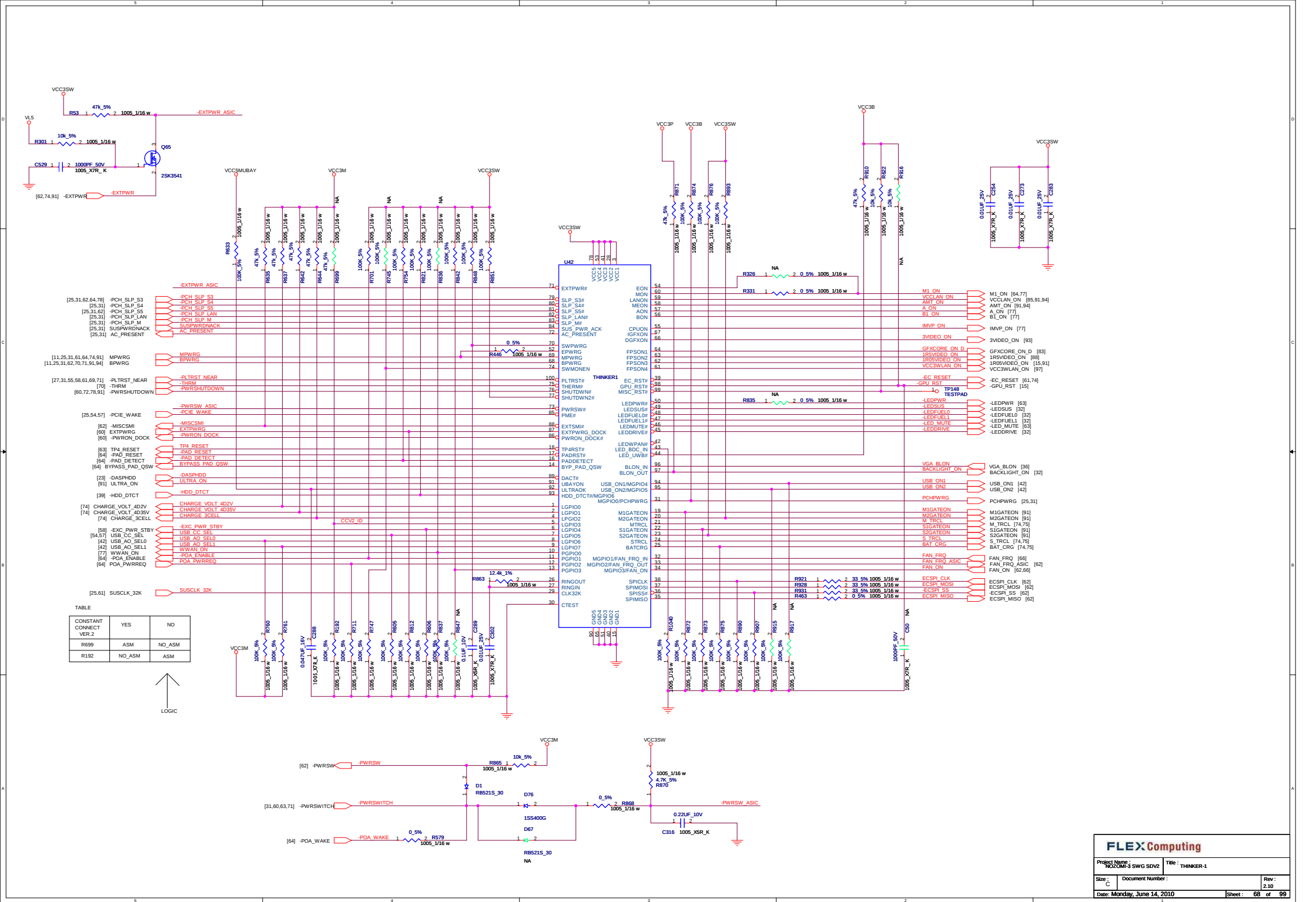


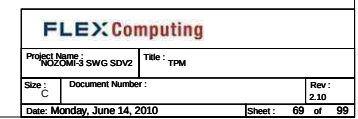




NZM3_SWG_SDV2_EC073

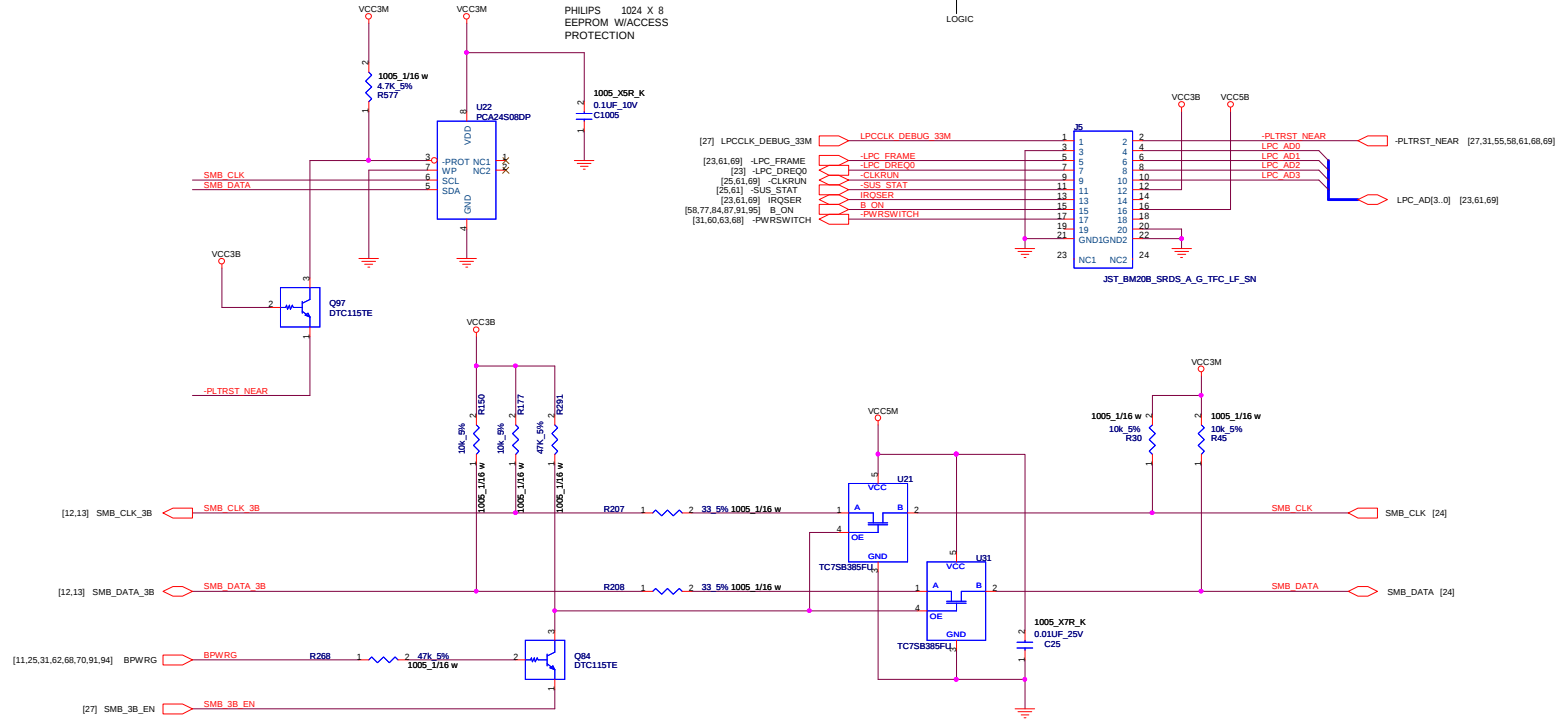


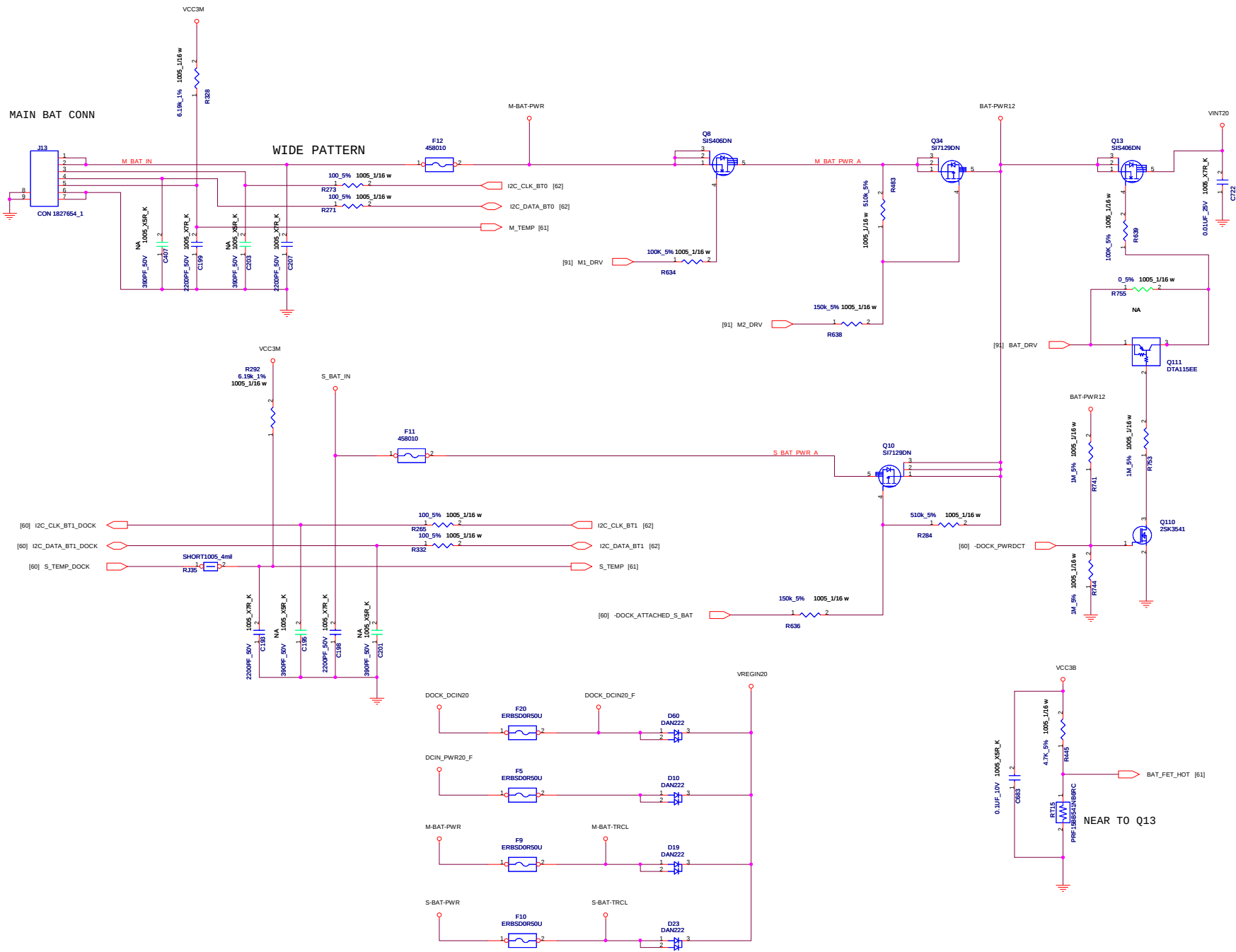


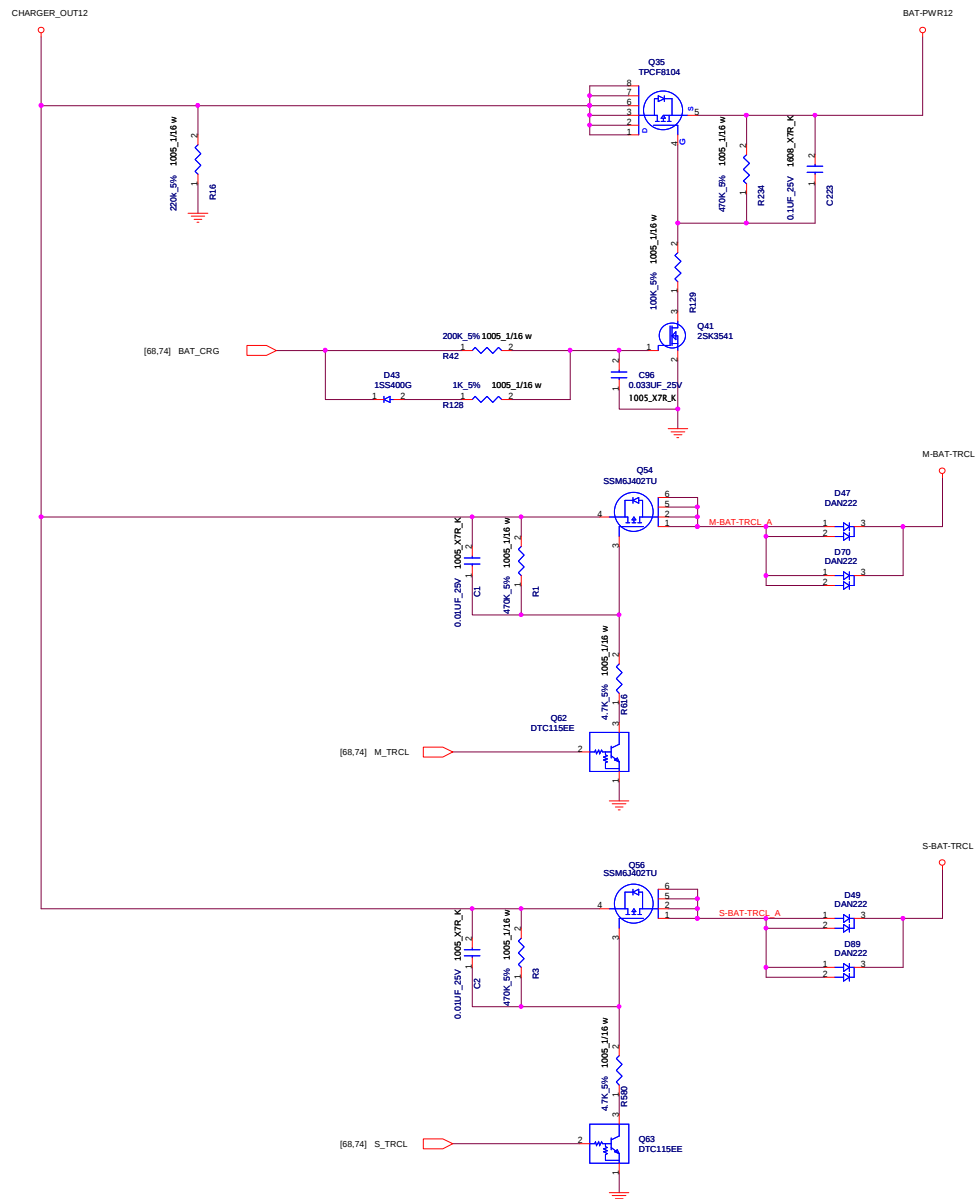


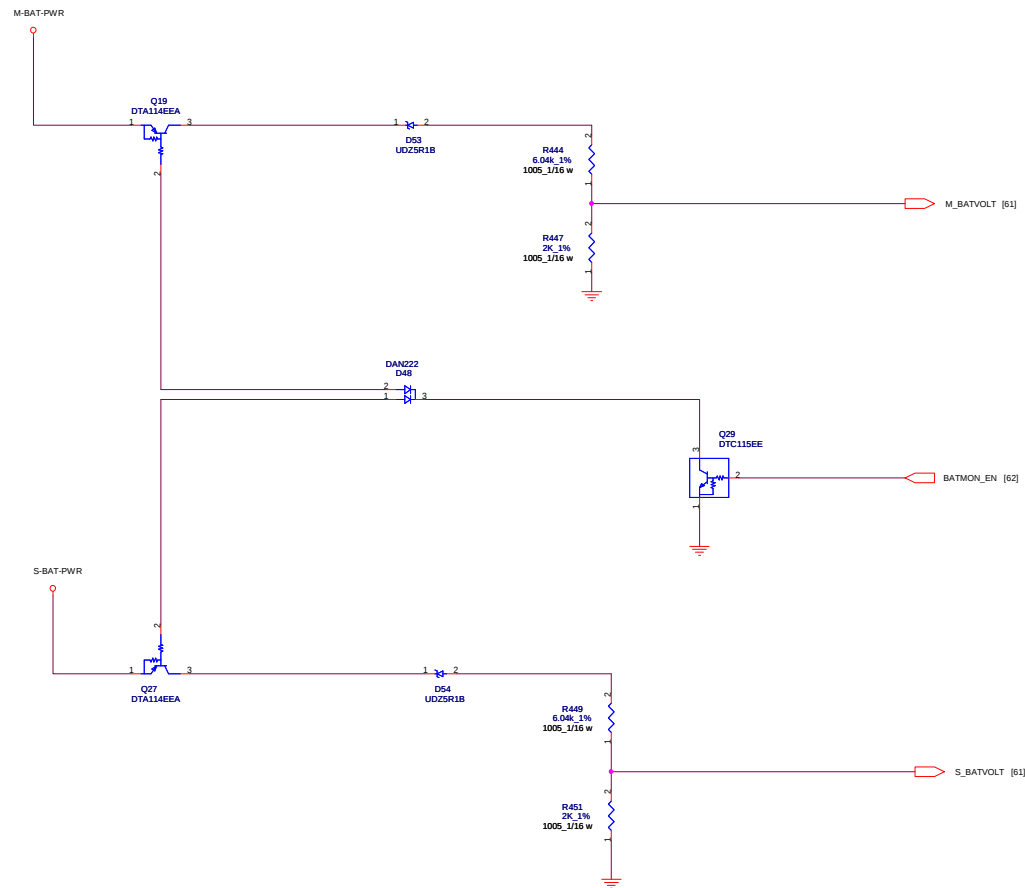
REF	DES	ENABLE	DISABLE
J5	ASM	NO_ASM	
R220	ASM	NO_ASM	

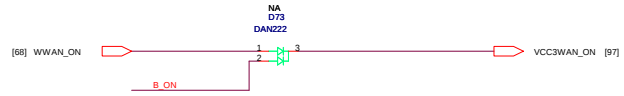
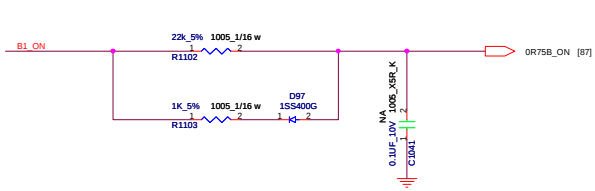
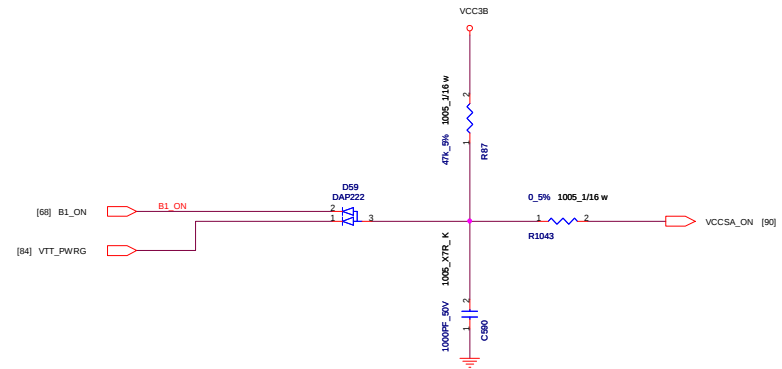
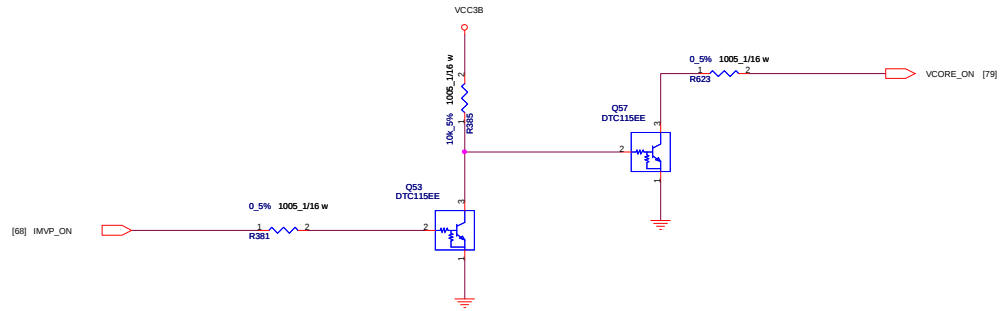
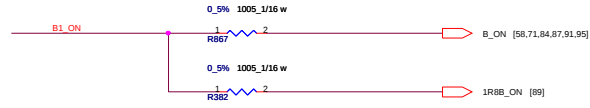
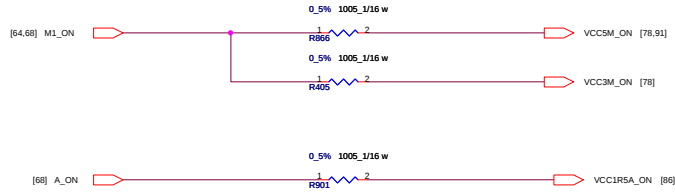
↑
LOGIC







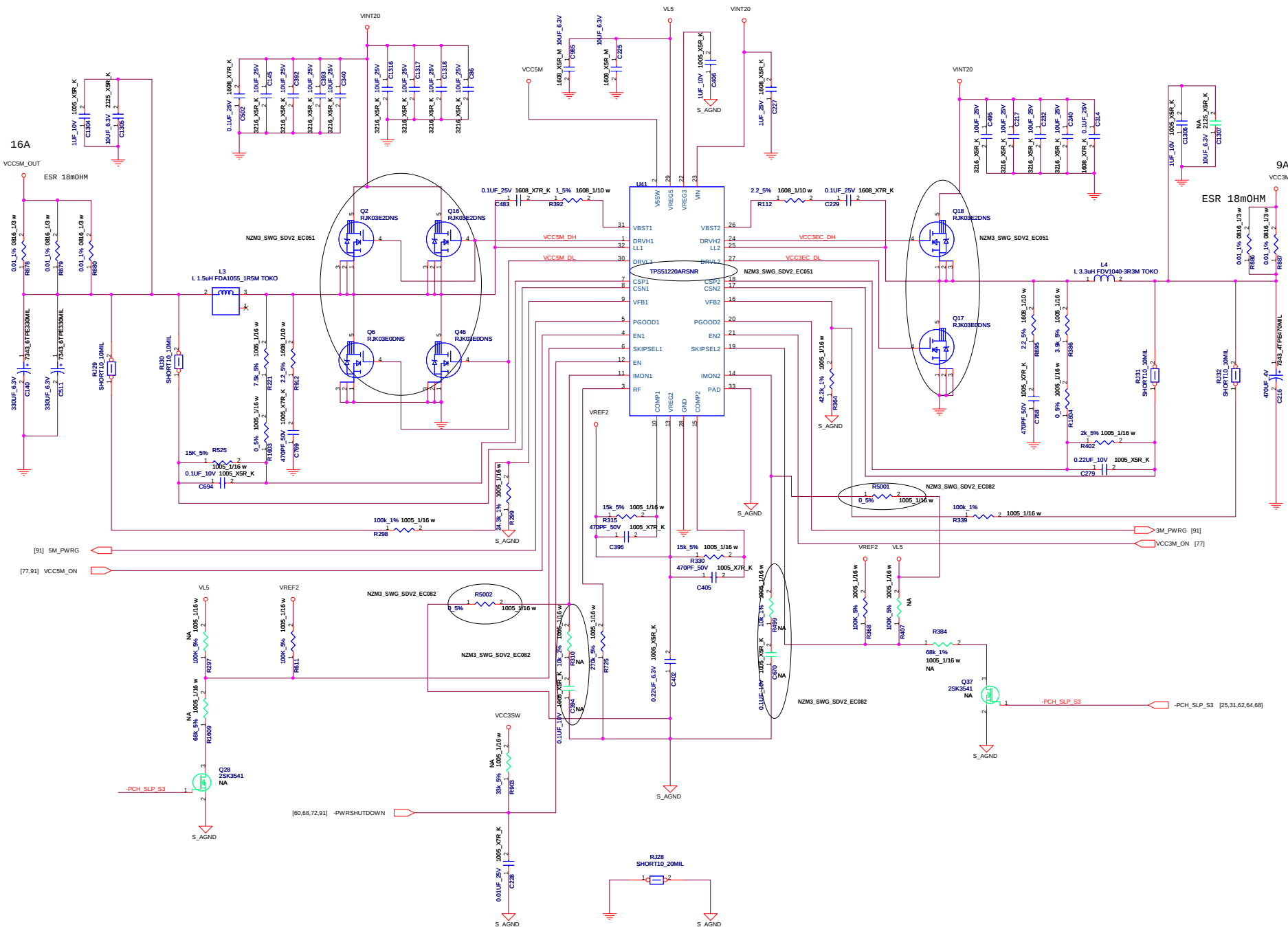


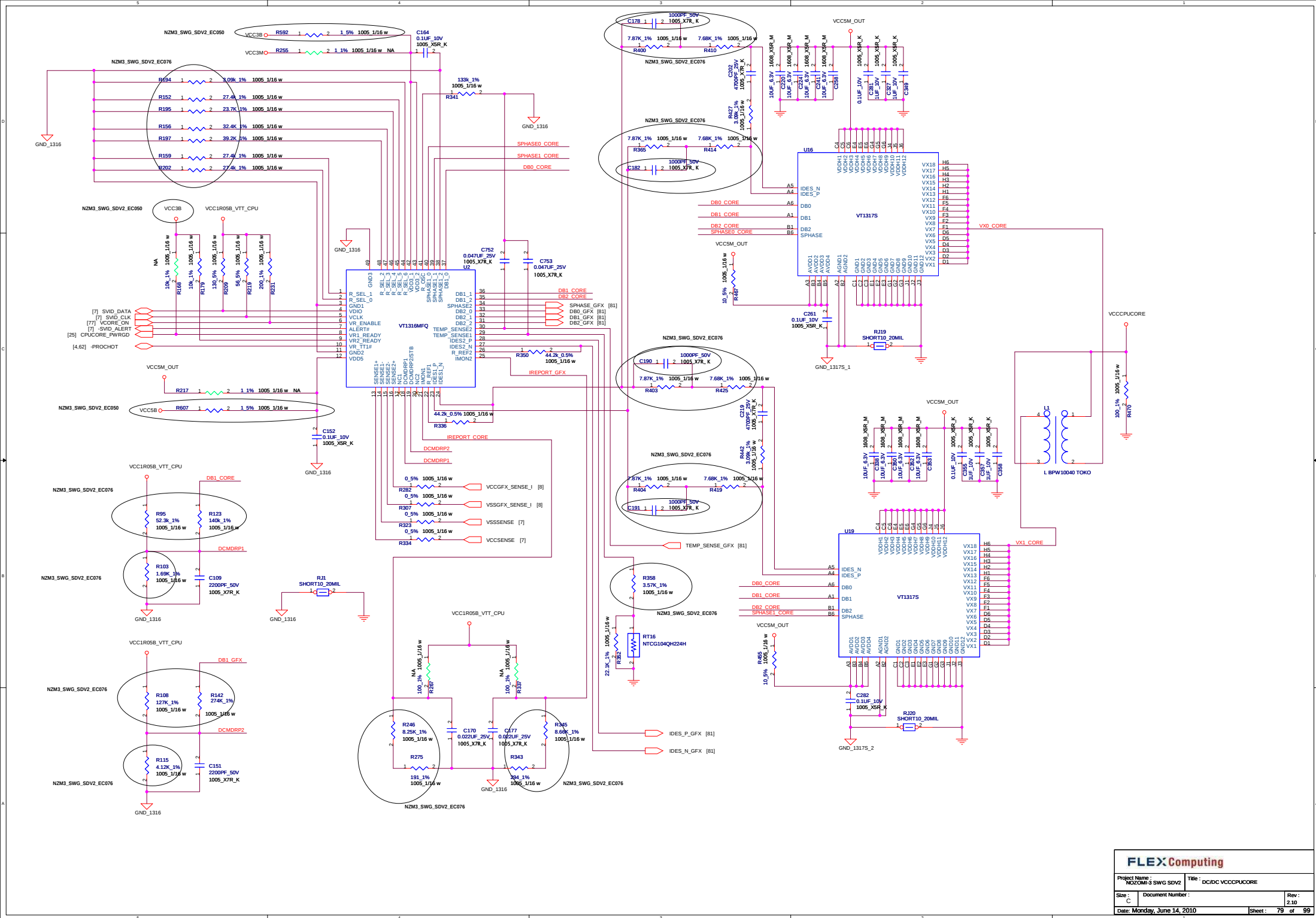


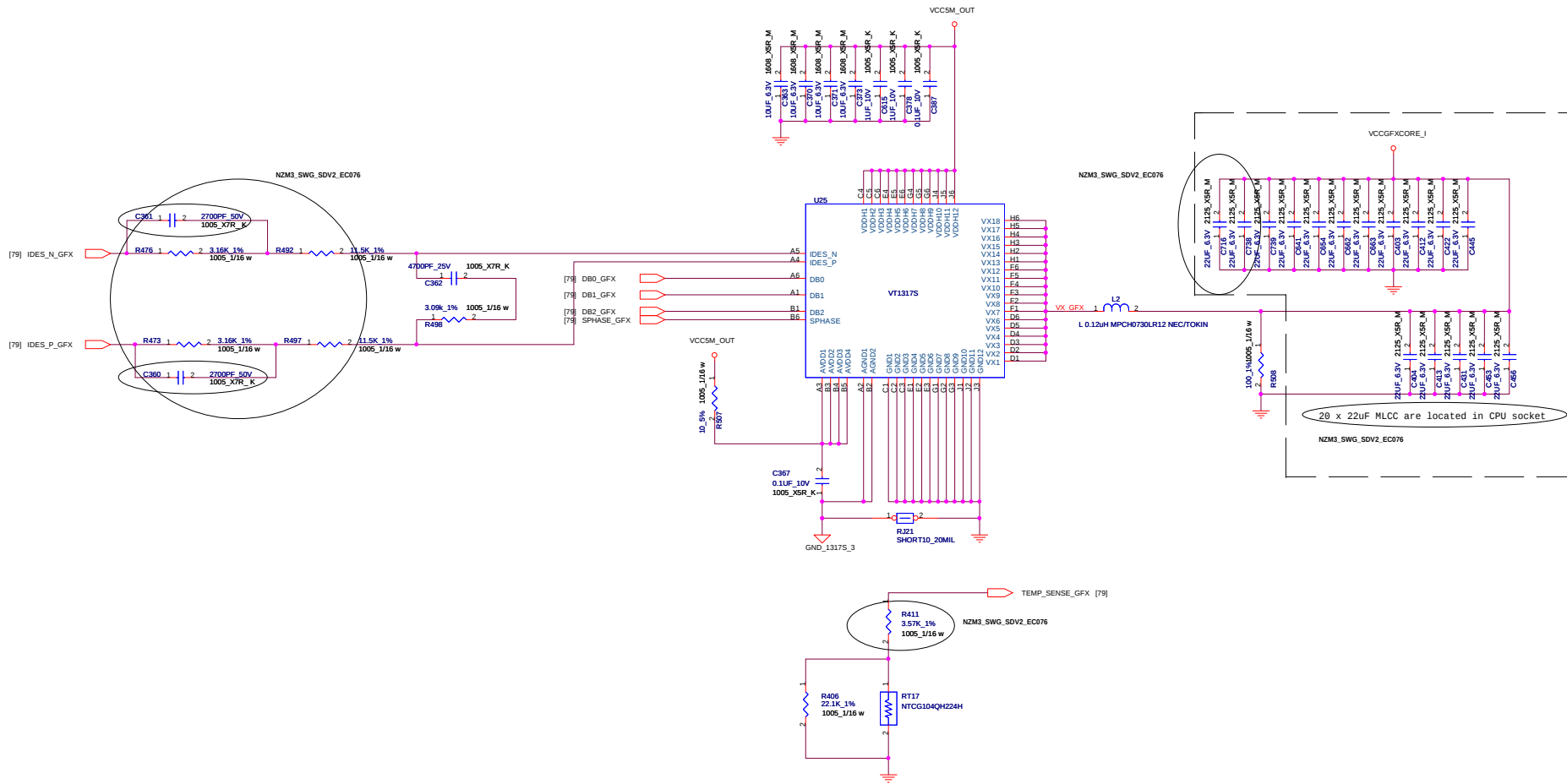
TABLE

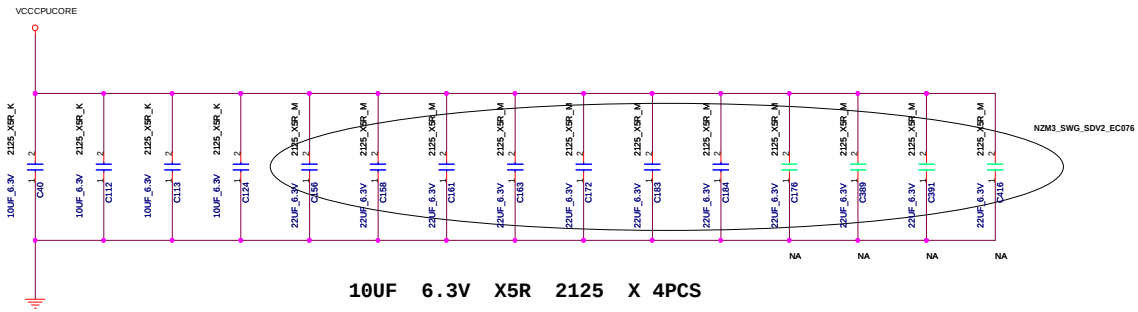
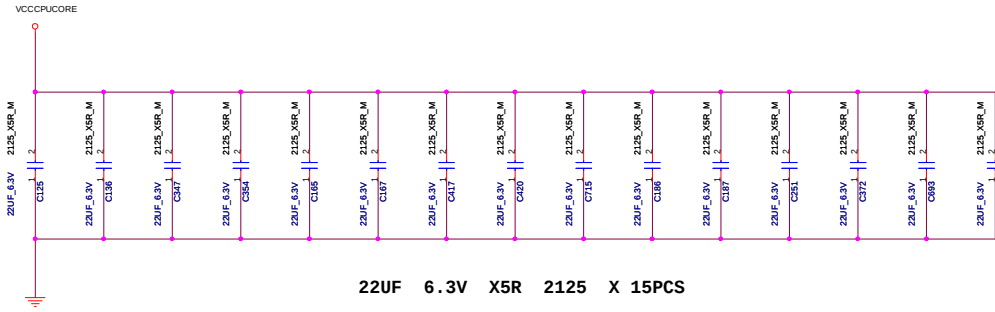
CONSTANT	VER . 2	VER . 1/NO
CONNECT	ASM	NO-ASM
D73		

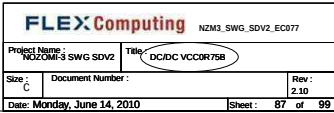
↑
LOGIC

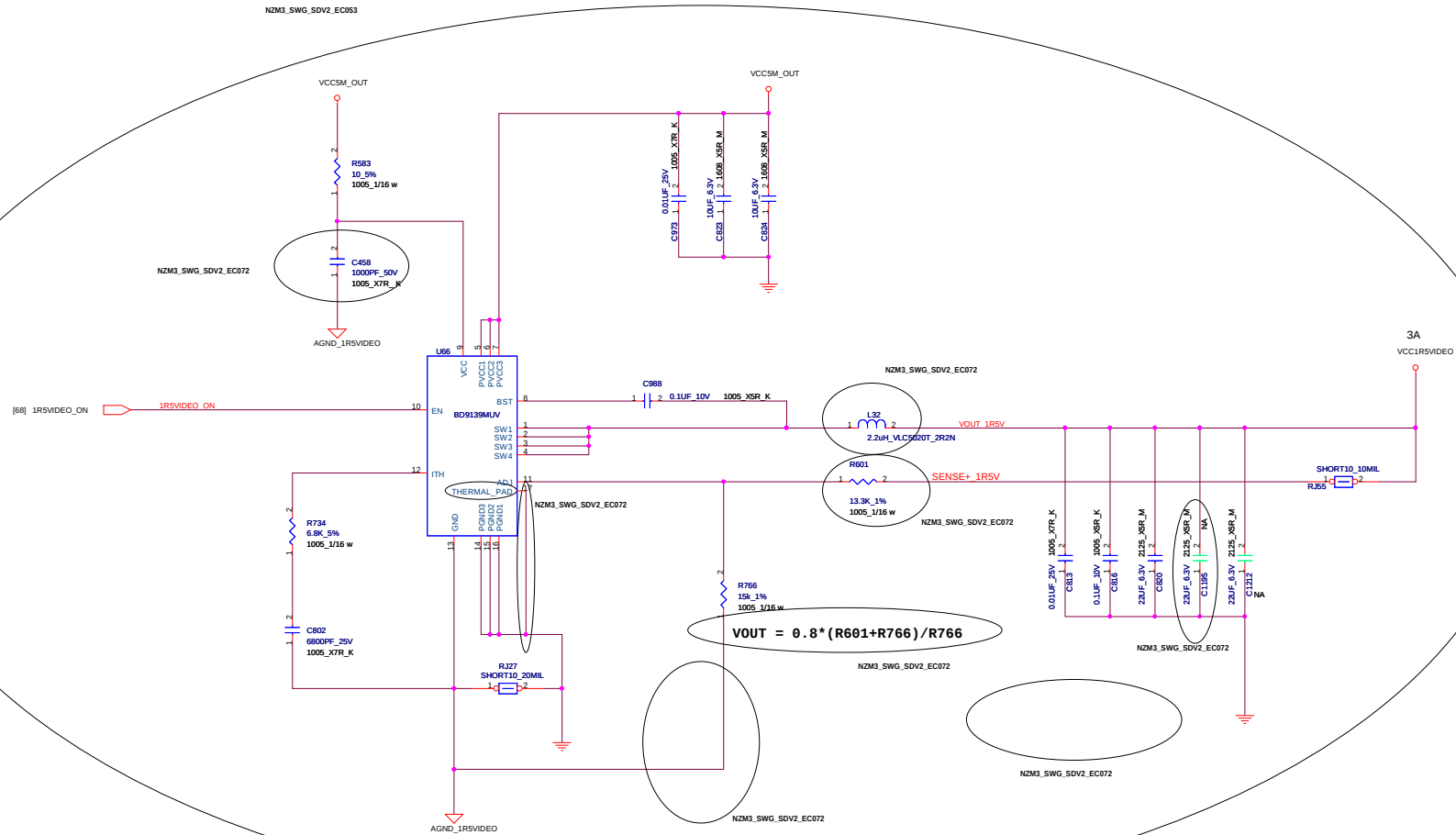


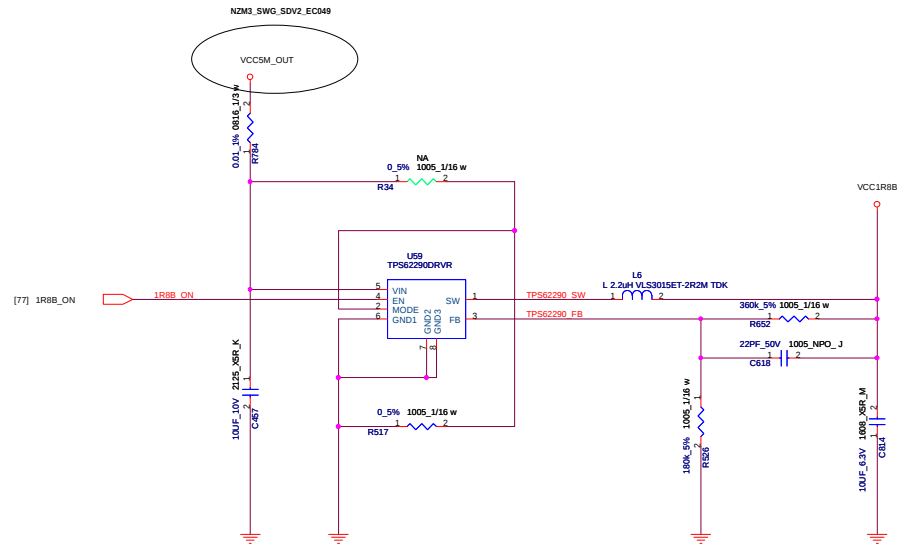


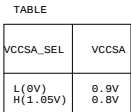


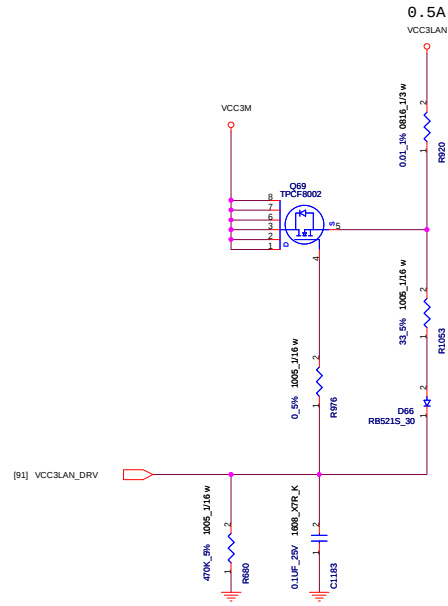


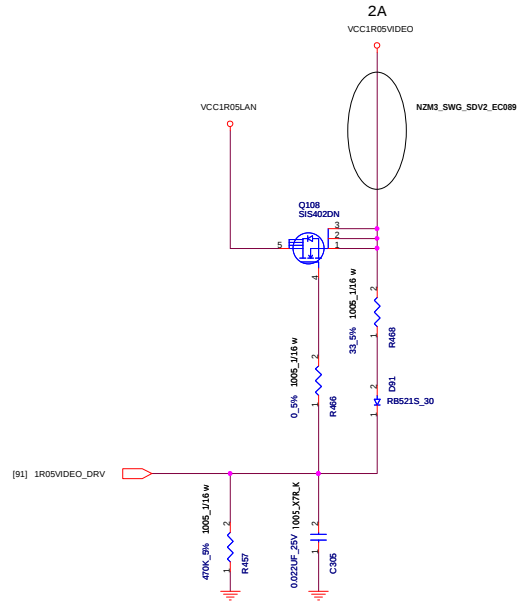
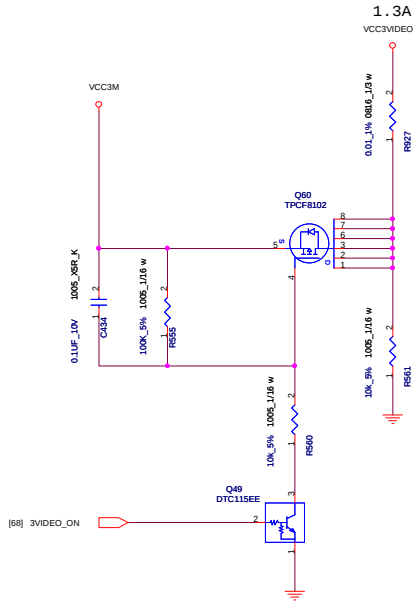






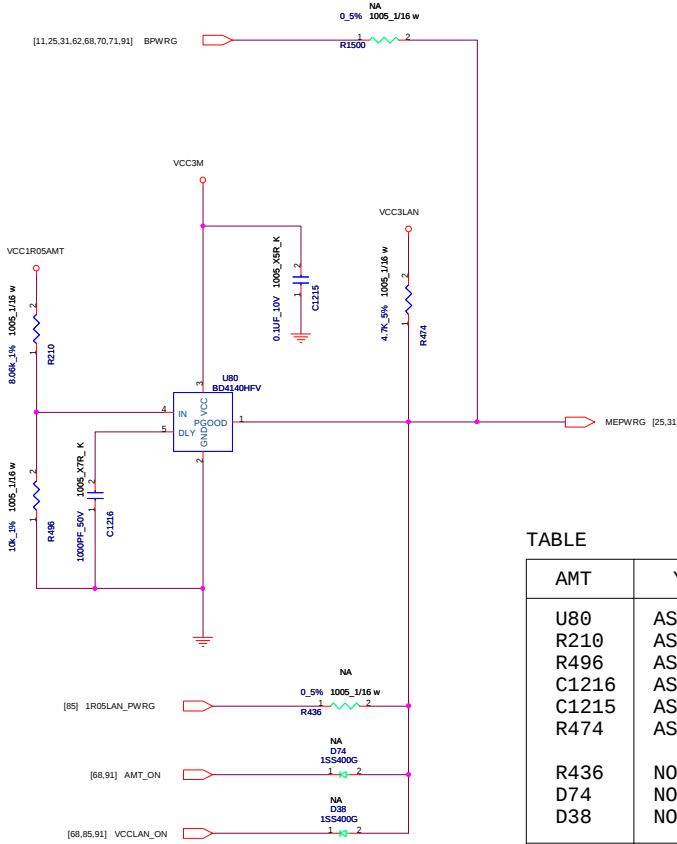
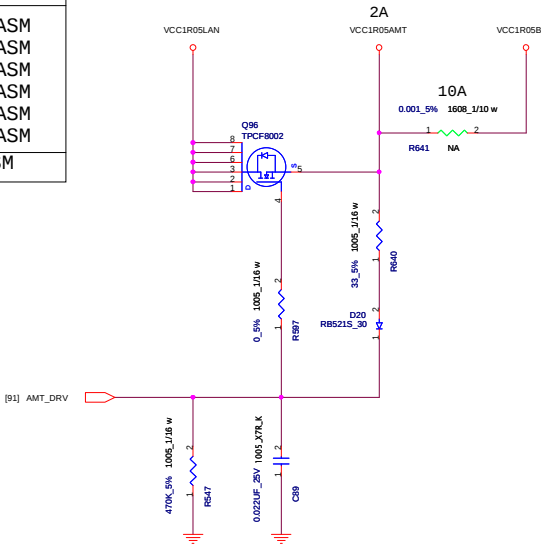






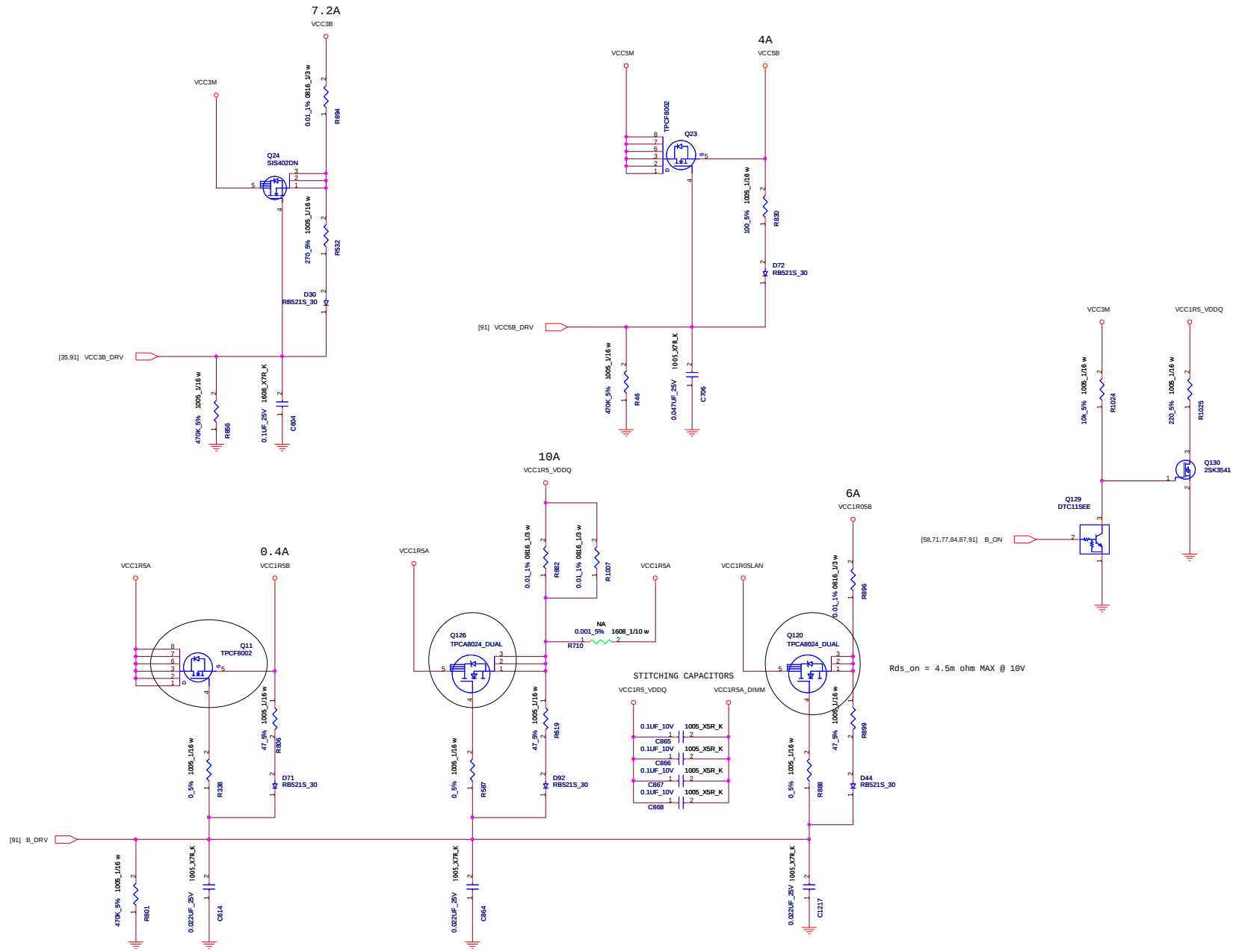
AMT	YES	NO
Q96	ASM	NO-ASM
R597	ASM	NO-ASM
R640	ASM	NO-ASM
D20	ASM	NO-ASM
R547	ASM	NO-ASM
C89	ASM	NO-ASM
R641	NO_ASM	ASM

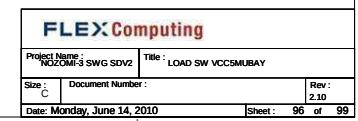
↑
 LOGIC

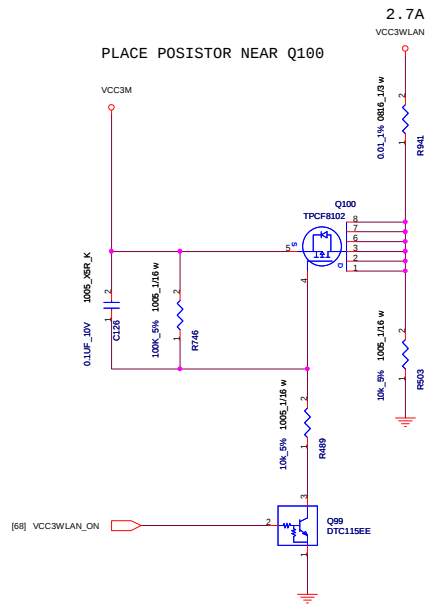


AMT	YES	NO
U80	ASM	NO-ASM
R210	ASM	NO-ASM
R496	ASM	NO-ASM
C1216	ASM	NO-ASM
C1215	ASM	NO-ASM
R474	ASM	NO-ASM
R436	NO_ASM	NO_ASM
D74	NO_ASM	NO_ASM
D38	NO_ASM	NO_ASM
R1500	NO_ASM	ASM

↑
 LOGIC



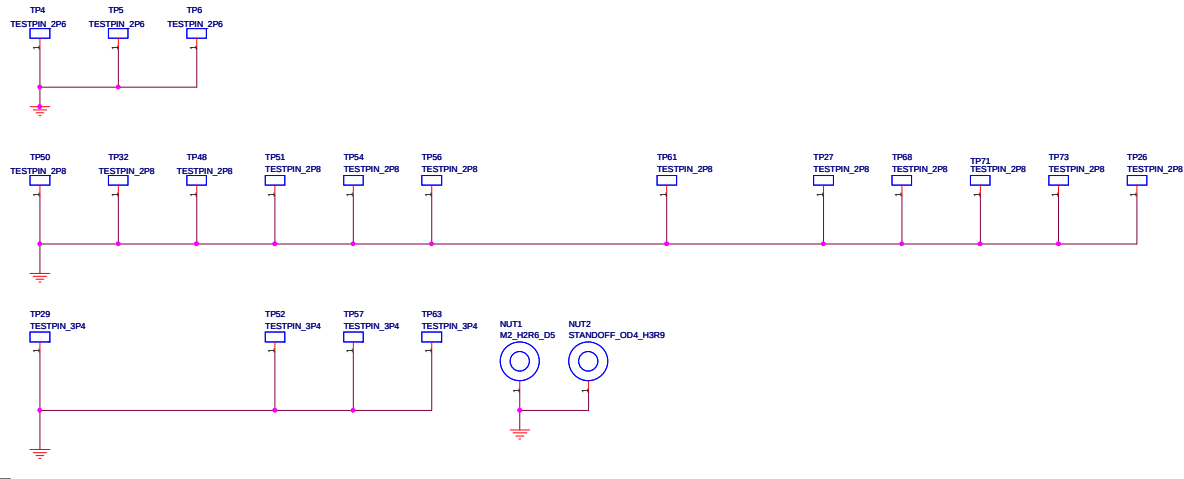




CONSTANT CONNECT	VER. 2	VER. 1/NO
R1055	NO_ASM	ASM
Q94	ASM	NO-ASM
R650	ASM	NO-ASM
R645	ASM	NO-ASM
C1084	ASM	NO-ASM
R647	ASM	NO-ASM
Q91	ASM	NO-ASM

FLEX Computing		
Project Name : NOZOMI-3 SWG SDV2		Title : LOAD SW WAN & WLAN
Size : C	Document Number :	Rev : 2.10
Date: Monday, June 14, 2010		Sheet : 97 of 99

PTH FOR SCREW HOLE



NPTH



FID
Board Area

FD1
●× NC, NO CONNECT TO ANY.

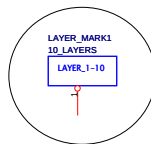
FD2
●× NC, NO CONNECT TO ANY.

FD3
●× NC, NO CONNECT TO ANY.

FD4
●× NC, NO CONNECT TO ANY.

FD5
●× NC, NO CONNECT TO ANY.

FD6
●× NC, NO CONNECT TO ANY.



FID
Component Area

CF1
●× NC, NO CONNECT TO ANY.

CF2
●× NC, NO CONNECT TO ANY.

CF3
●× NC, NO CONNECT TO ANY.

CF4
●× NC, NO CONNECT TO ANY.

CF5
●× NC, NO CONNECT TO ANY.

CF6
●× NC, NO CONNECT TO ANY.

CF7
●× NC, NO CONNECT TO ANY.

CF8
●× NC, NO CONNECT TO ANY.

CF9
●× NC, NO CONNECT TO ANY.

CF10
●× NC, NO CONNECT TO ANY.

CF11
●× NC, NO CONNECT TO ANY.

CF12
●× NC, NO CONNECT TO ANY.

CF13
●× NC, NO CONNECT TO ANY.

CF14
●× NC, NO CONNECT TO ANY.

CF15
●× NC, NO CONNECT TO ANY.

CF16
●× NC, NO CONNECT TO ANY.

CF17
●× NC, NO CONNECT TO ANY.

CF18
●× NC, NO CONNECT TO ANY.

