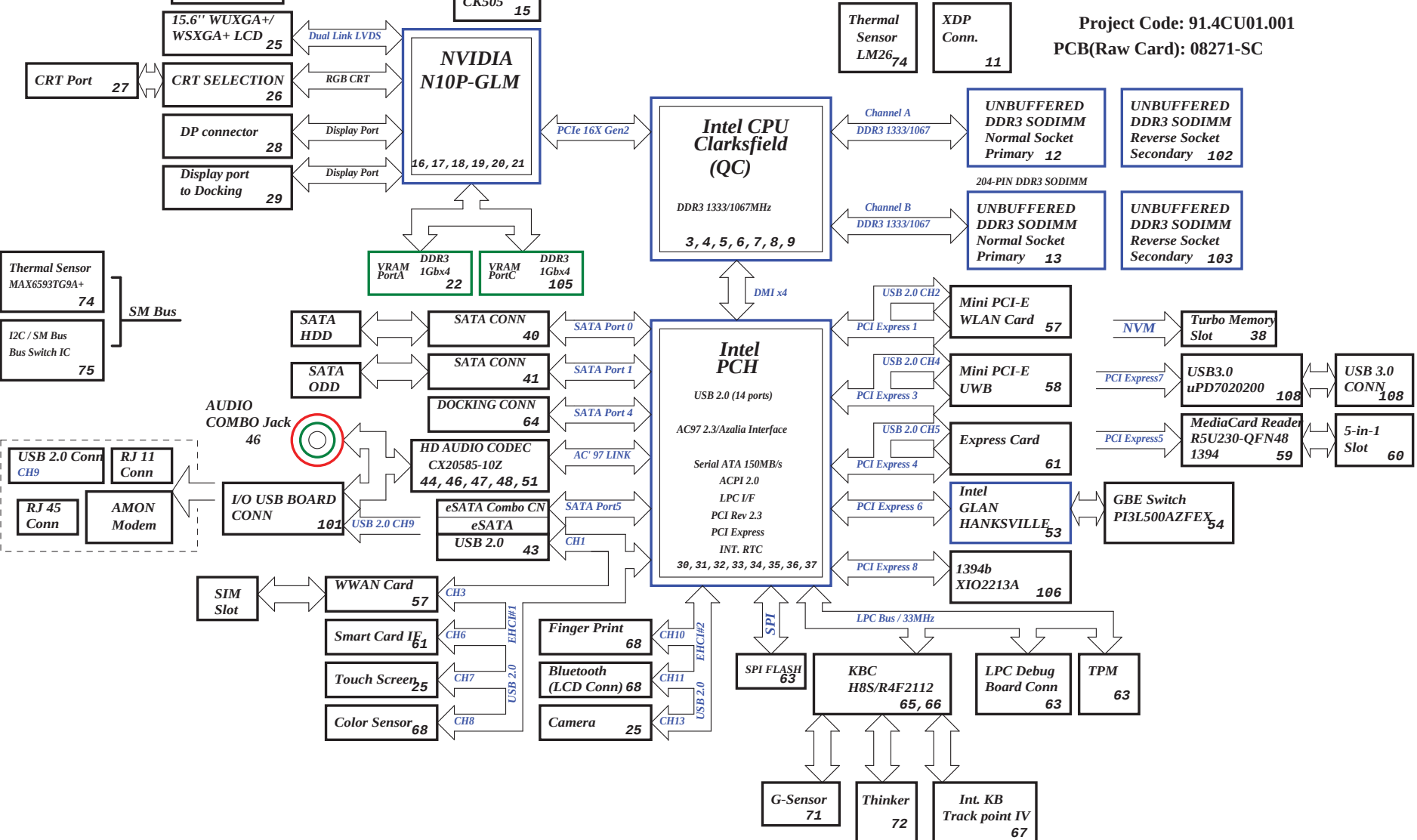




PCB Layer Stackup	
L1:TOP	
L2:Signal 1	
L3:GND	
L4:Signal 2	
L5:VCC	
L6:GND	
L7:Signal 3	
L8:GND	
L9:Signal4	
L10:BOTTOM	

Battery Charger/Selector	
BQ24741RHDR 78	
INPUTS	OUTPUTS
DOCK_PWR20_F	M-BAT-PWR
	S-BAT-PWR
System DC/DC	
TPS51222RTV 82	
VINT20	VCC5M
	VCC3M
CPU DC/DC	
ADP3212MNR2G 83	
VINT20	VCCCPUCORE
GFXCORE_D	
ADP3211 85	
VINT20	VCCGFXCORE
VCC1R5VIDEO	
MAX8792 91	
VCC5M	VCC1R5VIDEO
VCC1R5A/DDR3_REF	
VCC0R75B 89	
VCC5M	DDR3_VREF
	VCC0R75B
	VCC1R5A
VCC1R8B	
BD3551 92	
VCC3M	VCC1R8B
	VCC5M
VCC1R1B_VTT	
VCC1R05LAN 87	
ISL6228	
VINT20	VCC1R1B_VTT
VCC5M	VCC1R05LAN

Symbol name	Value	Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	Rating 0402=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
10KR3	10K Ohm	If no letter, it means J: 5%	1/16W, 75V	0603
33D3R5	33.3 Ohm	If no letter, it means J: 5%	1/10W, 100V	0805
1KR3F	1K Ohm	F: 1%	1/16W, 75V	0603

Symbol name	Value	Tolerance (M: +/-20, K: +/-10, Z: +80/-20)	Rating	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
SCD1U10V2MX-1	0.1uF	M/X5R	10V	0402
SC10U6D3V5MX	10uF	M/X5R	6.3V	0805
SC2D2U16V5ZY	2.2uF	Z/Y5V	16V	0805

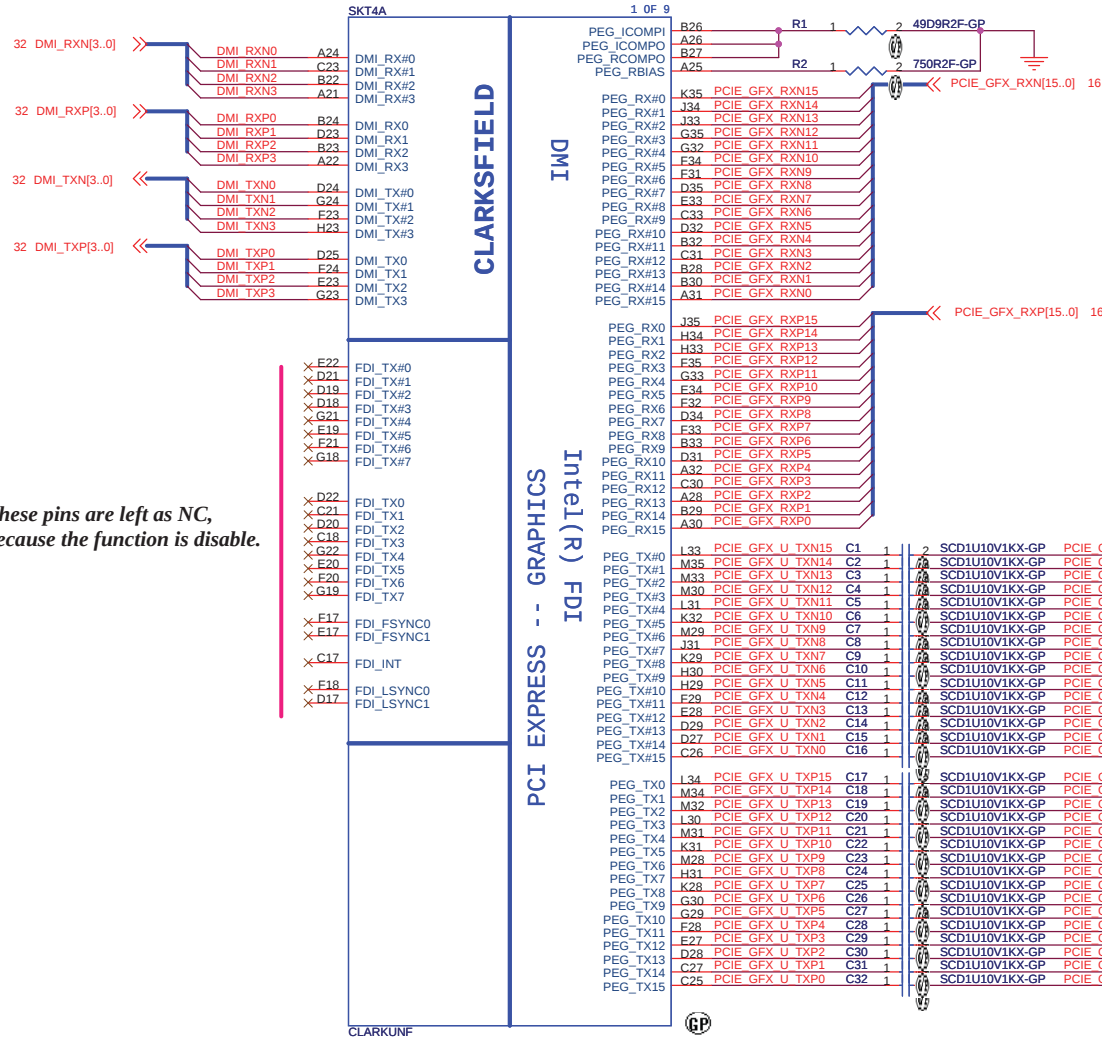
PCH GPIO _n	39	38	37	36	Planar ID Version	Planar PCB Version
PLANAR_ID _n	3	2	1	0		
	0	0	0	0	SDV	SA
	0	0	0	1	SDV-R	SB
	0	0	1	0	SIV	SC
	0	0	1	1		
	0	1	0	0		
	0	1	0	1		
	0	1	1	0		
	0	1	1	1		

[illegible]

DEVICE	IDSEL	IRQ (Default)	REQ# / GNT#
MINIPCI SLOT	AD18	F, G	REQ# 3/ GNT#
CARDBUS R5C811	AD16	SERIRQ	REQ#0 / GNT#
USB UHCI	AD29	A, C, D	
USB 2.0 EHCI	AD29	H	
DMI-to-PCI/ AC97 Modem/ AC97 Audio	AD30	B B	
LPC Bridge IDE SATA SMBus	AD31	C C B	
PCI Express	AD28	A, B, C, D	

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
		EC HISTORY	
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These pins are left as NC,
because the function is disable.



<Variant Name>

緯創資通

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Title

CPU(1/7):DMI/PEG/FDI

Size

Document Number

Kendo-1 WS

Rev

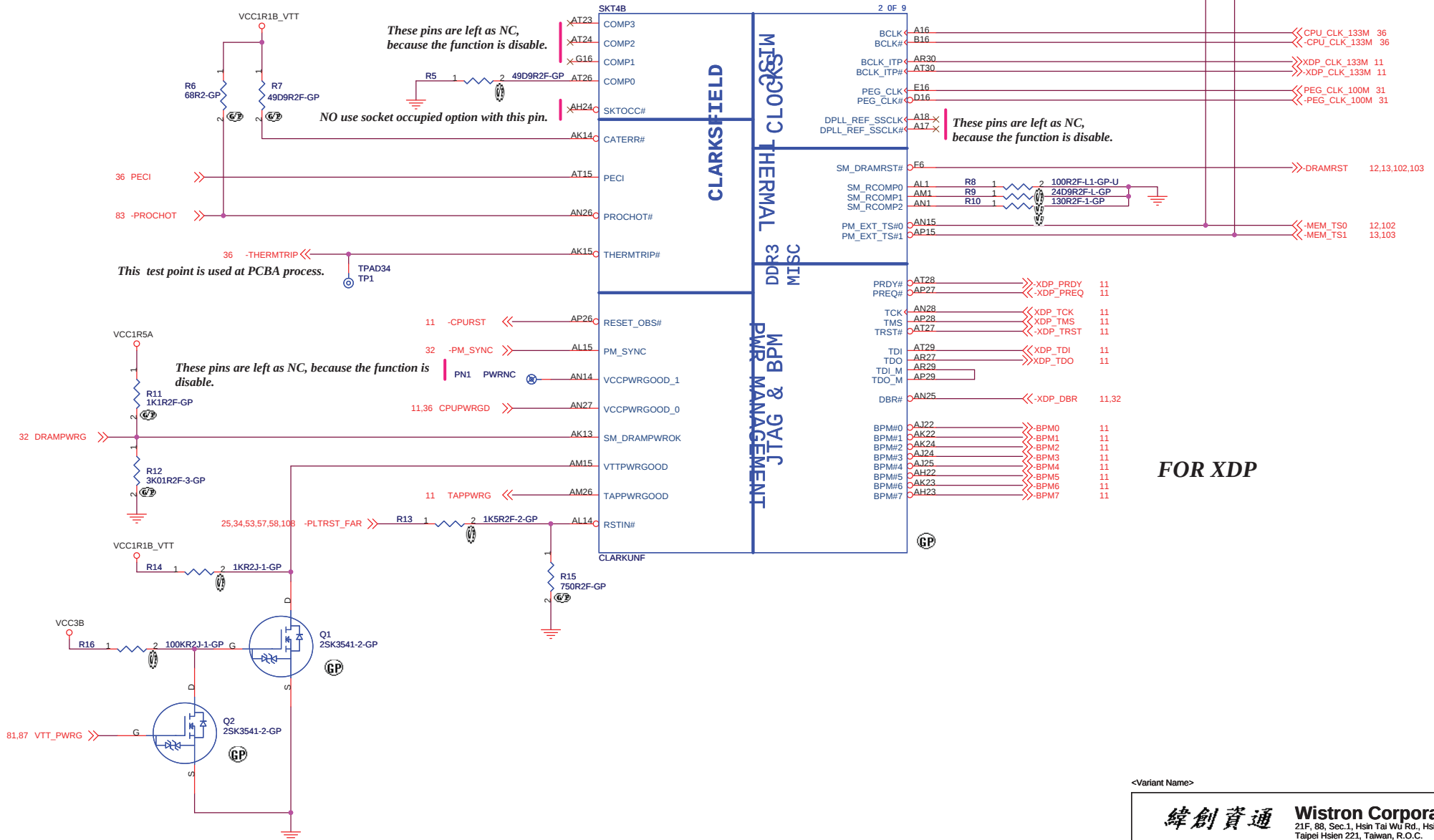
SC

Date: Tuesday, May 05, 2009

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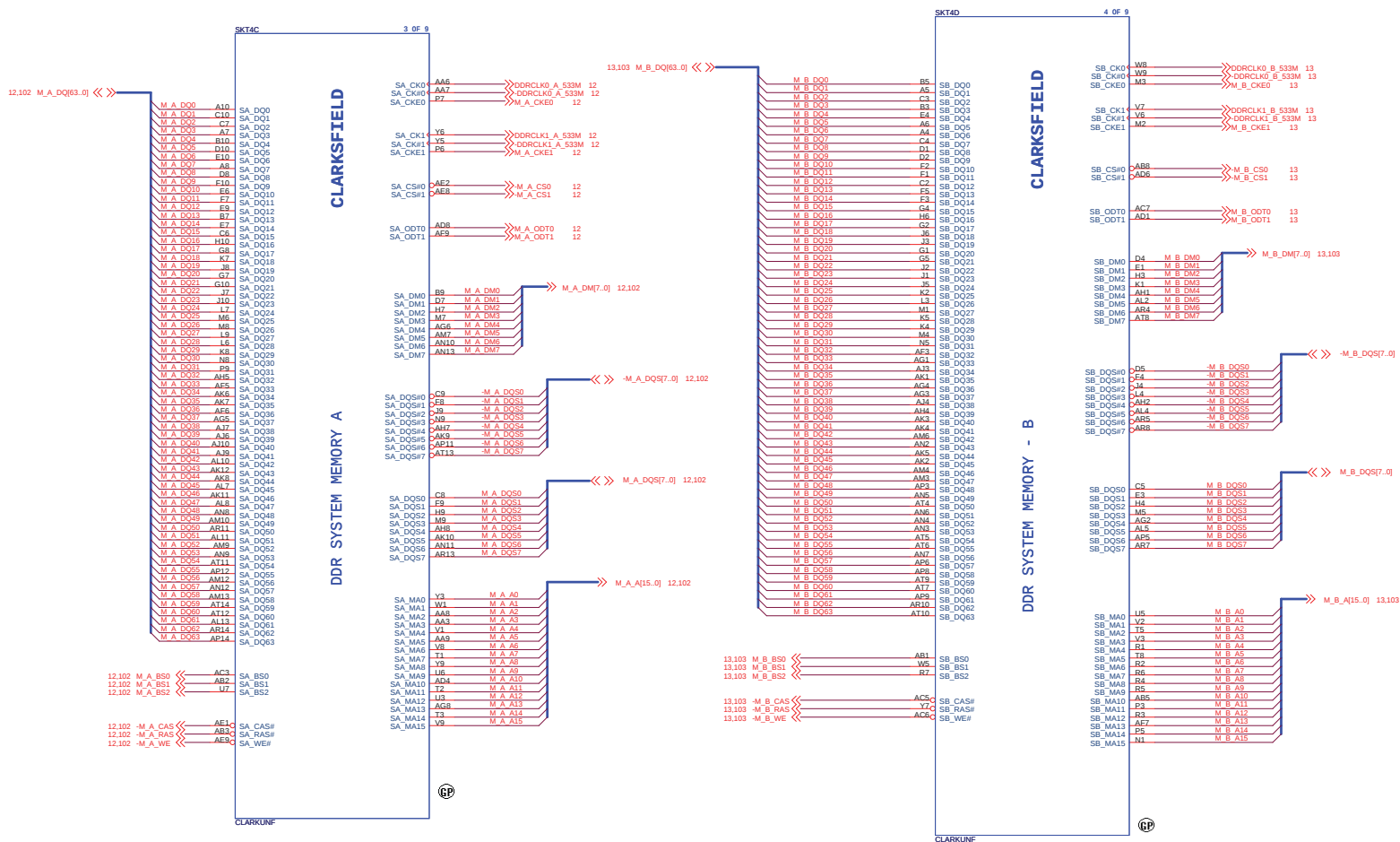
Need to refer Intel Design Guide to place CPU and route wire.

No use Catastrophic Error
Option with this pin.
Thermal Protection of CPU
at High Temp is controlled by LM26.

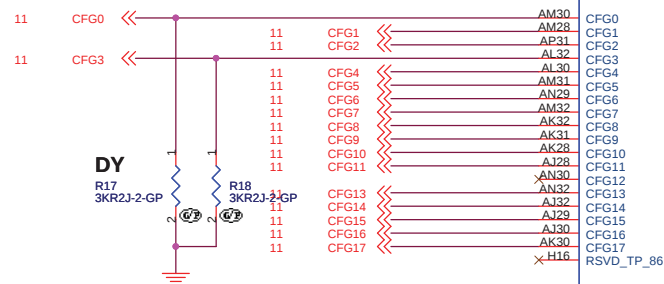


<Variant Name>

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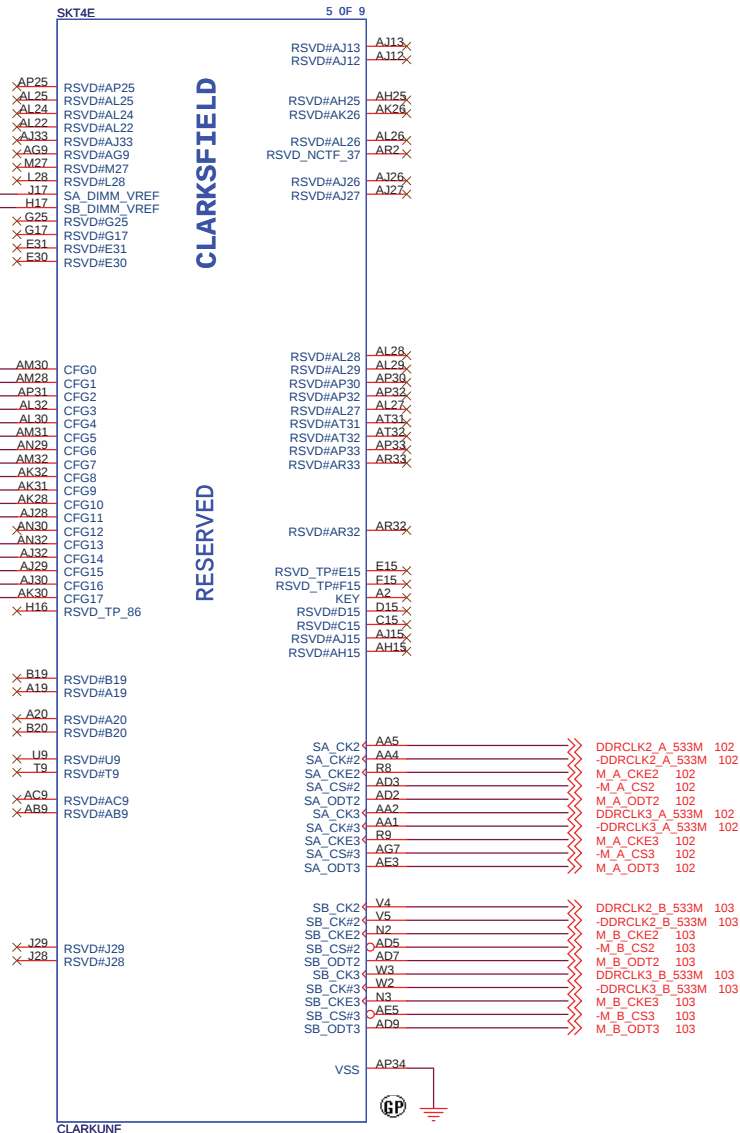
Need to confirm about Internall Pullup



CFG[1:0] => 11 : 1x16 PEG
10 : 2 x 8 PEG

CFG3 => 1 : Normal Operation
0 : Lane Numbers Reversed.

CFG[12]: This pin is left as NC,
because the GFX function is disabled.



4-DIMM SUPPORT

<Variant Name>

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Title

CPU(4/7): CFG/RSVD

Size
A3

Document Number

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SC

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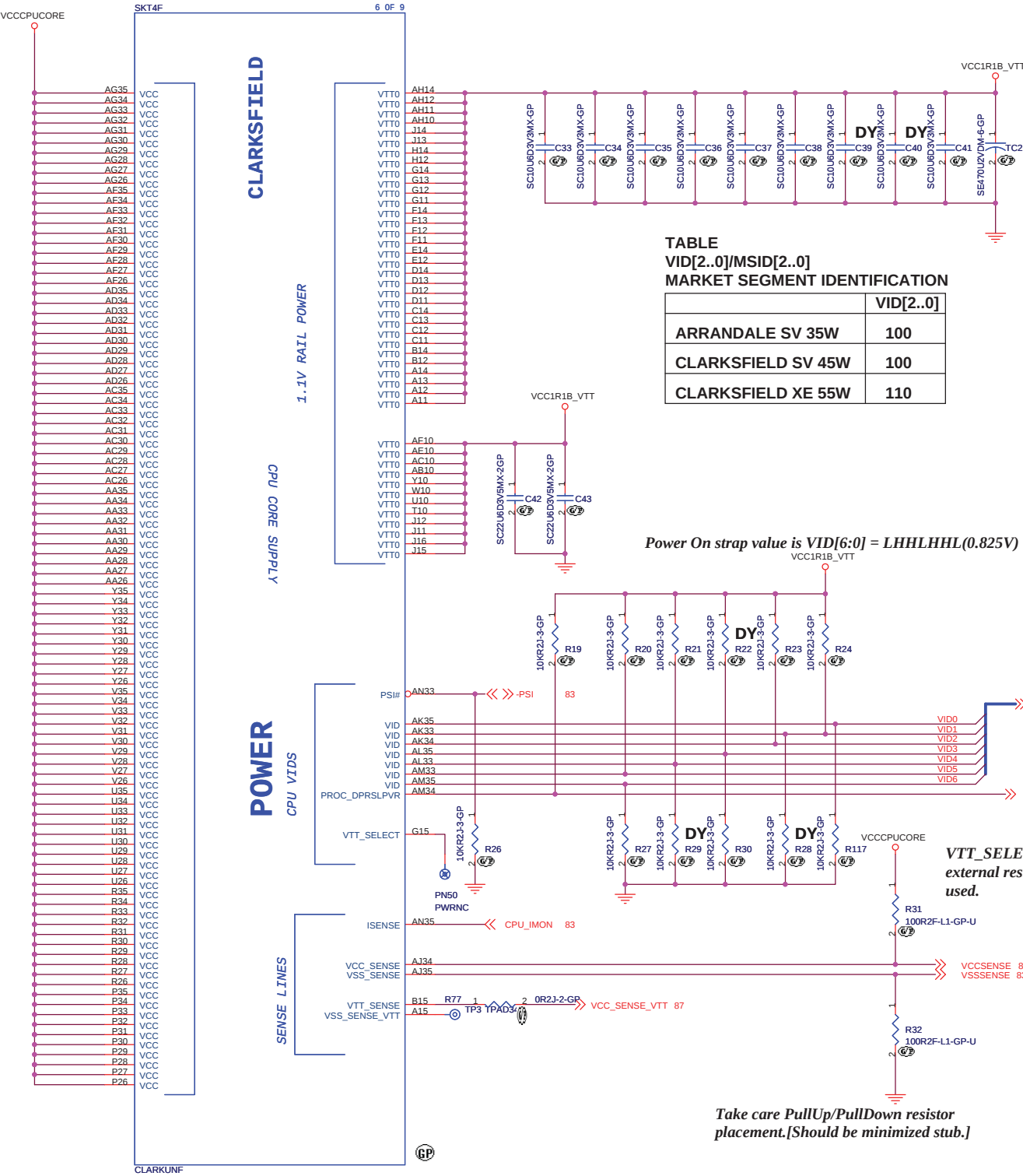


TABLE
VID[2..0]/MSID[2..0]
MARKET SEGMENT IDENTIFICATION

	VID[2..0]
ARRANDALE SV 35W	100
CLARKSFIELD SV 45W	100
CLARKSFIELD XE 55W	110

TABLE
VID[5..3]/CSC[2..0]
CURRENT SENSE CONFIGURATION

IMAX	VID[5..3]
DISABLE	000
20A	001
30A	010
40A	011
50A	100
60A	101
70A	110
80A	111

ARD SV Iccmax=48A
CF SV Iccmax=52A
CF XE Iccmax=65A

Power On strap value is VID[6:0] = LHHLLHHL(0.825V)

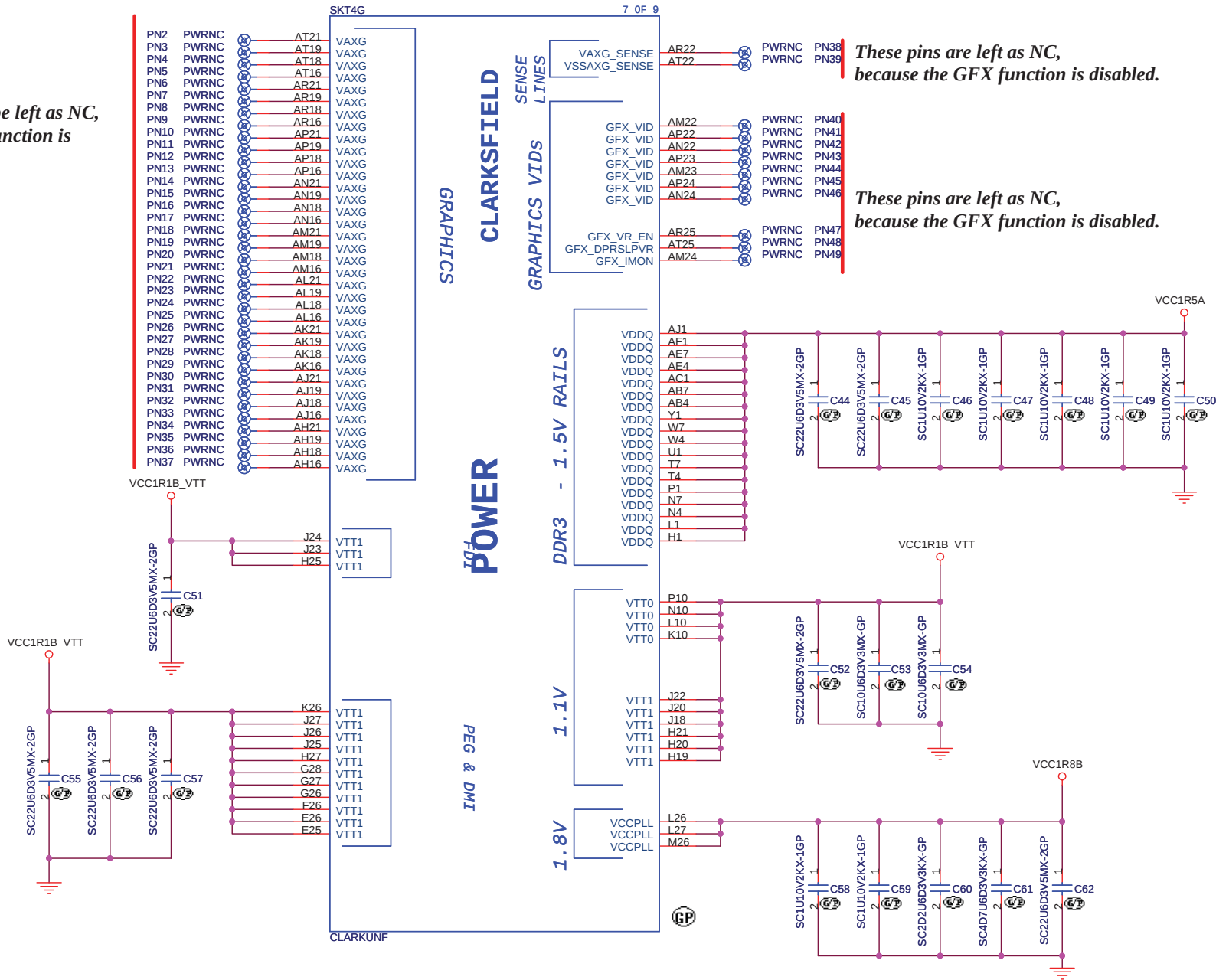
VTT_SELECT [G15]: Because VCC1R05B_VTT is fixed voltage with external resistor in DC/DC circuits, the VTT_SELECT signal is not used.

Take care PullUp/PullDown resistor placement.[Should be minimized stub.]

These pins should be left as NC, because the GFX function is disabled.

*These pins are left as NC,
because the GFX function is disabled.*

*These pins are left as NC,
because the GFX function is disabled.*



<Variant Name>

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Title

CPU(6/7): GFX/PWR

Size

Document Number

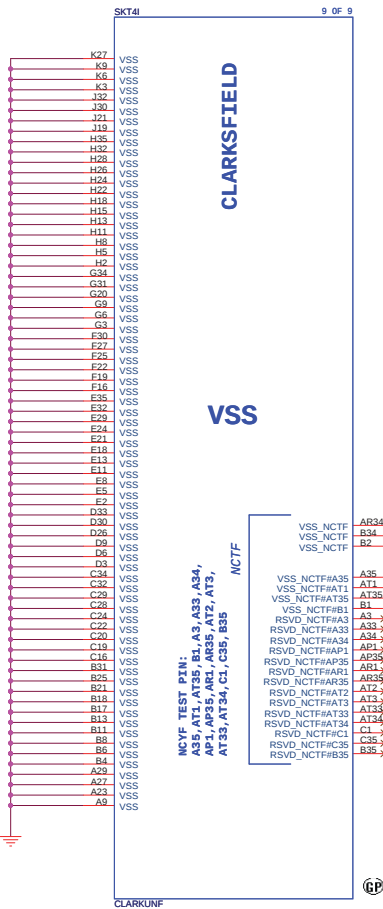
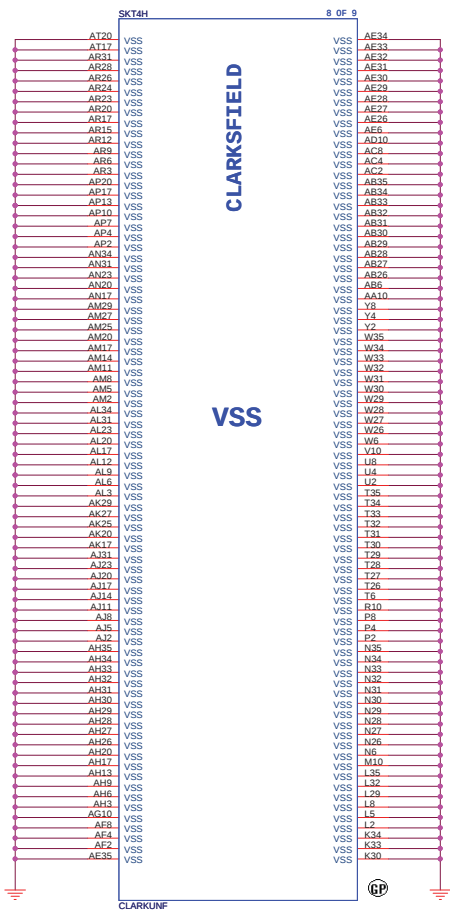
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Rev

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NCVF TEST PIN:
A35, AT1, AT35, B1, A3, A33, A34,
AP1, AP35, AR1, AR35, AT2, AT3,
AT33, AT34, C1, C35, B35

NCTF

VSS_NCTF A35
VSS_NCTF B34
VSS_NCTF B2
VSS_NCTF#A35
VSS_NCTF#AT1
VSS_NCTF#AT35
VSS_NCTF#B1
RSVD_NCTF#A3
RSVD_NCTF#A33
RSVD_NCTF#A34
RSVD_NCTF#AP1
RSVD_NCTF#AP35
RSVD_NCTF#AR1
RSVD_NCTF#AR35
RSVD_NCTF#AT2
RSVD_NCTF#AT3
RSVD_NCTF#AT33
RSVD_NCTF#AT34
RSVD_NCTF#C1
RSVD_NCTF#C35
RSVD_NCTF#B35

For Solder Crack Detection.

AR34
B34
B2
A35
AT1
AT35
B1
TP6 TPAD34
TP7 TPAD34
TP8 TPAD34
TP9 TPAD34

BLANK

<Variant Name>

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Title			
BLANK			
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		E	

XDP Present function does not use.

Short cut usage.

DEBUG Interface for Processor.

Short cut usage.

DEBUG Interface for PCH.

TCK needs external pull up not pull down.

Use these jumper, if pin count is overflow.

I2C Interface does not use.

PCH PIN	REF DES	PCH ES1 JTAG		PCH ES2 JTAG		MP	
		ENABLE	DISABLE	ENABLE	DISABLE	ENABLE	DISABLE
TDO	R40	NO_ASM	NO_ASM	200	NO_ASM	NO_ASM	NO_ASM
	R53	NO_ASM	NO_ASM	100	NO_ASM	NO_ASM	NO_ASM
	R515	NO_ASM	NO_ASM	NO_ASM	NO_ASM	51	NO_ASM
TMS	R37	200	NO_ASM	200	NO_ASM	NO_ASM	NO_ASM
	R51	100	NO_ASM	100	NO_ASM	NO_ASM	NO_ASM
	R510	NO_ASM	NO_ASM	NO_ASM	NO_ASM	51	NO_ASM
TDI	R38	200	20K	200	NO_ASM	NO_ASM	NO_ASM
	R52	100	10K	100	NO_ASM	NO_ASM	NO_ASM
	R506	NO_ASM	NO_ASM	NO_ASM	NO_ASM	51	NO_ASM
TCK	R50	51	51	51	51	51	51
TRST#	R35	20K	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM
	R49	10K	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM
	R498	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM

LOGIC

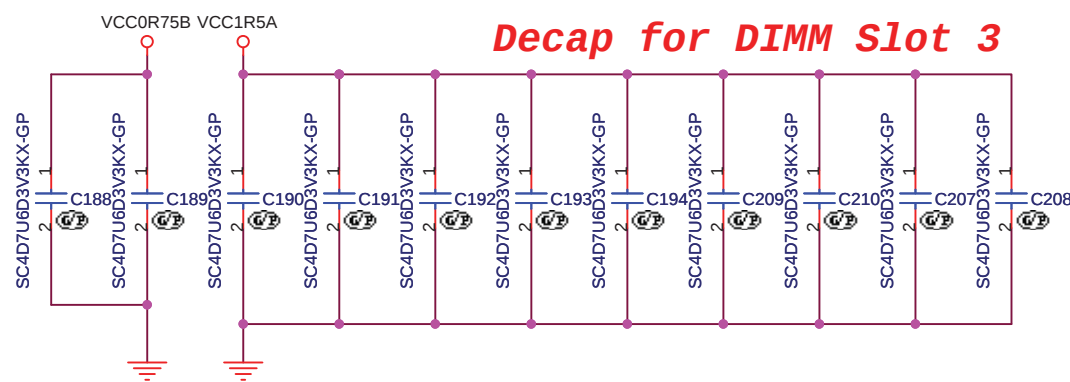
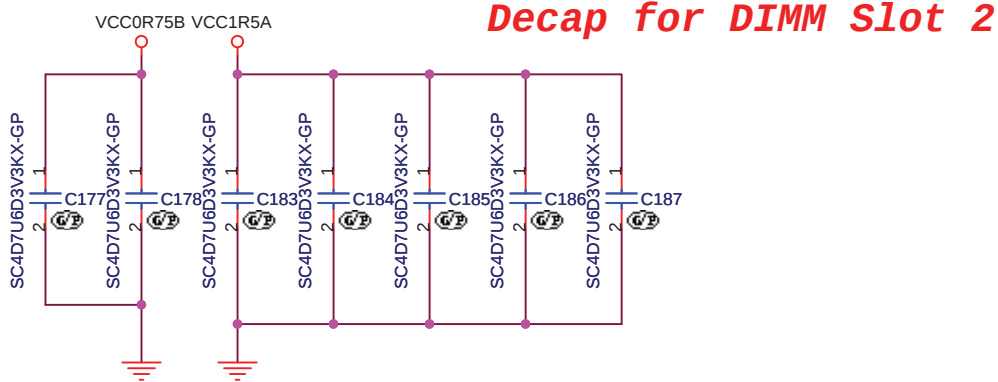
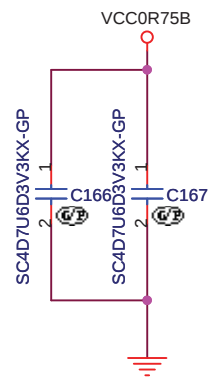
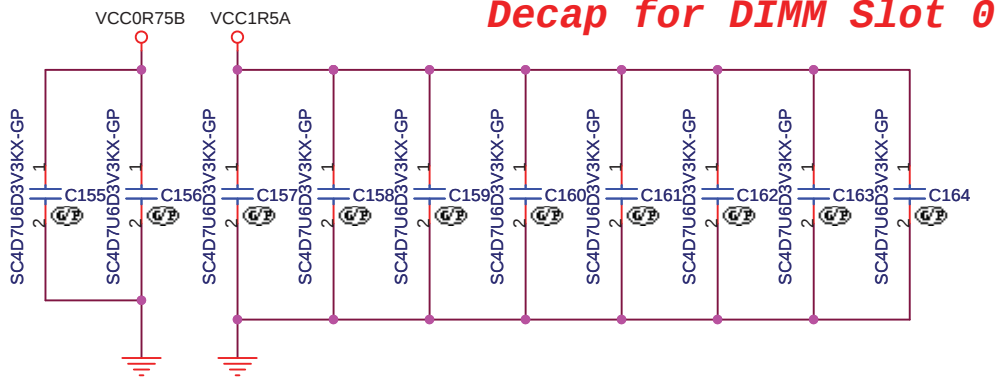
<Variant Name>

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XDP CONNECTOR

Size A2 Document Number Kendo-1 WS Rev SC

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<Variant Name>

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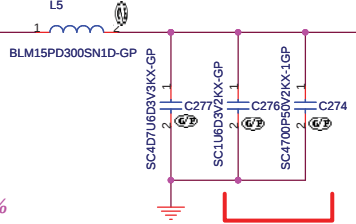
Title
DECAP For DIMMs

Size A4	Document Number Kendo-1 WS	Rev SC
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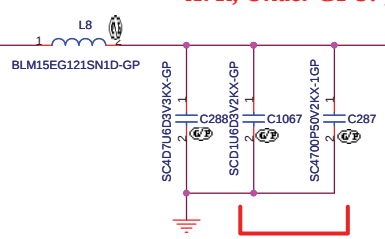
Silego Recommended Conditions:
Normal Frequency: 14.31818MHz.
Frequency Tolerance: +/- 30ppm.
Load Frequency: 20 pF.
Effective Shunt Capacitance: 5pF.

1.8V +/-3%
200mA
VCC1R8VIDEO



X7R, Under GPU.

1.05V +/-3%
200mA
VCC1R05VIDEO_PLL



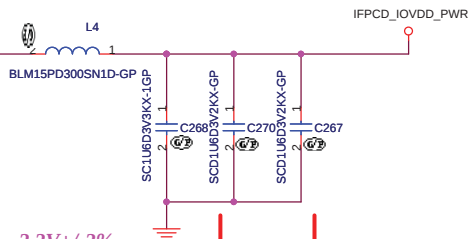
X7R, Under GPU.

28 SYSTEM_DP0P <<> C66 1 2 SCD1U10V2KX-5GP D0P AM7
28 SYSTEM_DP0N <<> C264 1 2 SCD1U10V2KX-5GP D0N AM6
28 SYSTEM_DP1P <<> C265 1 2 SCD1U10V2KX-5GP D1P AL5
28 SYSTEM_DP1N <<> C266 1 2 SCD1U10V2KX-5GP D1N AM5
28 SYSTEM_DP2P <<> C367 1 2 SCD1U10V2KX-5GP D2P AM3
28 SYSTEM_DP2N <<> C368 1 2 SCD1U10V2KX-5GP D2N AM4
28 SYSTEM_DP3P <<> C369 1 2 SCD1U10V2KX-5GP D3P AP1
28 SYSTEM_DP3N <<> C516 1 2 SCD1U10V2KX-5GP D3N AR2

IFPCD_IOVDD_PWR
IFPCD_PLLVDD_PWR

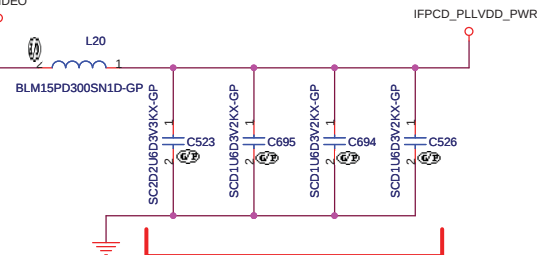
28 SYSTEM_DP_AUXP <<> AP2
28 SYSTEM_DP_AUXN <<> AN3

1.05V +3%
500mA
VCC1R05VIDEO



3.3V +/-3%
200mA
VCC3VIDEO

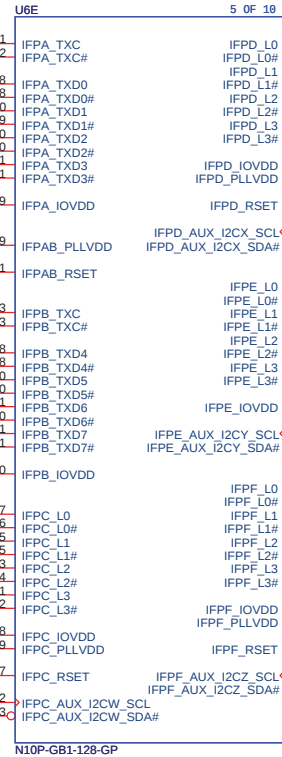
Under GPU.



Under GPU.

25 TXCLK_LP <<> AM11
25 TXCLK_LN <<> AM12
25 TXOUT_L0P <<> AM8
25 TXOUT_L0N <<> AL8
25 TXOUT_L1P <<> AM10
25 TXOUT_L1N <<> AM9
25 TXOUT_L2P <<> AL10
25 TXOUT_L2N <<> AL10
25 TXOUT_L3P <<> AK11
25 TXOUT_L3N <<> AL11

25 TXCLK_UP <<> AP13
25 TXCLK_UN <<> AN13
25 TXOUT_U0P <<> AN8
25 TXOUT_U0N <<> AP8
25 TXOUT_U1P <<> AP10
25 TXOUT_U1N <<> AN10
25 TXOUT_U2P <<> AR10
25 TXOUT_U2N <<> AP11
25 TXOUT_U3P <<> AP11
25 TXOUT_U3N <<> AP11



AR6 DOCKA_DP0P C C286 1 2 SCD1U10V1KX-GP DOCKA_DP0P 64
AR7 DOCKA_DP0N C C289 1 2 SCD1U10V1KX-GP DOCKA_DP0N 64
AP7 DOCKA_DP1P C C1066 1 2 SCD1U10V1KX-GP DOCKA_DP1P 64
AN7 DOCKA_DP1N C C1068 1 2 SCD1U10V1KX-GP DOCKA_DP1N 64
AN5 DOCKA_DP2P C C1069 1 2 SCD1U10V1KX-GP DOCKA_DP2P 64
AP5 DOCKA_DP2N C C1070 1 2 SCD1U10V1KX-GP DOCKA_DP2N 64
AR5 DOCKA_DP3P C C1071 1 2 SCD1U10V1KX-GP DOCKA_DP3P 64
AR4 DOCKA_DP3N C C1072 1 2 SCD1U10V1KX-GP DOCKA_DP3N 64

IFPD_IOVDD
IFPD_PLLVDD
IFPD_RSET
IFPD_AUX_I2CX_SCL
IFPD_AUX_I2CX_SDA#

AH6 DOCKB_DP0P C C1073 1 2 SCD1U10V1KX-GP DOCKB_DP0P 64
AH5 DOCKB_DP0N C C1074 1 2 SCD1U10V1KX-GP DOCKB_DP0N 64
AH4 DOCKB_DP1P C C1075 1 2 SCD1U10V1KX-GP DOCKB_DP1P 64
AH4 DOCKB_DP1N C C1076 1 2 SCD1U10V1KX-GP DOCKB_DP1N 64
AE4 DOCKB_DP2P C C1077 1 2 SCD1U10V1KX-GP DOCKB_DP2P 64
AE5 DOCKB_DP2N C C1078 1 2 SCD1U10V1KX-GP DOCKB_DP2N 64
AE6 DOCKB_DP3P C C1079 1 2 SCD1U10V1KX-GP DOCKB_DP3P 64
AE5 DOCKB_DP3N C C1080 1 2 SCD1U10V1KX-GP DOCKB_DP3N 64

IFPE_IOVDD
IFPE_PLLVDD
IFPE_RSET
IFPE_AUX_I2CX_SCL
IFPE_AUX_I2CX_SDA#

AL2 X
AL3 X
AJ3 X
AJ2 X
AJ1 X
AH1 X
AH2 X
AH3 X

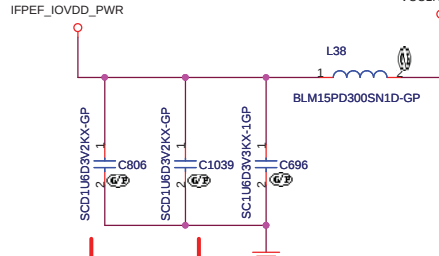
AD7
AJ6
AL1 R98 1 2 1KR2F-3-GP
AE3
AE2

IFPEF_IOVDD_PWR
IFPEF_PLLVDD_PWR

IFPEF_RSET
IFPEF_AUX_I2CX_SCL
IFPEF_AUX_I2CX_SDA#

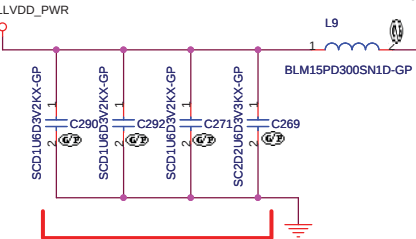
IFPEF_AUX_I2CW_SCL
IFPEF_AUX_I2CW_SDA#

1.05V +3%
500mA
VCC1R05VIDEO

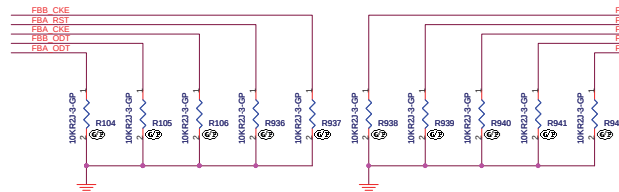
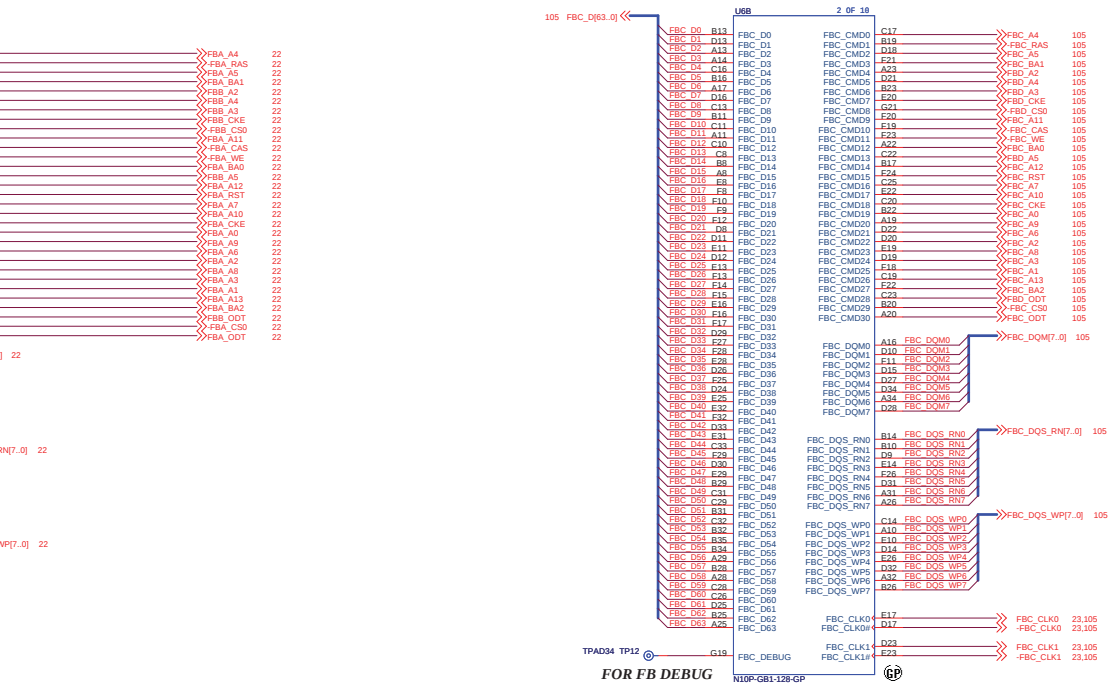
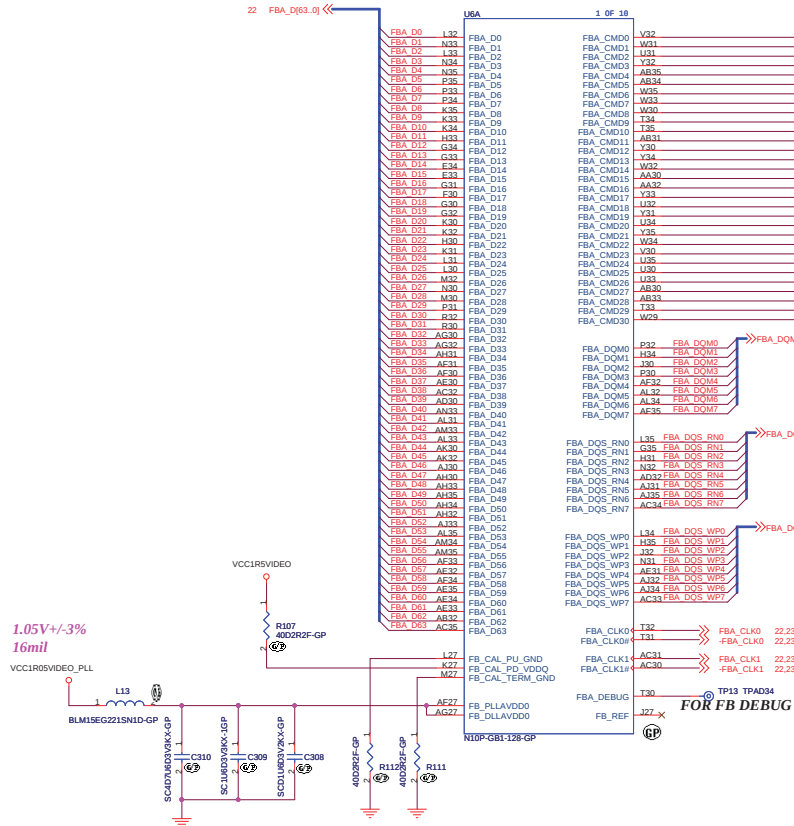


Under GPU.

3.3V +/-3%
200mA
VCC3VIDEO



Under GPU.

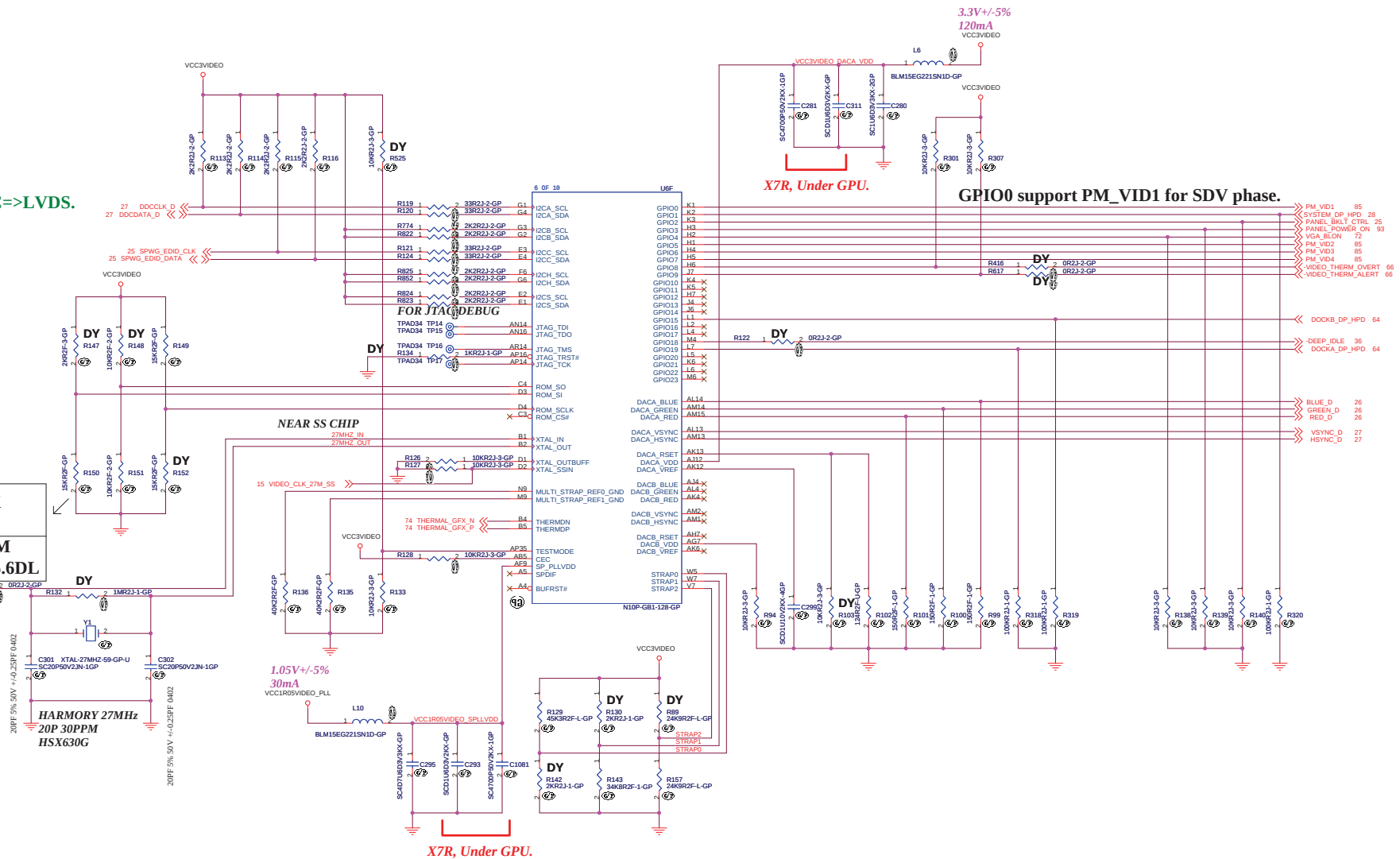


<Core Design>

I2CA=>CRT, I2CC=>LVDS.

TABLE
KENDO VIDEO MEMORY

	SAMSUNG 0011	HYNIX 0010
R150	20KOHM 64.20025.6DL	15KOHM 64.15025.6DL



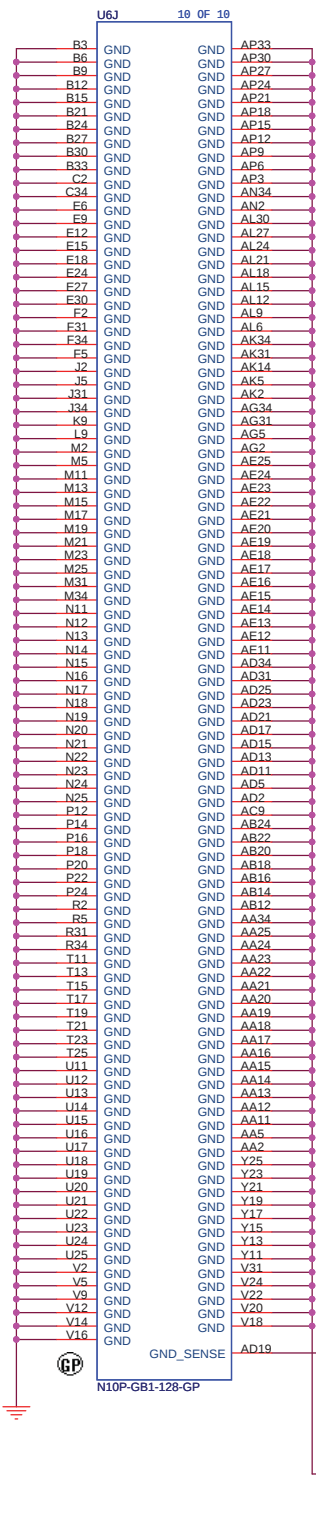
**TABLE
KENDO NIVIDA**

	VENDOR P/N	WISTRON P/N
1	N10P-GLM-GB1-128 (40nm) Workstation	71.0N10P.A2U
2	N10P-GS-GB1-128 (40nm) Discrete	71.0N10P.B2U

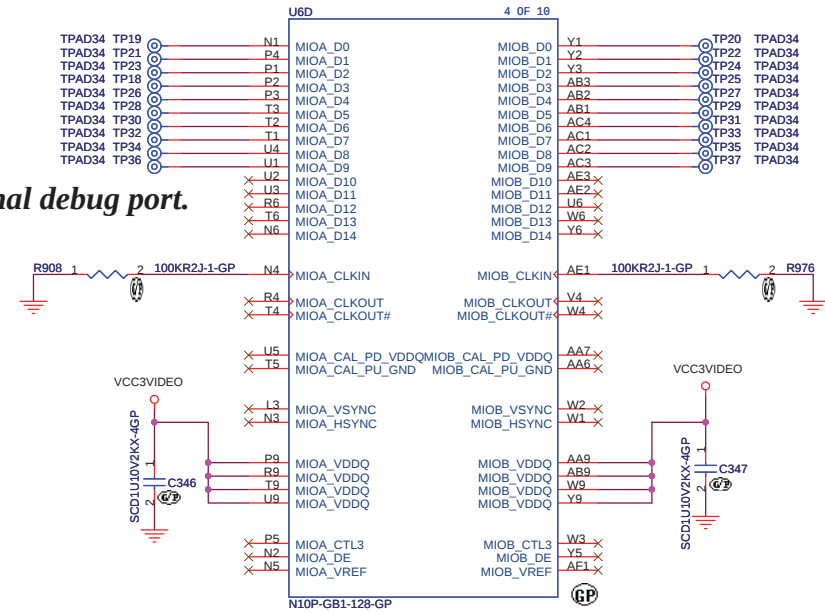
PCI_DEVID Straps

	N10P/M-GS	N10P-GLM
R89	NO_ASM	ASM
R157	ASM	NO_ASM

LOGIC



MIOA_D0-D9 and MIOB_D0-D9 are optional debug port.



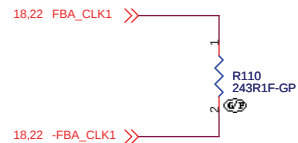
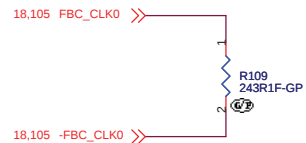
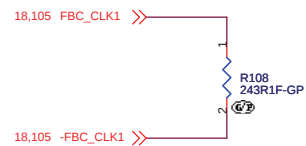
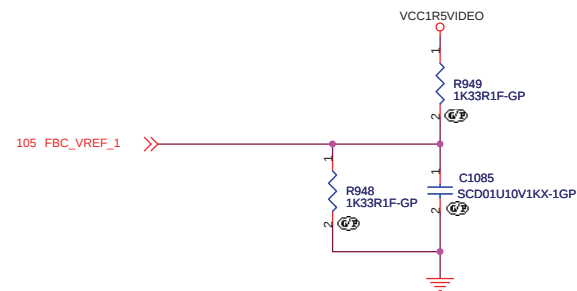
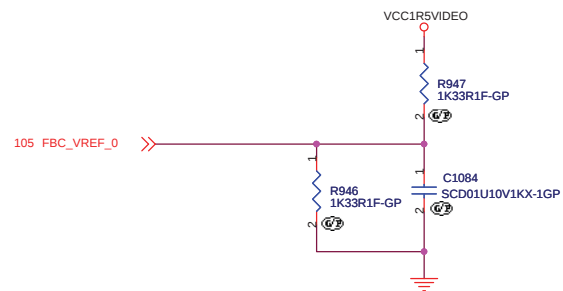
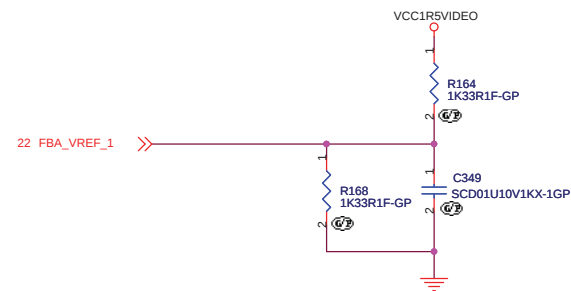
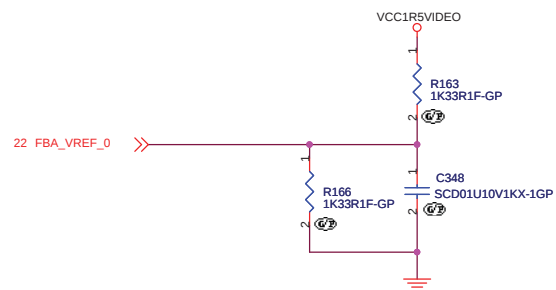
<Core Design>

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Title N10P-GS/GLM (6/6): GND

Size A3 Document Number Kendo-1 WS Rev SC

Date: Tuesday, May 05, 2009 Sheet 21 of 109



FBCLK Termination

<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title DYNAMIC MEMORY TERMINATION		
Size A3	Document Number Kendo-1 WS	Rev SC
Date: Tuesday, May 05, 2009		
Sheet 23 of 109		

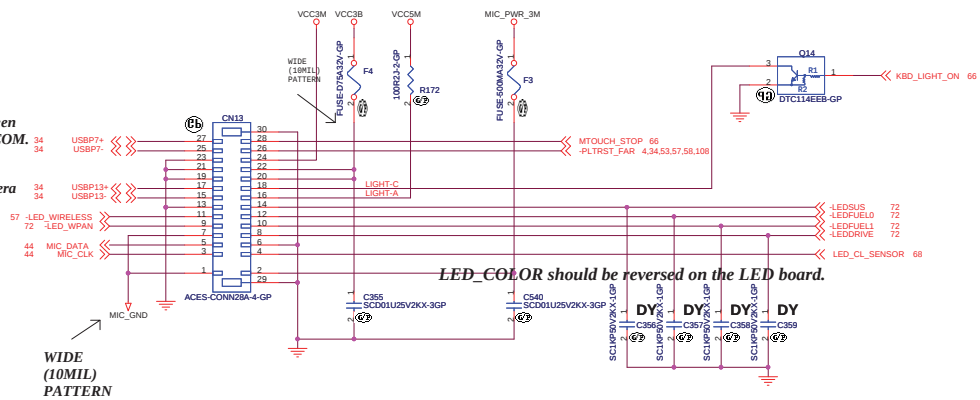
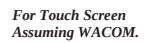
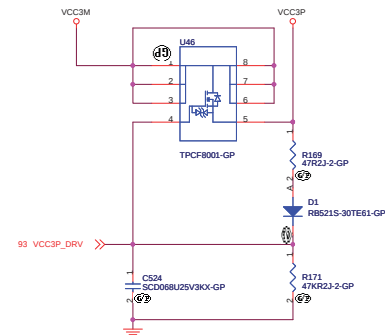
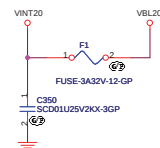
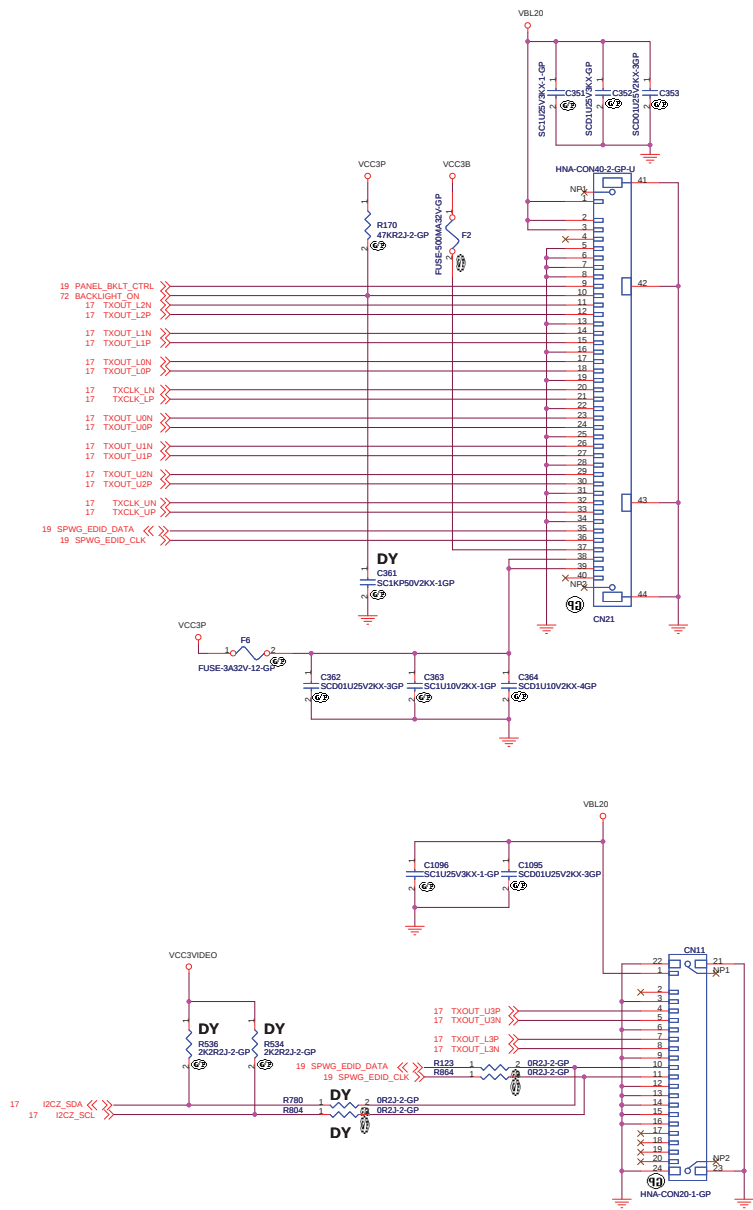
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<Variant Name>

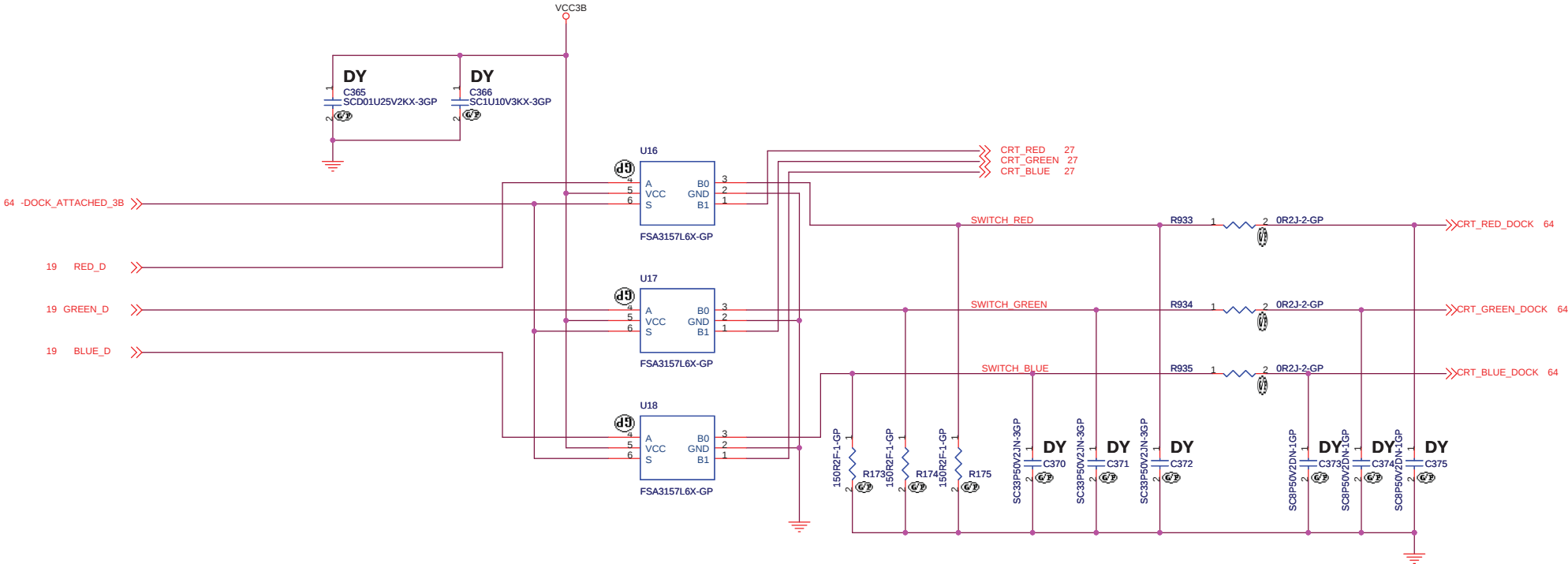
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		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title BLANK			
Size A3	Document Number Kendo-1 WS		Rev SC
Date: Tuesday, May 05, 2009		Sheet 24	of 109

E

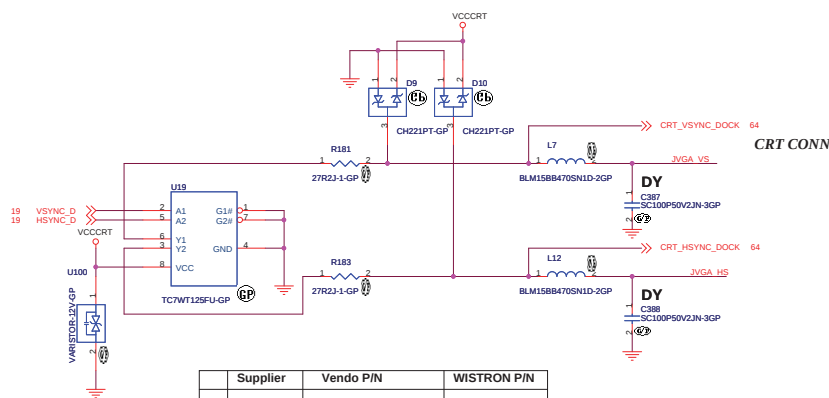
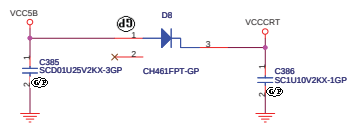
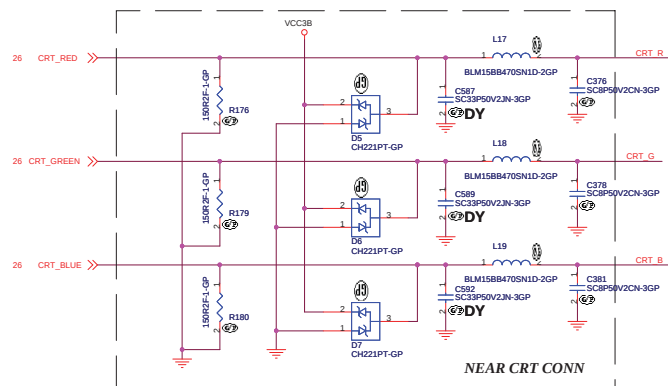
LCD / Inverter Connector



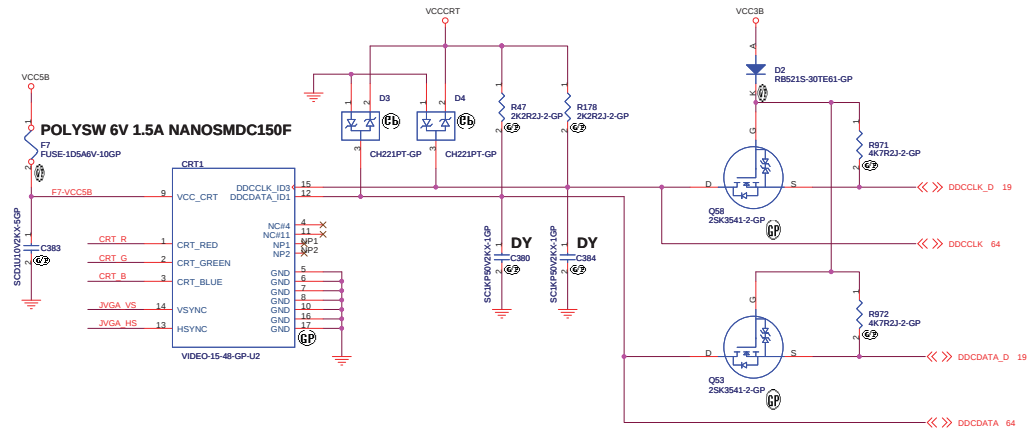
LED_COLOR should be reversed on the LED board.



	Supplier	Vendo P/N	WISTRON P/N
1	FAIRCHILD	FSA3157L6X-GP	73.03157.G0H
2	ONSEMI	NLASB3157MTR2G	73.03157.003
3	TI	SN74LVC1G3157DRYR	73.03157.H0H



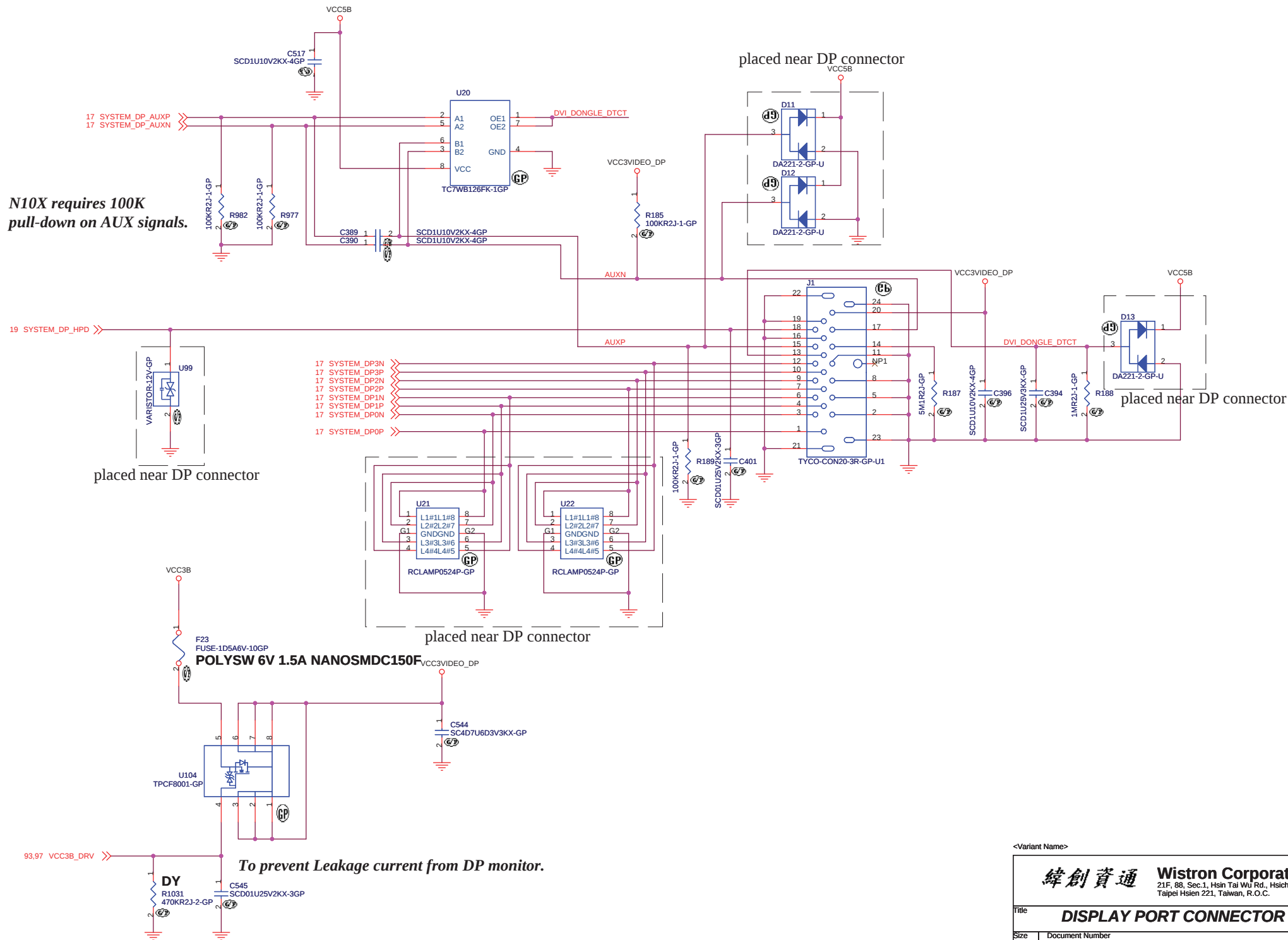
	Supplier	Vendo P/N	WISTRON P/N
1	TOSHIBA	TC7WT125FU-GP	73.7W125.007
2	TI	SN74LVC2G125DCTR	73.2G125.007

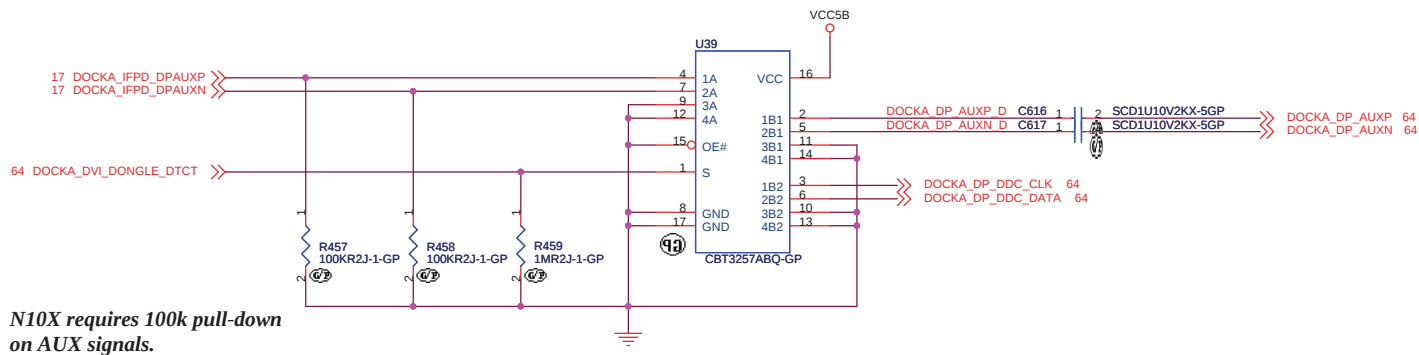


To avoid leak current from a monitor.

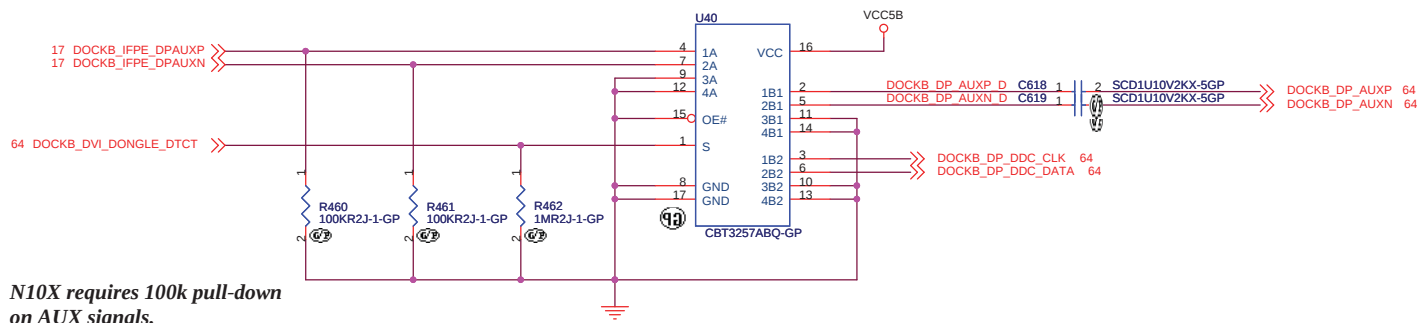
<Variant Name>

緯創資通 Wistron Corporation 21F, 8th, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
EXT CRT INTERFACE			
File	Document Number	Kendo-1 WS	Rev SC
Size A2	Document Number	Kendo-1 WS	Rev SC
Date: Tuesday, May 05, 2009	Sheet 27	of	109





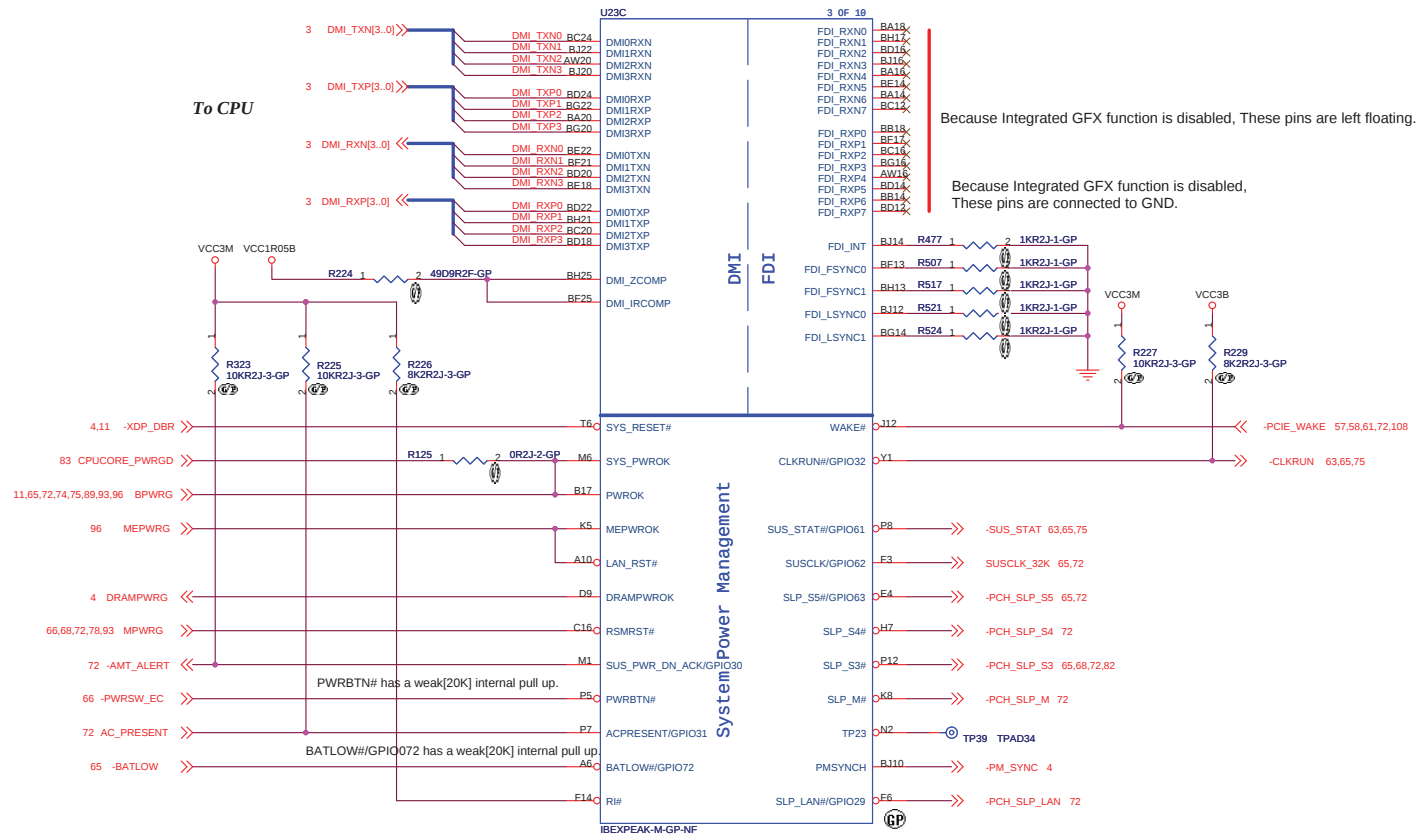
		U27, U39, U40	Wistron part number
1	NXP	CBT3257ABQ-GP	73.03257.A03
2	OnSemi	74FST3257MNTWG-GP	73.5A121.00H



INPUTS		INPUT/OUTPUT	FUNCTION
		A	
L	L	B1	A port = B1 port
L	H	B2	A port = B2 port
H	X	Z	Disconnect

<Variant Name>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title		
DISPLAY PORT MUX		
Size A3	Document Number	Rev SC
Date: Tuesday, May 05, 2009	Sheet 29 of 109	Kendo-1 WS



<Variant Name>

緯創資通 Wistron Corporation
21F, 86, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title		
PCH (3/8):DMI/FDI/PM		
Size	Document Number	Rev
Custom	Kendo-1 WS	SC
Date: Tuesday, May 05, 2009		Sheet 32 of 109

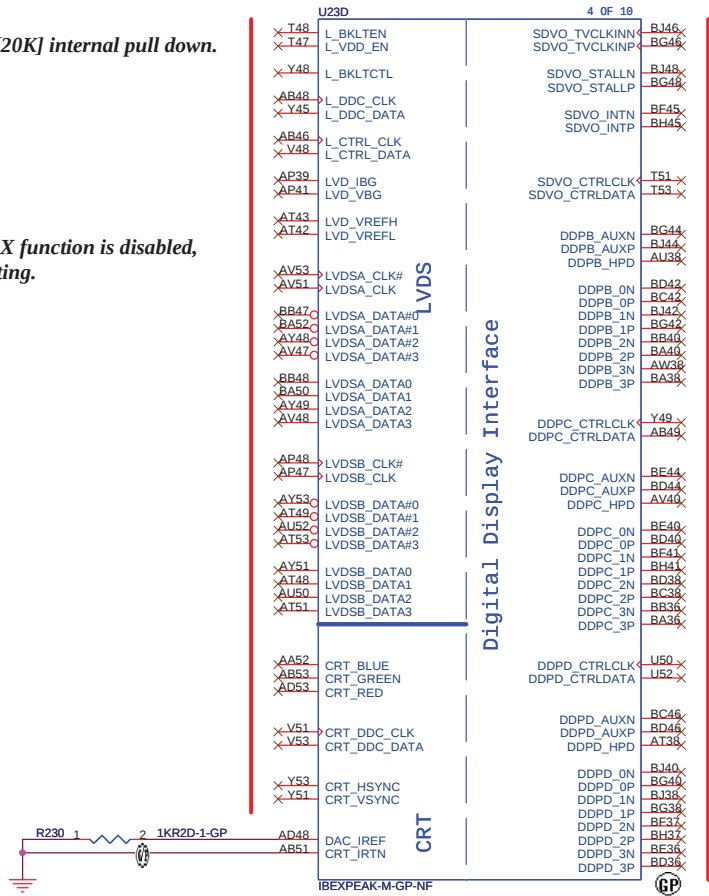
L_DDC_DATA has a weak[20K] internal pull down.

Because Integrated GFX function is disabled,
These pins are left floating.

SDVO_CTRLCLK/SDVO_CTRLCLK has a weak[20K] internal pull down.

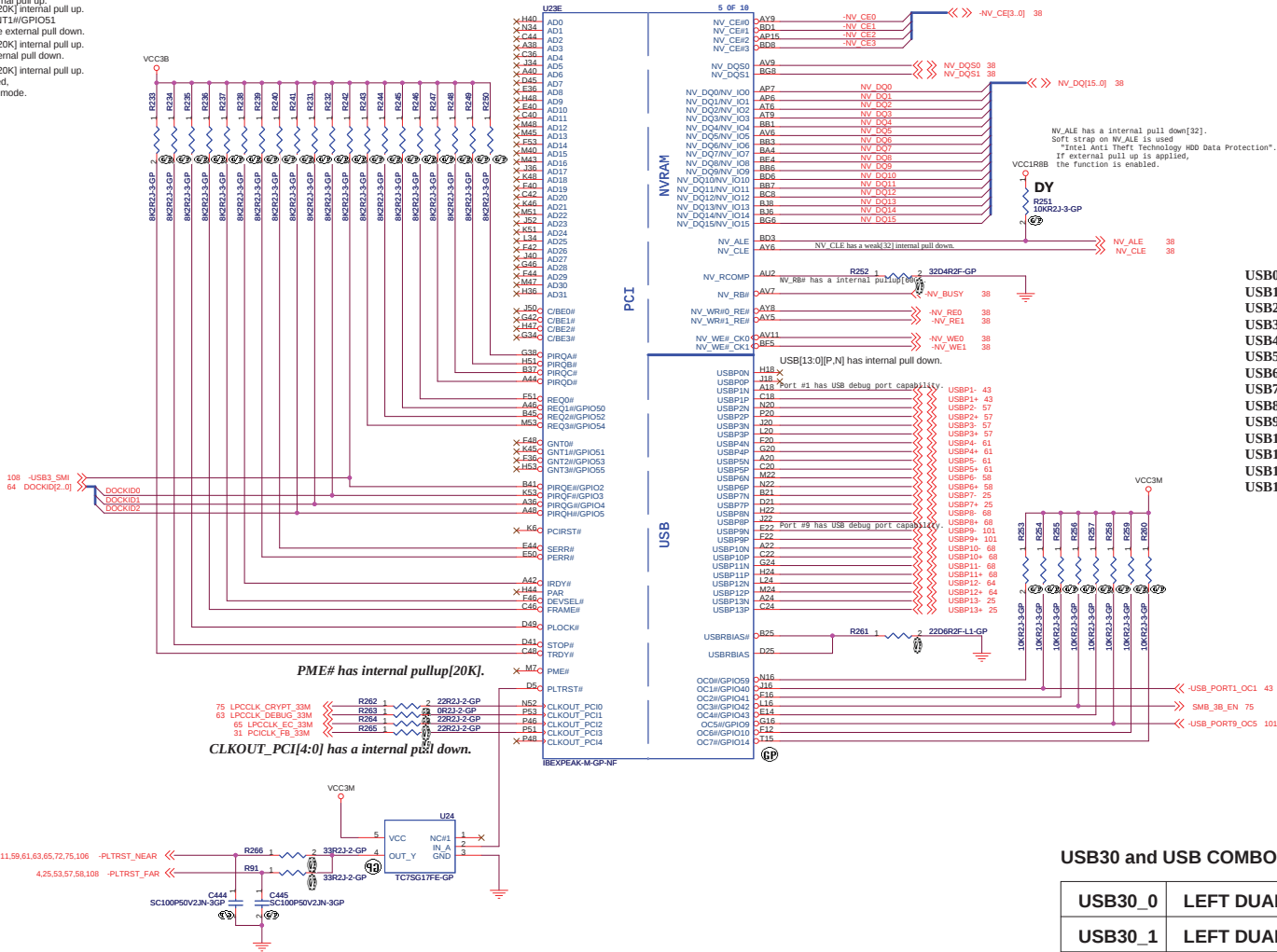
Because Integrated GFX function is disabled,
These pins are left floating.

DDP[D:C]_CTRLCLK has a weak[10K] internal pull down.
DDP[D:C]_CTRLDATA has a weak[20K] internal pull down.



Because PCI IF is not used, ADx.C/BEx and GNTx are left as NC.

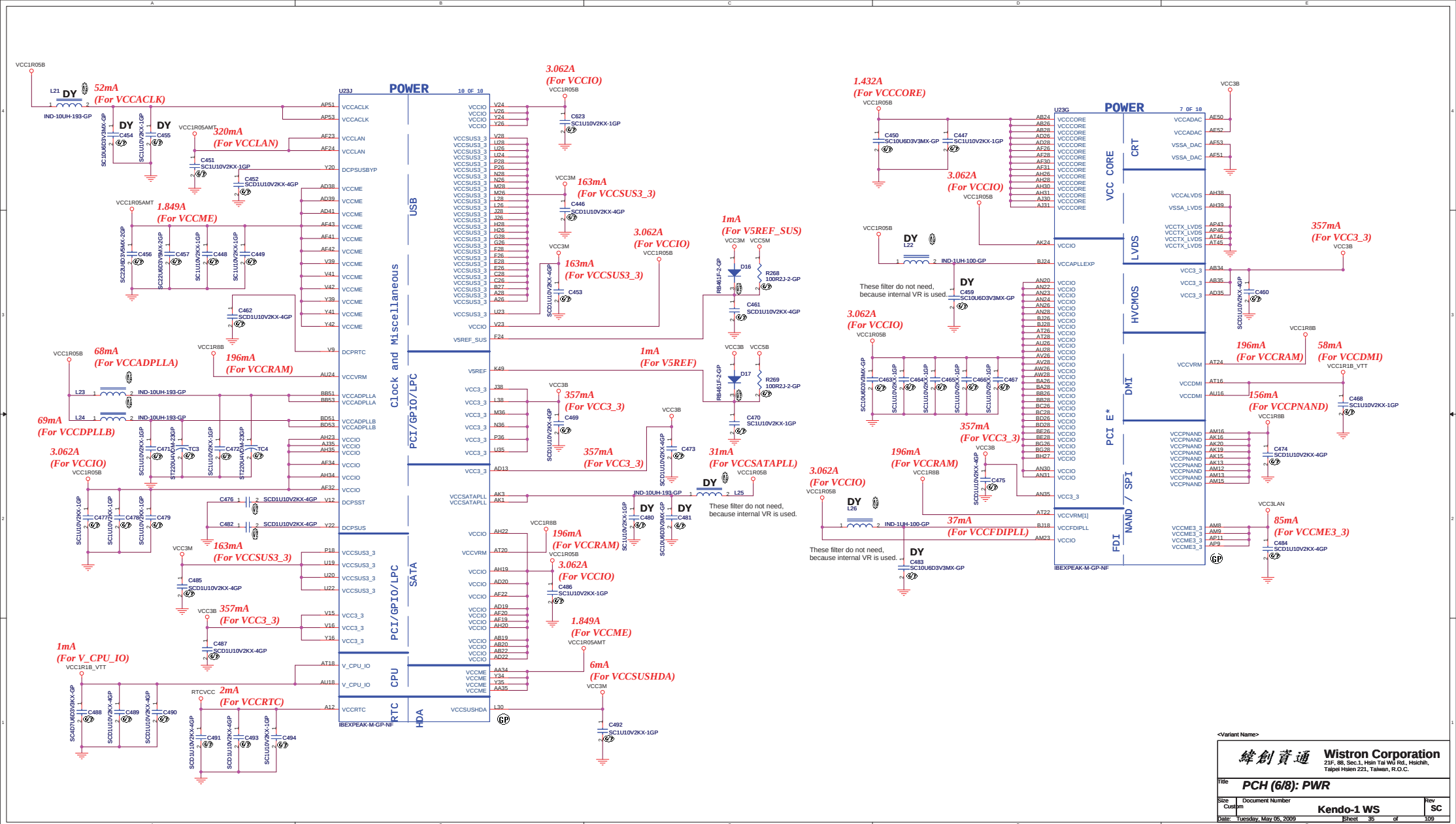
GNT0# has a weak[20K] internal pull up.
GNT1#/GPIO51 has a weak[20K] internal pull up.
To use SPI IF flash BIOS, GNT1#/GPIO51
and GNT0# should not place external pull down.
GNT2#/GPIO53 has a weak[20K] internal pull up.
This pin should not have external pull down.
GNT3#/GPIO55 has a weak[20K] internal pull up.
If external pull down is applied,
PCH will be "topblock swap" mode.

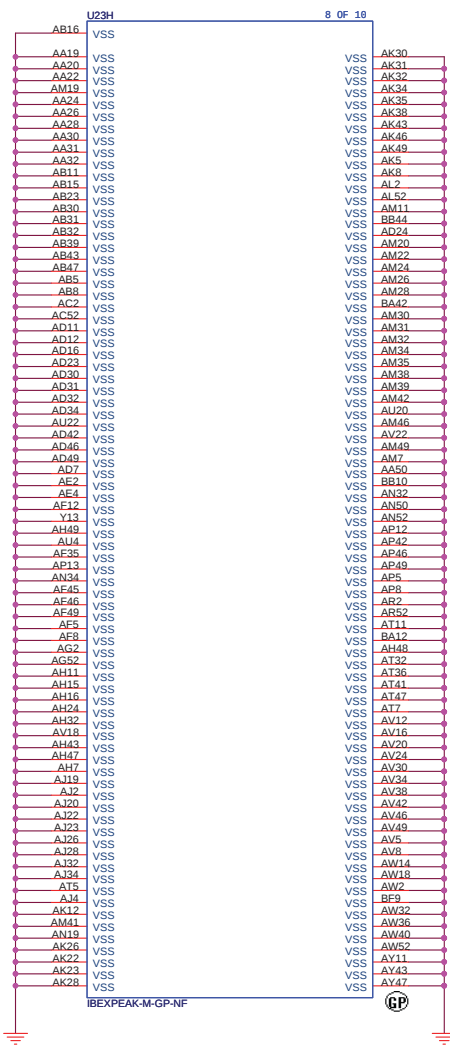
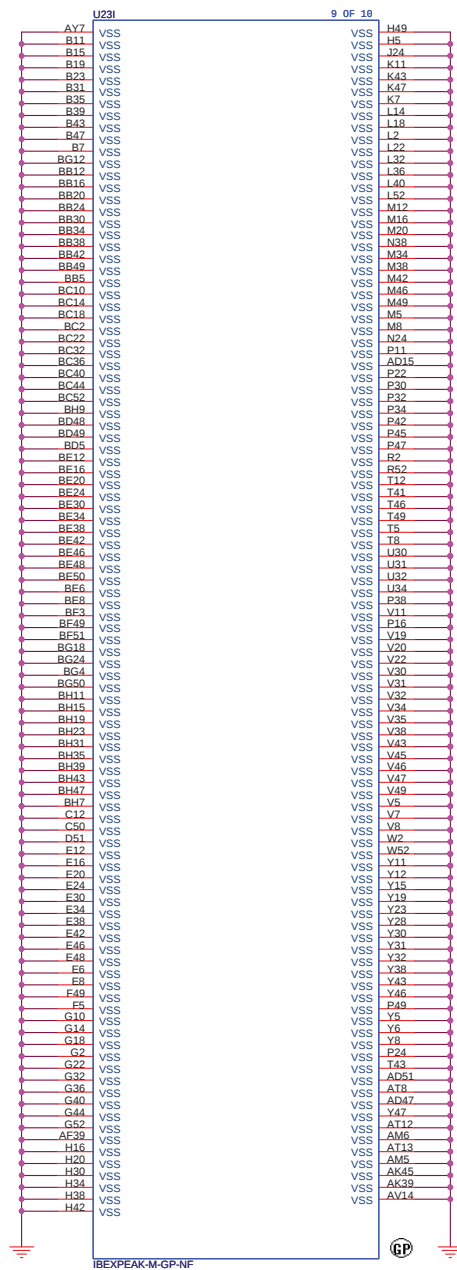


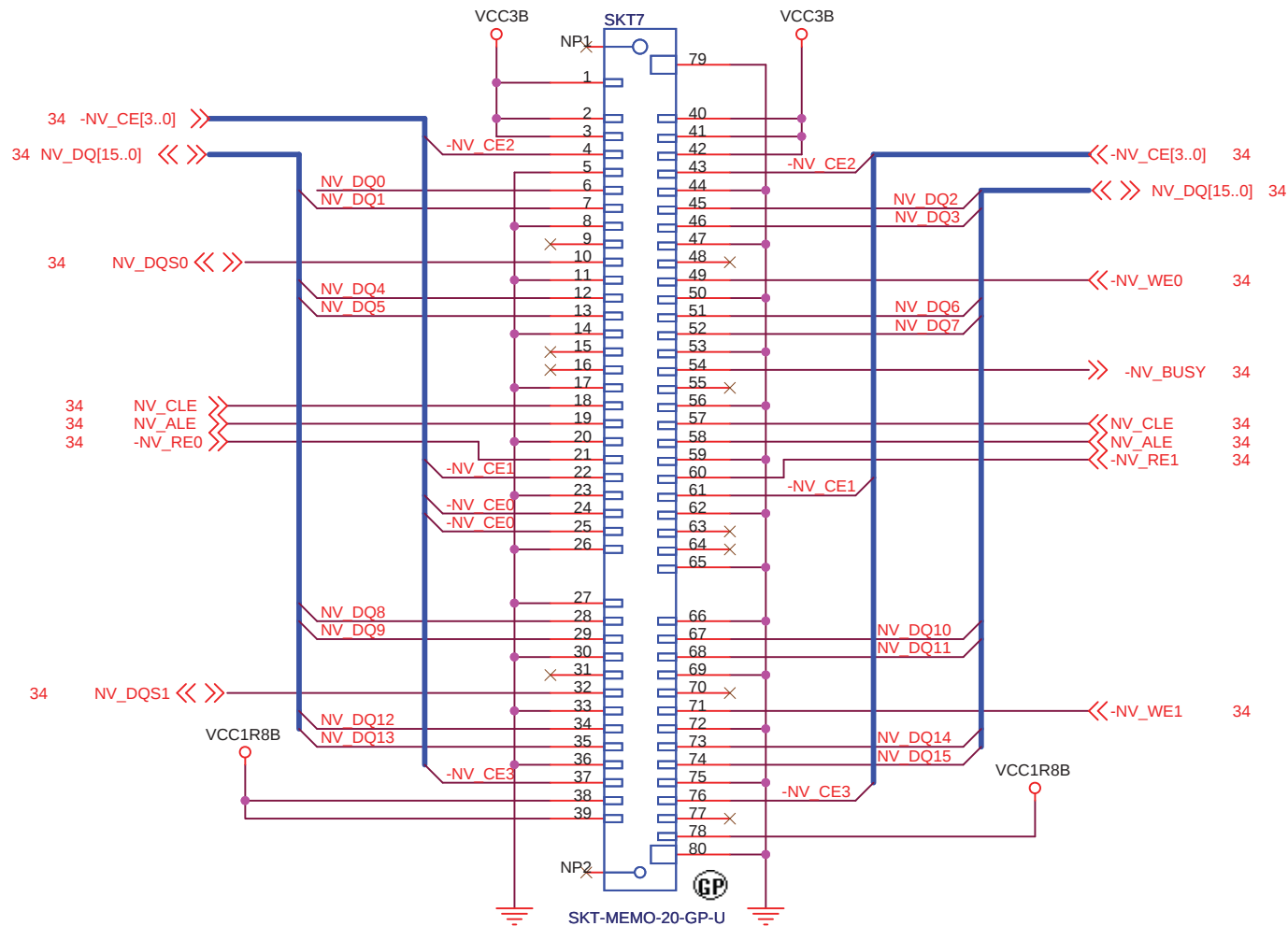
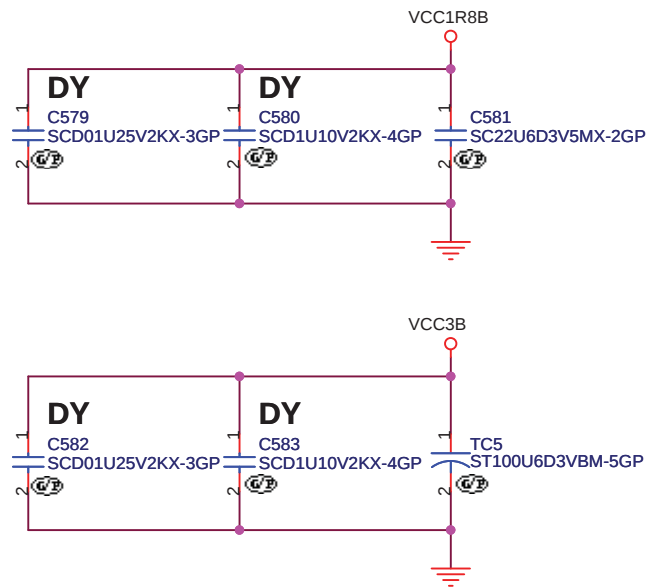
- USB0 :Reserved
USB1 : To System onboard USB port(eSATA combo)
USB2 : To WiMAX/WLAN Mini Card Slot
USB3 : To WWAN Mini Card Slot
USB4 : To Smart Card I/F
USB5 : To Express Card Slot
USB6 : To WUSB Mini Card Slot
USB7 : To Touch Screen
USB8 : To Color Sensor
USB9 : To System Subcard USB port
USB10 : To FPR
USB11: To Bluetooth
USB12: To Docking
USB13: To Camera

USB30 and USB COMBO Table

USB30_0	LEFT DUAL CONN	USB30_ON0	-USB30_OC0
USB30_1	LEFT DUAL CONN	USB30_ON1	-USB30_OC1
USBP1	LEFT COMBO CONN	USB_ON1	-USB_PORT1_OC1
USBP9	RIGHT SUB CARD	USB_ON2	-USB_PORT9_OC5







<Variant Name>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

TURBO MEMORY SLOT

Size
A4

Document Number

Kendo-1 WS

Rev
SC

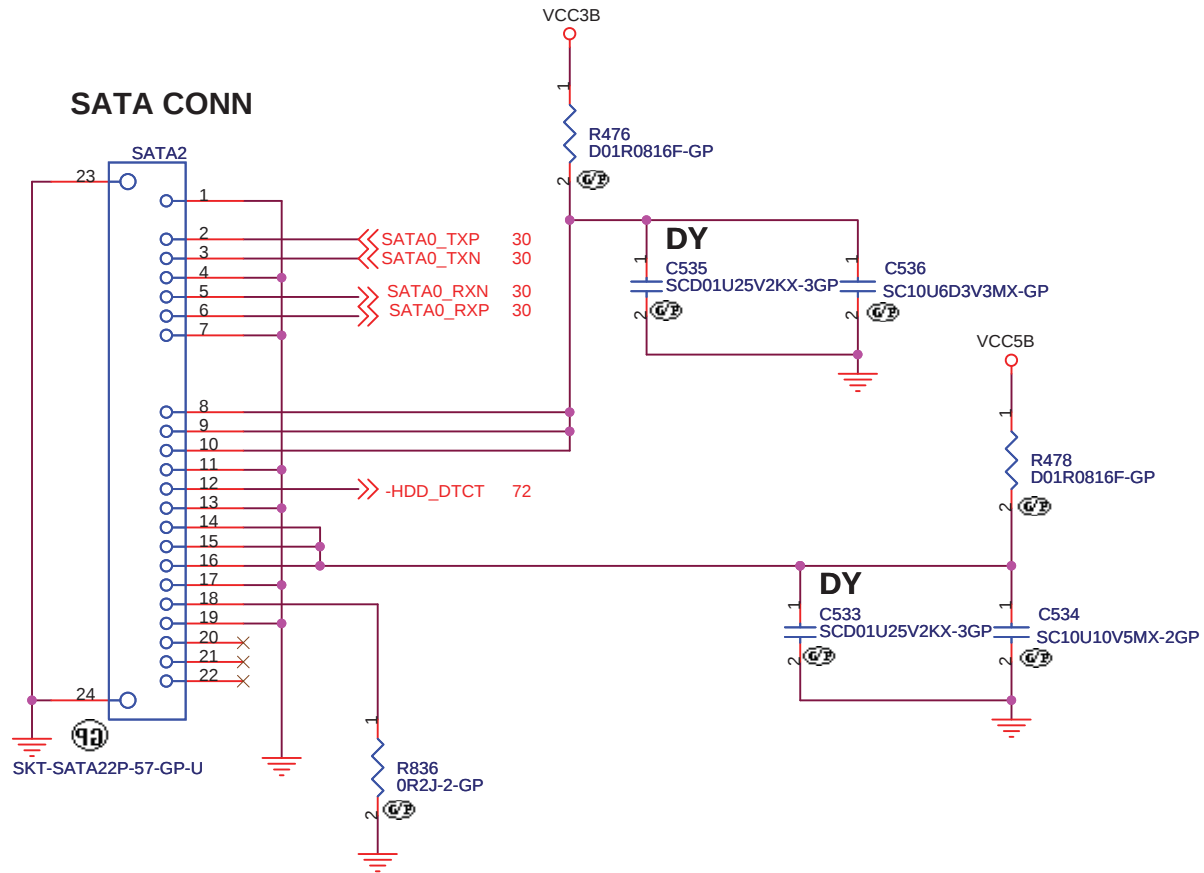
Date: Tuesday, May 05, 2009

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<Variant Name>

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Title BLANK			
Size A3	Document Number Kendo-1 WS		Rev SC
Date: Tuesday, May 05, 2009	Sheet E	39 of	109



<Variant Name>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

SATA HDD CONN

Size
A4

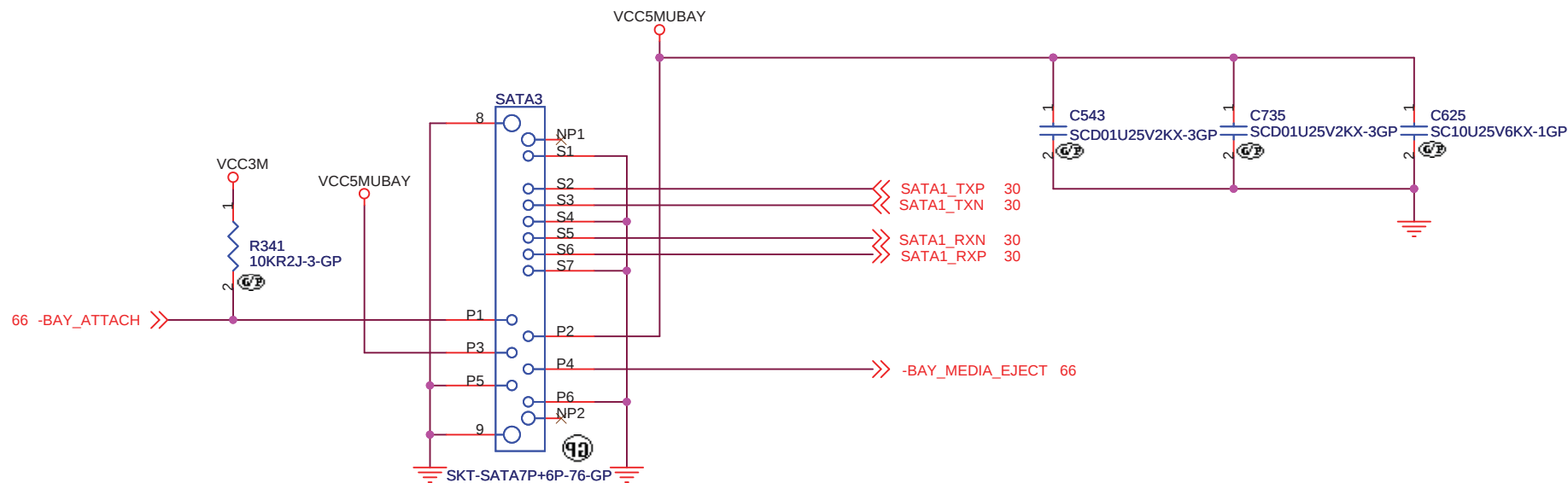
Document Number

Kendo-1 WS

Rev
SC

Date: Tuesday, May 05, 2009

Sheet 40 of 109



<Variant Name>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

SATA BAY I/F CONN

Size
A4

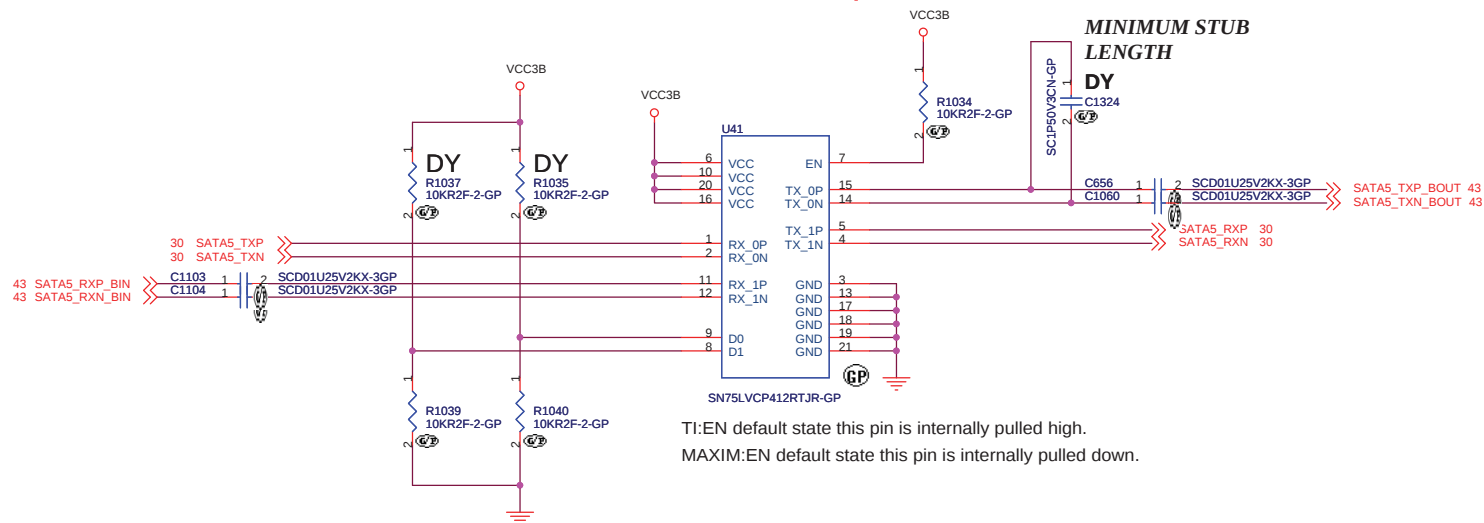
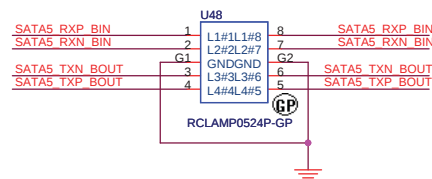
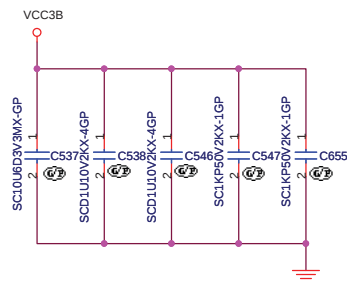
Document Number

Kendo-1 WS

Rev
SC

Date: Tuesday, May 05, 2009

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TABLE

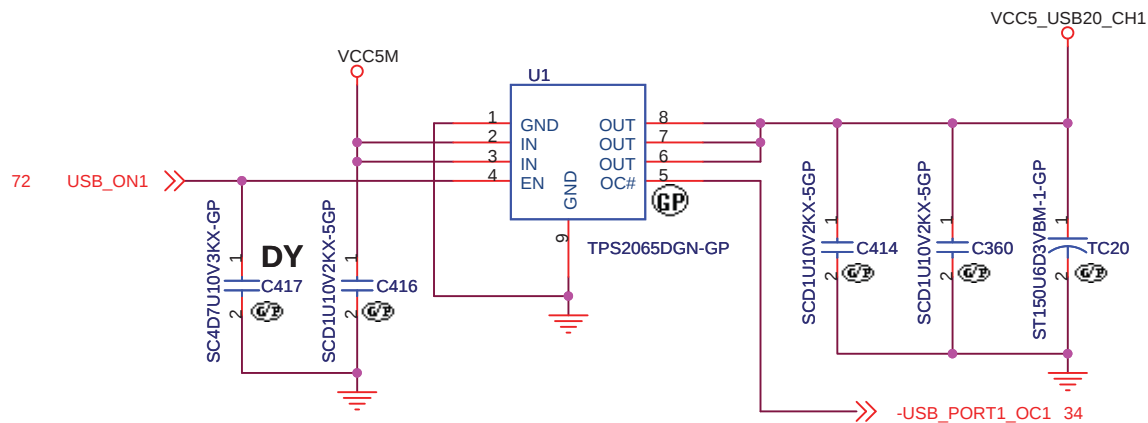
EN	D0	D1	CH - 0	CH - 1
0	X	X	STANDBY	STANDBY
1	0	0	STANDARD	STANDARD
1	1	0	BOOST	STANDARD
1	0	1	STANDARD	BOOST
1	1	1	BOOST	BOOST

← LOGIC

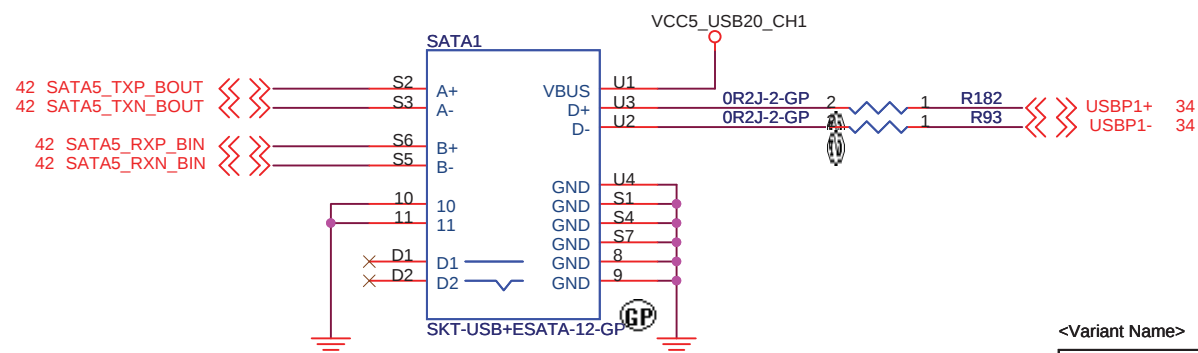
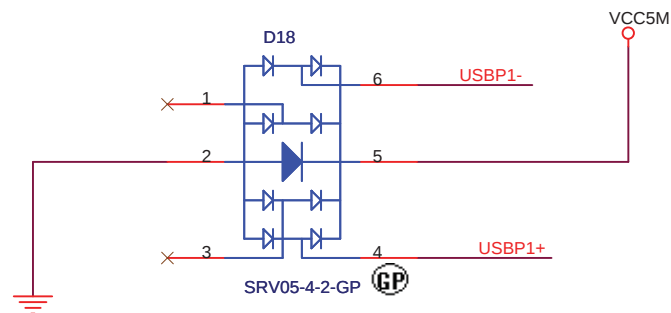
		U41	Wistron part number
1	TI	SN75LVCP412RTJR-GP	71.75412.003
2	MAXIM	MAX4951CTP-T-GP	71.04951.A03

<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title ESATA CONNECTOR	
Size A3	Document Number Kendo-1 WS
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Rev SC	



	Supplier	Vendo P/N	WISTRON P/N
1	TI	TPS2065DGN-GP	74.02065.079
2	TI	TPS2065DGN-1-GP	74.02065.A79
3	ROHM	BD8014FVJ-GP	74.08014.07G



eSATA Combo

<Variant Name>

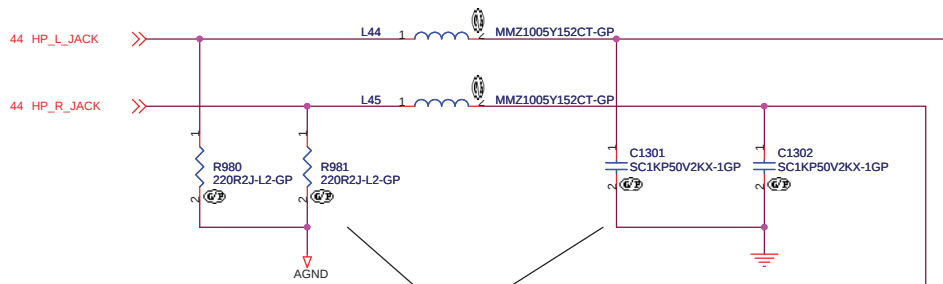
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
USB POWER/ CONN	
Size	Document Number
A4	Kendo-1 WS
Date:	Rev
Tuesday, May 05, 2009	SC
Sheet	43 of 109

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<Variant Name>

緯創資通		Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title			
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Size	Document Number		Rev
A3	Kendo-1 WS		SC
Date:	Tuesday, May 05, 2009		Sheet 45 of 109

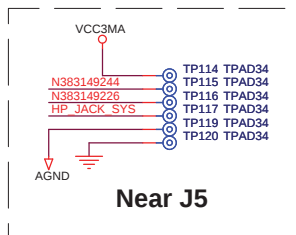
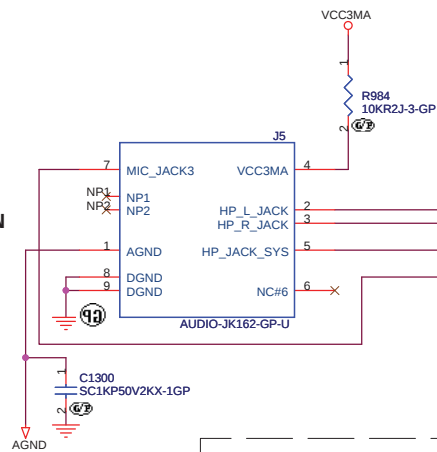
NEAR HEADPHONE CONN



WIDE AND SHORT PATTERN

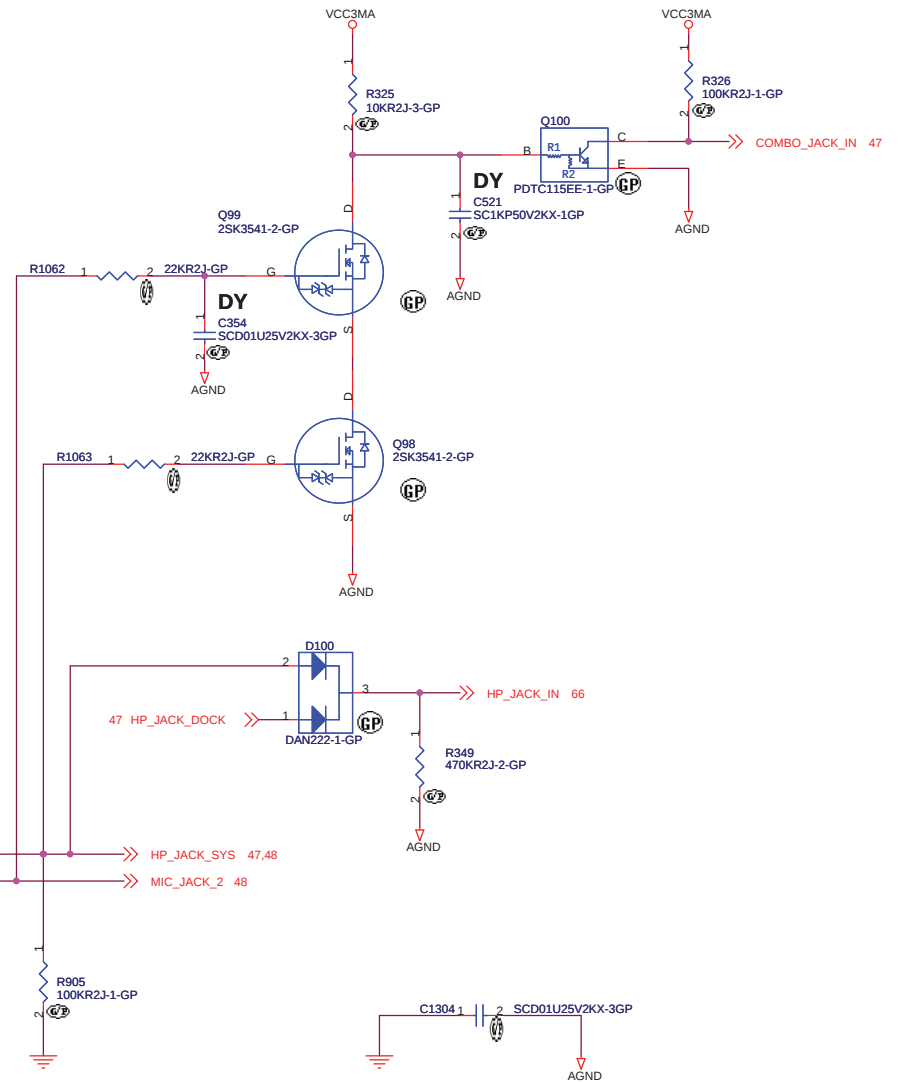
ESD Protection diodes will be change to other parts from SIV phase if it failed.

WIDE PATTERN



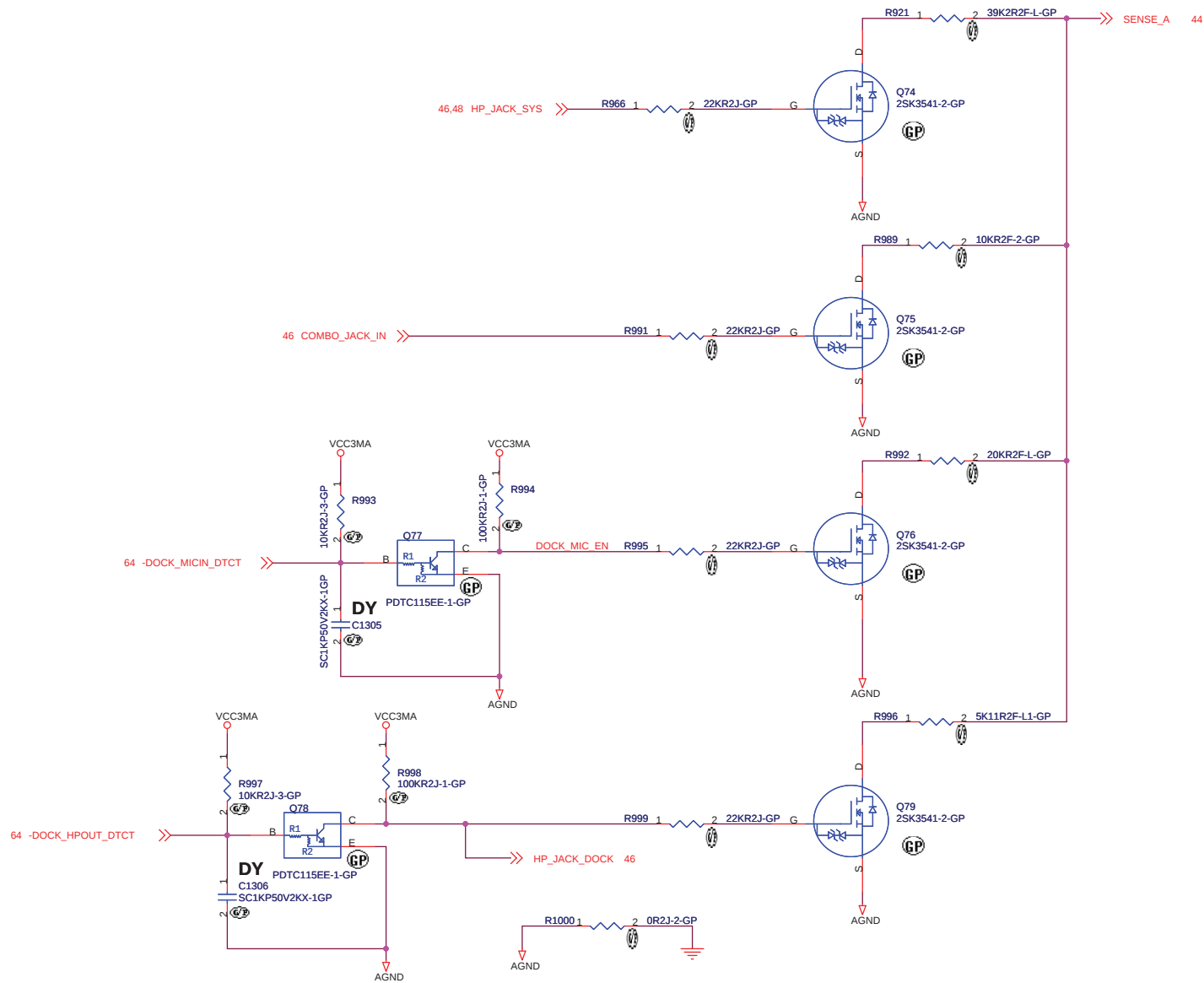
Near J5

	Supplier	Vendo P/N	WISTRON P/N
1	OnSemi	ESD5B5D0ST1G-GP	83.ESD5B.0AF
2	ROHM	RSB5.6S	83.RSB56.0AF



<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
AUDIO CONNECTOR	
Title Size A3 Date: Tuesday, May 05, 2009	Document Number Kendo-1 WS Sheet 46 of 109
Rev SC	



<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

AUDIO JACK SENSE

Size
A3

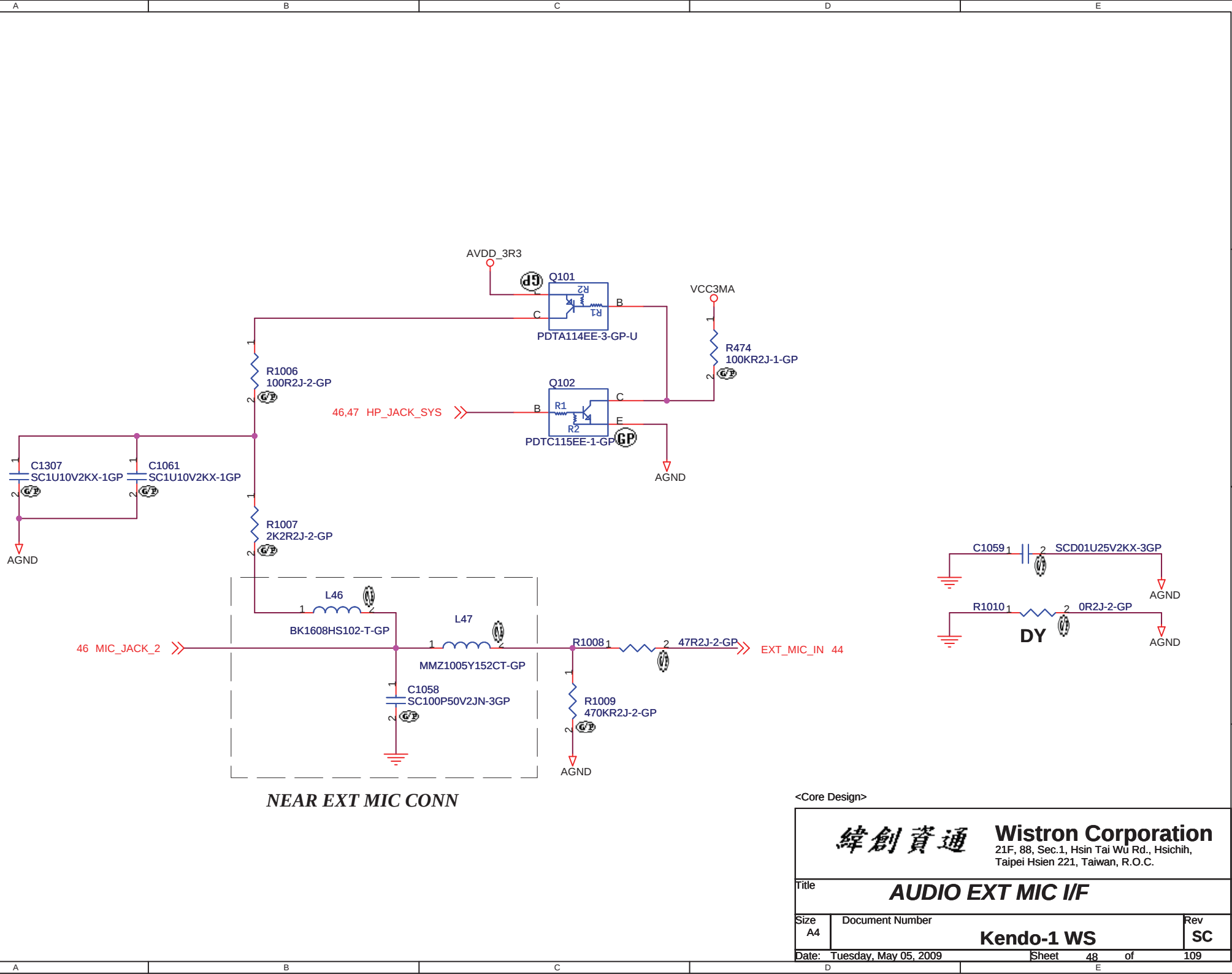
Document Number

Kendo-1 WS

Rev
SC

Date: Tuesday, May 05, 2009

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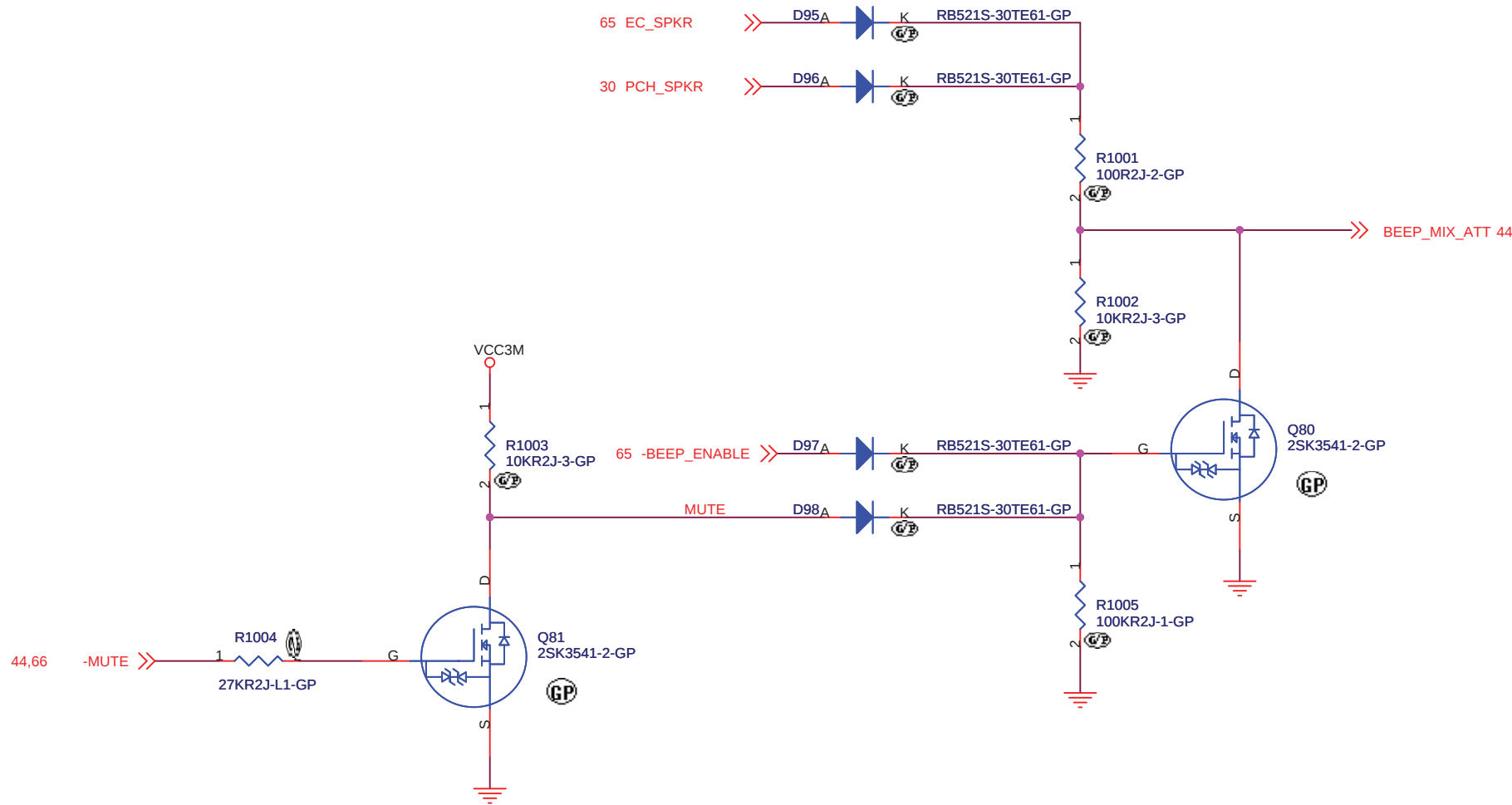
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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
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Size	Document Number		Rev
A3	Kendo-1 WS		SC
Date:	Tuesday, May 05, 2009		Sheet 49 of 109

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Size A3	Document Number Kendo-1 WS		Rev SC
Date: Tuesday, May 05, 2009		Sheet 50	of 109

E



<Core Design>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
BEEP CONTROL			
Size A4	Document Number Kendo-1 WS		Rev SC
Date: Tuesday, May 05, 2009	Sheet	51	of 109

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<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

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Size
A3

Document Number

Date: Tuesday, May 05, 2009

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Rev
SC

PU is Placed on PCH page.

4,25,34,57,58,108

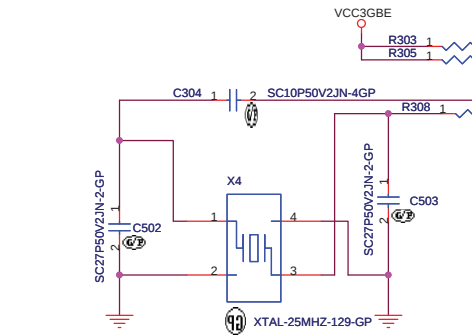
31 -CLKREQ_GBE
-PLTRST_FAR
31 PCIE_CLK_GBE
31 -PCIE_CLK_GBE
31 PCIE_GBE_RXP
31 PCIE_GBE_RXN
31 PCIE_GBE_TXP
31 PCIE_GBE_TXN

31 SML0_CLK
31 SML0_DATA

36 LANPHYPC

54 -R345_LINKUP
54 -R345_ACTIVITY

SMBUS DEVICE ADDRESSES 0XC8



KDS 25MHz
18P 30PPM
DSX321G

NOTE: VCC1GBE WILL WORK AT 0.95V TO 1.15V

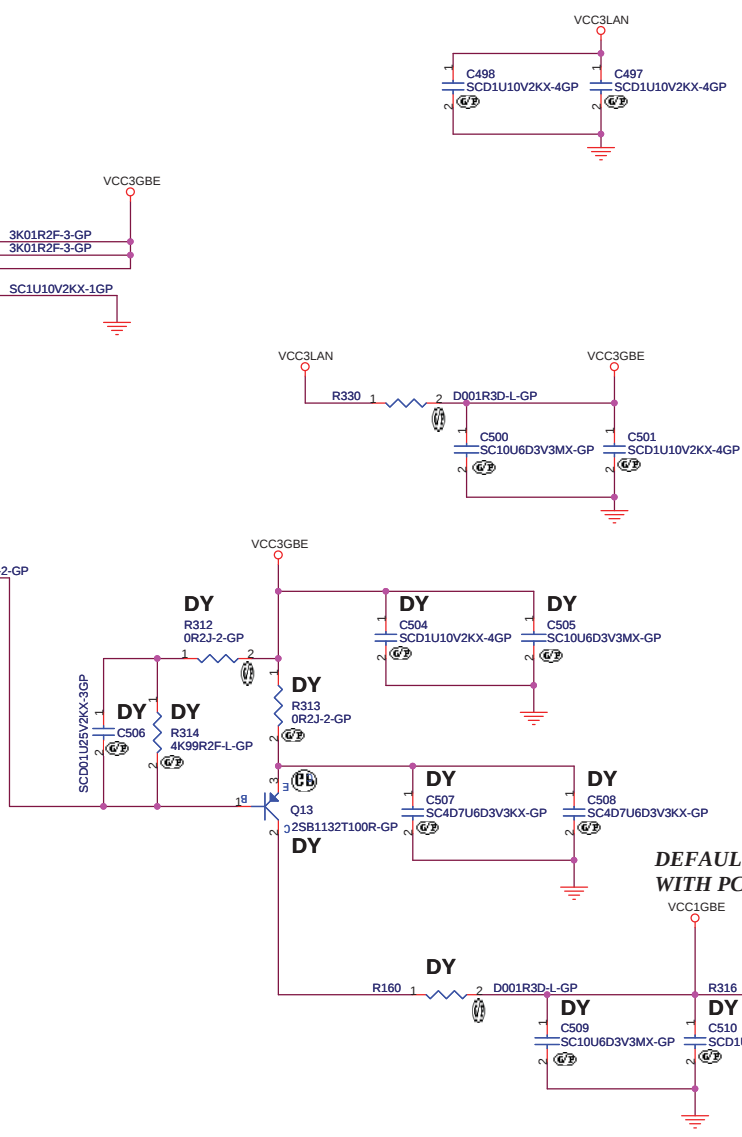
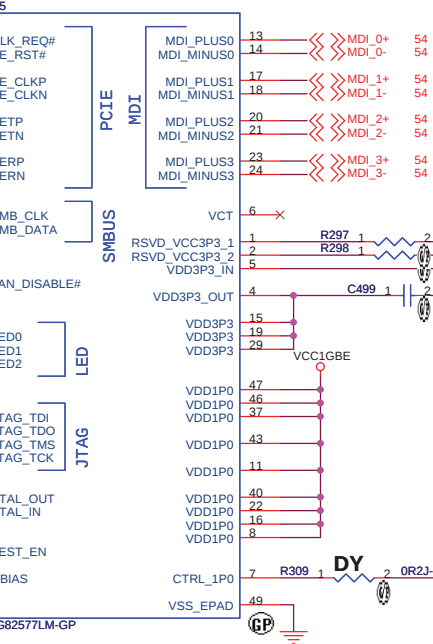
	Supplier	Vendo P/N	WISTRON P/N
1	KDS	DSX321G 25M 18P 30PPM	82.30020.B11
2	H.ELE	HSX321S 25M 18P 30PPM	82.30020.B21

KDS Recommended Conditions:
Normal Frequency: 25MHz.
Frequency Tolerance: +/- 30ppm.
Load Frequency: 18pF.
Effective Series Resistance: 50-ohm.
Effective Shunt Capacitance: 2pF.

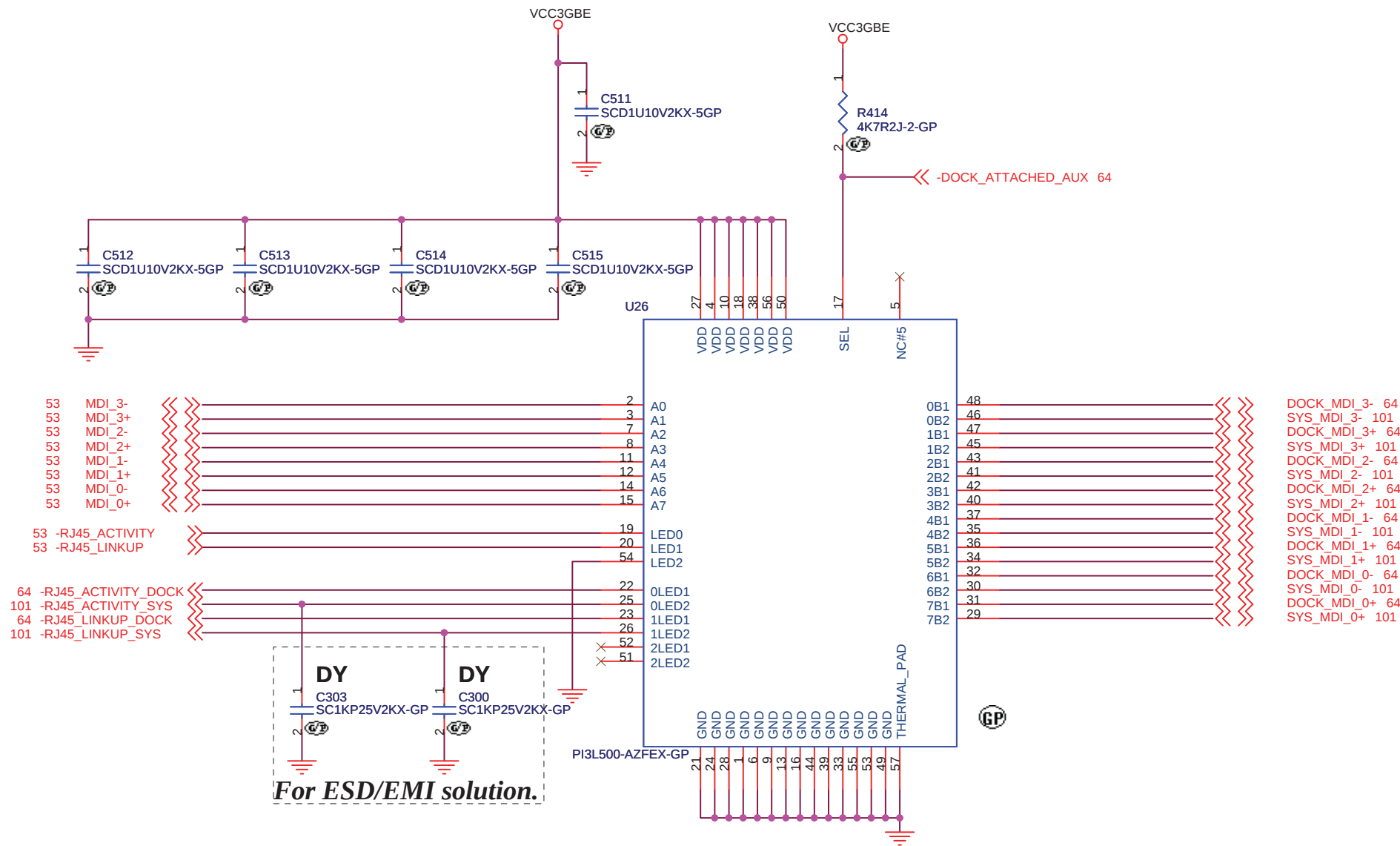
HELE Recommended Conditions:
Normal Frequency: 25MHz.
Frequency Tolerance: +/- 30ppm.
Load Frequency: 18pF.
Effective Series Resistance: 50-ohm.
Effective Shunt Capacitance: 2pF.

Intel Recommended Conditions:
Normal Frequency: 25MHz.
Frequency Tolerance: +/- 30ppm.
Load Frequency: 18pF.
Effective Series Resistance: 50-ohm.
Effective Shunt Capacitance: 6pF.

PU should be VCC3GBE.



DEFAULT SOLUTION: SHARDED
WITH PCH VCC1R05LAN SVR



[Source Cadidate]				
1st	Pericom	PI3L500AZFEX	73.3L500.003	41R0539AA
2nd	TI	TS3L500AERHUR	73.3L500.A0V	41R0539BA

<Variant Name>

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
GBE LAN SW			
Size A4	Document Number Kendo-1 WS		Rev SC
Date: Tuesday, May 05, 2009	Sheet 54	of	109

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<Variant Name>

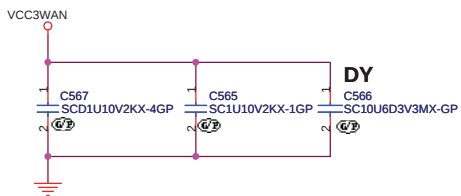
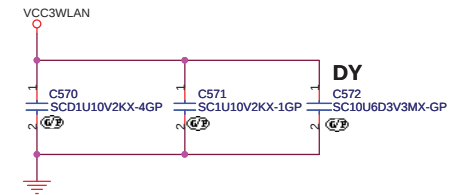
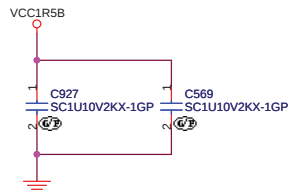
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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title			
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Size	Document Number		Rev
A3	Kendo-1 WS		SC
Date:	Tuesday, May 05, 2009		Sheet 55 of 109
		E	

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<Variant Name>

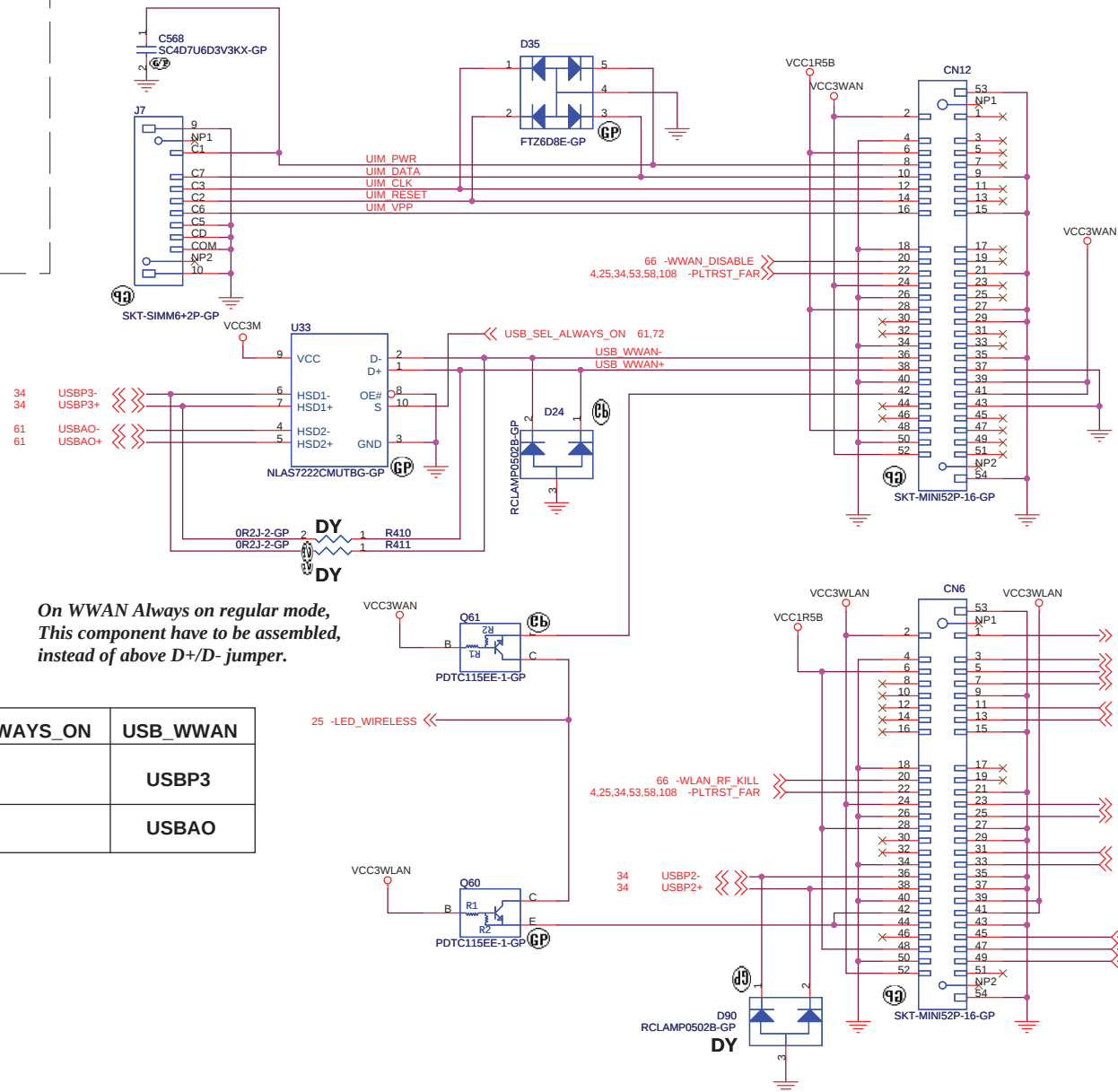
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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title			
BLANK			
Size	Document Number		Rev
A3	Kendo-1 WS		SC
Date:	Tuesday, May 05, 2009		Sheet 56 of 109

Schematic diagram of the SW-SLIDE57-GP-U switch assembly. The switch (SW1) has three terminals: 1 (labeled NP1), 2 (labeled NP2), and 3. Terminal 1 is connected to a VCC3M supply through a 100kR2J-1-GP resistor (R409). Terminal 2 is connected to a common ground. Terminal 3 is connected to a common ground through a capacitor C564 (SCD1U10V2KX-4GP). The switch is also connected to a signal line labeled GLOBAL_WL_DISABLE.



Always ON	YES	NO
U33	ASM	No_ASM
R410	No_ASM	ASM
R411	No_ASM	ASM

↑
LOGIC



***On WWAN Always on regular mode,
This component have to be assembled,
instead of above D+/D- jumper.***

USB_SEL_ALWAYS_ON	USB_WWAN
L	USBP3
H	USBA0

<Variant Name>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	PCIE MINI CARD SLOT (1/2)
-------	----------------------------------

Size A3	Document Number Kendo-1 WS	Rev SC
Date: Tuesday, May 05, 2009	Sheet 57 of 109	

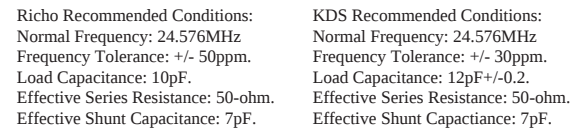
7.0H CONNECTOR



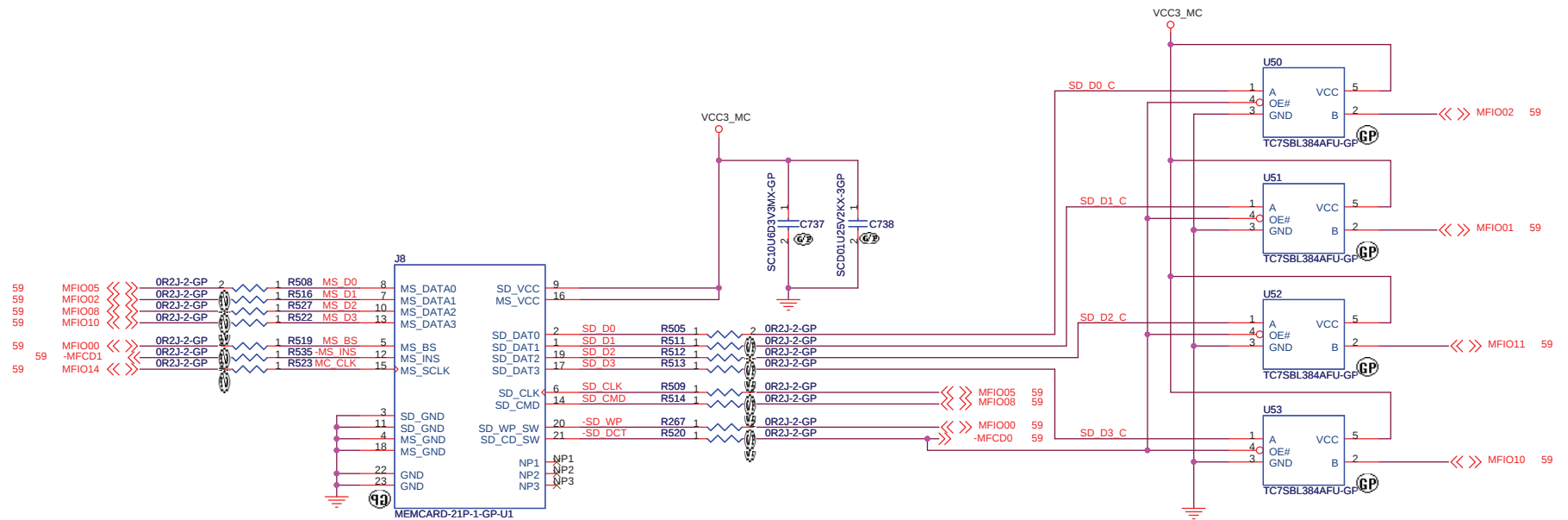
Title	PCIE MINI CARD SLOT (2/2)
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MEDIA I/F	SD/MMC	MEMORYSTICK	XD
MFIO00	SDWP#	MSBS	XD_D7
MFIO01	SD_D1		XD_D6
MFIO02	SD_D0	MS_D1	XD_D5
MFIO03	(SD_D7)		XD_D4
MFIO04	(SD_D6)	(MS_D5)	XD_D3
MFIO05	SD_CLK	MS_D0	XD_D2
MFIO06			XD_D1
MFIO07	(SD_D5)	(MS_D4)	XD_D0
MFIO08	SD_CDM	MS_D2	XD_WP#
MFIO09	(SD_D4)	(MS_D6)	XD_WE#
MFIO10	SD_D3	MS_D3	XD_ALE
MFIO11	SD_D2		XD_CLE
MFIO12			XD_CE#
MFIO13		(MS_D7)	XD_RE#
MFIO14		MS_CLK	XD_R/B
MFCD0#	SDCD#		XDCD0#
MFCD1#		MSINS#	XDCD1#



Title			
1394/MEDIA CARD CONTROLLER			
Size	Document Number		Rev
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		U50, U51, U52, U53	Wistron part number
1	TOSHIBA	TC7SBL384AFU-GP	73.7S384.007
2	NXP	74LVC1G384GW	73.1G384.AHH
3	TI	TS5A3167DCKR	74.53167.A9F

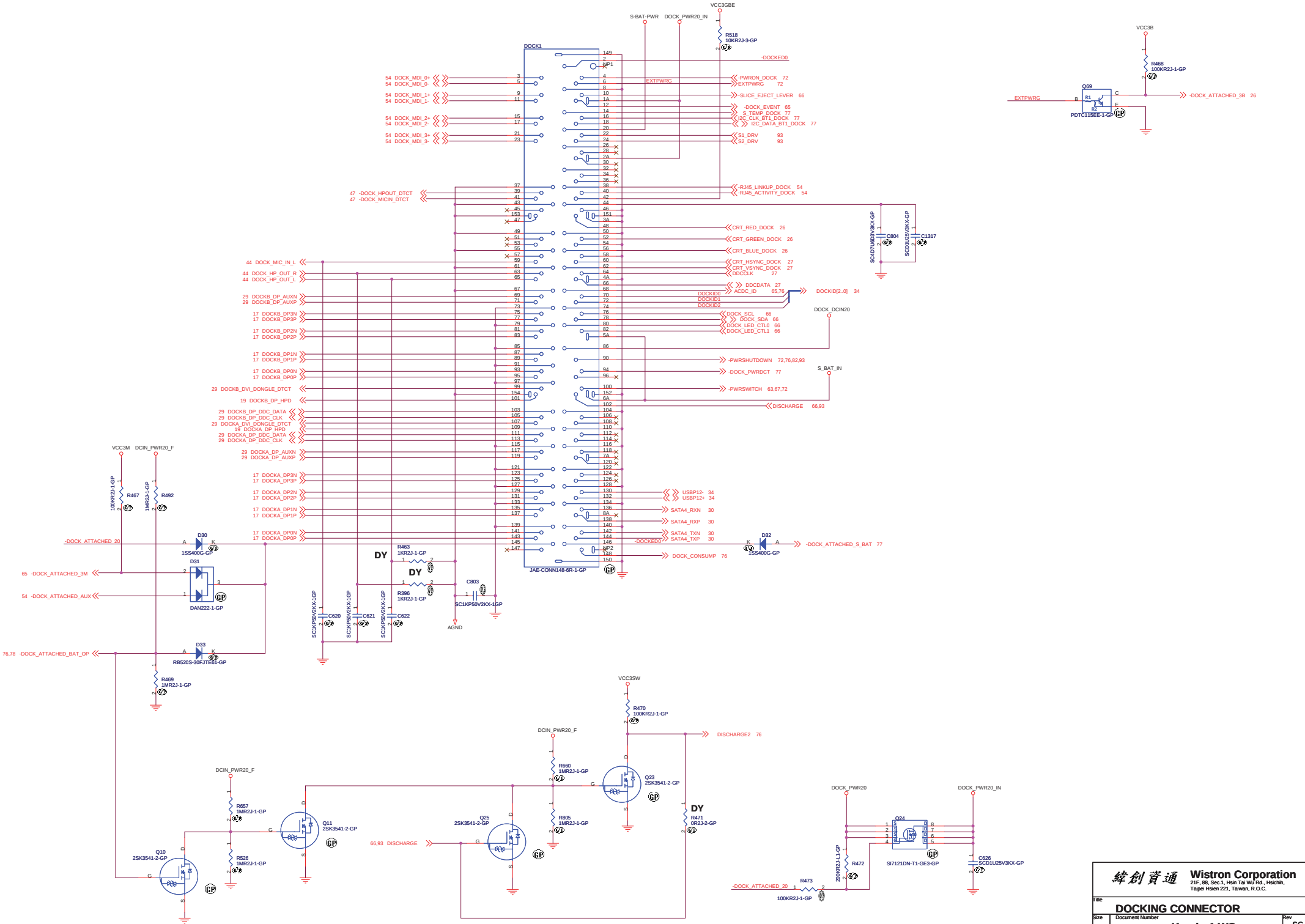
<Variant Name>

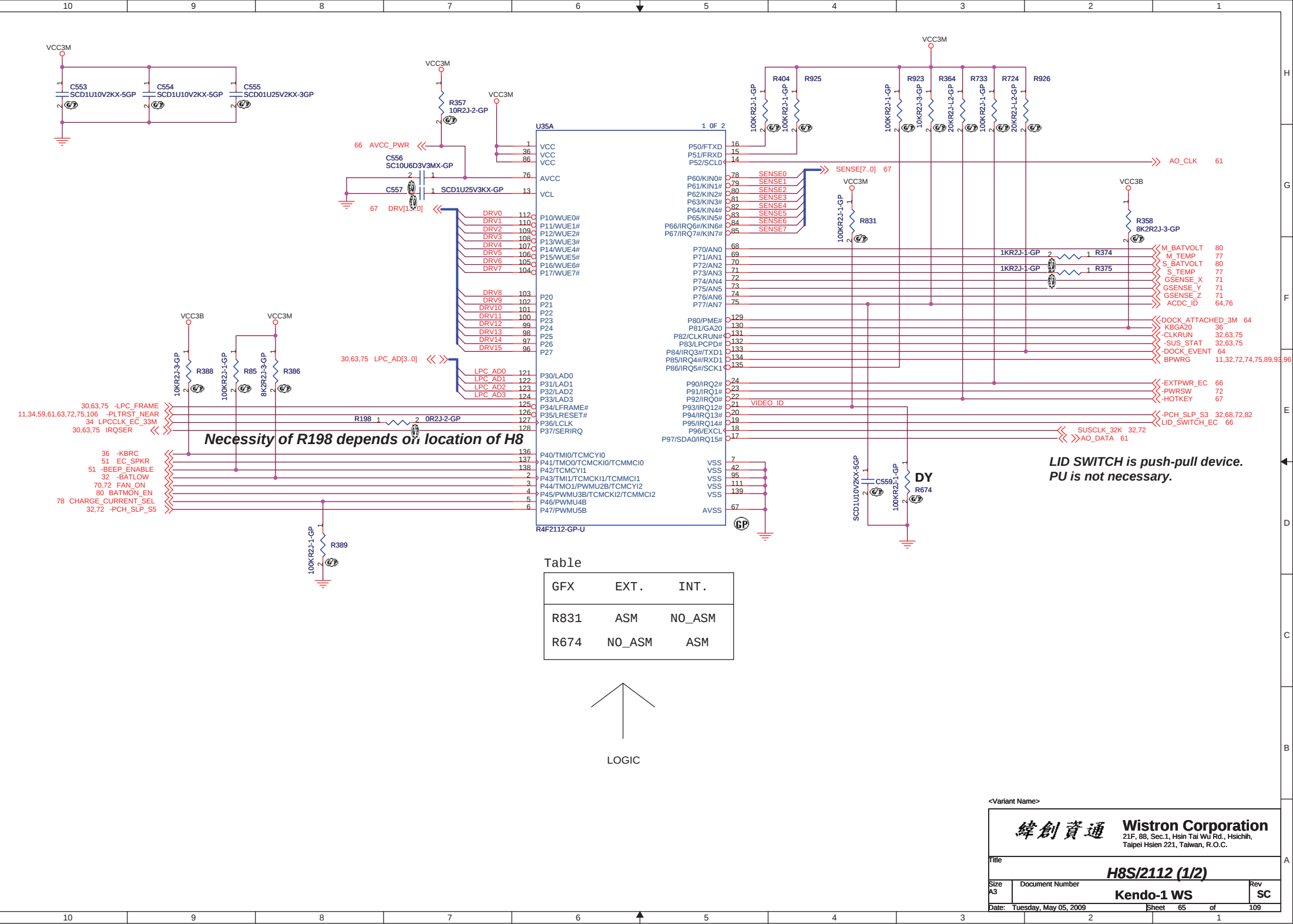
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		Title	
		MEDIA CARD INTERFACE	
Size A3	Document Number	Rev	
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<Variant Name>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
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Size	Document Number		Rev
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		E	





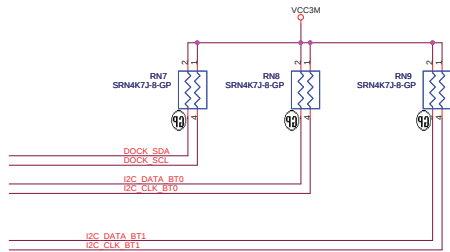
Necessity of R198 depends on location of H8

Table

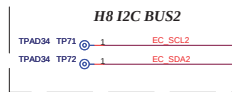
GFX	EXT.	INT.
R831	ASM	NO_ASM
R674	NO_ASM	ASM

LOGIC

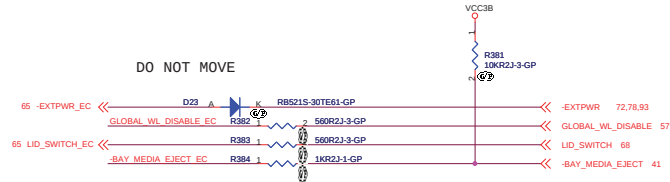
LID SWITCH is push-pull device.
PU is not necessary.



TEST PAD X2
BOTTOM SIDE
DO NOT MOVE AFTER FIX

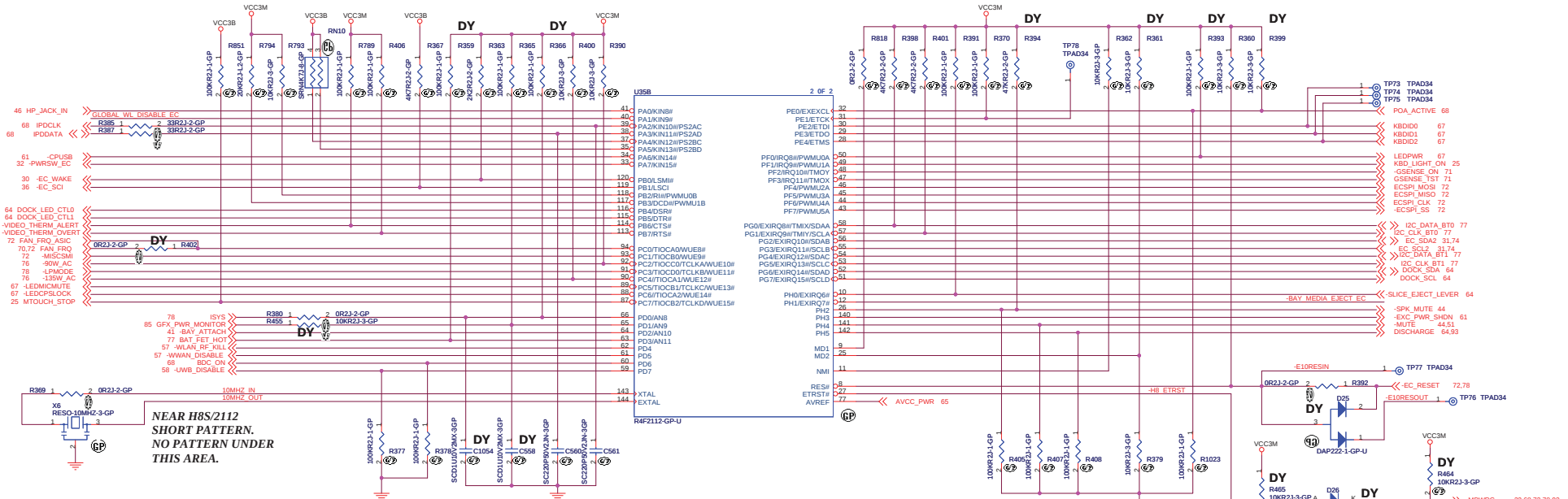


DO NOT MOVE



Power rail for RN10 can be either VCC3B or VCC5B.

EXC Power IC has internal PU
for PIN#20(-EXC_PWR_SHDN)



NEAR H8S/2112
SHORT PATTERN.
NO PATTERN UNDER
THIS AREA.

pls. modify this logic table

E10A Debug I/F	Enable	Disable
R392	NO ASM	ASM
R466	NO ASM	ASM
D25	ASM	NO ASM
D26	ASM	NO ASM
D20	ASM	NO ASM
R360	ASM	ASM
R361	ASM	NO ASM
R464	ASM	NO ASM
R465	ASM	NO ASM
R379	NO ASM	ASM

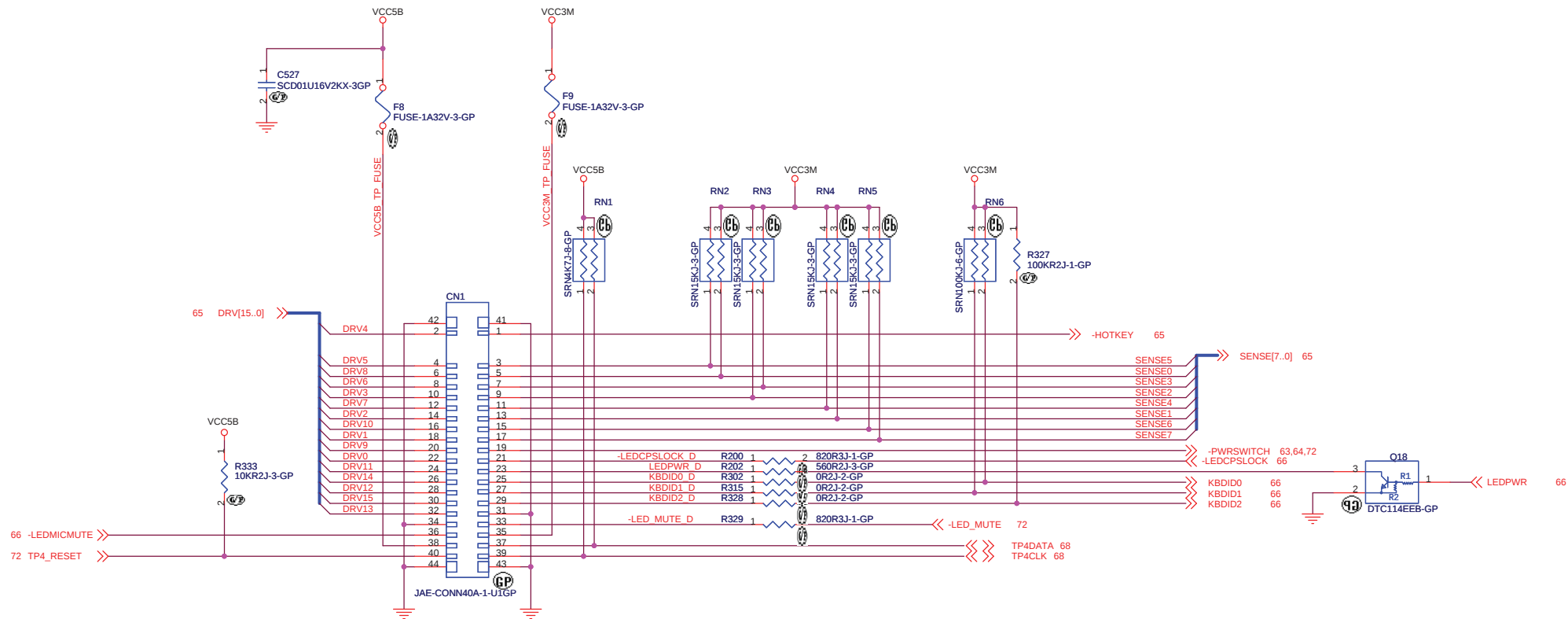
LOGIC

<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsinchu,
Taipei Hsein 221, Taiwan, R.O.C.

File: H8S/2112(2/2)
Size: A2
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Keyboard Connector



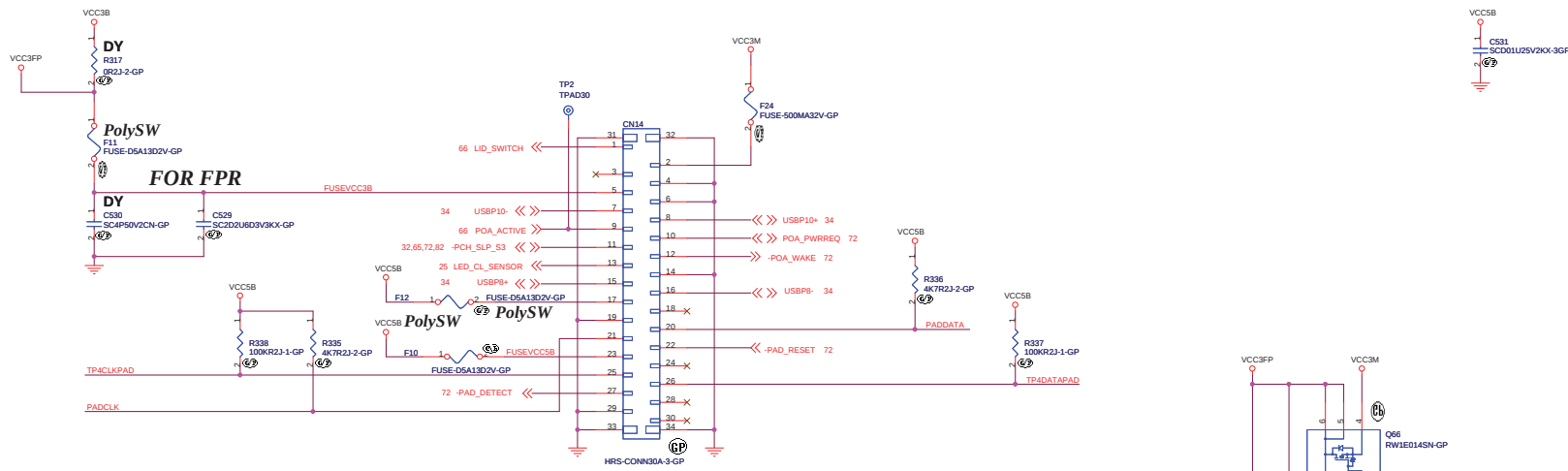
HOTKEY# IPDCLK
 DRV4 to R425/Q18

Keyboard Connector Top View

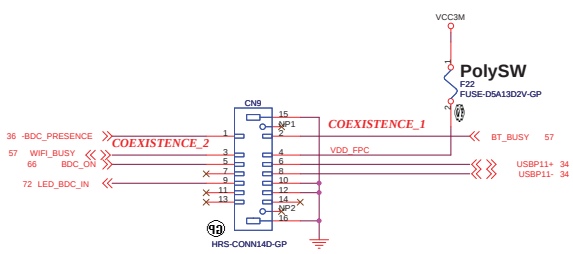
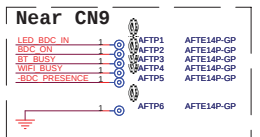
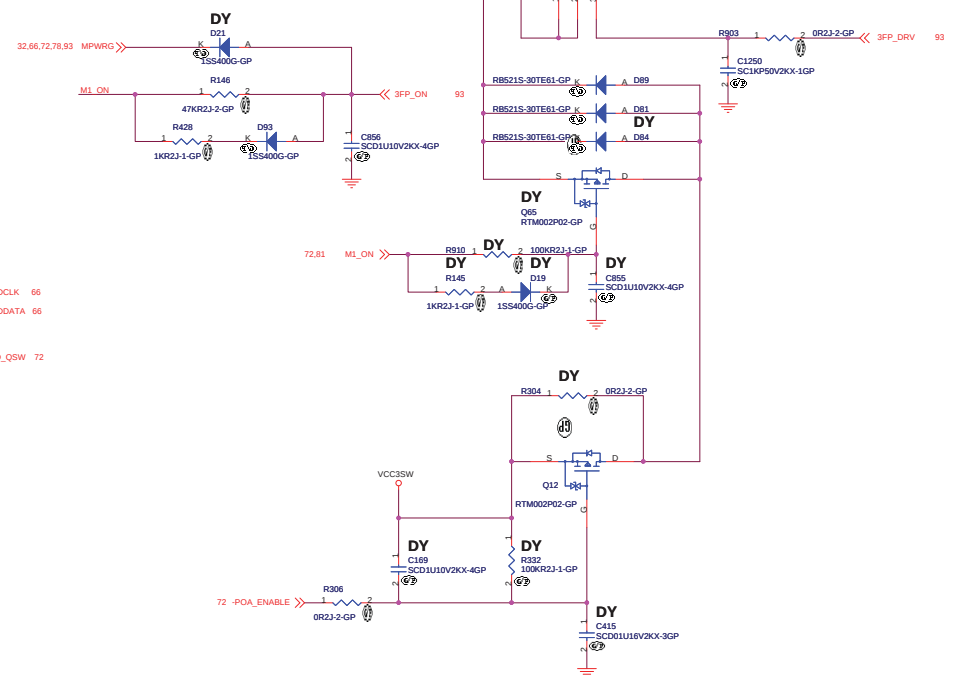
Near CN1
 -PWRSWITCH TP81 TPAD60

<Variant Name>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
KEYBOARD CONN	
Size A3	Document Number
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Need to use 30pin connector



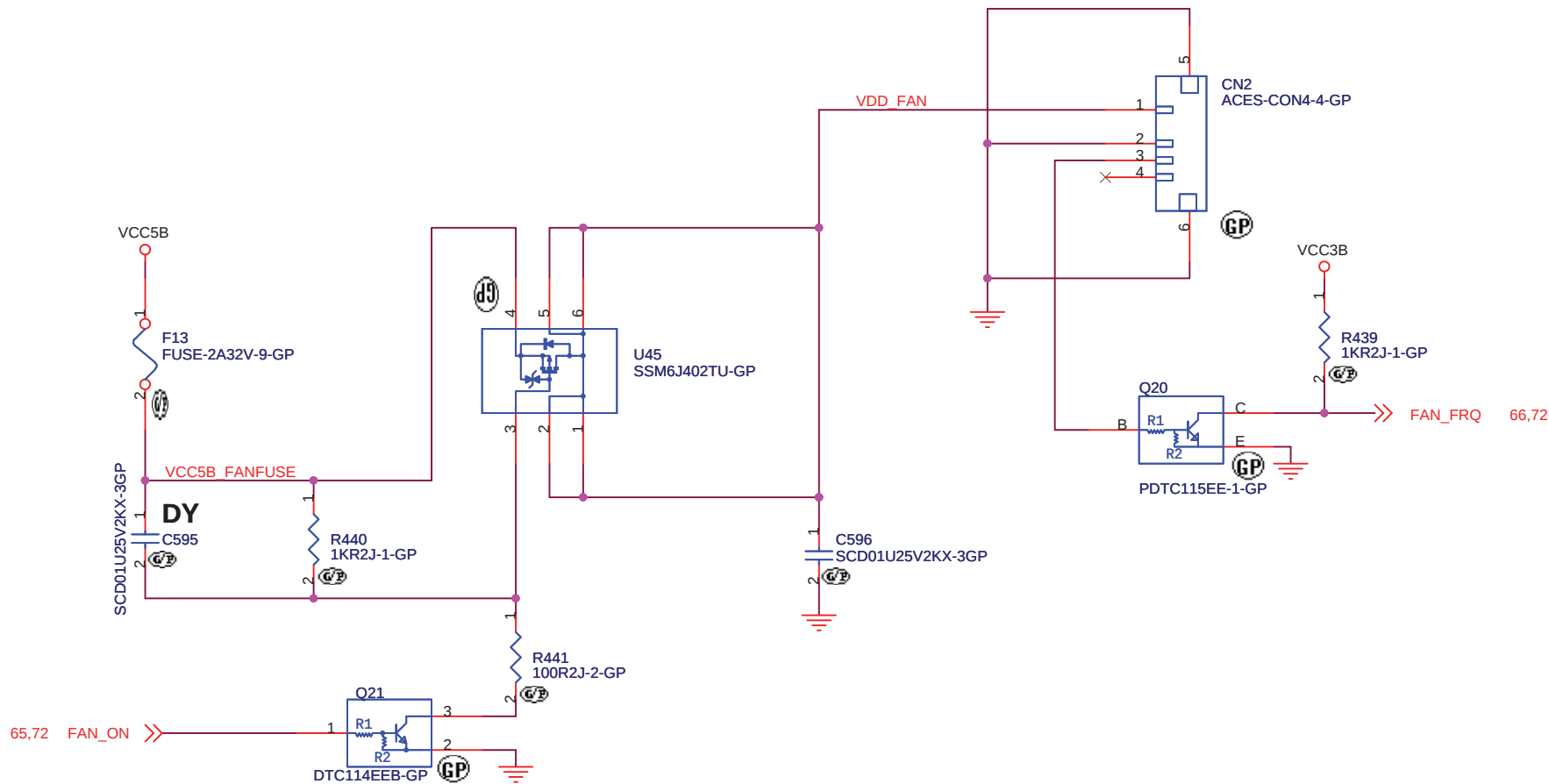
Bluetooth CONN

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<Variant Name>

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Title BLANK			
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<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

FAN CONTROL

Size
A4

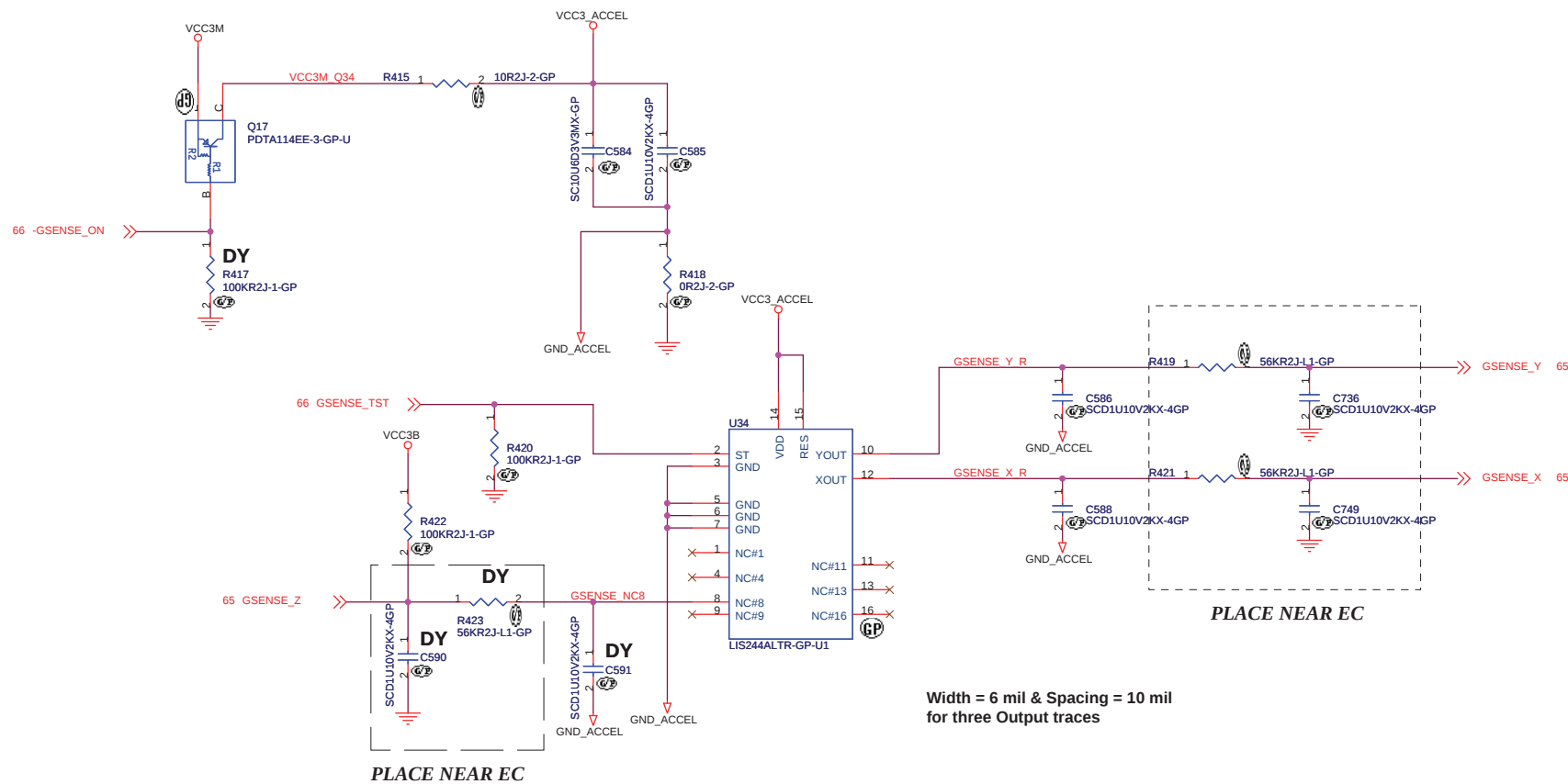
Document Number

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SC

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LIS244AL LIS34AL		NO ACC.
R417	NO-ASM	ASM
R420	ASM	ASM
U34	ASM	NO-ASM
Q17	ASM	NO-ASM
R415	10-OHM	NO-ASM
C585	ASM	NO-ASM
C584	ASM	NO-ASM
C586	ASM	NO-ASM
C588	ASM	NO-ASM
R419	56K	NO-ASM
C736	ASM	NO-ASM
R421	56K	NO-ASM
C749	ASM	NO-ASM
C591	NO-ASM	NO-ASM
R423	NO-ASM	NO-ASM
C590	NO-ASM	NO-ASM
R422	ASM	ASM

Layout Comment :

(1) Place C586, C588, Q17, R415, R417, C584, C585, R420 close to U34.

(2) Avoid routing under DCDC switching area.

<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title		
G-SENSOR		
Size A3	Document Number	Rev SC
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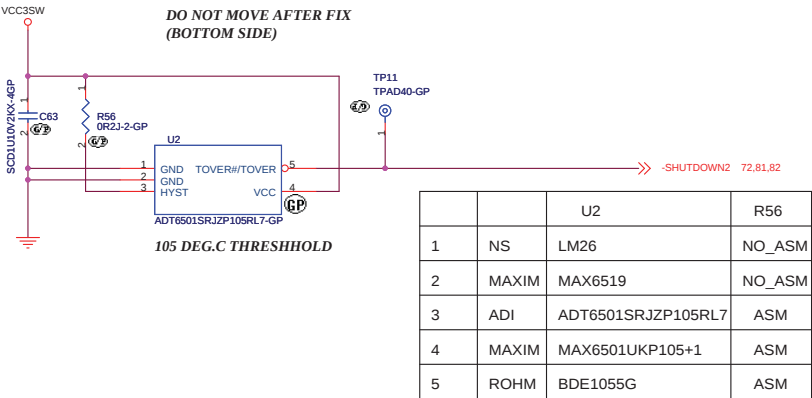
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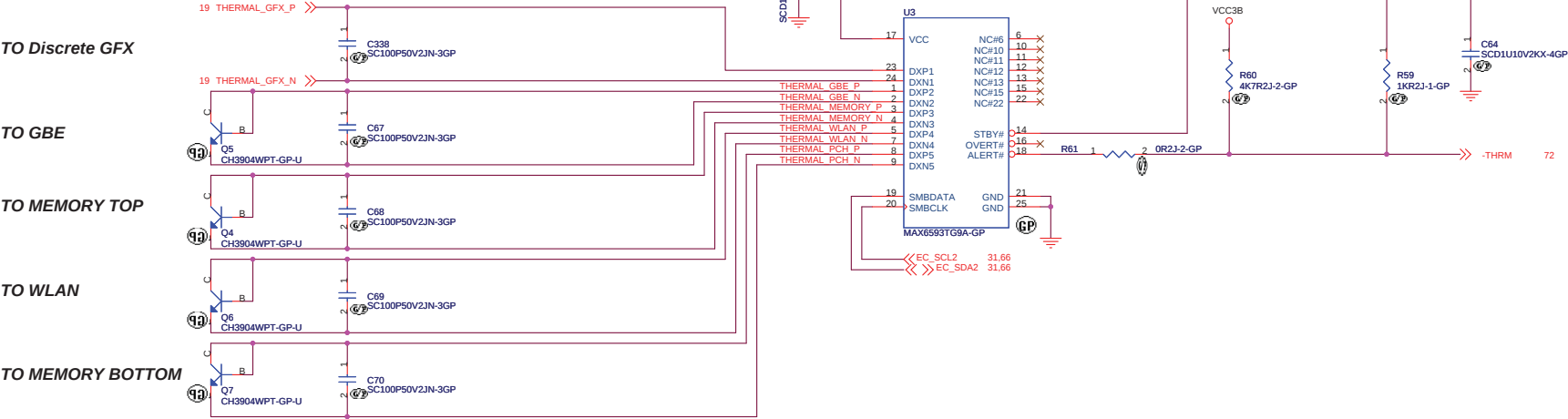
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		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
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Size A3	Document Number Kendo-1 WS		Rev SC
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Thermal Sensor for CPU

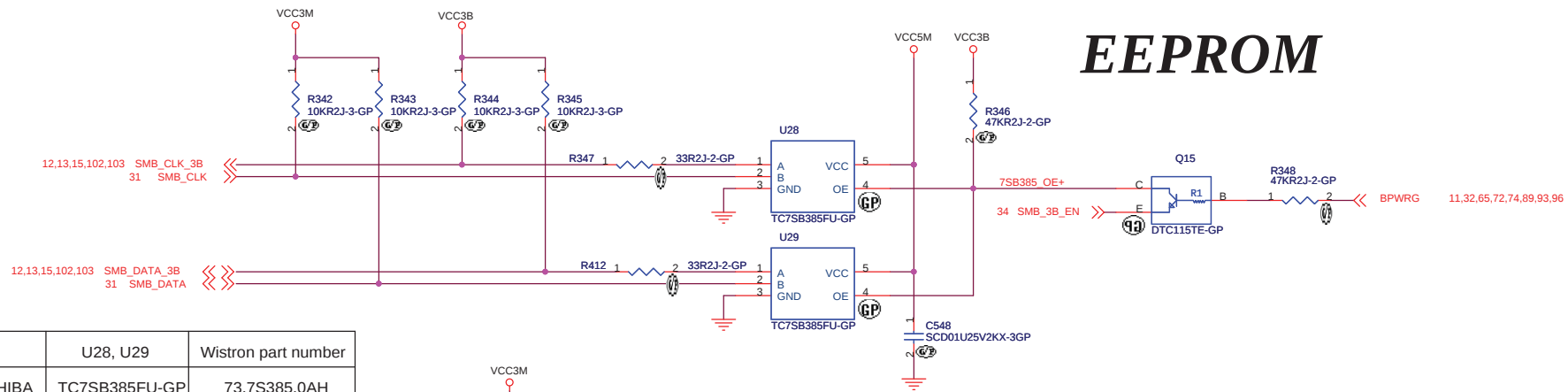


THESE CAPS MUST BE PLACED AS
CLOSE AS POSSIBLE TO MAX6593

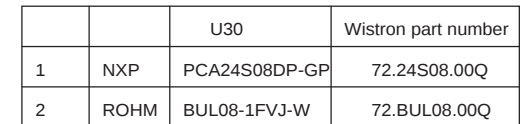


Layout Comment :

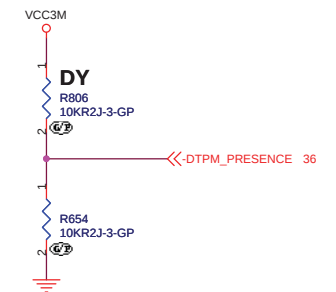
(1) Thermal sensor trace lines should not be overlapped with other high frequency trace lines in other layers.
(2) Also, it should not be overlapped with large amplitude trace lines either.



11,34,59,61,63,65,72,106 -PLTRST_NEAR >>



```
11,34,59,61,63,65,72,106  -PLTRST_NEAR >>
```



please close for each pin

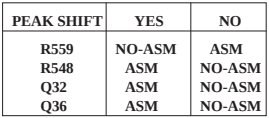
緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

EEPROM/TCPA

Kendo-1 WS

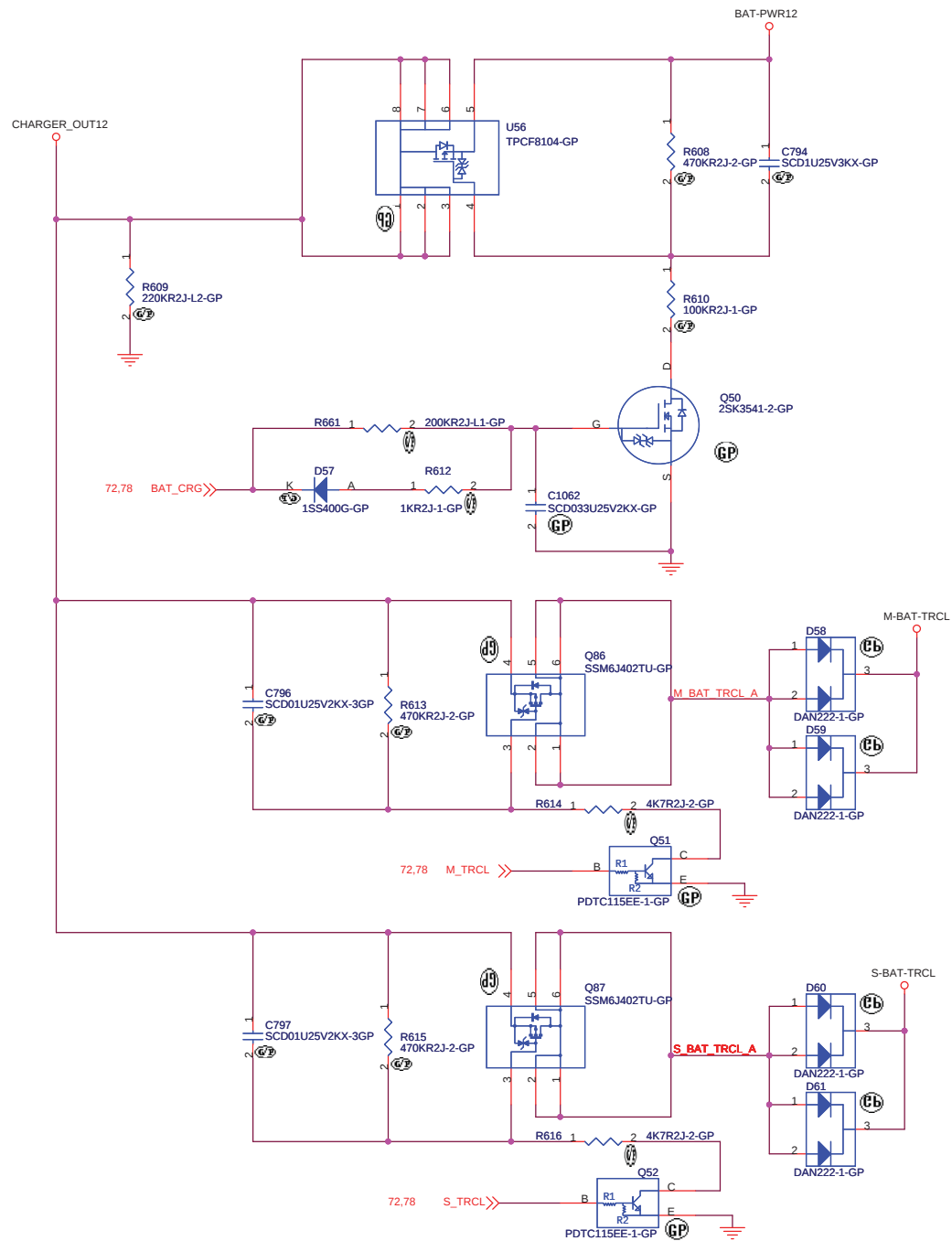
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SC	



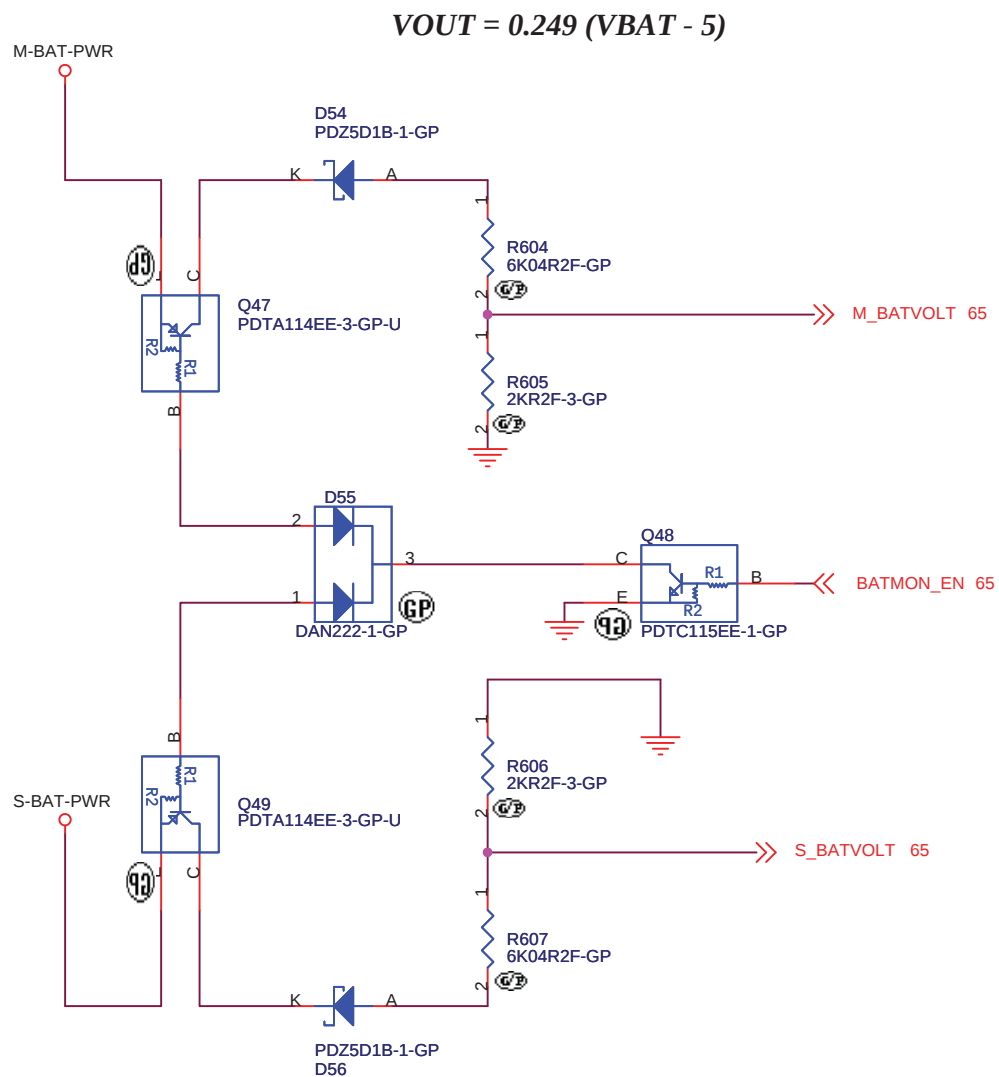
AC Adapter	#135W_AC	#90W_AC
135W	L	L
90W	H	L
65W	H	H

Title			
DC-IN			
Size	Document Number	Rev	
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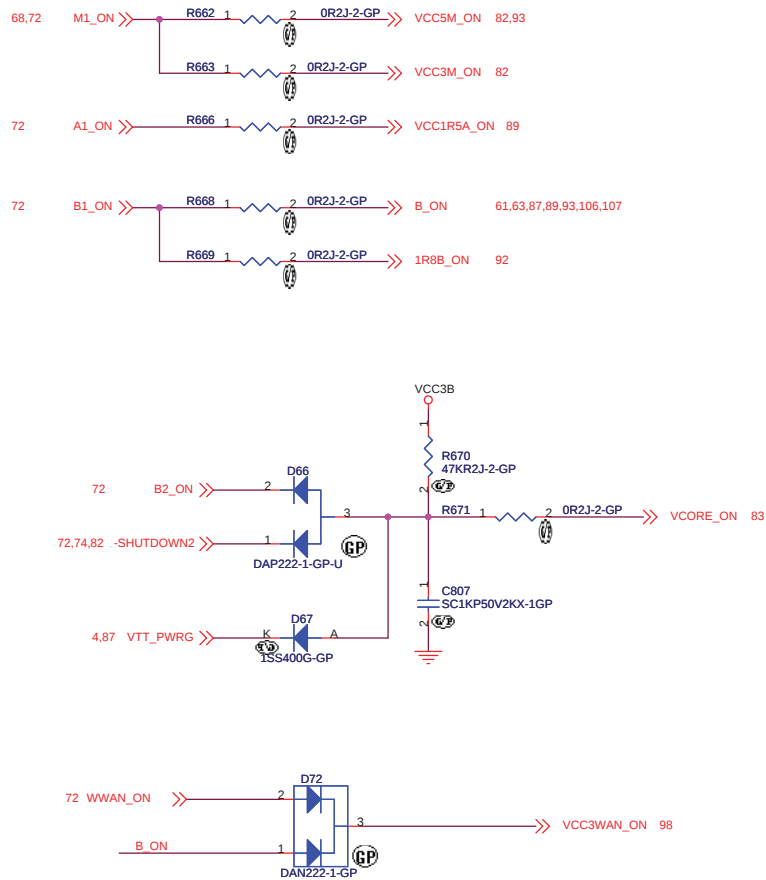
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緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
CHARGER SELECT		
Title Size A3	Document Number Kendo-1 WS	Rev SC
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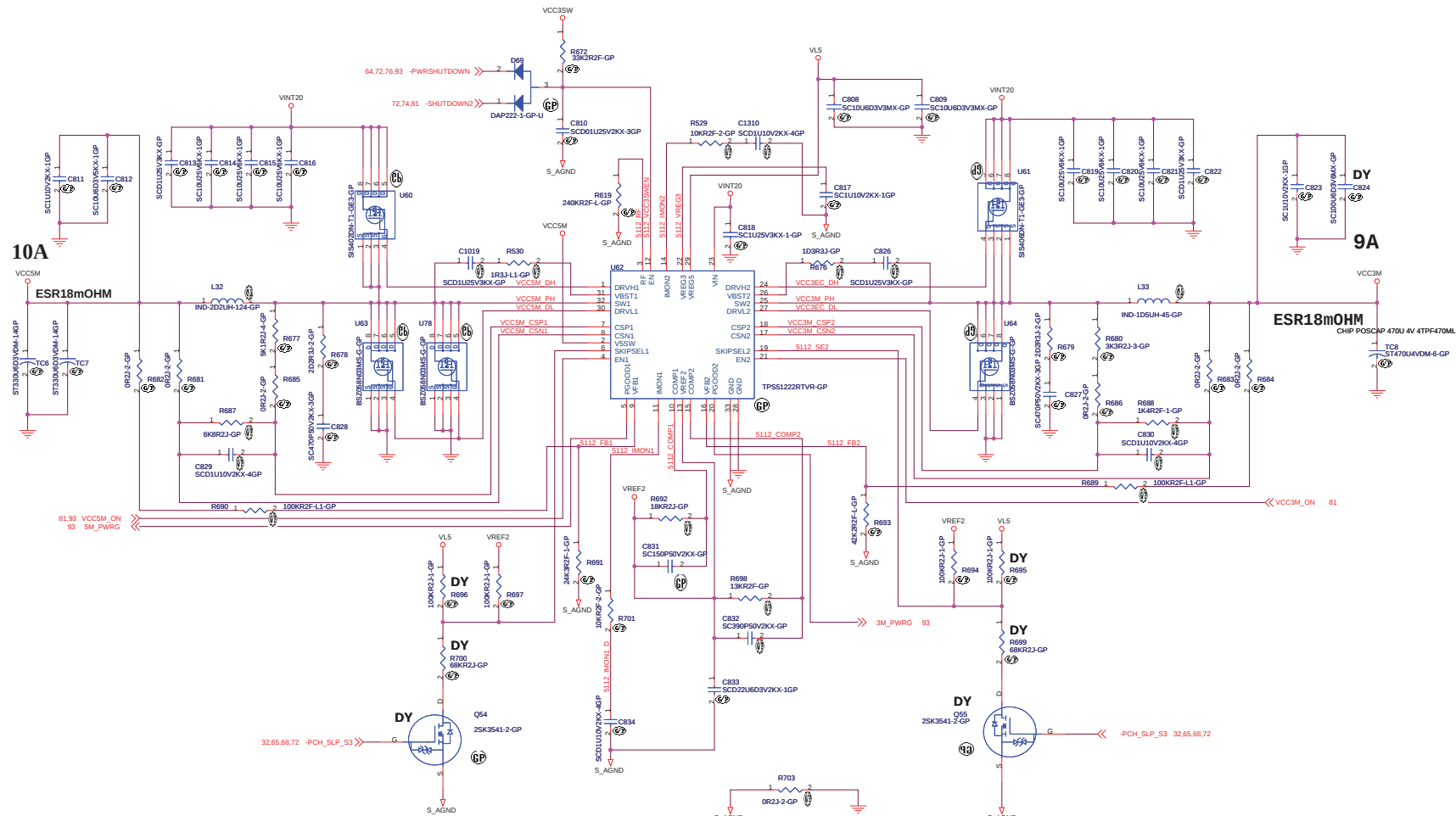
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Title BATTERY MONITOR	
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Table

ALWAYS ON	YES	NO
D72	ASM	NO_ASM



BLANK

<Variant Name>

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<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title

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Size

A3

Document Number

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<Variant Name>

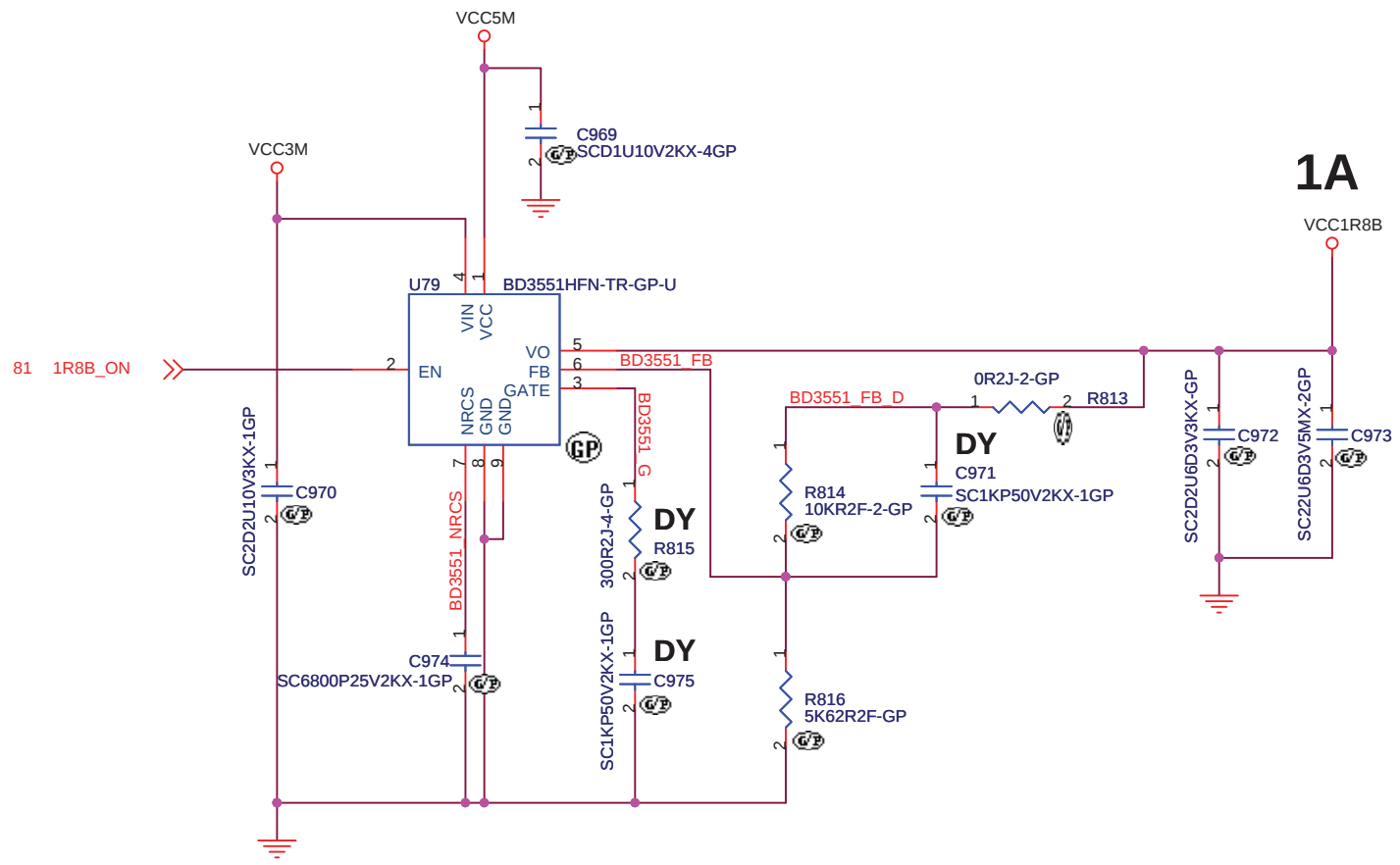
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Size	Document Number		Rev
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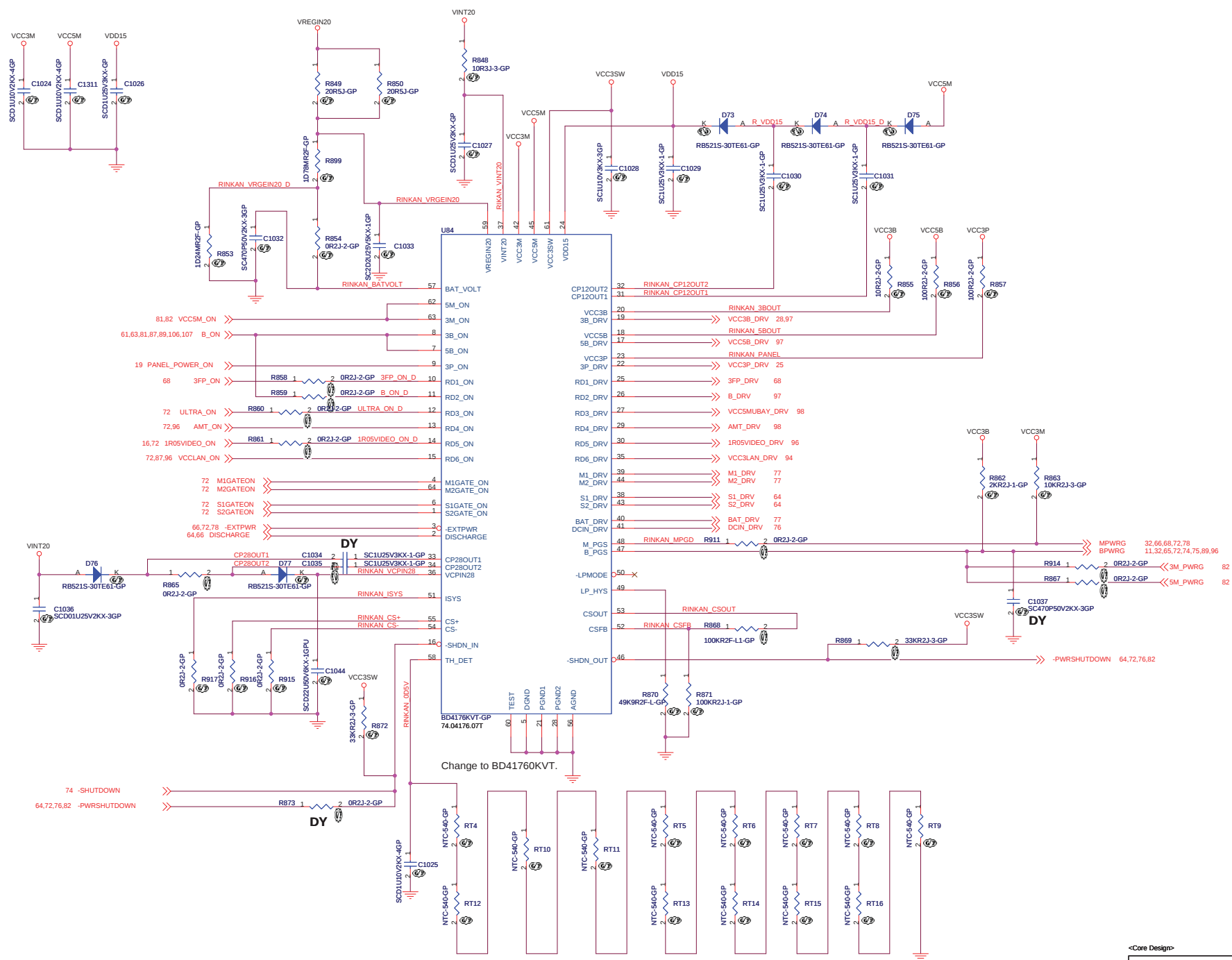
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<Core Design>

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
DC-DC VCC1R8B			
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<Core Design>

緯創資通 Wistron Corporation
21F, 80, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			DC/DC RINKAN-2
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緯創資通

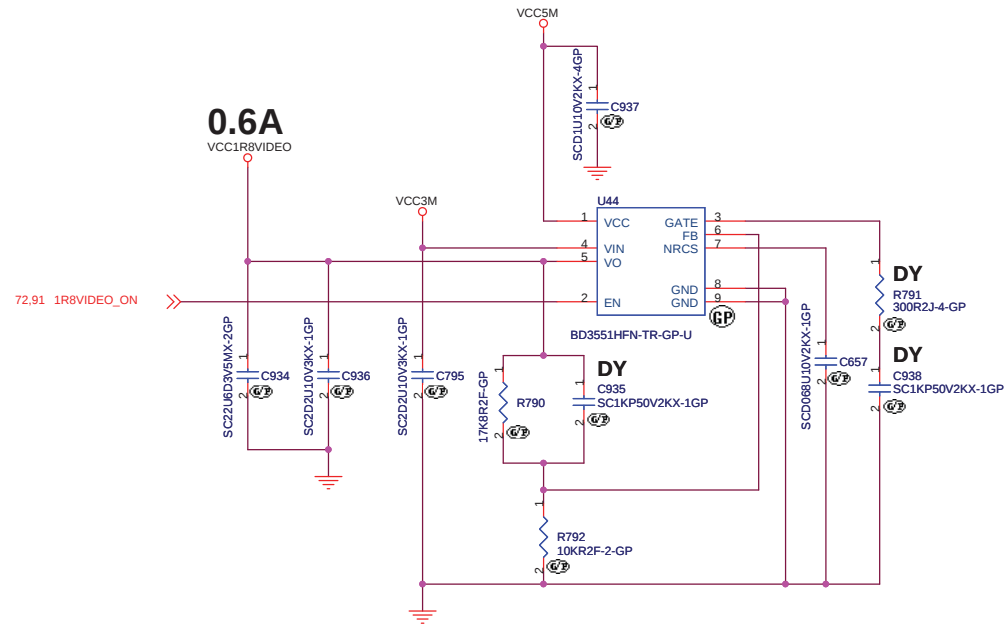
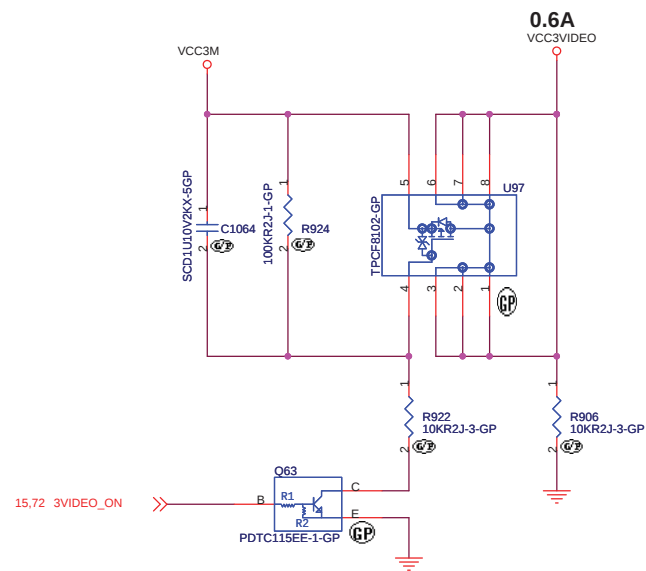
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LOAD SW LAN & LANPWRG

Kendo-1 WS

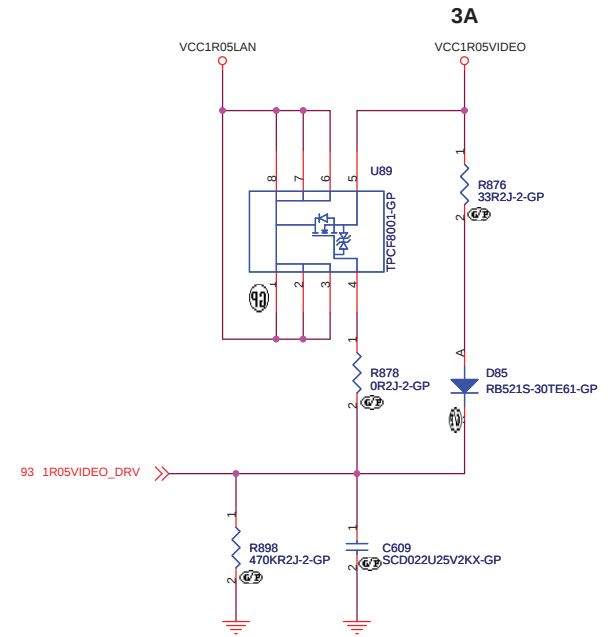
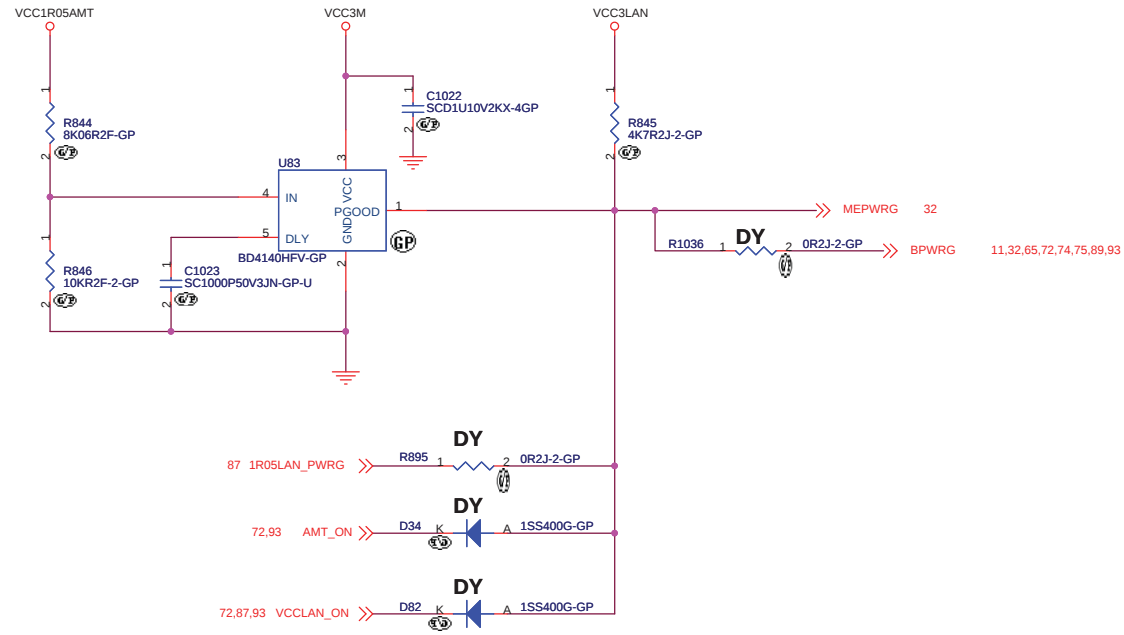
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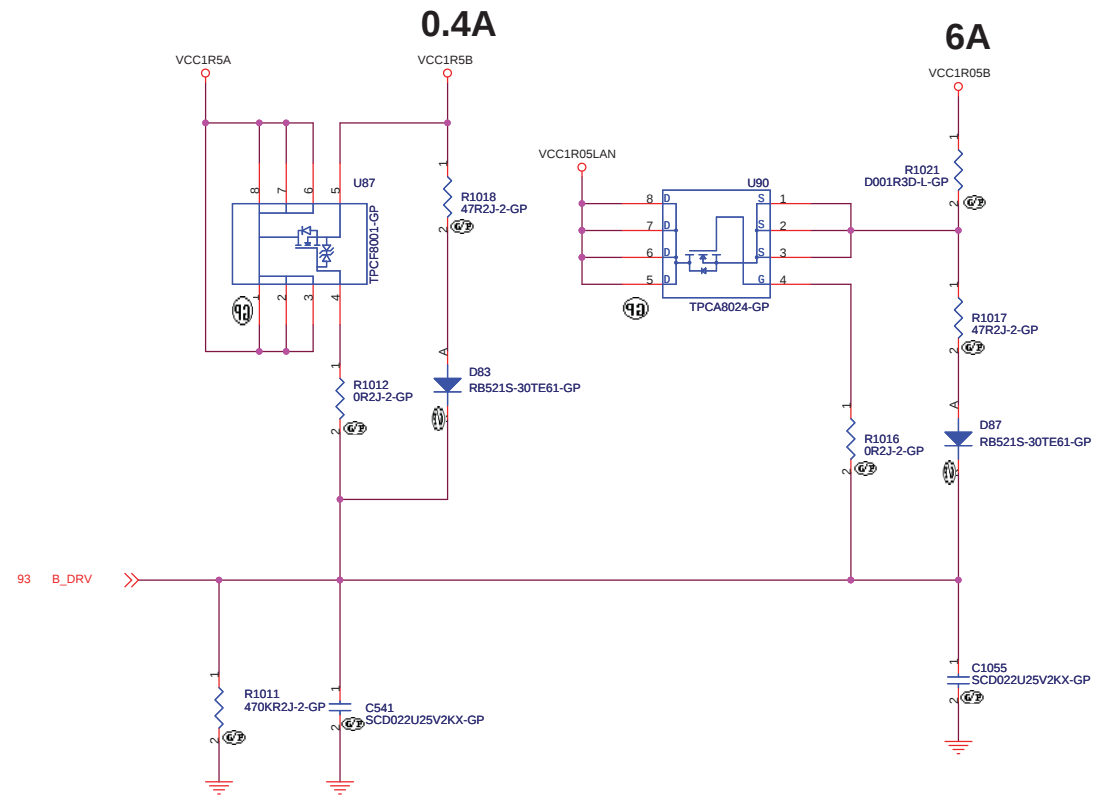
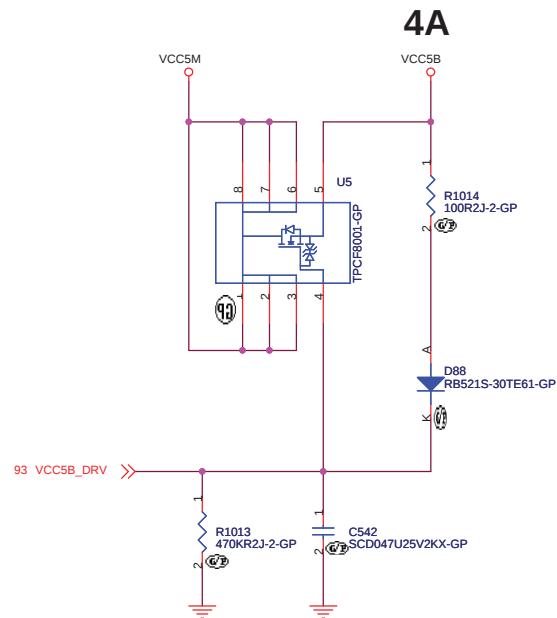
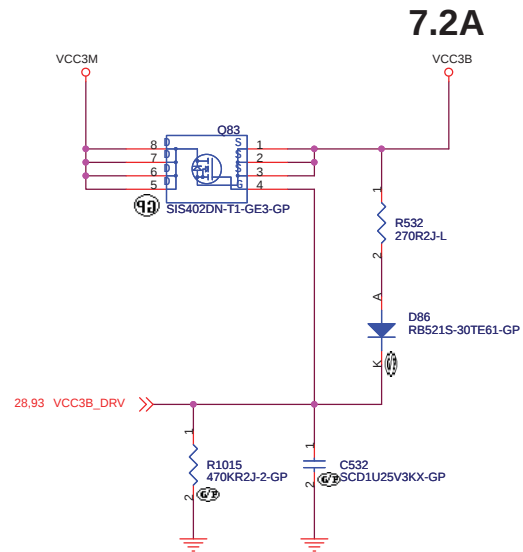
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<Core Design>

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
LOAD SW VIDEO			
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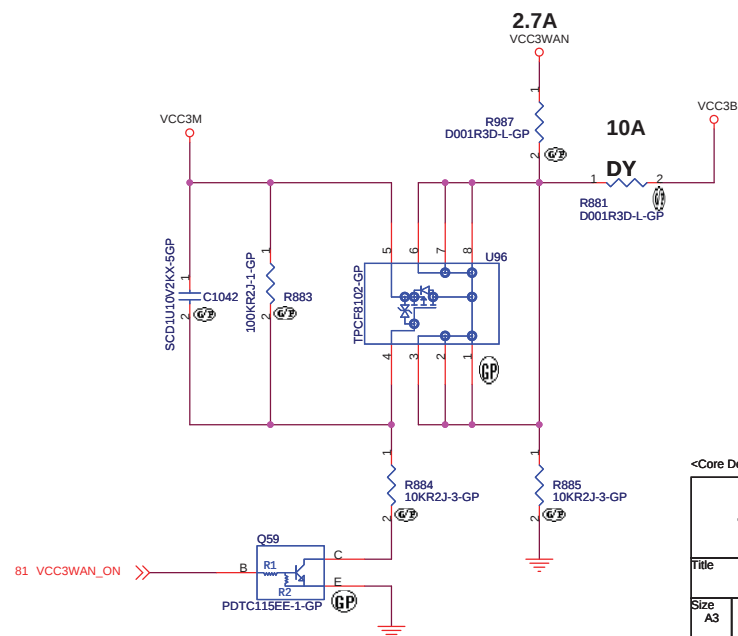
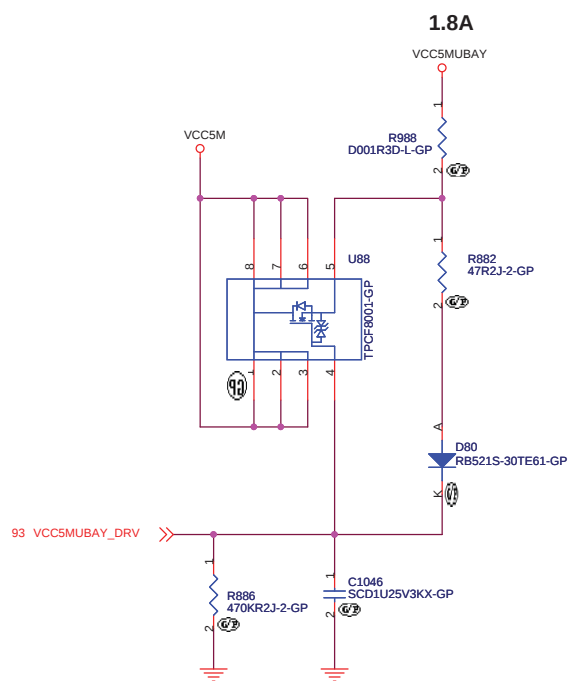
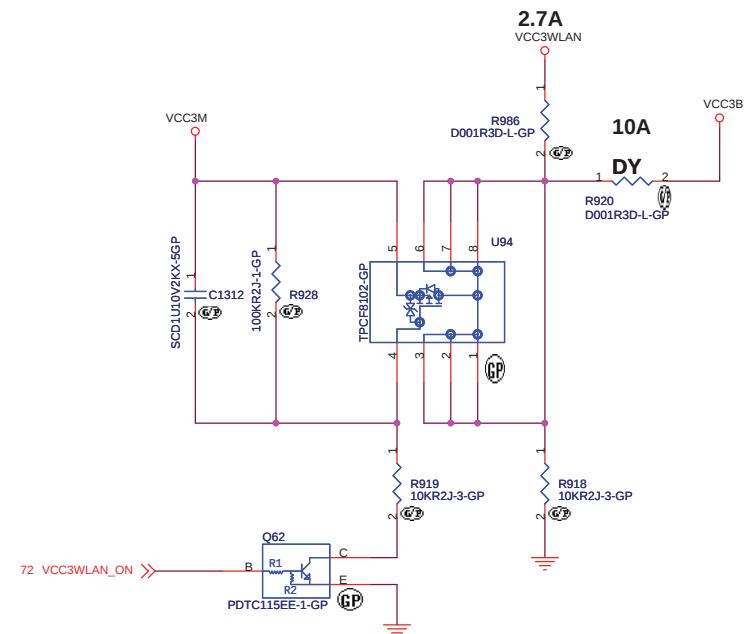
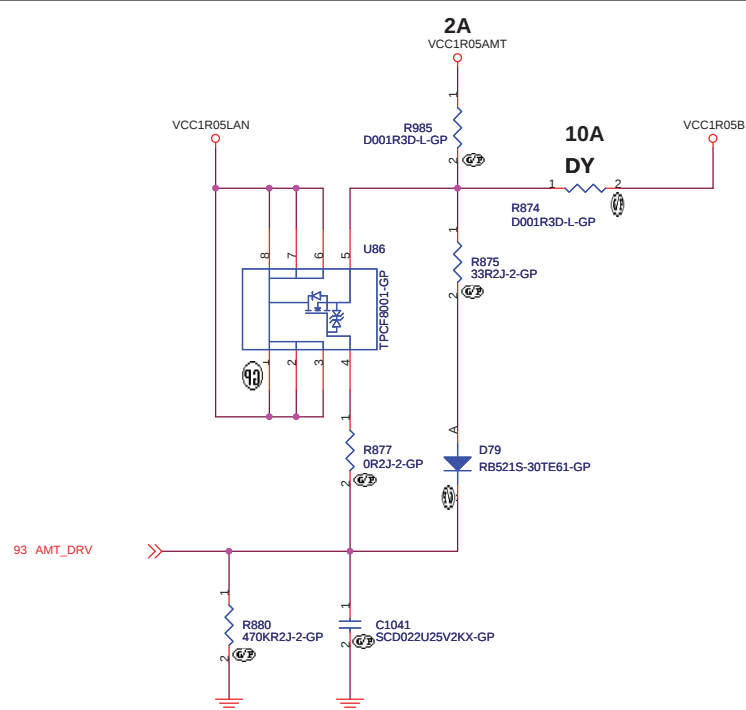




<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			
LOAD SW B			
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	REG/LITE	BB/NO
R881	NO-ASM	ASM
Q70	ASM	NO-ASM
R885	ASM	NO-ASM
R883	ASM	NO-ASM
C1042	ASM	NO-ASM
R884	ASM	NO-ASM
Q59	ASM	NO-ASM

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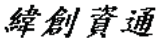
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

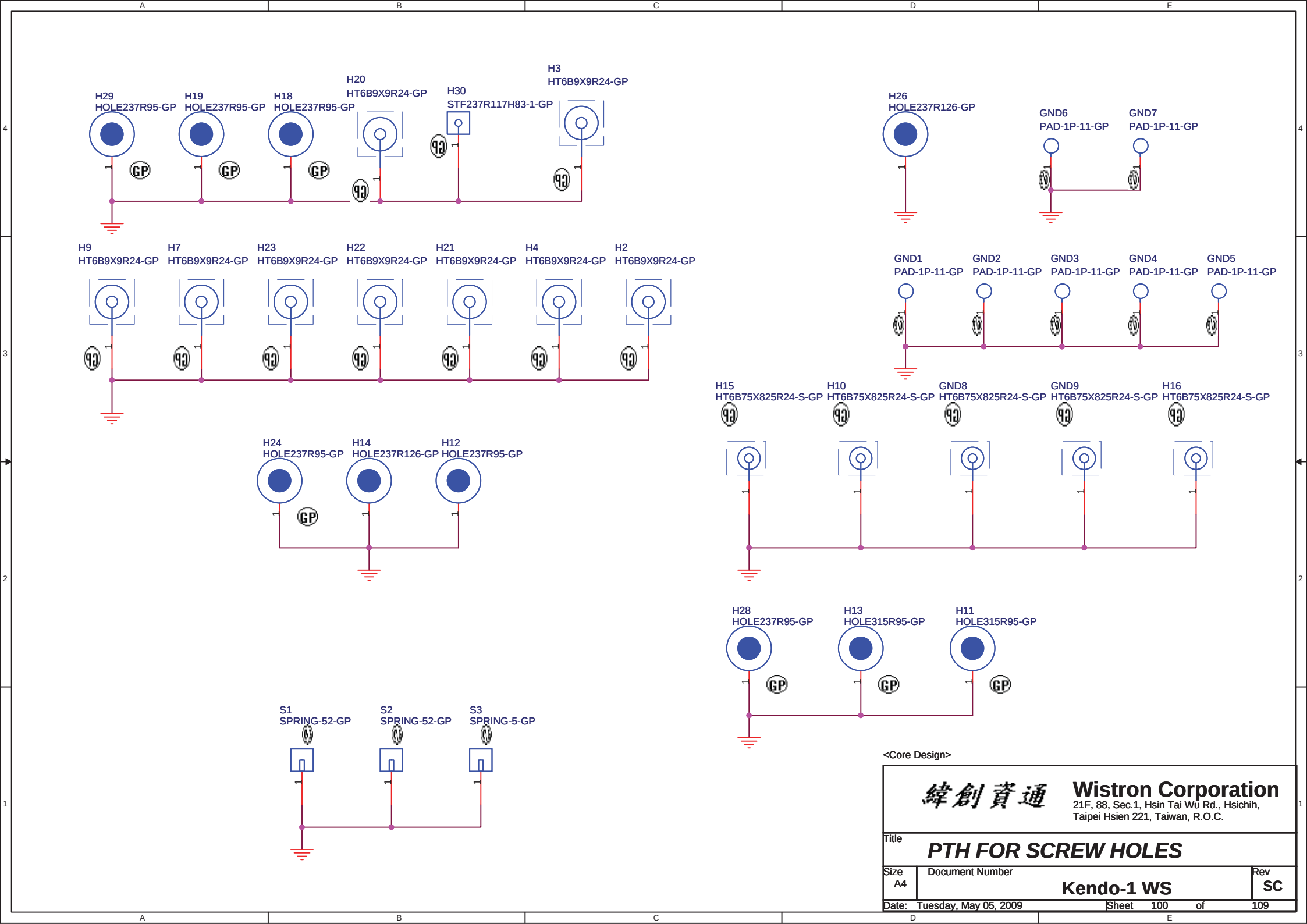
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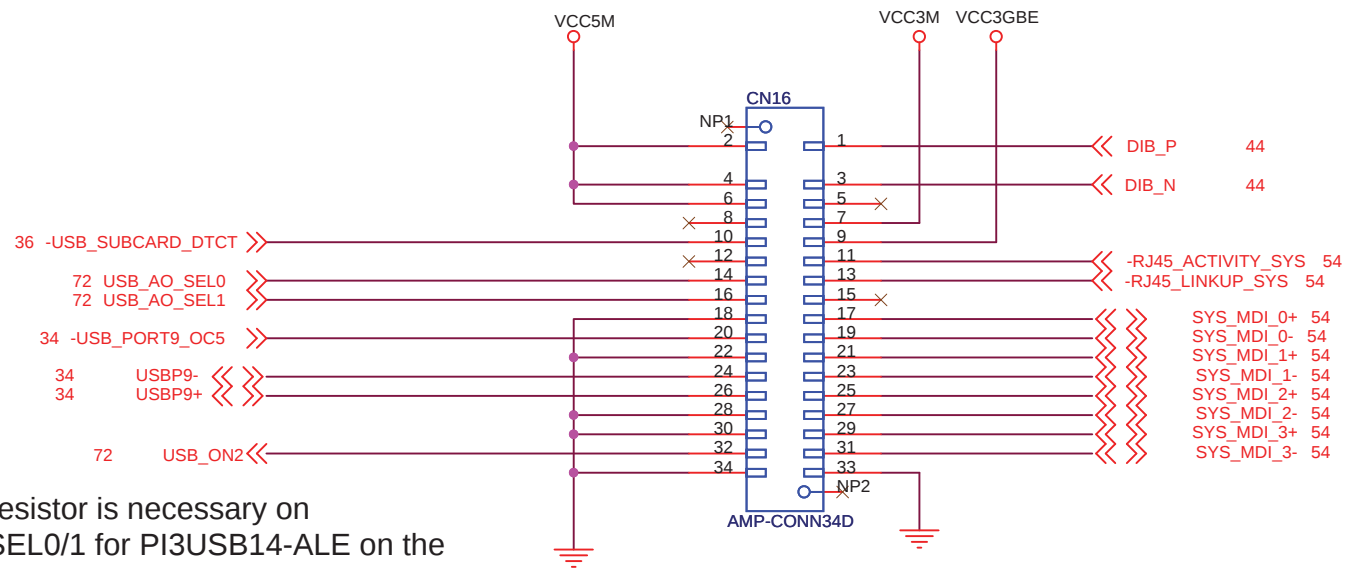
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Title BLANK			
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<Core Design>

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
PTH FOR SCREW HOLES			
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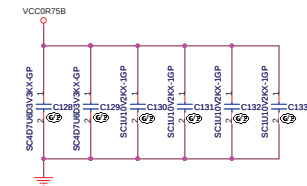
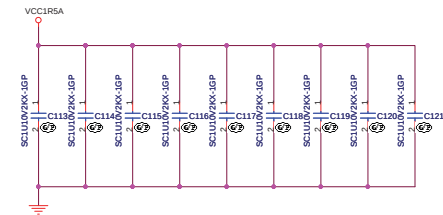
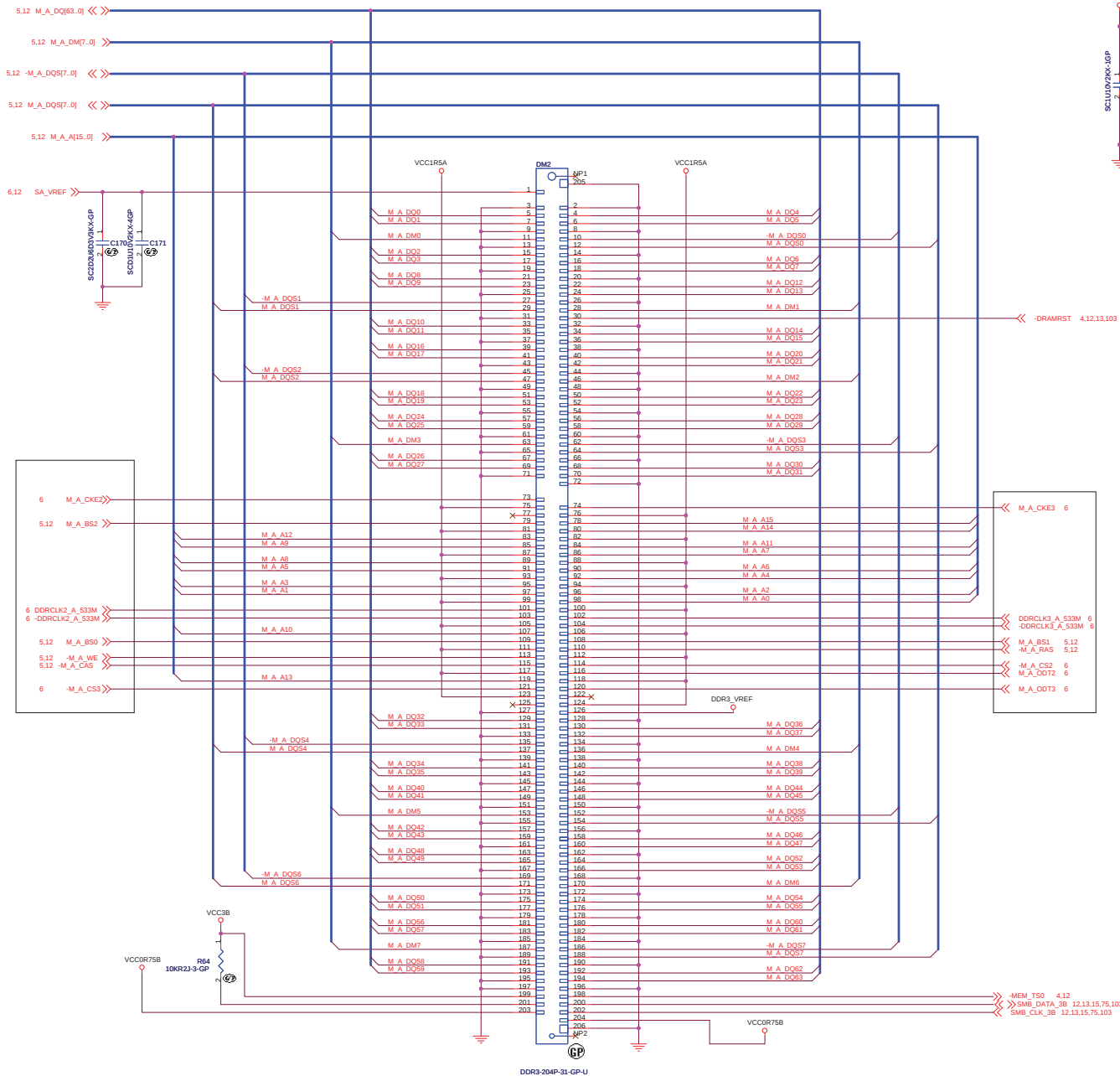
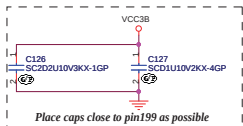
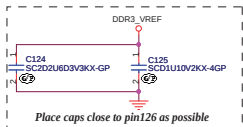
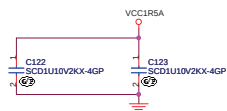


Pull-down resistor is necessary on
USB_AO_SEL0/1 for PI3USB14-ALE on the
USB BRD.

Change Pin order and pin number to 34.

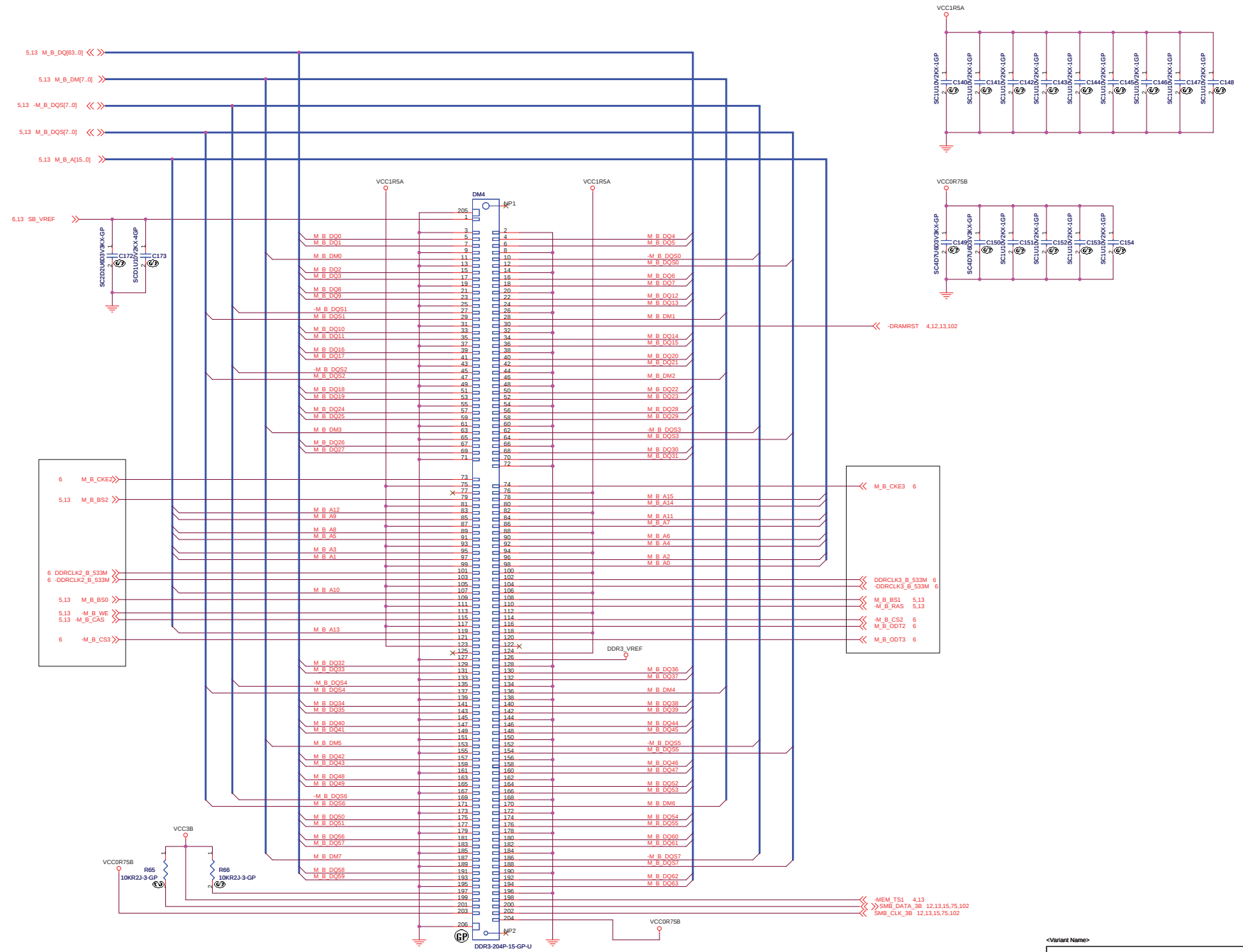
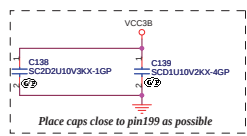
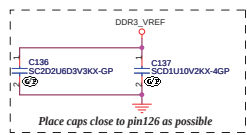
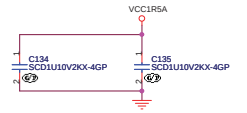
<Variant Name>

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
USB BOARD CONN			
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Date:	Tuesday, May 05, 2009	Sheet 101 of	109



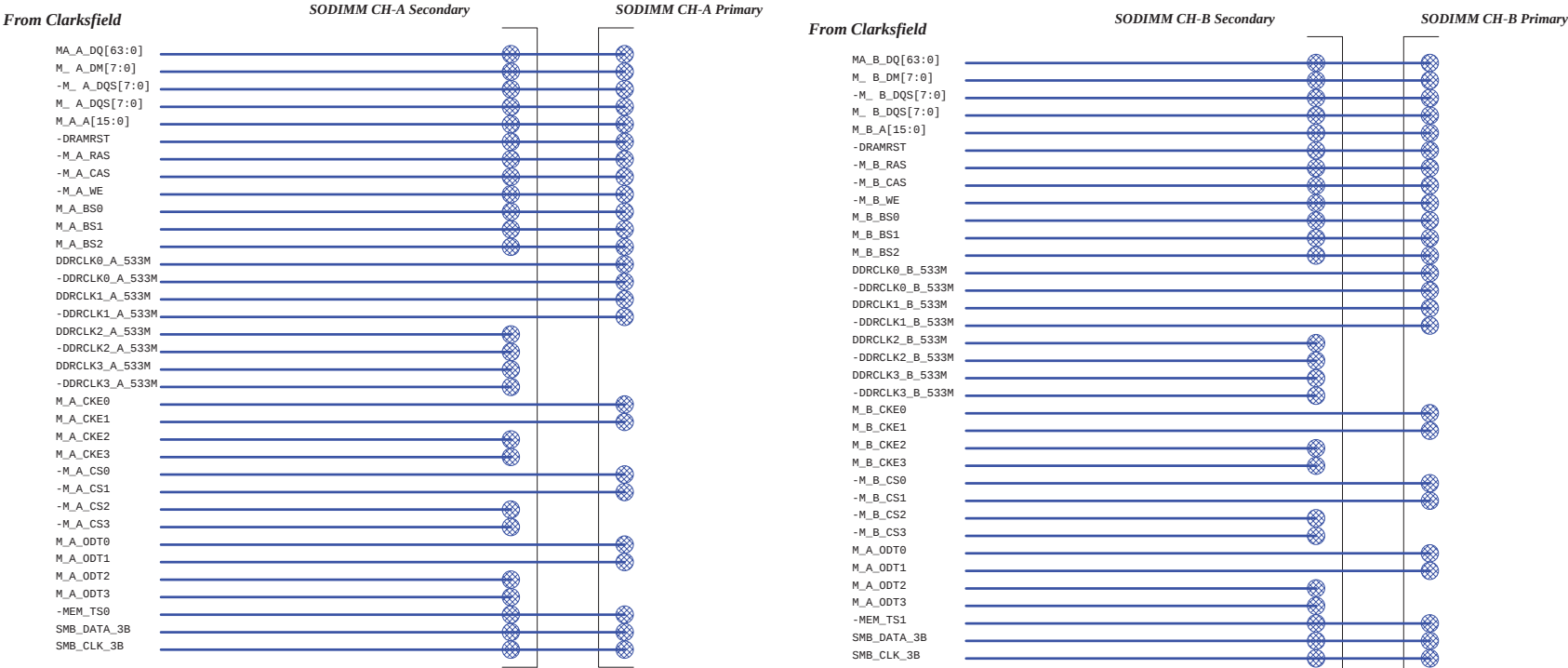
SPD Address : 52h

This connector should be placed on near side from CPU.



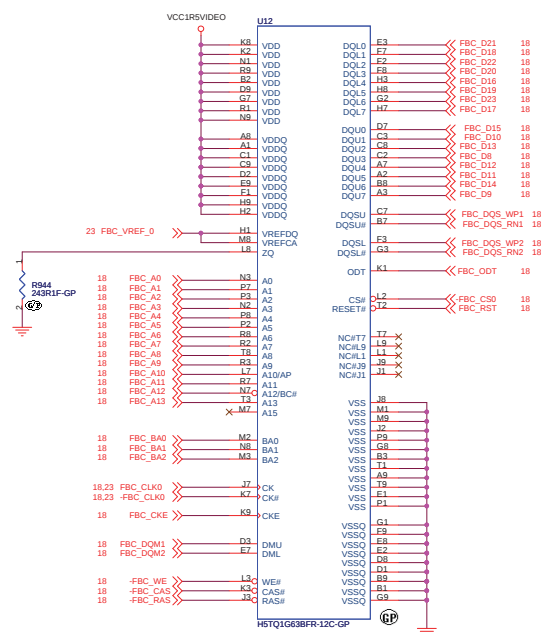
SPD Address : 53h
 This connector should be placed on near side from CPU.

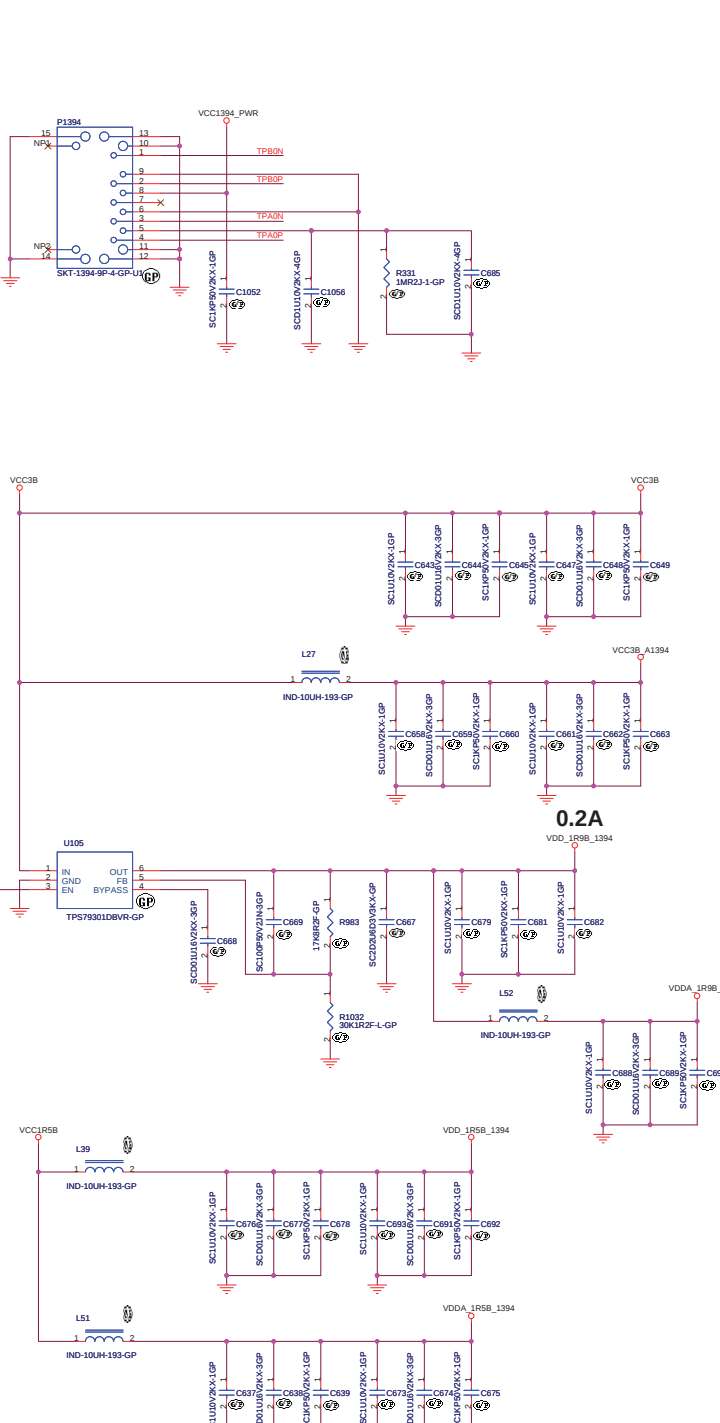
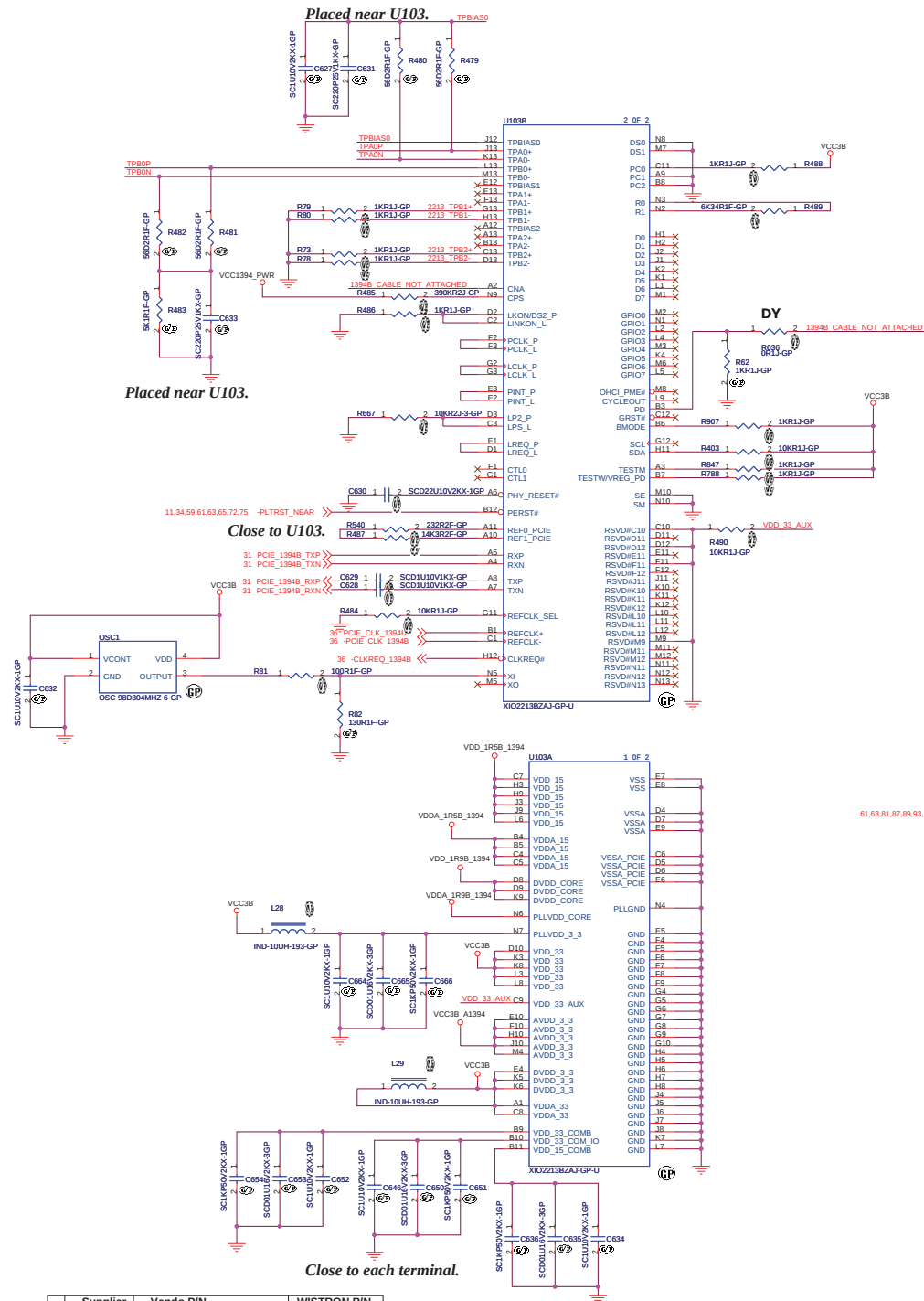
SODIMM should be installed on CH0 Primary at first.



SODIMM IIC Address :
CH-A Primary : 50h
CH-B Primary : 51h
CH-A Secondary : 52h
CH-B Secondary : 53h

Pin1 on SODIM connector (VREF_DQ) only connects to DDR Voltage divider.
Clarksfield H17/J17 is left.





Supplier	Vendo PIN	WISTRON P/N
1 KDS	DSO321SR 98.304M 50PPM	82.20075.061
2 HARMONY	HSO321S 98.304M 50PPM	82.20075.071

Long power trace EMI decoupling caps

<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

EMI DECOUPLING

Size

Document Number

Rev

Custom

Kendo-1 WS

SC

Date: Tuesday, May 05, 2009

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