

August ' 06

Pecan-1 Block Diagram

07234-2-Final

Project Code: 91.48Q01.001
PCB(Raw Card): 48.48Q01.0SA

PCB Layer Stackup

L1:TOP
L2:Signal 1
L3:GND
L4:Signal 2
L5:VCC
L6:GND
L7:Signal 3
L8:BOTTOM

Battery Charger/Selector

INPUTS	OUTPUTS
DOCK_PWR20_F	M-BAT-PWR S-BAT-PWR

System DC/DC

TPS51221	48
VINT20	VCC5M VCC3M

CPU DC/DC

VT1311	49
VINT20	VCCCPUCORE

GMCH GFX CORE

VT451&VT450	50
VINT20	VCCGFXCORE

VCC1R05AMT/VCC1R5A

VT351	51, 52
VCC5V_OUT	VCC1R05AMT VCC1R5A

VCC0R75AMT

MAX1510	53
VCC1R5A	VCC0R75AMT

VCC1R8B

BD3550	54
VCC3M	VCC1R8B

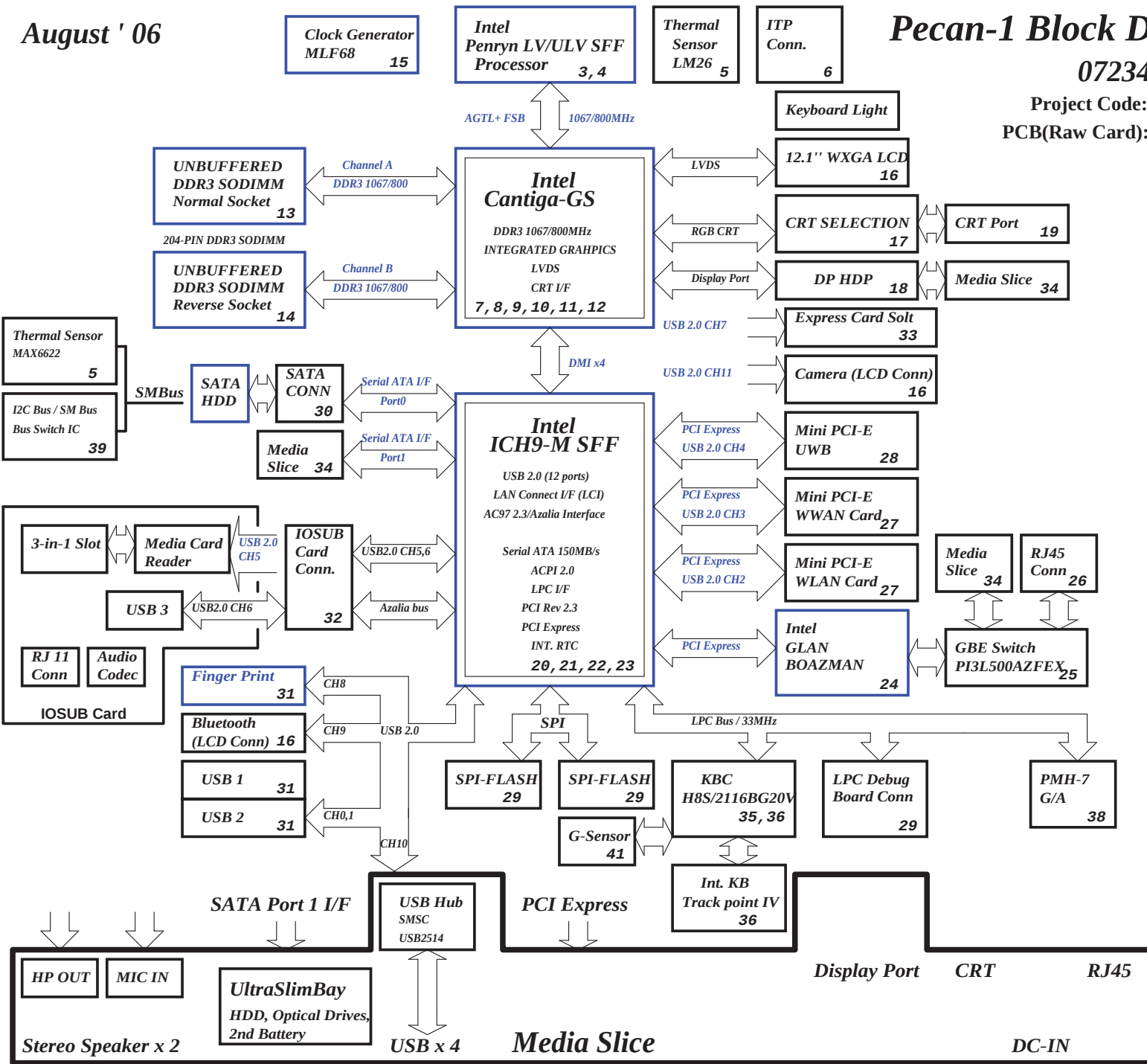
VCC1R05AUX/1R8AUX

BD3550	55
VCC3M	VCC1R05AUX VCC1R8AUX

<Variant Name>

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Taipei Hsien 221, Taiwan, R.O.C.

Title	Size	Document Number	Rev
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RESISTOR

Symbol name	Value	Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	Rating 0402>> 1/16W, 25V 0603 >> 1/16W, 75V 0805 >> 1/10W, 100V	Size 2>>0402, 3>>0603, 5>>0805, 6>>1206, 0>>1210
10KR3	10K Ohm	If no letter, it means J: 5%	1/16W, 75V	0603
33D3R5	33.3 Ohm	If no letter, it means J: 5%	1/10W, 100V	0805
1KR3F	1K Ohm	F: 1%	1/16W, 75V	0603

The naming rule is value + R + size + tolerance
 For the value, it can be read by the number before R. (R means resistor)
 For the tolerance, it can be read from the last letter.
 For the rating, we don't show on the symbol name.
 For the size, R2=>0402, R3=>0603, R5=>0805,.....

CAPACITOR

Symbol name	Value	Tolerance (M: +/-20, K: +/-10, Z: +80/-20)	Rating	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
SCD1U10V2MX-1	0.1uF	M/X5R	10V	0402
SC10U6D3V5MX	10uF	M/X5R	6.3V	0805
SC2D2U16V5ZY	2.2uF	Z/Y5V	16V	0805

The naming rule is
Capacitor type + value + rating + size + tolerance + material
SCD1U10V2MX-1
SC=> SMT Ceramic, TC=> POS cap or SP cap
D1U => 0.1uF
10V => the voltage rating is 10V
2=> 0402, 3=>0603, 5=>0805
M=>tolerance M, K, Z
X=> X7R/X5R, Y=> Y5V
-1 => symbol version, nonsense to EE characteristic

PLANAR_ID[3..0]

ICH9-M GPIO#	39	38	37	36	Planar ID Version	Planar PCB Version
PLANAR_ID#	3	2	1	0		
	0	0	0	0	Pecan-1 Pre-DV	SA
	0	0	0	1	Pecan-1 DV	SB
	0	0	1	0	Pecan-1 SIV	SC
	0	0	1	1	Pecan-1 SIT	SD
	0	1	0	0	Pecan-1 SVT	-1
	0	1	0	1	Pecan-1 SOVP	-2
	0	1	1	0		
	0	1	1	1		

EC HISTORY

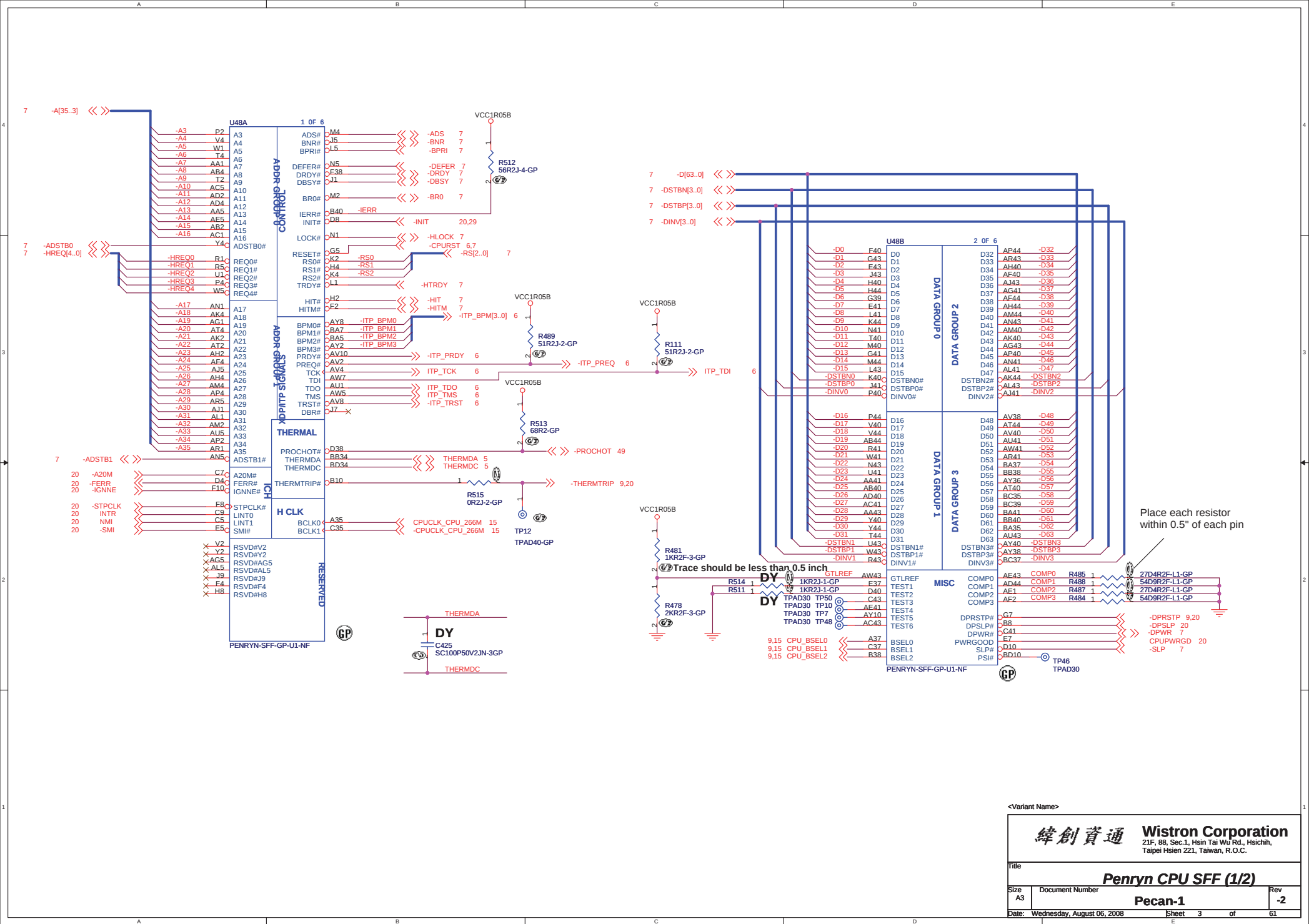
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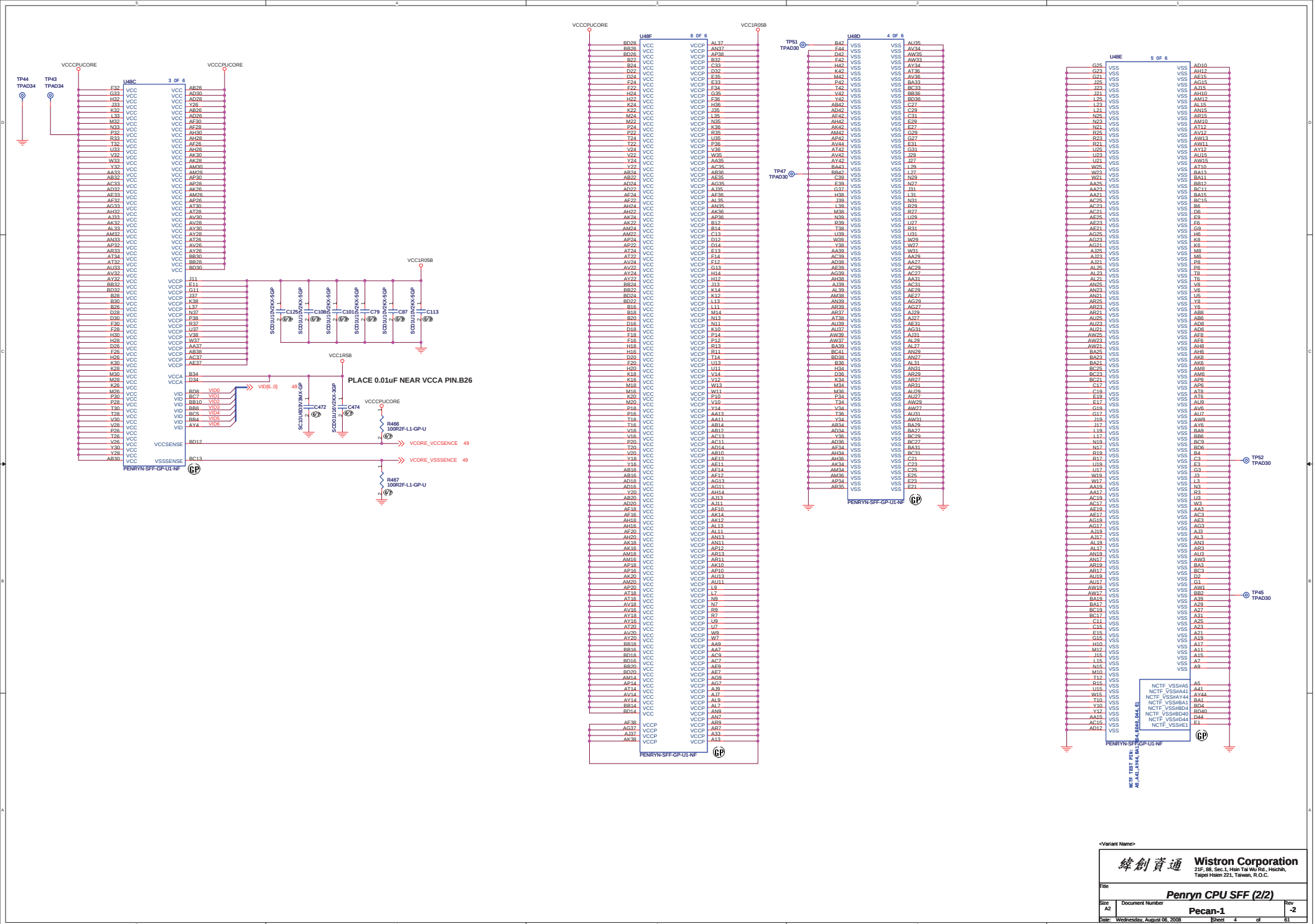
PCI TABLE

DEVICE	IDSEL	IRQ (Default)	REQ# / GNT#
MINIPCI SLOT	AD18	F, G	REQ# 3/ GNT#
CARDBUS R5C811	AD16	SERIRQ	REQ#0 / GNT#
USB UHCI	AD29	A, C, D	
USB 2.0 EHCI	AD29	H	
DMI-to-PCI/ AC97 Modem/ AC97 Audio	AD30	B B	
LPC Bridge IDE SATA SMBus	AD31	C C B	
PCI Express	AD28	A, B, C, D	

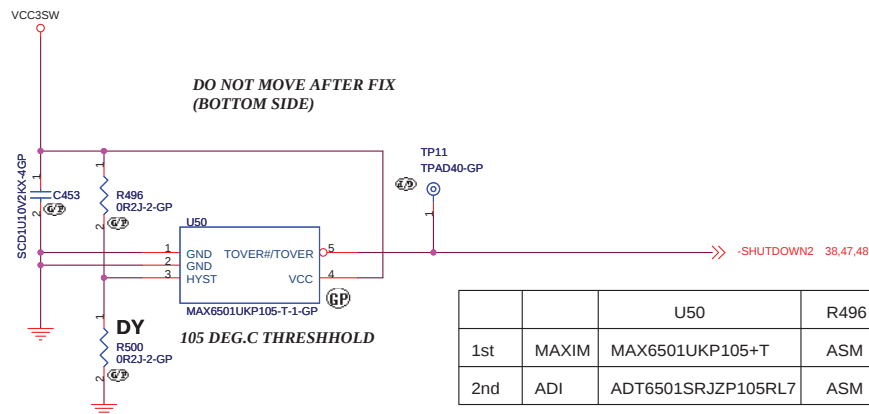
<Variant Name>

 緯創資通 Wistron Corporation 21F, 8B, Sec. 1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
Reference Pecan-1	
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Thermal Sensor for CPU



		U50	R496	R500
1st	MAXIM	MAX6501UKP105+T	ASM	NO_ASM
2nd	ADI	ADT6501SRJZP105RL7	ASM	NO_ASM

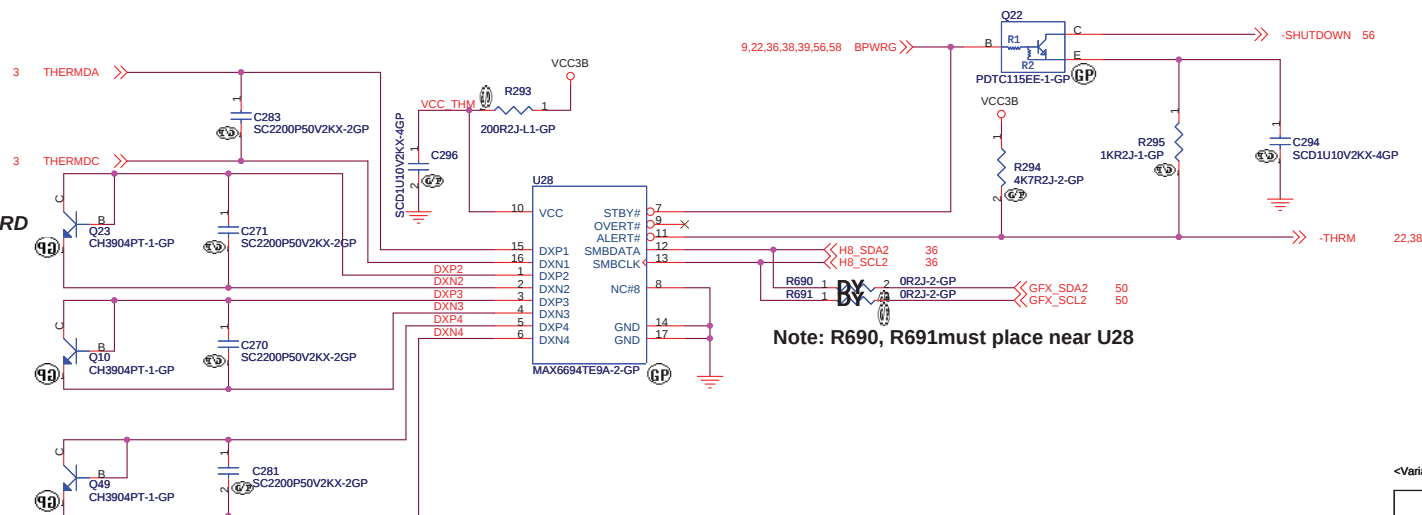
THESE CAPS MUST BE PLACED AS CLOSE AS POSSIBLE TO MAX6694

TO CPU DIE

TO EXPRESS CARD

TO VCORE FET

TO MINI CARD



PLACE UNDER PC-CARD SLOT

H8 I2C Bus 2 ADDRESS : 9AH

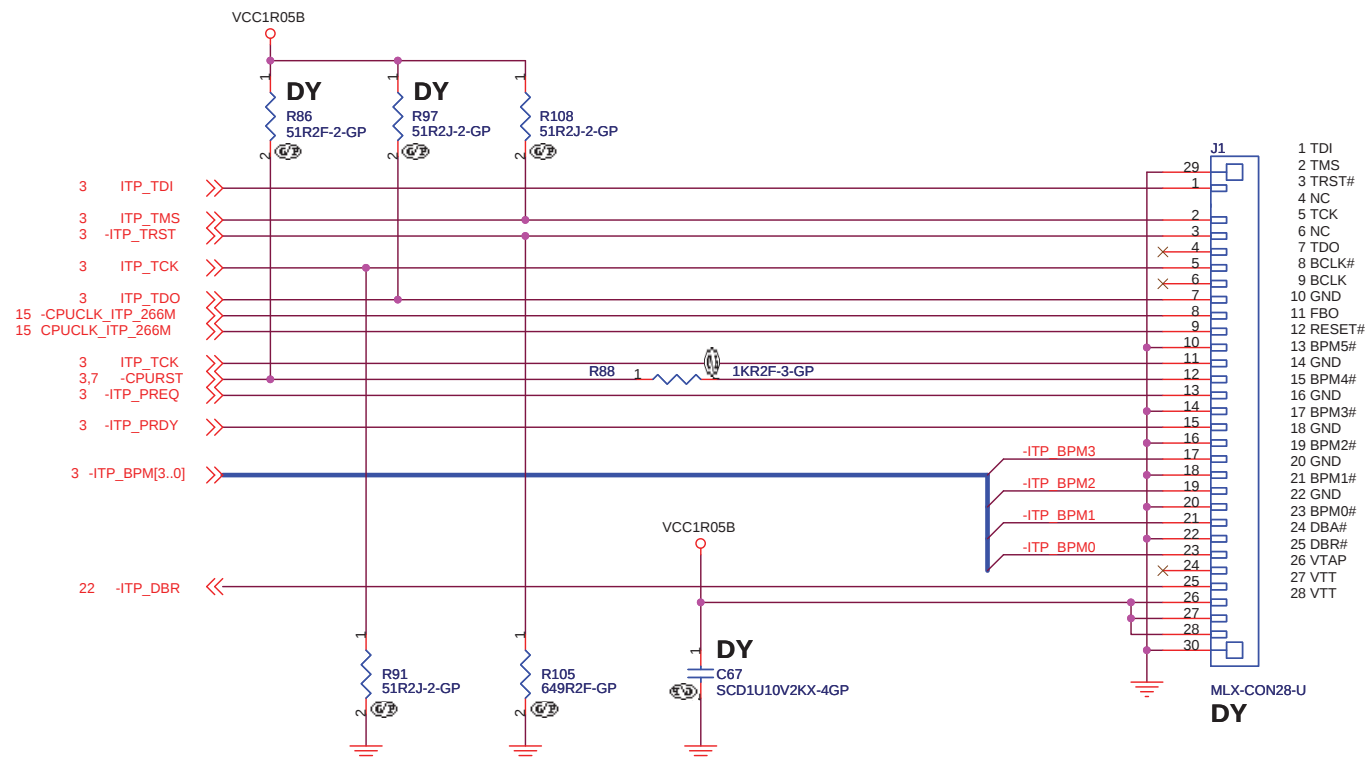
TEST PAD FOR BOARD MFG TEST

Layout Comment :

- (1) Thermal sensor trace lines should not be overlapped with other high frequency trace lines in other layers.
- (2) Also, it should not be overlapped with large amplitude trace lines either.

<Variant Name>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	THERMAL SENSOR
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- 1 TDI
2 TMS
3 TRST#
4 NC
5 TCK
6 NC
7 TDO
8 BCLK#
9 BCLK
10 GND
11 FBO
12 RESET#
13 BPM5#
14 GND
15 BPM4#
16 GND
17 BPM3#
18 GND
19 BPM2#
20 GND
21 BPM1#
22 GND
23 BPM0#
24 DBA#
25 DBR#
26 VTAP
27 VTT
28 VTT

MLX-CON28-U
DY

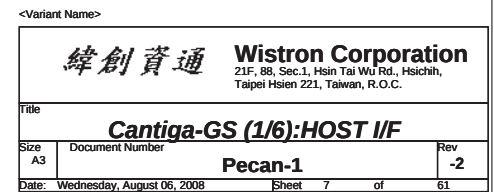
(*1) TCK SIGNAL IS BRANCHED AT CPU's PIN

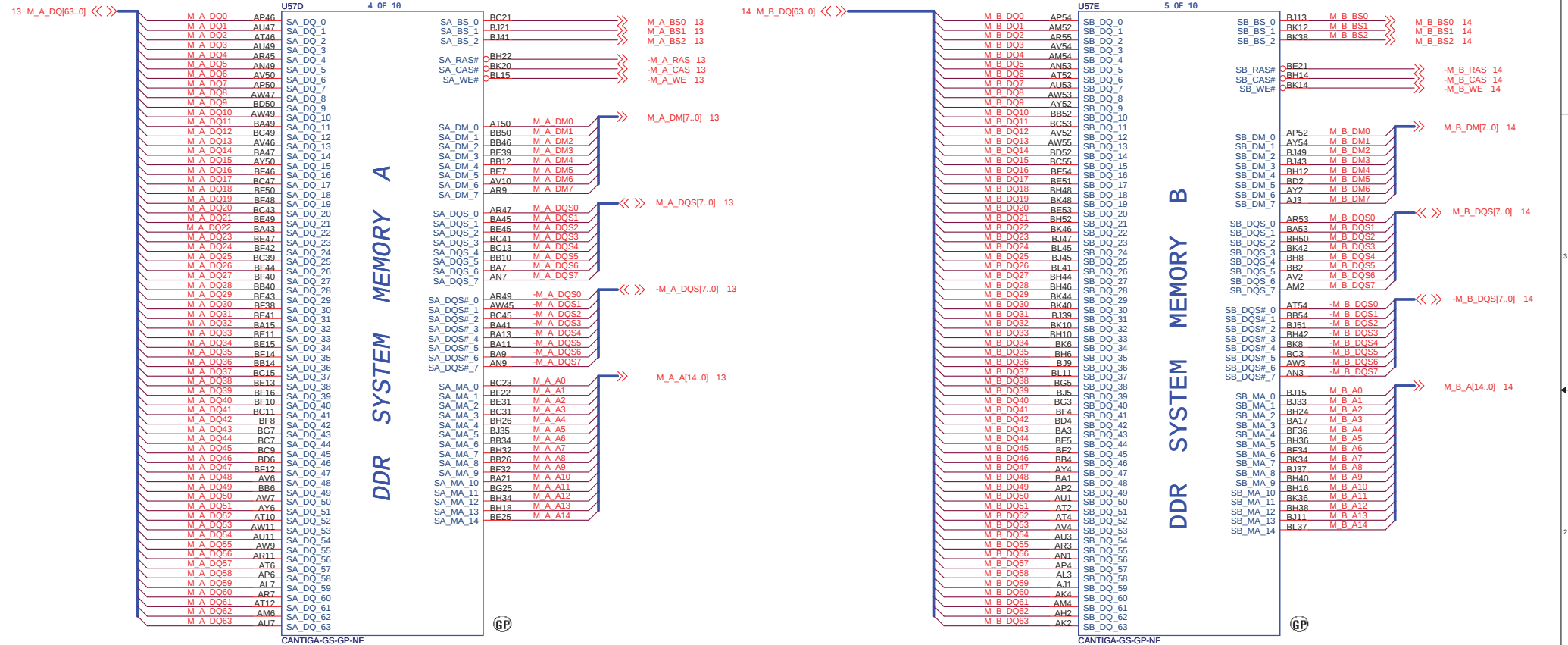
(*2) CPURST# SIGNAL IS BRANCHED AT GMCH's PIN

Ref Des	For ITP-XDP
J1	NO_ASM-->ASM
C67	NO_ASM-->ASM
R88	ASM (NO Change)
R105	ASM (No Change)
R86	NO_ASM (NO Change)
R97	NO_ASM-->ASM
R91	ASM (No Change)
R108	ASM (No Change)

<Variant Name>

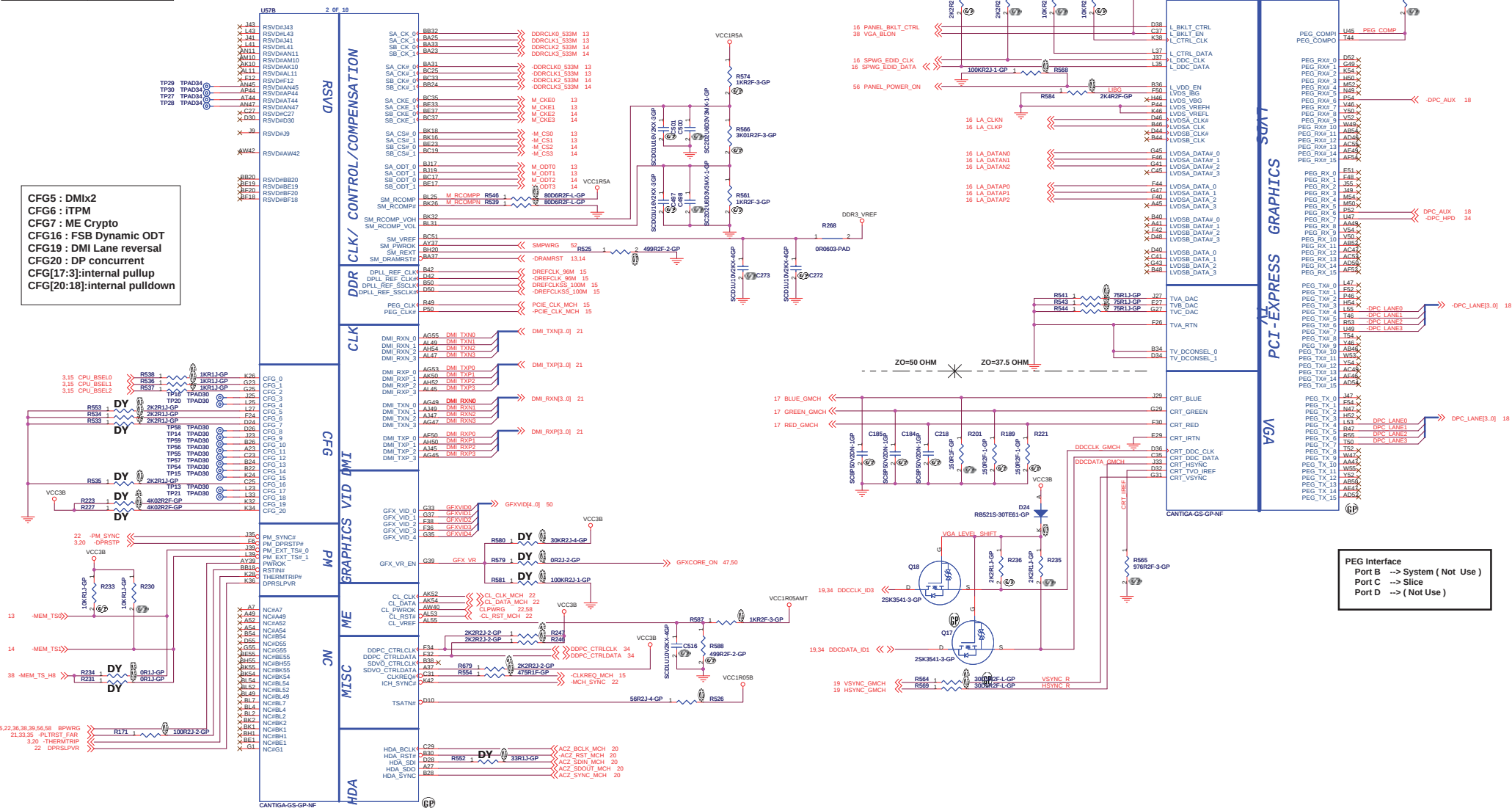
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Title		ITP_XDP CONN	
Size Custom	Document Number Pecan-1	Rev -2	
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ME DEBUG PORT PIN OUT TABLE

RESERVED#AN45	ME_JTAG_TCK
RESERVED#AP44	ME_JTAG_TDI
RESERVED#AT44	ME_JTAG_TDO
RESERVED#AN47	ME_JTAG_TMS



<Variant Name>

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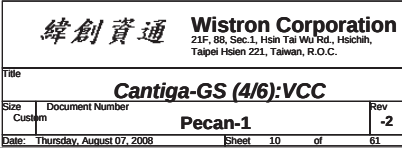
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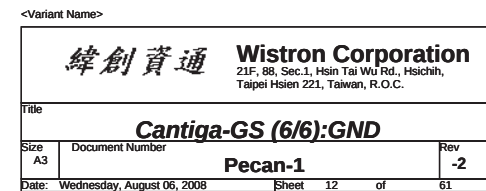
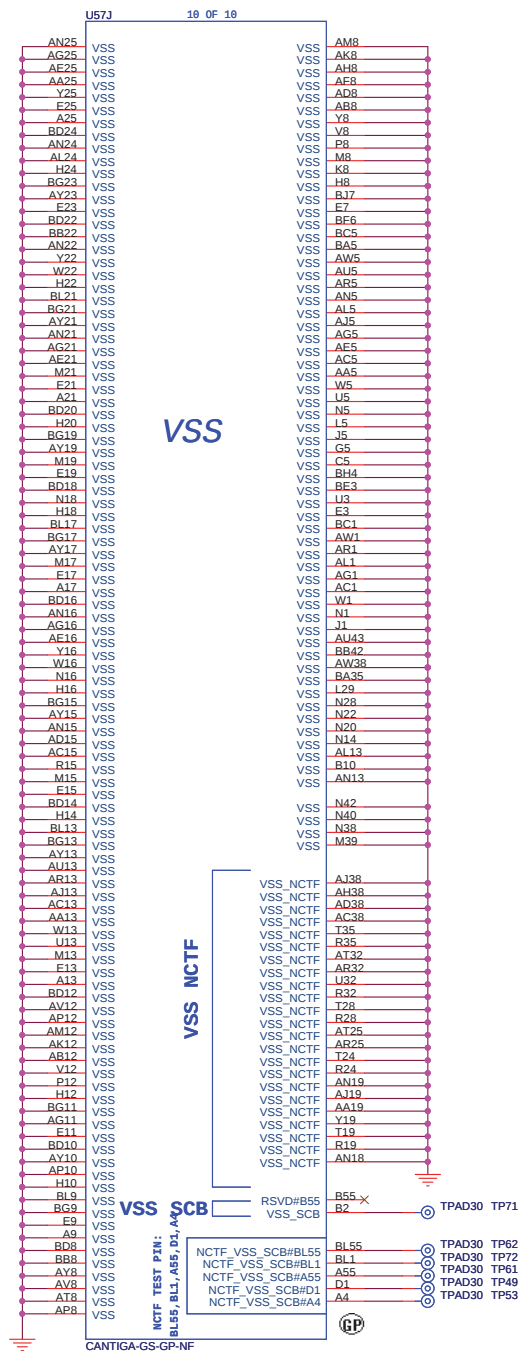
Cantiga-GS (3/6):DMI/PM/CFG/GF

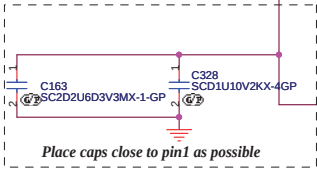
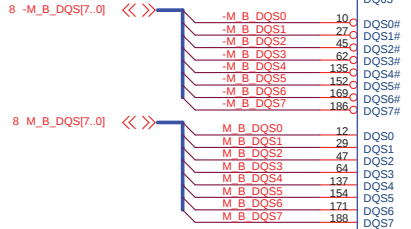
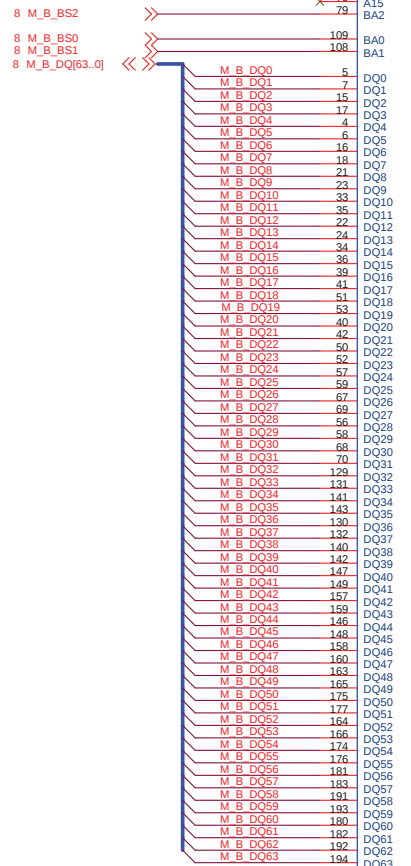
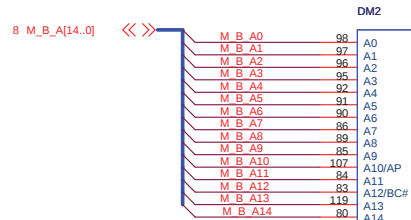
Rev -2

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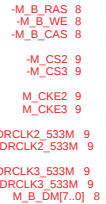




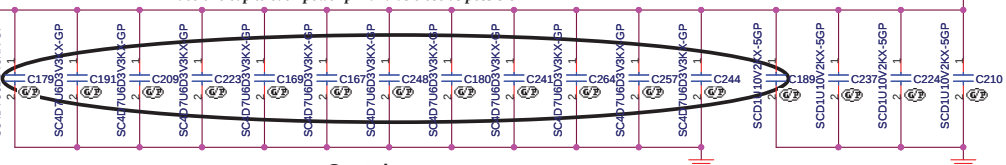


DDR3-204P-15-GP

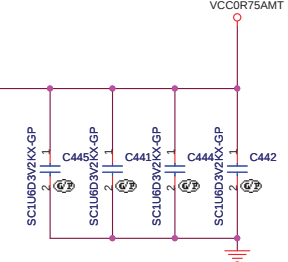
REVERSE TYPE



Place one cap to each power pin and as close as possible



Cost down



<Variant Name>

緯創資通

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Title		
DDR3 SODIMM-B (REVERSE TYPE)		
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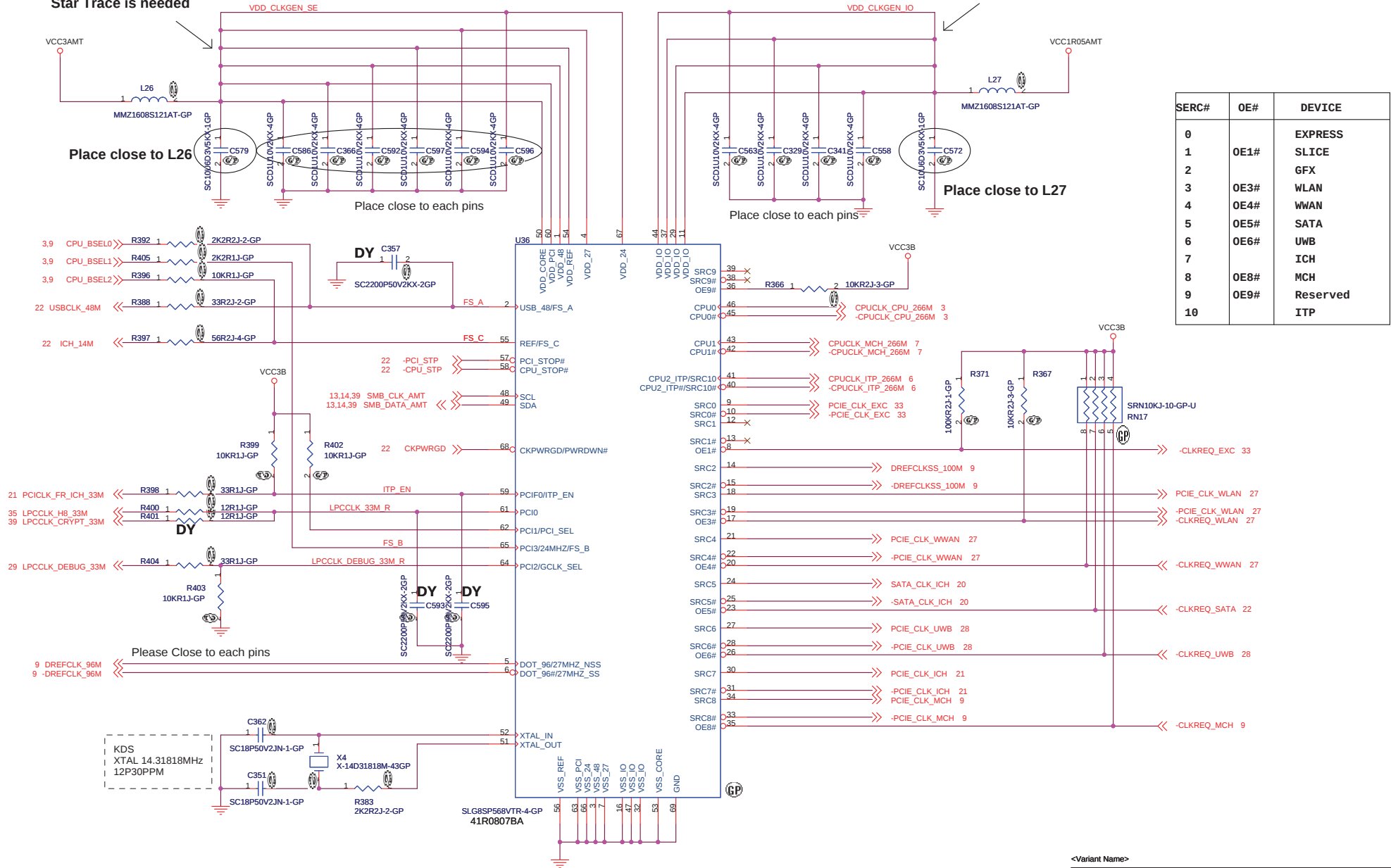
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2

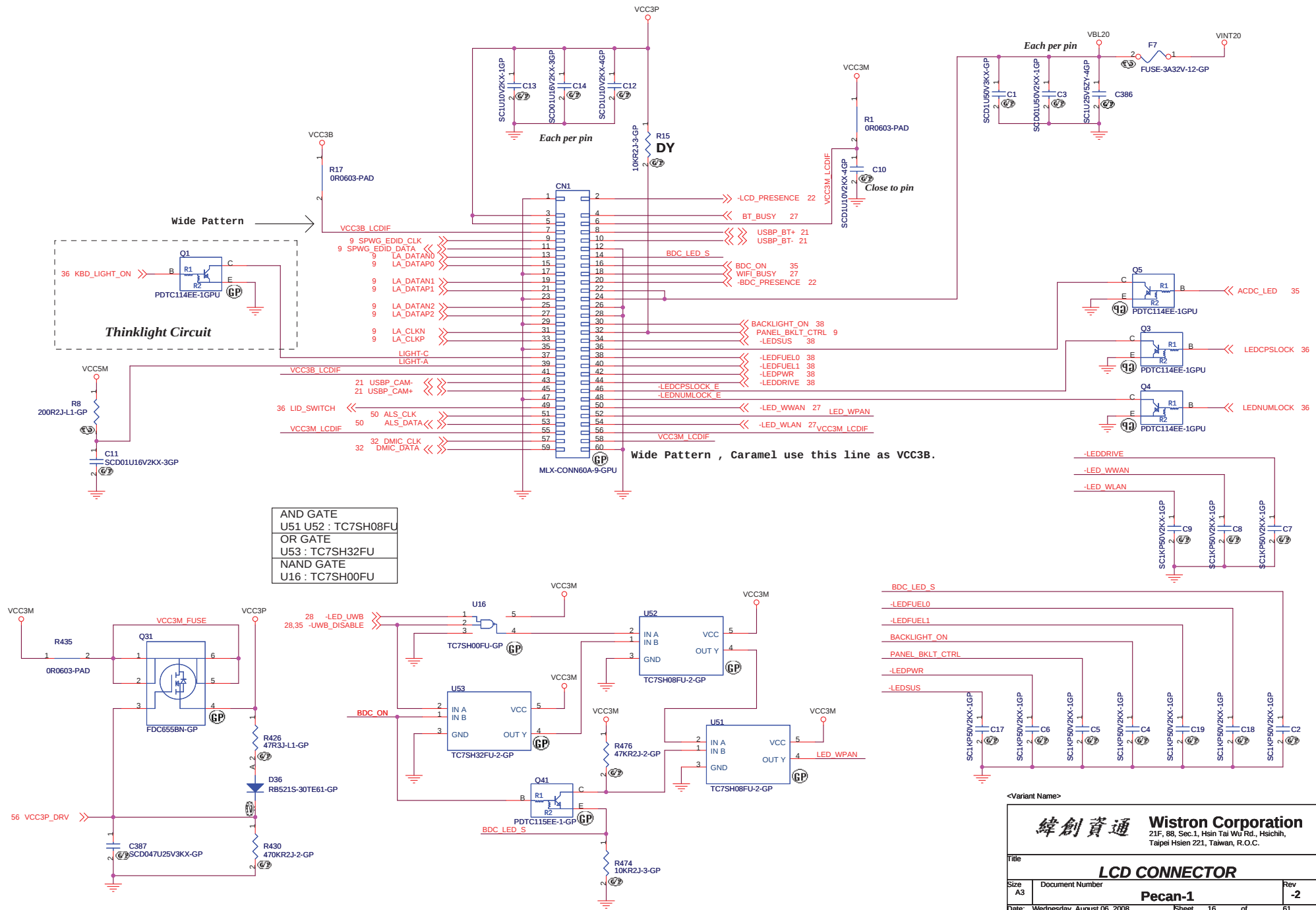
1

Star Trace is needed

Star Trace is needed



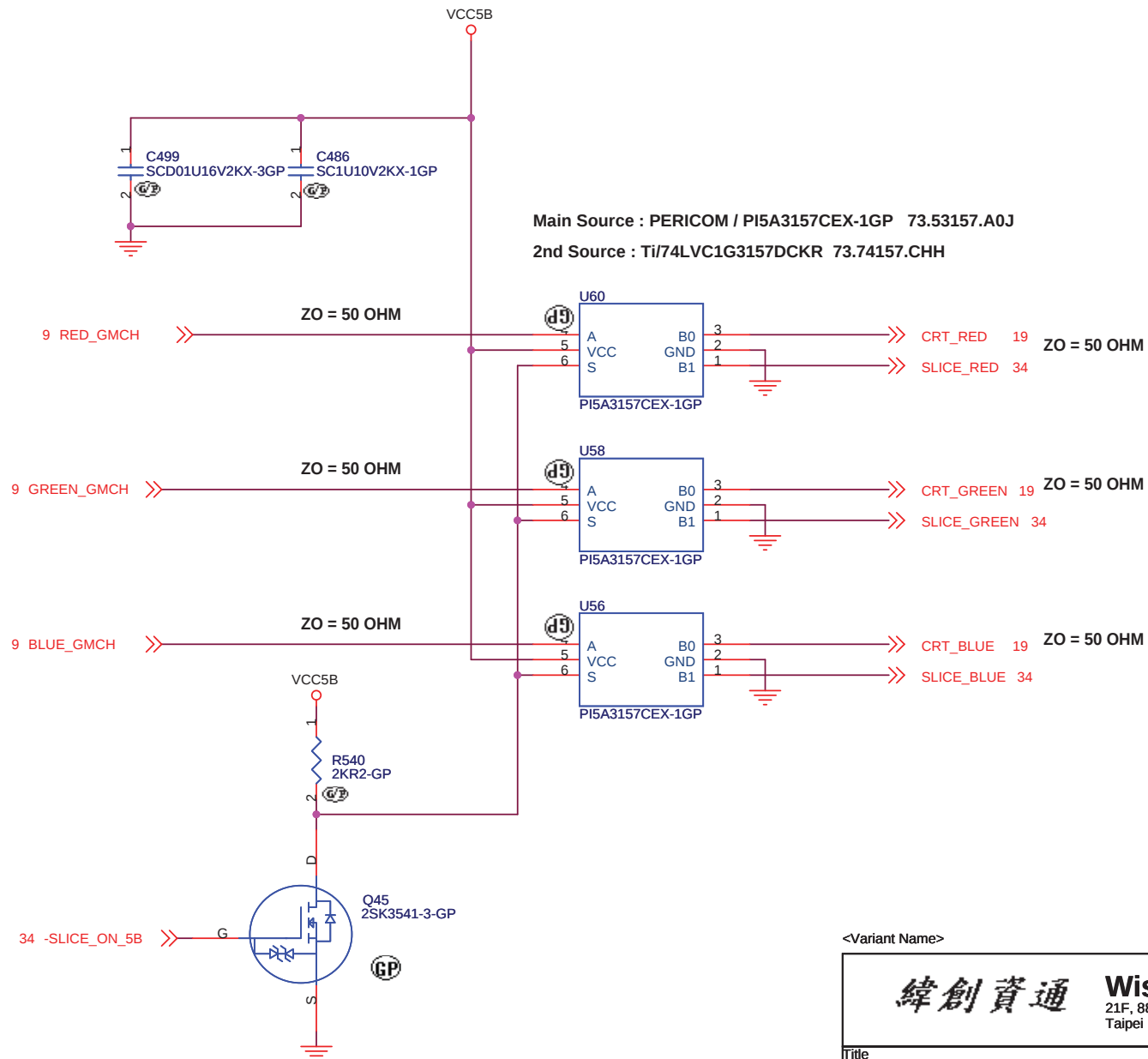
LCD / Inverter Connector



<Variant Name>

緯創資通 Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

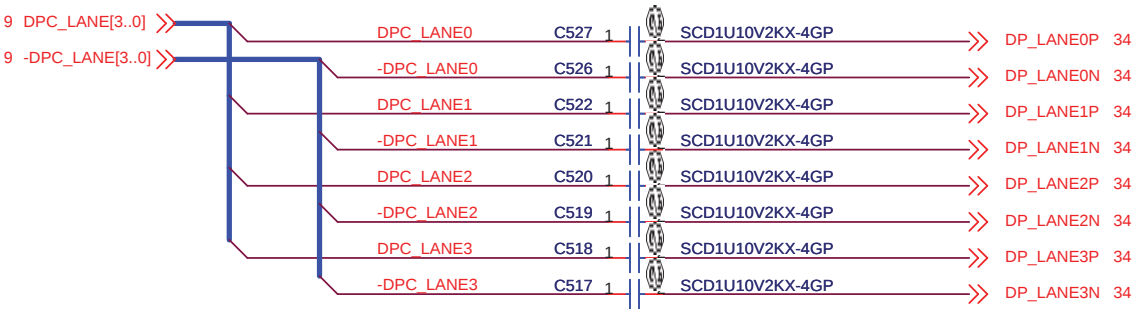
Title			
LCD CONNECTOR			
Size A3	Document Number	Rev Pecan-1 -2	
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<Variant Name>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
CRT SELECTOR	
Size A4	Document Number Pecan-1
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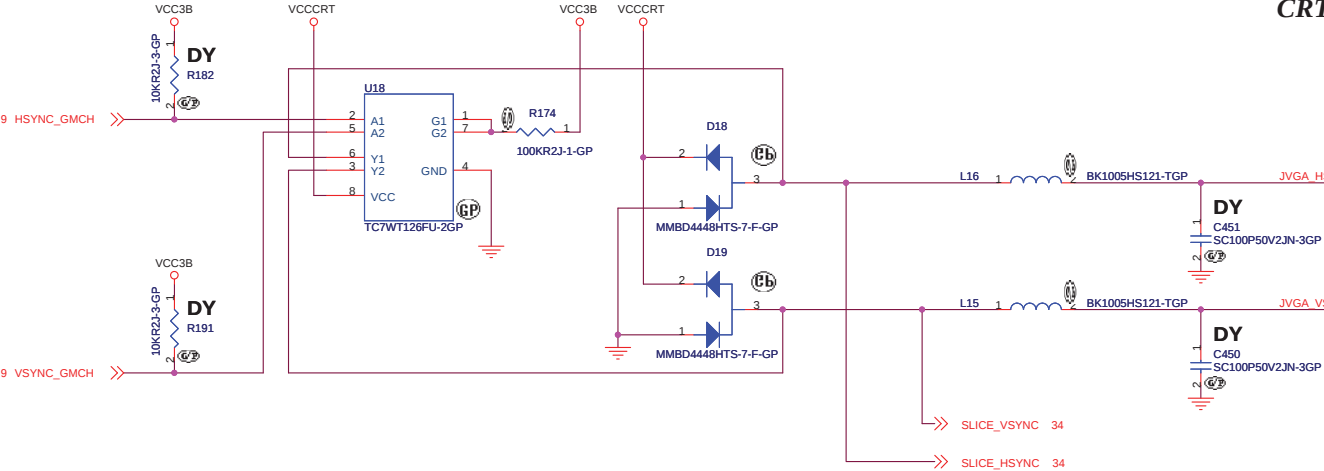
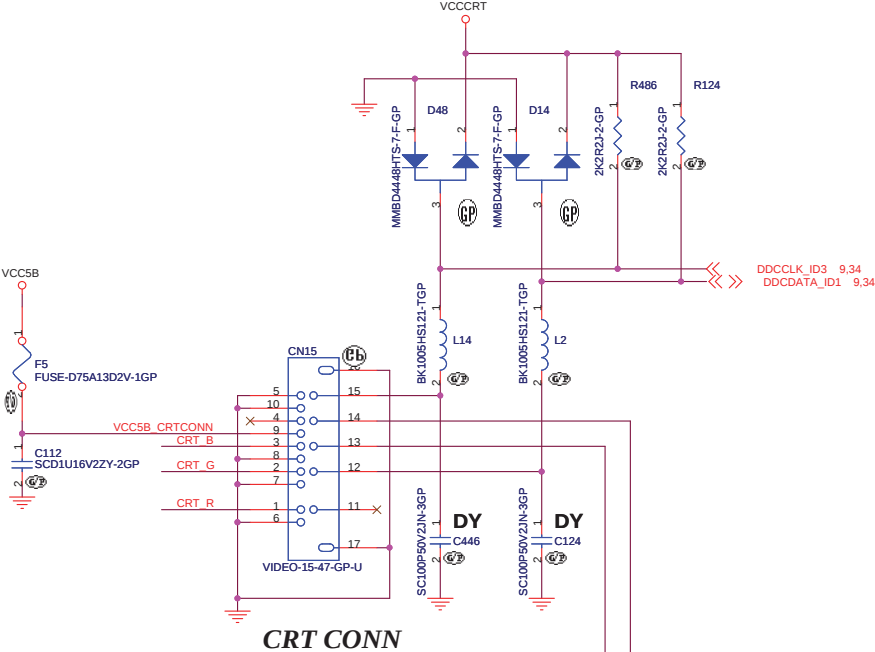
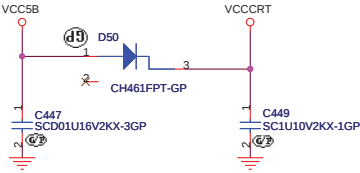
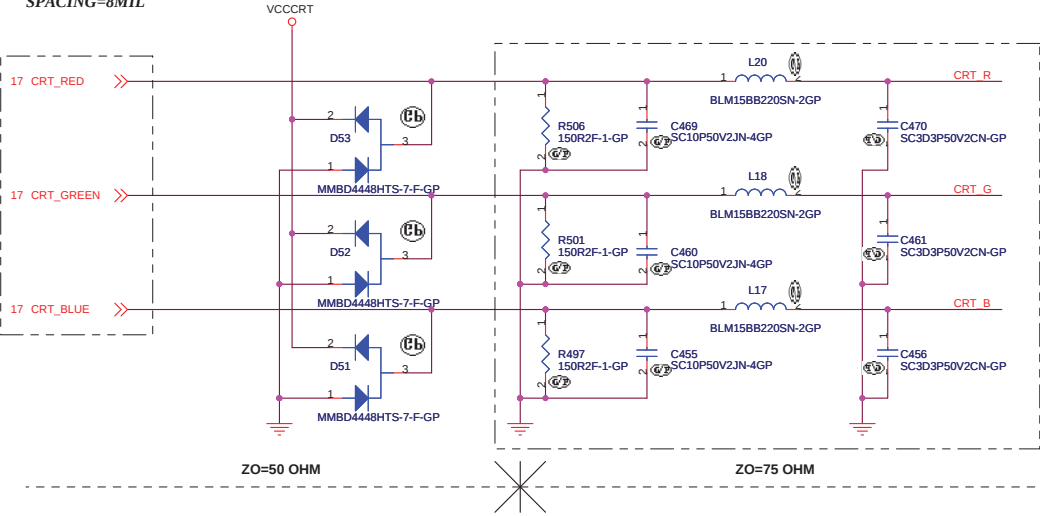
System DP: GMCH to SLICE Connector

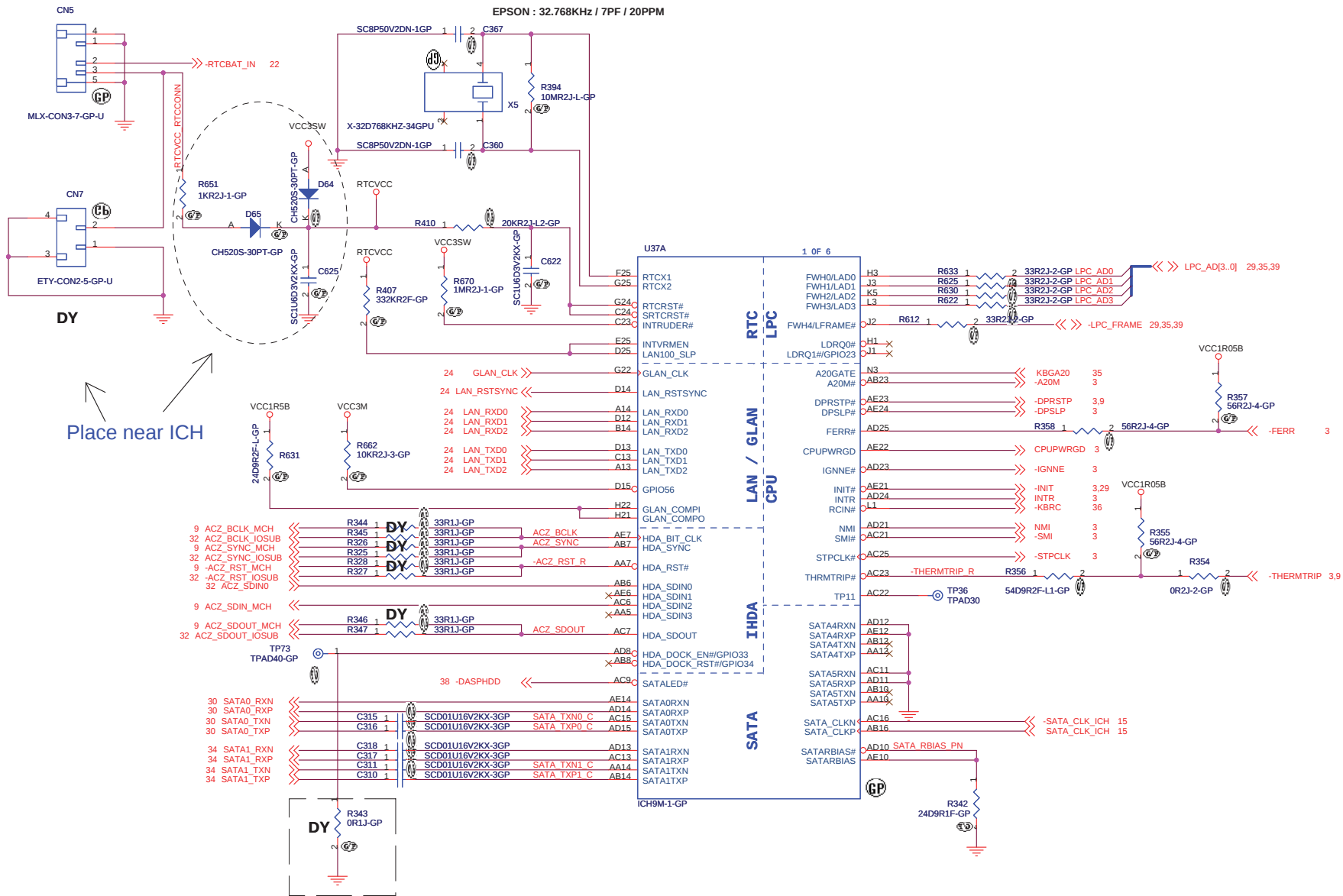


<Variant Name>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title Display Port AC Coupling		
Size A4	Document Number Pecan-1	Rev -2
Date: Wednesday, August 06, 2008		

GND GUARDING
EACH SIGNAL WIDTH DEPENDS ON ZO(TRACE IMPEDANCE)
SPACING=8MIL

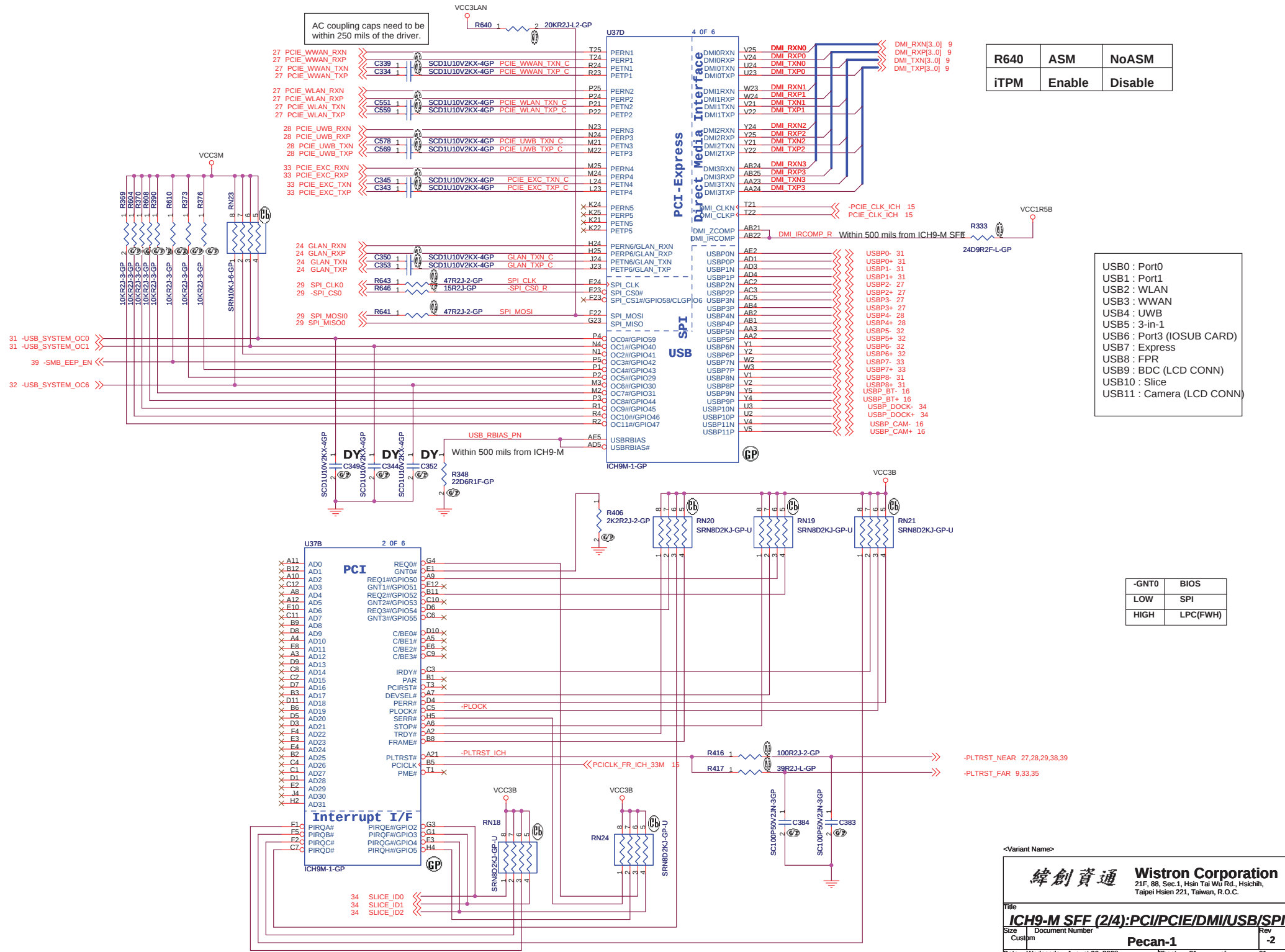




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Taipei Hsien 221, Taiwan, R.O.C.

Title			
ICH9-M SFF (1/4):ATA/AC97/LPC			
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AC coupling caps need to be within 250 mils of the driver.



R640	ASM	NoASM
iTPM	Enable	Disable

- USB0 : Port0
- USB1 : Port1
- USB2 : WLAN
- USB3 : WWAN
- USB4 : UWB
- USB5 : 3-in-1
- USB6 : Port3 (IOSUB CARD)
- USB7 : Express
- USB8 : FPR
- USB9 : BDC (LCD CONN)
- USB10 : Slice
- USB11 : Camera (LCD CONN)

-GNT0	BIOS
LOW	SPI
HIGH	LPC(FWH)

<Variant Name>

緯創資通 **Wistron Corporation**
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Title
ICH9-M SFF (2/4):PCI/PCIE/DMI/USB/SPI

Size Document Number
Custom

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-2

CLGPIO5 PHYSICAL PRESENCE
ACTIVE HIGH
CAN USE SOFTWARE OVERRIDE FLAG

	PECAN_ID
Mocha-1	H
Pecan-1	L
Caramel-1	L

<Variant Name>

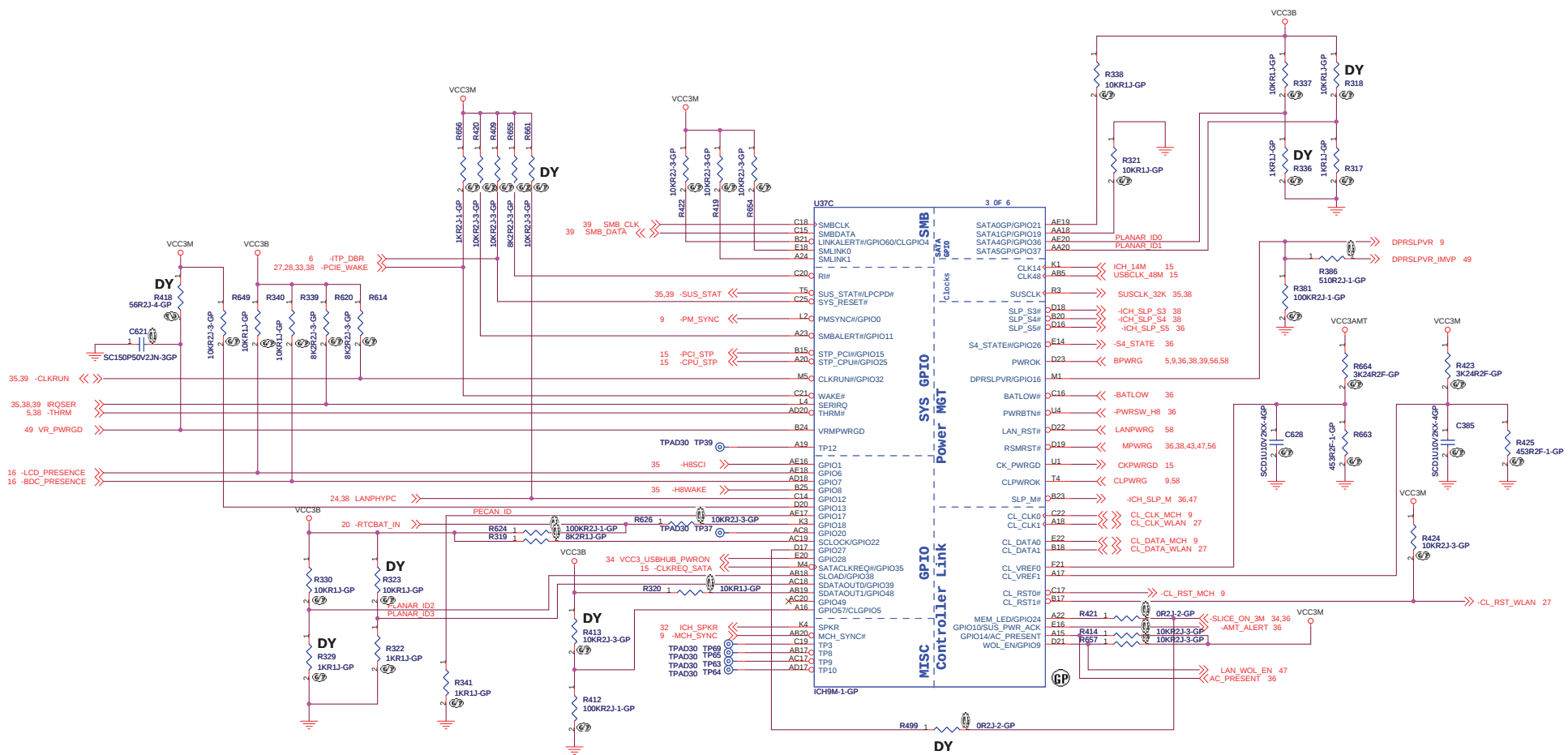
緯創資通 Wistron Corporation
21F, 88, Sec 1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

File: **ICH9-M SFF (3/4):PM/SMB/GPIO**

Size: Document Number

CusNm: **Pecan-1** Rev: -2

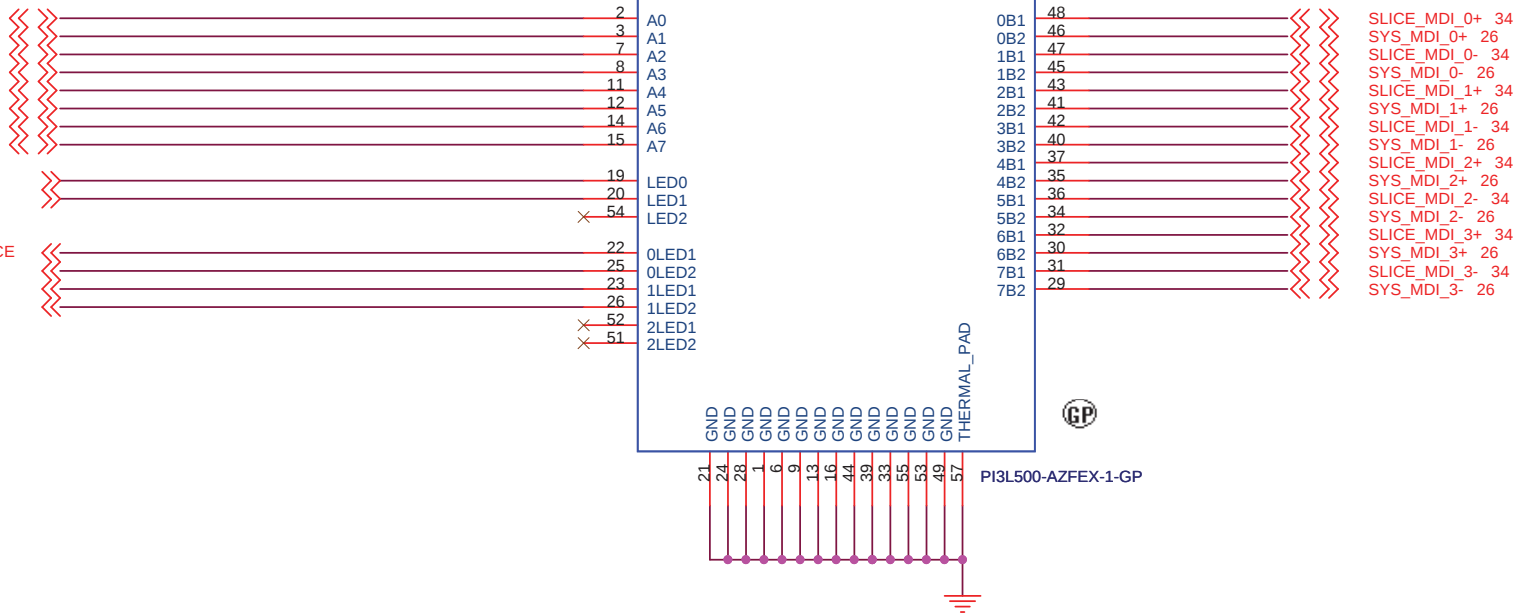
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24 MDI_0+
24 MDI_0-
24 MDI_1+
24 MDI_1-
24 MDI_2+
24 MDI_2-
24 MDI_3+
24 MDI_3-

24 -RJ45_ACTIVITY
24 -RJ45_LINKUP

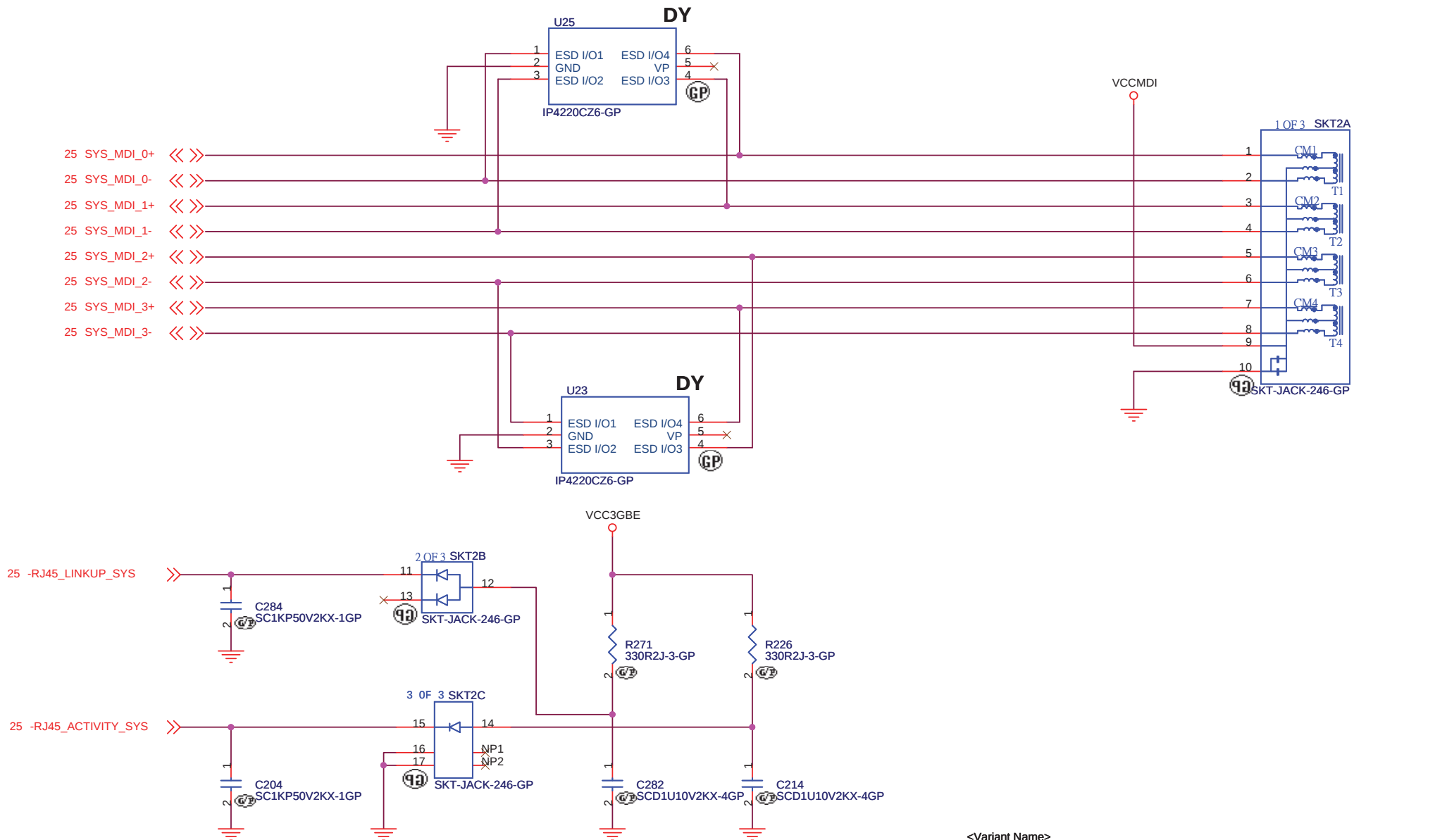
34 -RJ45_ACTIVITY_SLICE
26 -RJ45_ACTIVITY_SYS
34 -RJ45_LINKUP_SLICE
26 -RJ45_LINKUP_SYS



[Source Cadidate]				
1st	Pericom	PI3L500AZFEX	73.3L500.003	41R0539AA
2nd	TI	TS3L500AERHUR	73.3L500.A0V	41R0539BA

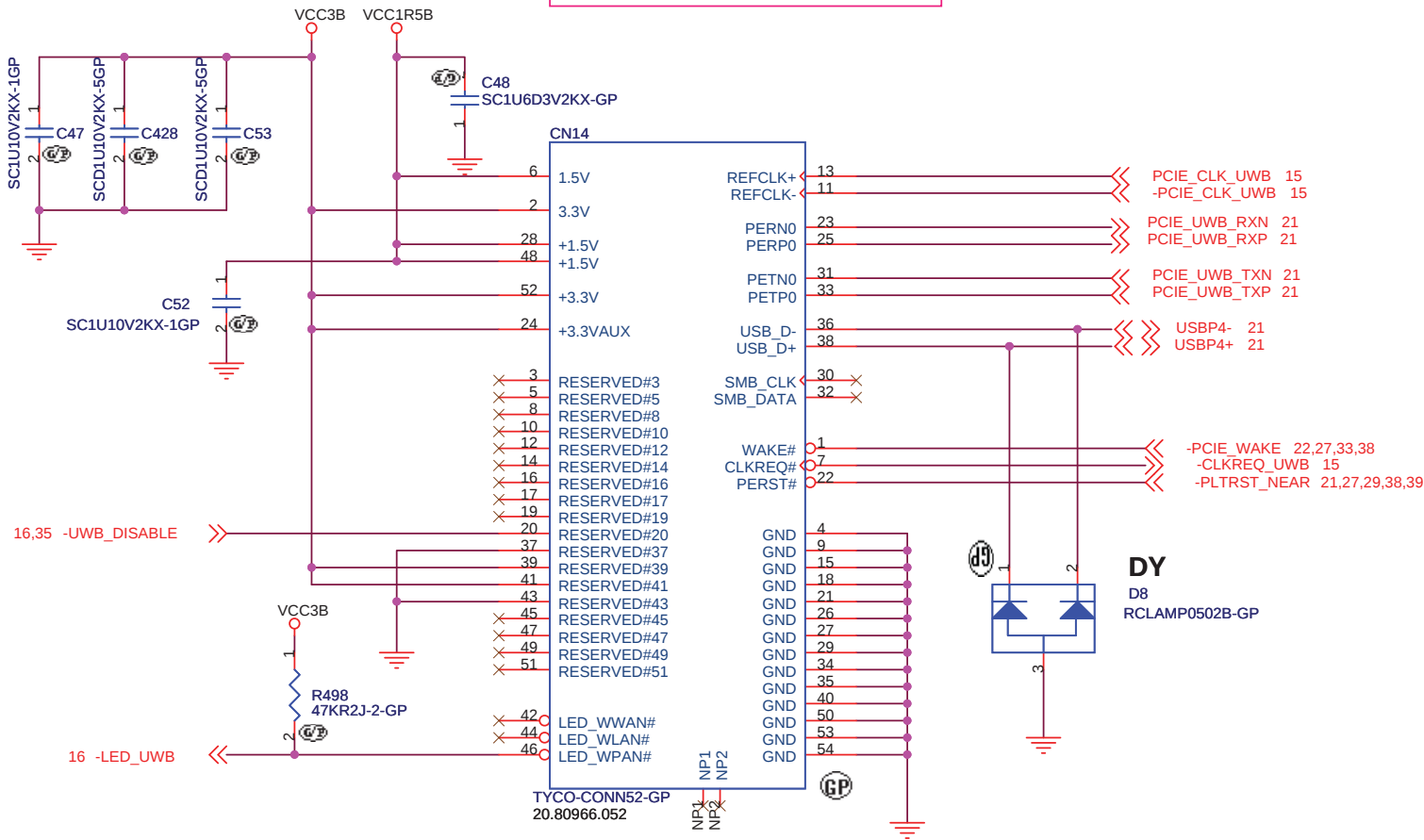
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 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
GBE LAN SW	
Size A4	Document Number Pecan-1
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<Variant Name>

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
RJ45 CONN			
Size A4	Document Number Pecan-1		Rev -2
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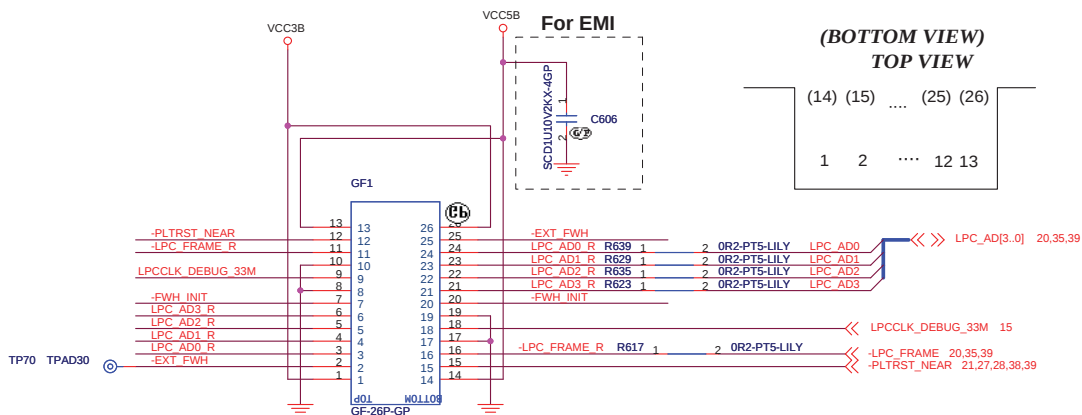


CN14
1st: 20.80966.052(TYCO)
2st:

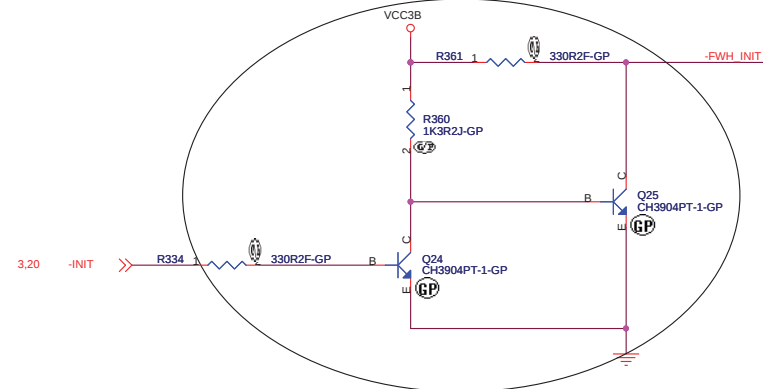
<Variant Name>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
MINI CARD SLOT 2			
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Golden Finger for Debug Board



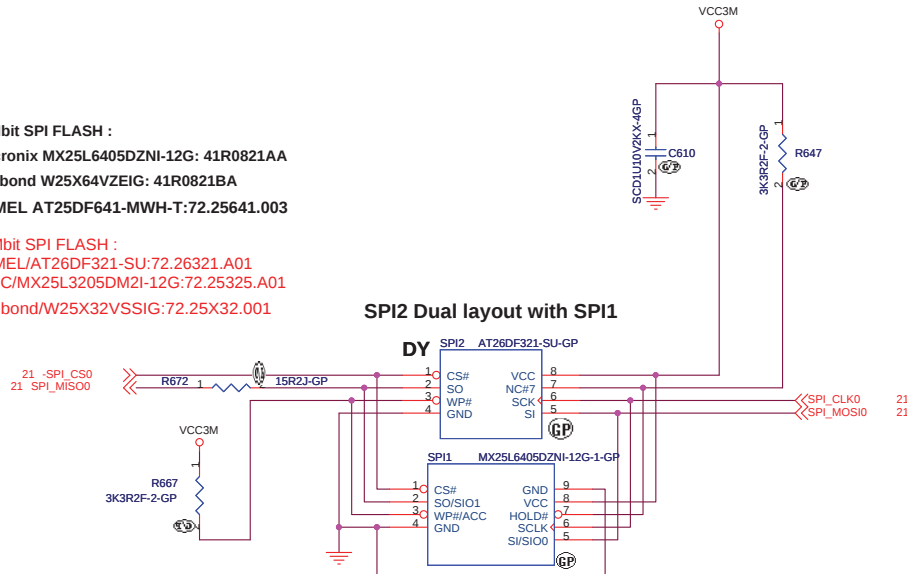
Check need to ALL NoASM the same KS-3



64Mbit SPI FLASH :
 Macronix MX25L6405DZNI-12G: 41R0821AA
 Winbond W25X64VZEIG: 41R0821BA
 ATMEL AT25DF641-MWH-T:72.25641.003

32Mbit SPI FLASH :
ATMEL/AT26DF321-SU:72.26321.A01
MXIC/MX25L3205DM2I-12G:72.25325.A01
Winbond/W25X32VSSIG:72.25X32.001

SPI2 Dual layout with SPI1



SO8 and WSON8 are both supported!

<Variant Name>

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Title

SPISize
A3

Document Number

Pecan-1

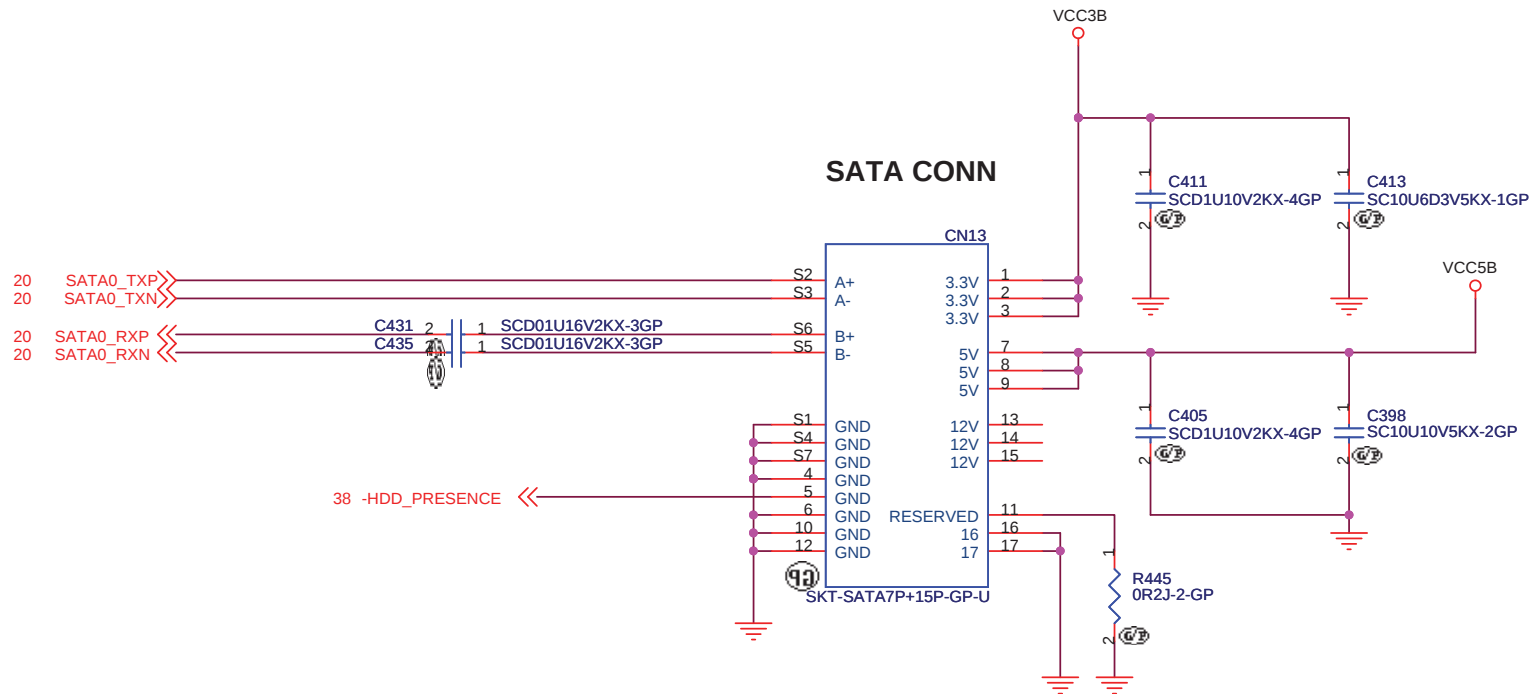
Rev

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<Core Design>

緯創資通

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Title

2.5 inch SATA Connector

Size
A4

Document Number

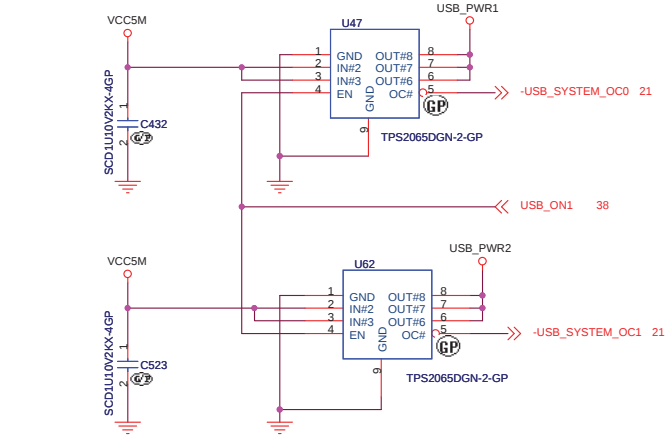
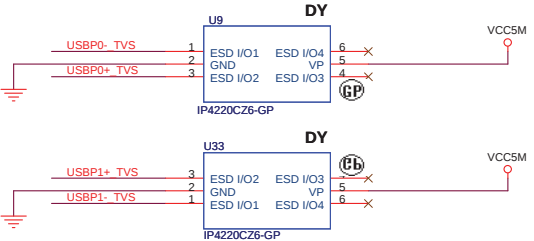
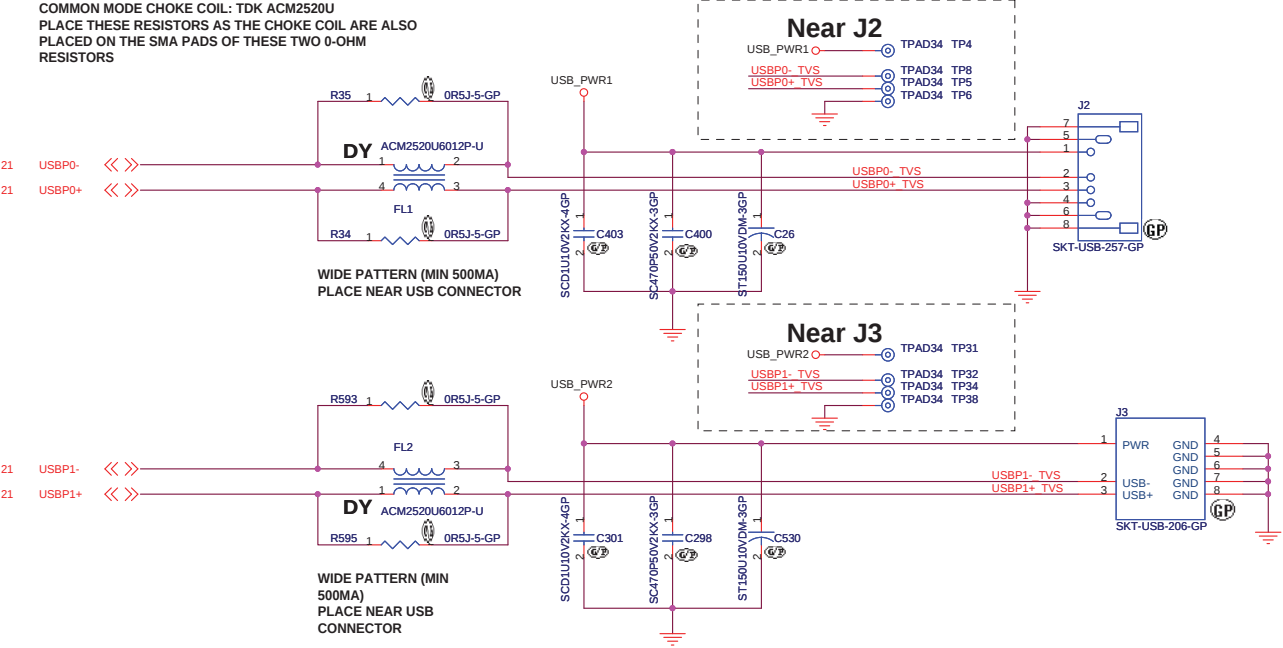
Pecan-1

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-2

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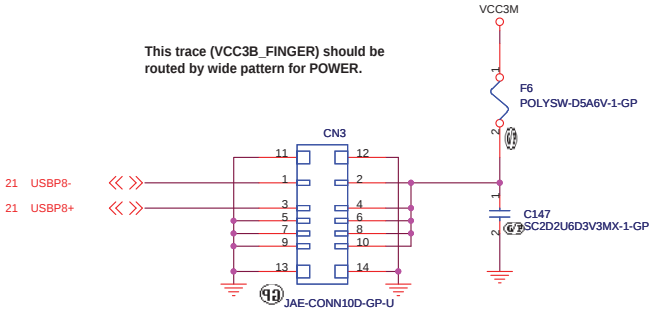
COMMON MODE CHOKE COIL: TDK ACM2520U
PLACE THESE RESISTORS AS THE CHOKE COIL ARE ALSO
PLACED ON THE SMA PADS OF THESE TWO 0-OHM
RESISTORS



	TI P/N	WISTRON P/N
1	TPS2065DGN-GP	41A1229AA
2	TPS2065DGN-1-GP	74.02065.A79
3	BD8010FVJ-GP	74.08010.07G

Fingerprint Reader

This trace (VCC3B_FINGER) should be routed by wide pattern for POWER.

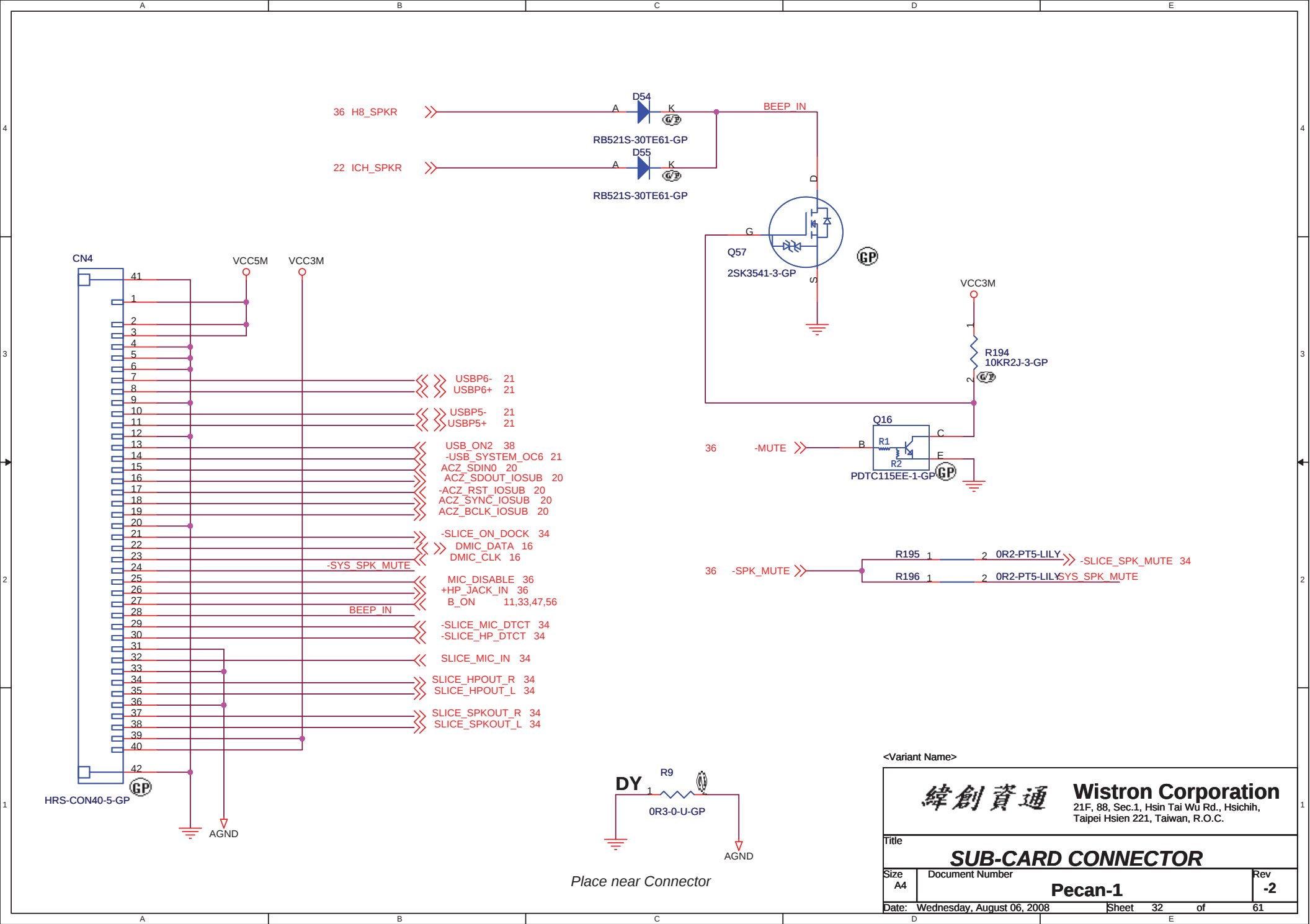


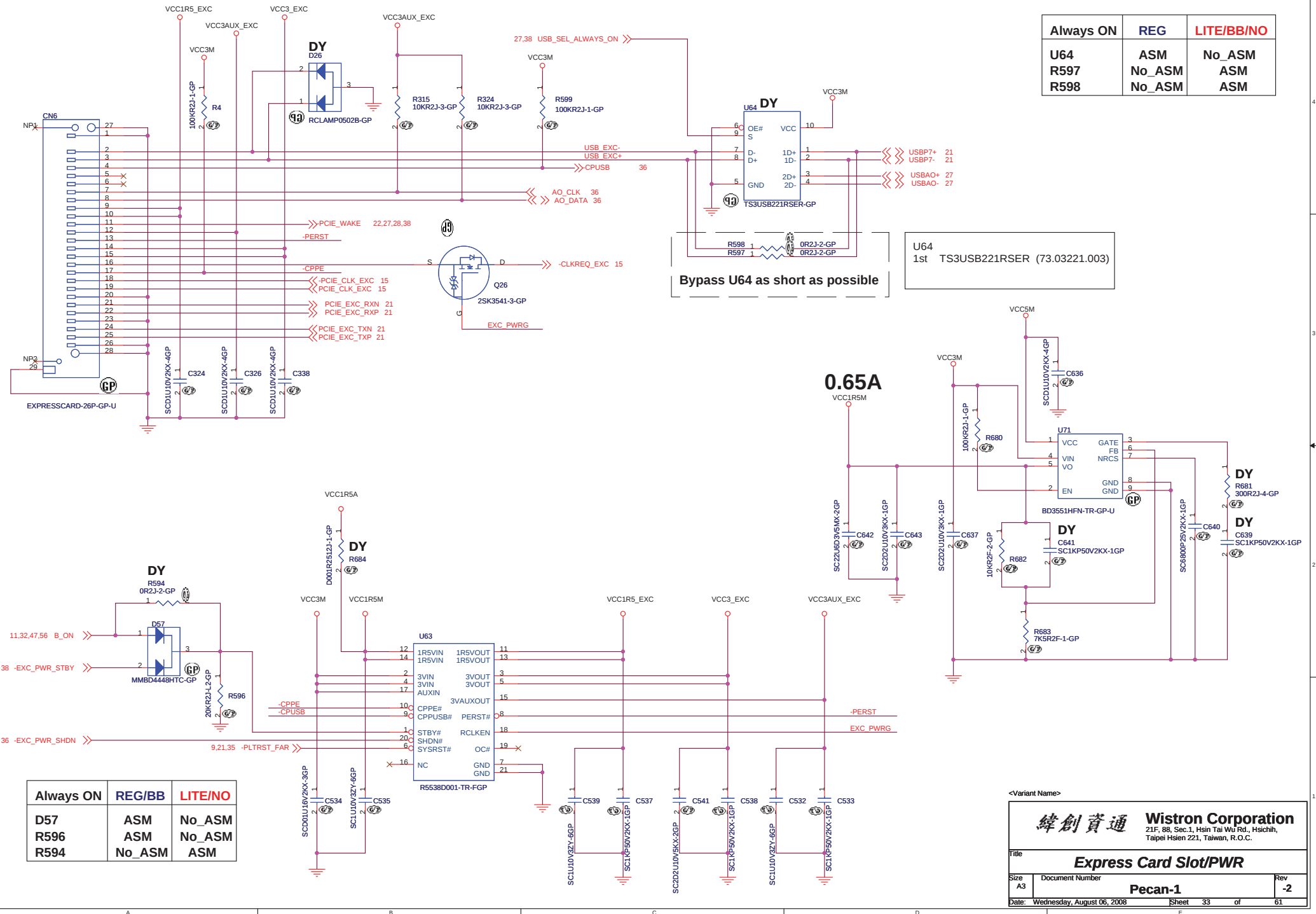
緯創資通 Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

Title: **USB POWER / CONNECTOR**

Size: A3 Document Number: **Pecan-1** Rev: **-2**

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Always ON	REG	LITE/BB/NO
U64	ASM	No_ASM
R597	No_ASM	ASM
R598	No_ASM	ASM

U64
1st TS3USB221RSER (73.03221.003)

0.65A

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Title

Express Card Slot/PWR

Size

A3

Document Number

Pecan-1

Rev

-2

Date:

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Sheet

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of

61

1. NEAR H8S/2116:
2. SHORT PATTERN
3. NO PATTERN UNDER THIS AREA

E10A Debug I/F	Enable	Disable
R222	NO ASM	ASM
R213	ASM	NO ASM
D23	ASM	NO ASM
R215	NO ASM	ASM
R211	ASM	NO ASM
R216	ASM	NO ASM
D22	ASM	NO ASM
R210	NO ASM	ASM
R198	ASM	NO ASM
R199	ASM	NO ASM
R239	NO ASM	ASM

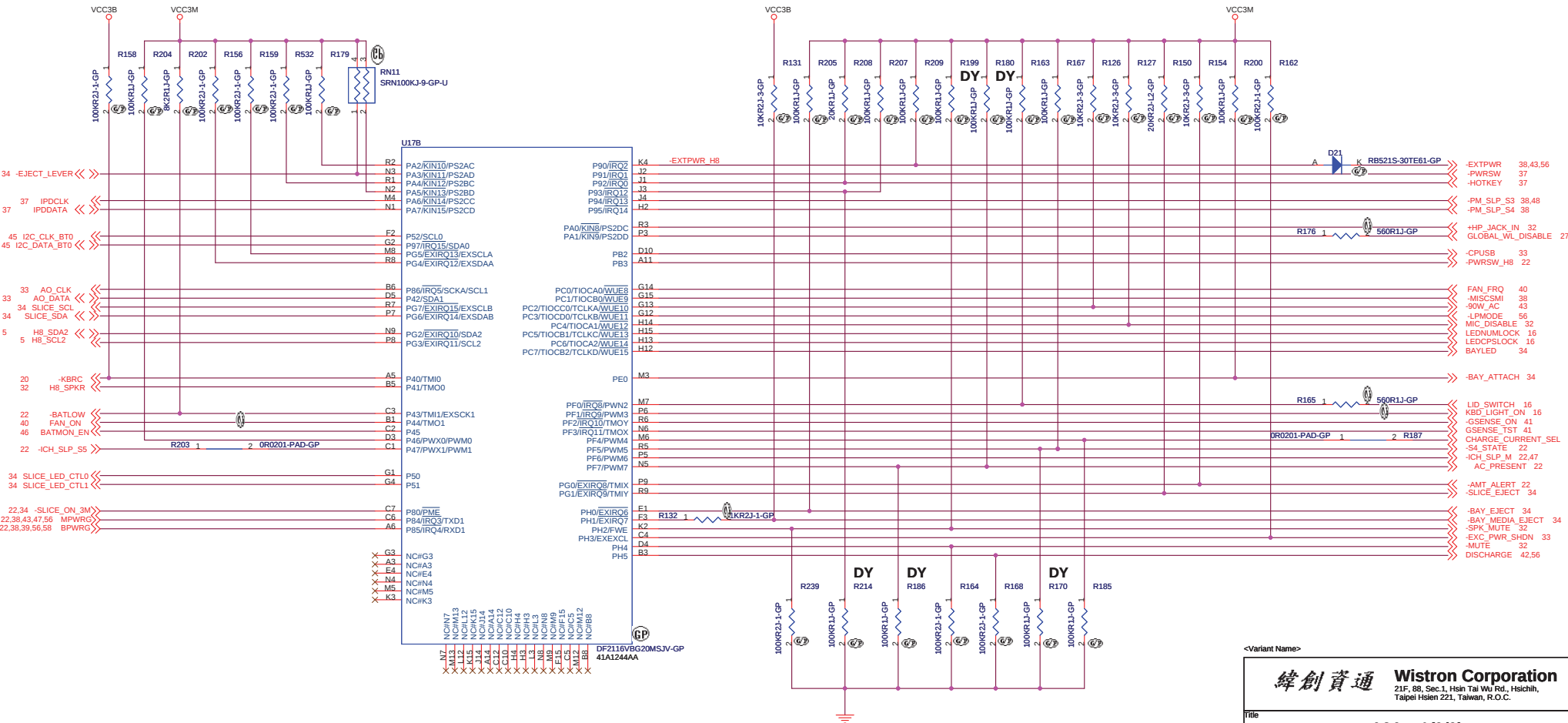
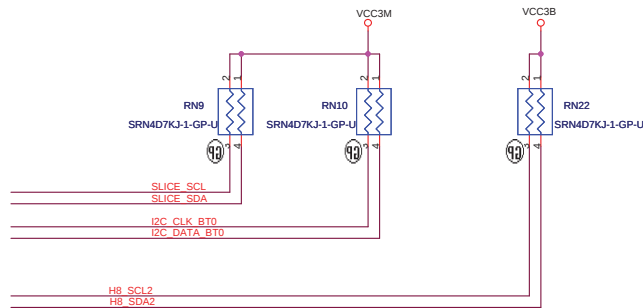
<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

File _____

Size Custom Document Number _____ Rev **-2**

Date: Wednesday, August 06, 2008 Sheet 35 of 61



<Variant Name>

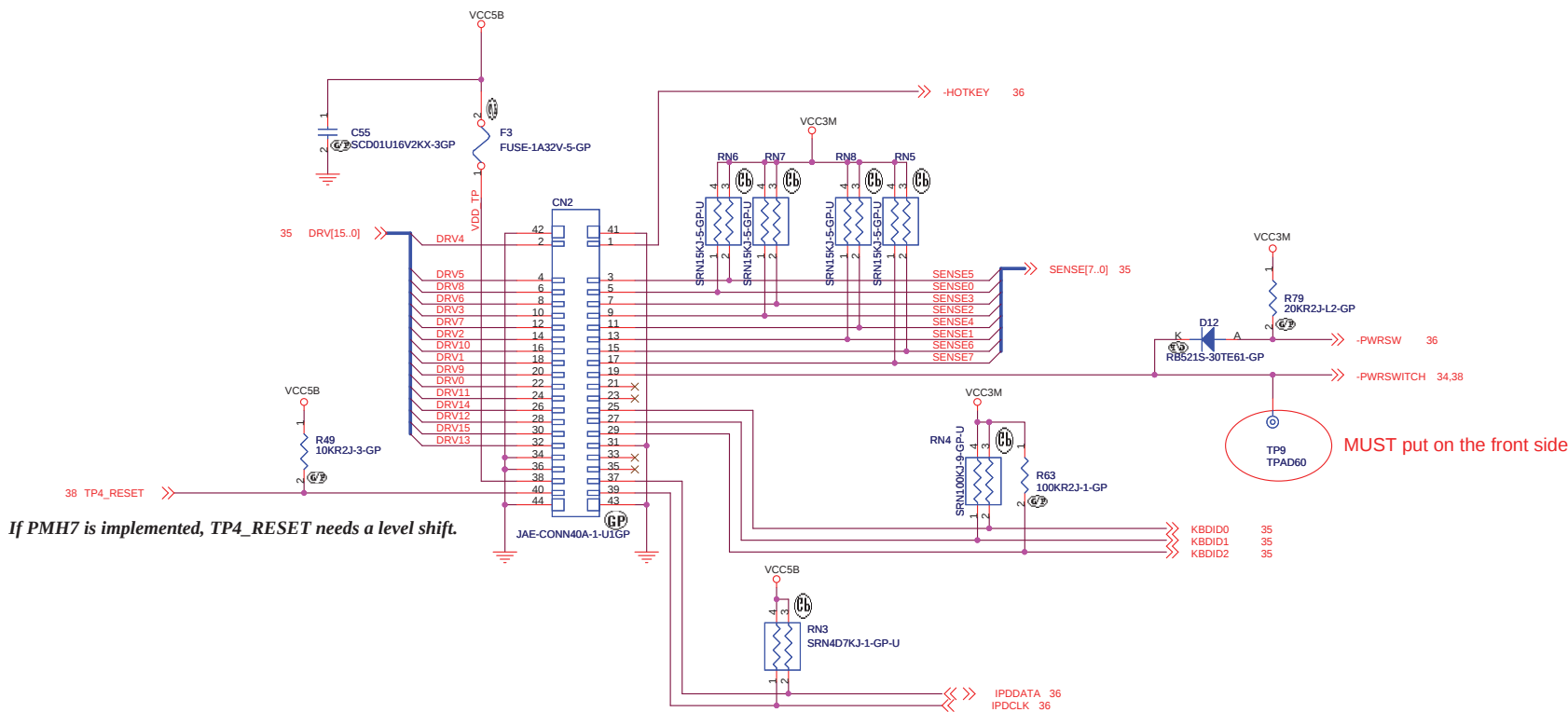
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **H8S2116(2/2)**

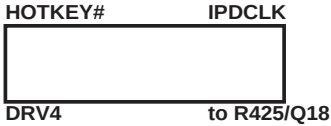
Size: Custom Document Number: **Pecan-1** Rev: **-2**

Date: Wednesday, August 06, 2008 Sheet: 36 of 61

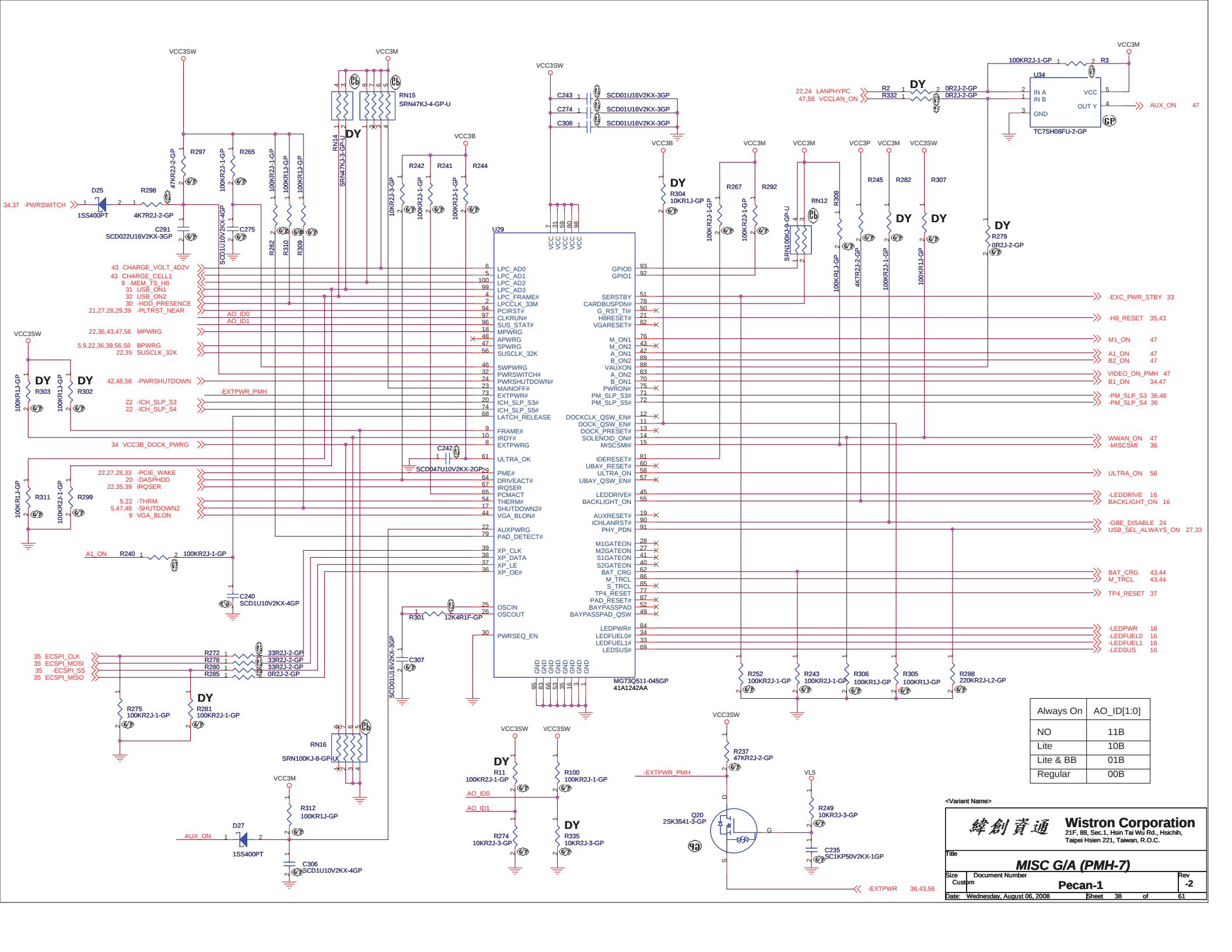
Keyboard Connector



If PMH7 is implemented, TP4_RESET needs a level shift.



Keyboard Connector Top View



Always On	AO_ID[1:0]
NO	11B
Lite	10B
Lite & BB	01B
Regular	00B

File

Size

Document Number

Custom

Date

Wednesday, August 06, 2008

Rev

-2

Always On

NO

Lite

Lite & BB

Regular

11B

10B

01B

00B

Wistron Corporation

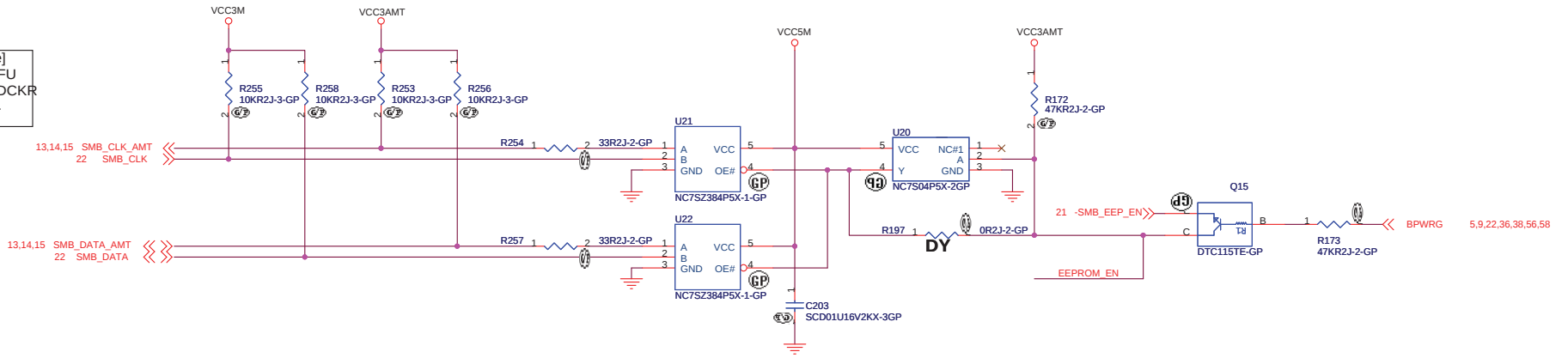
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichin, Taipei Hsien 221, Taiwan, R.O.C.

MISC GIA (PMH-7)

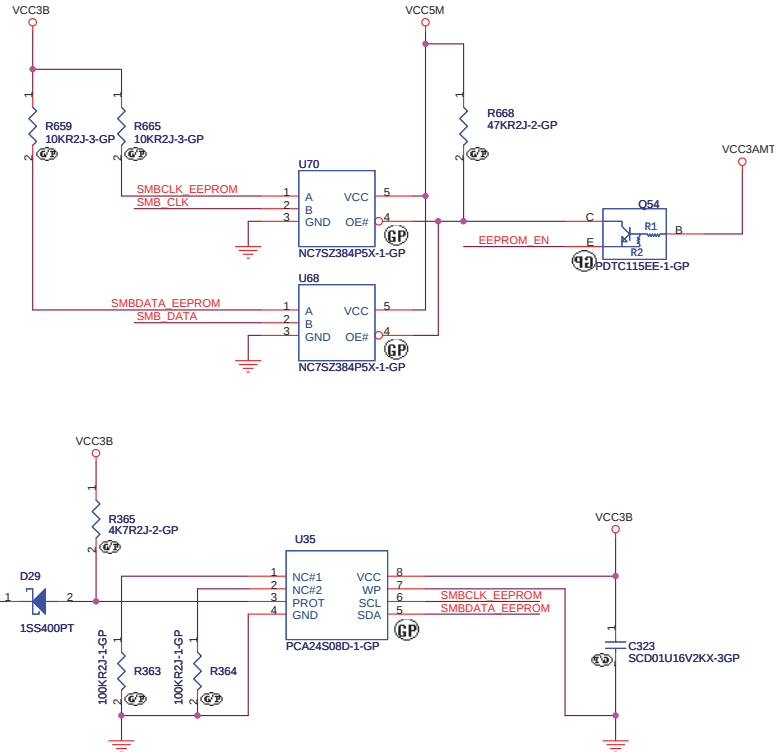
Pecan-1

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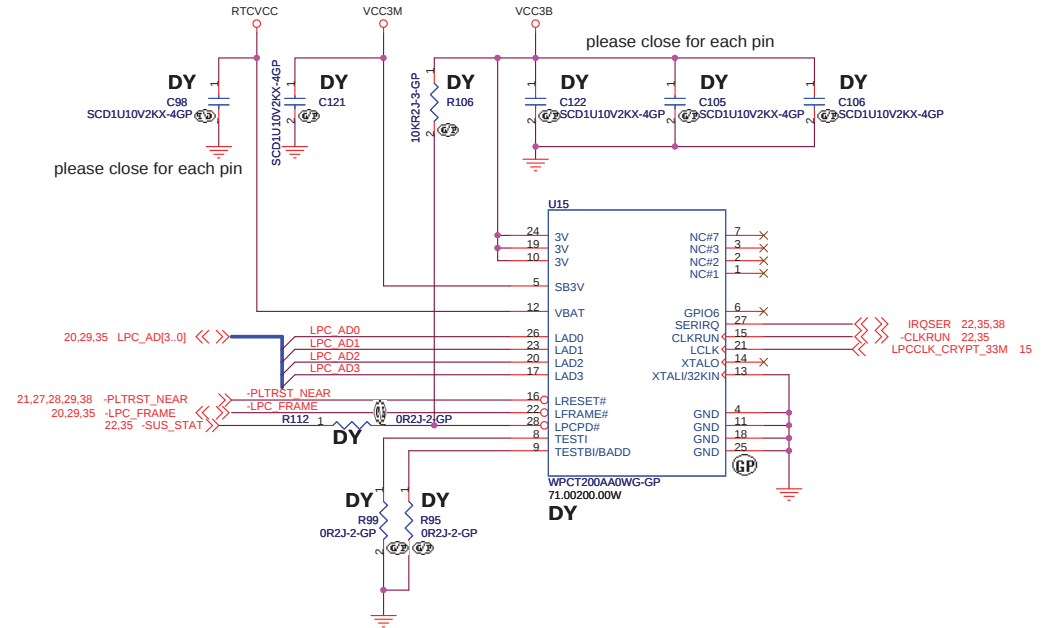
[Sourcing Candidate]
Toshiba TC7SB384FU
TI SN74CBT1G384DCKR
Fairchild NC7SZ384



EEPROM



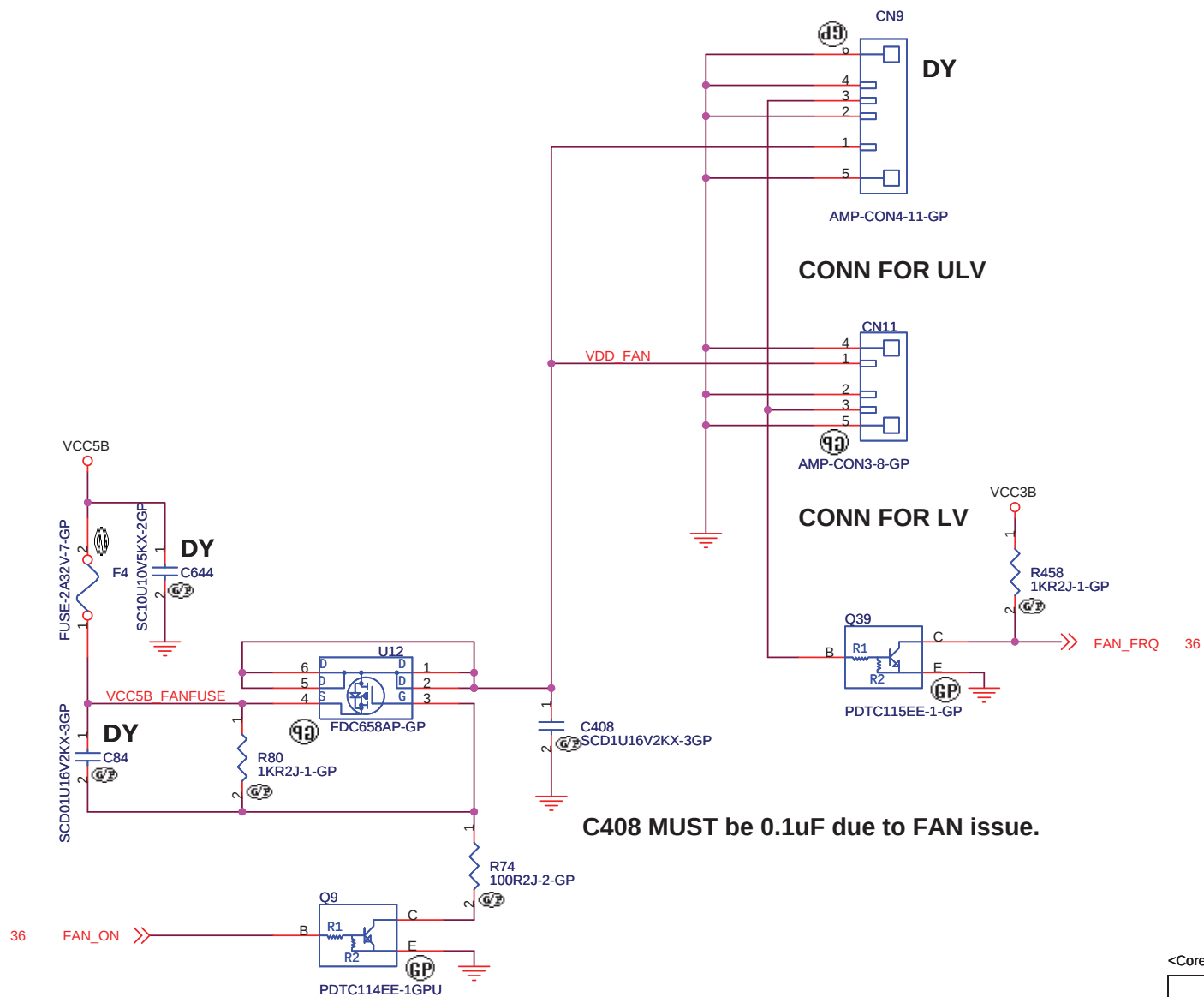
TCPA



<Core Design>

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Title		
EEPROM/TCPA		
Size	Document Number	Rev
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Title

FAN CONTROL

Size
A4

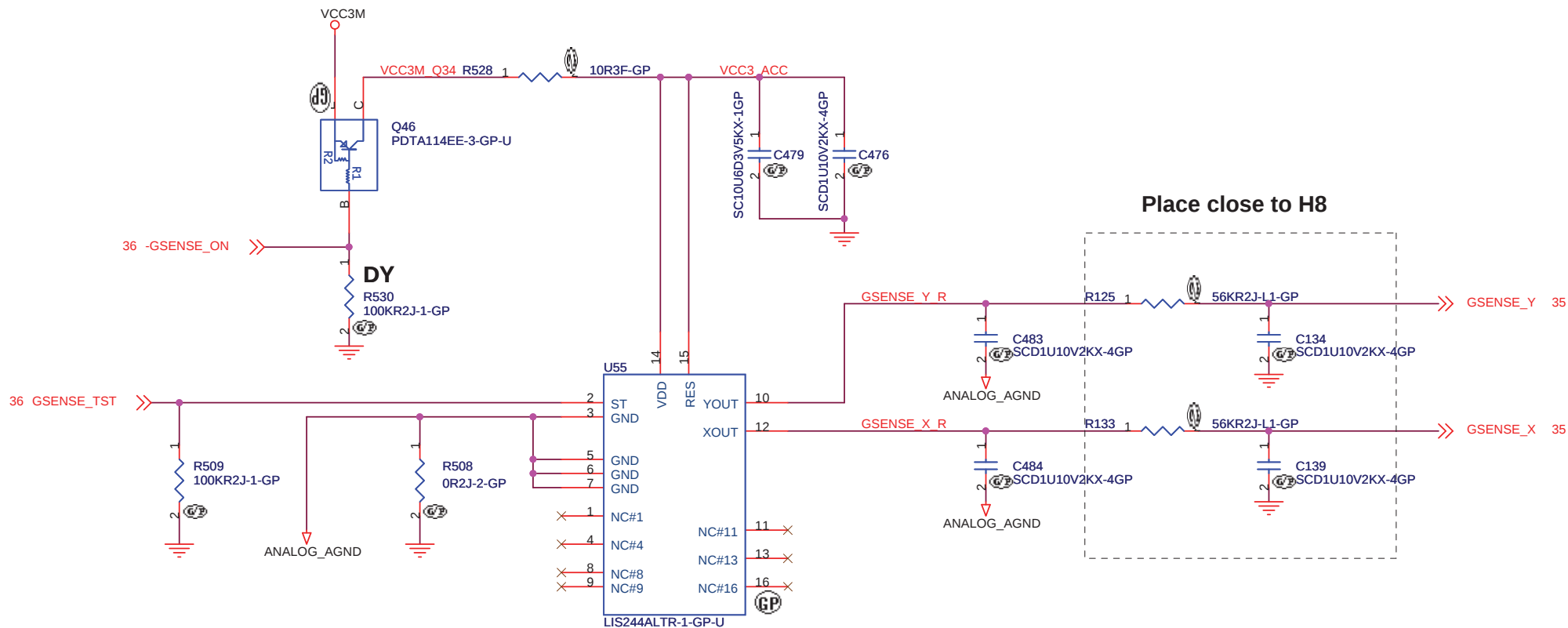
Document Number

Pecan-1

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-2

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2-axis	ADI	ADXL322	41R0828BA
	ST	LIS244AL	41R0525AA
3-axis	ST	LIS34AL	45R0828AA

Width = 6 mil & Spacing = 10 mil
for three Output traces

	ADXL322 LIS244AL LIS34AL	No Accel
R530	NO_ASM	ASM
R509	ASM	ASM
All other	ASM	NO_ASM

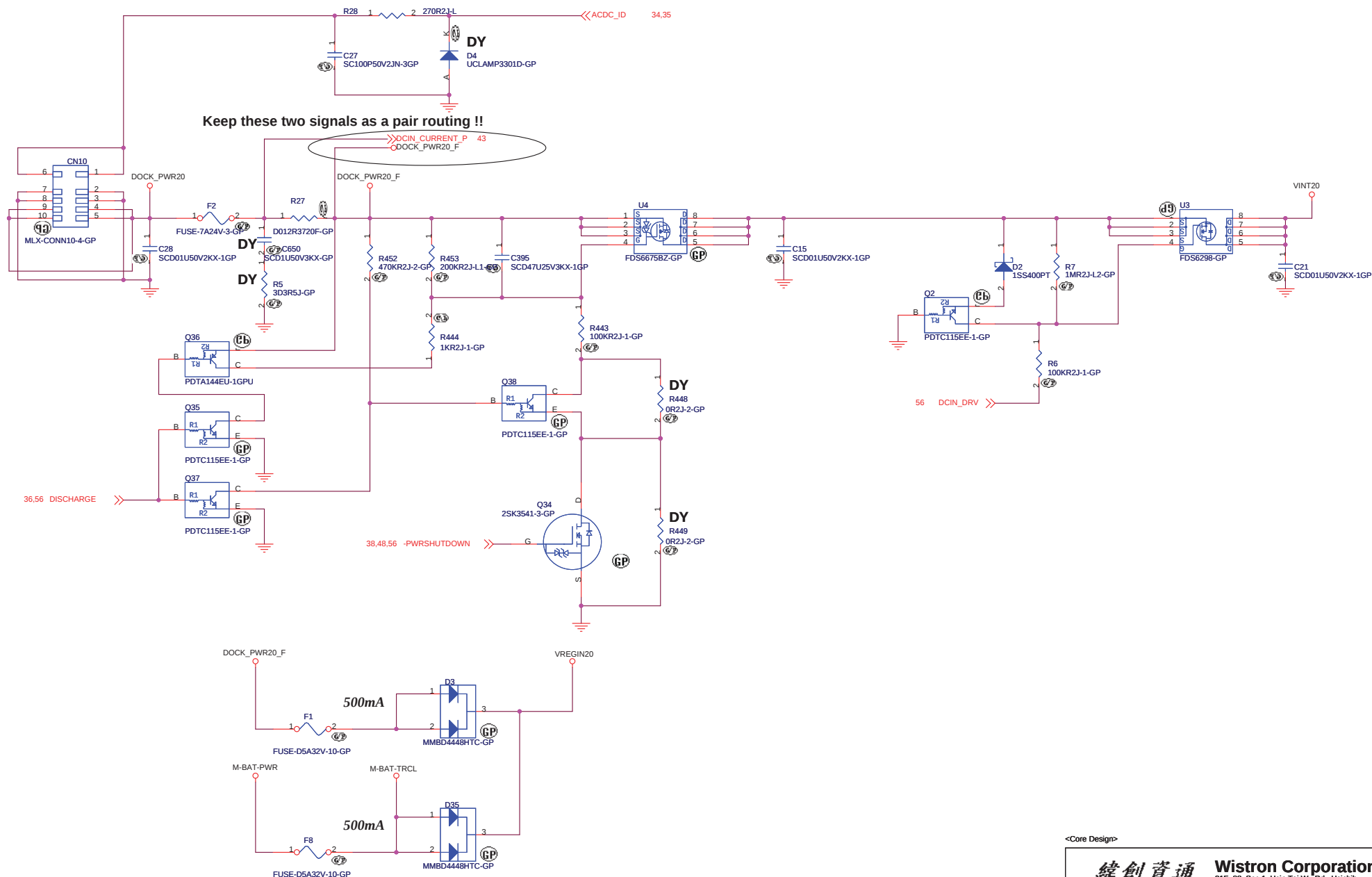
Layout Comment :

(1) Place C483, C484, Q46, R528, R530, C479, C476, R509, R508 close to U55.

(2) Avoid routing under DCDC switching area.

<Core Design>

 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
G-SENSOR	
Title Size A4 Date: Thursday, August 07, 2008	Document Number Pecan-1 Sheet 41 of 61
Rev	-2



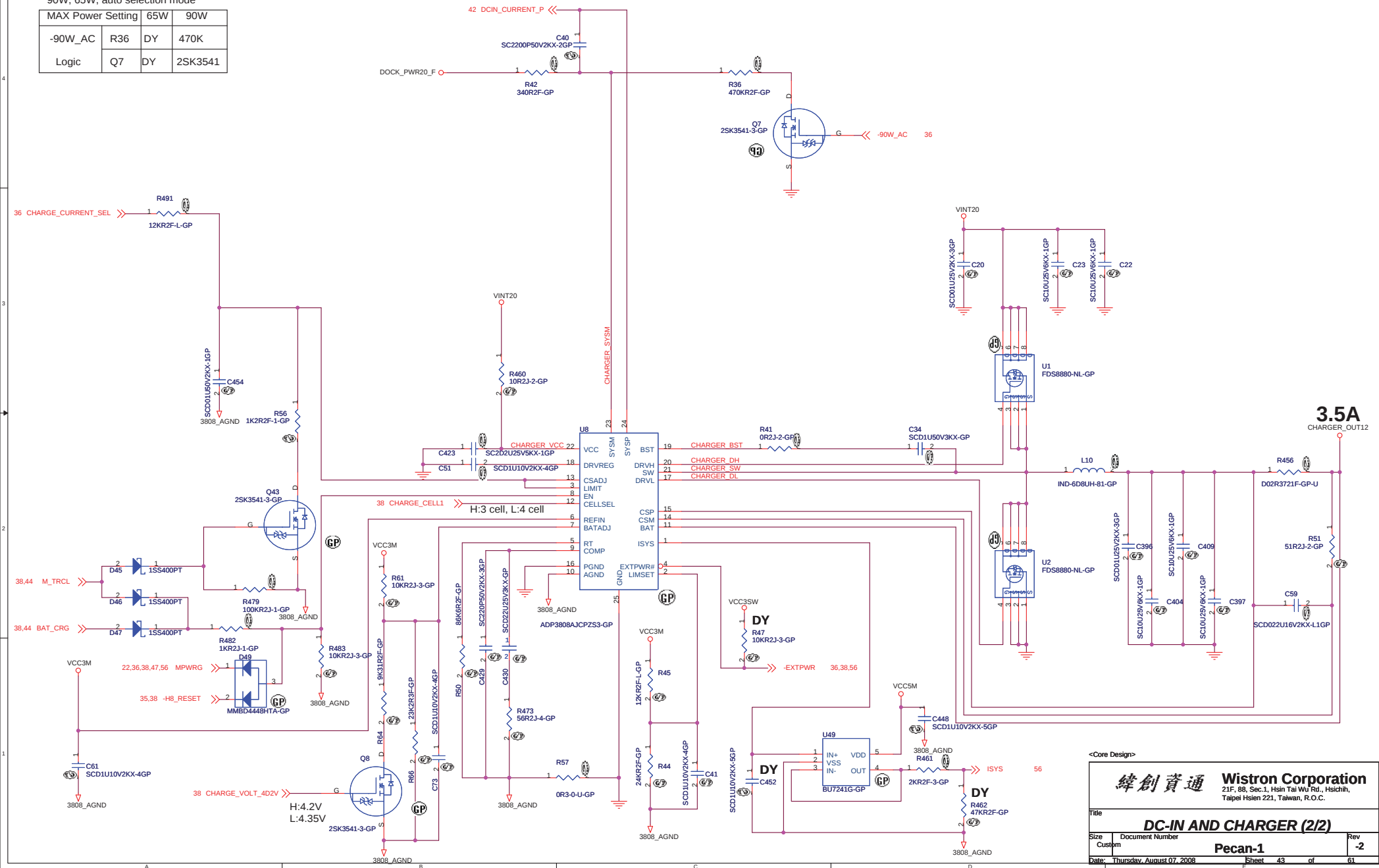
<Core Design>

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Taipei Hsien 221, Taiwan, R.O.C.

Title		
DC-IN AND CHARGER (1/2)		
Size	Document Number	Rev
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90W, 65W, auto selection mode

MAX Power Setting		65W	90W
-90W_AC	R36	DY	470K
Logic	Q7	DY	2SK3541

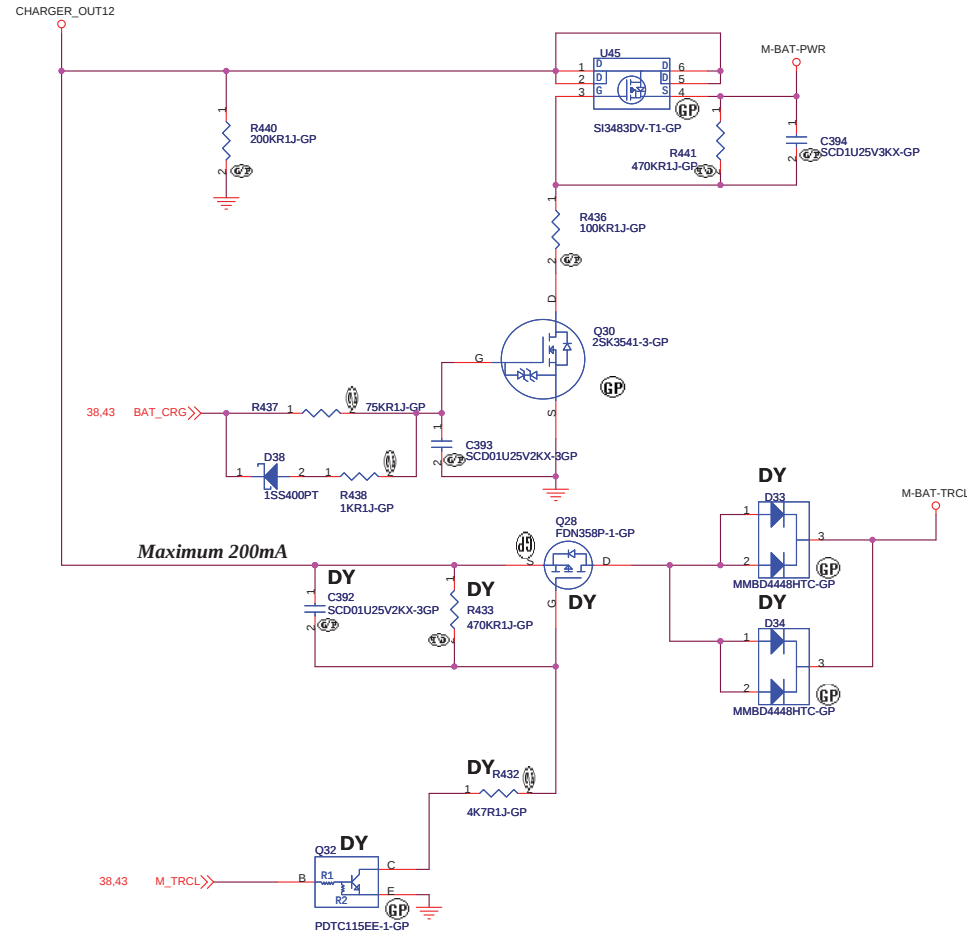


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緯創資通

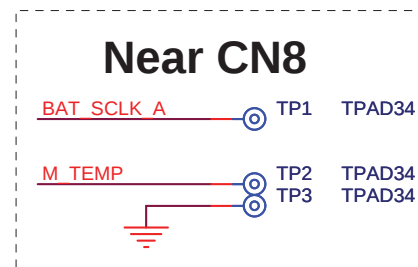
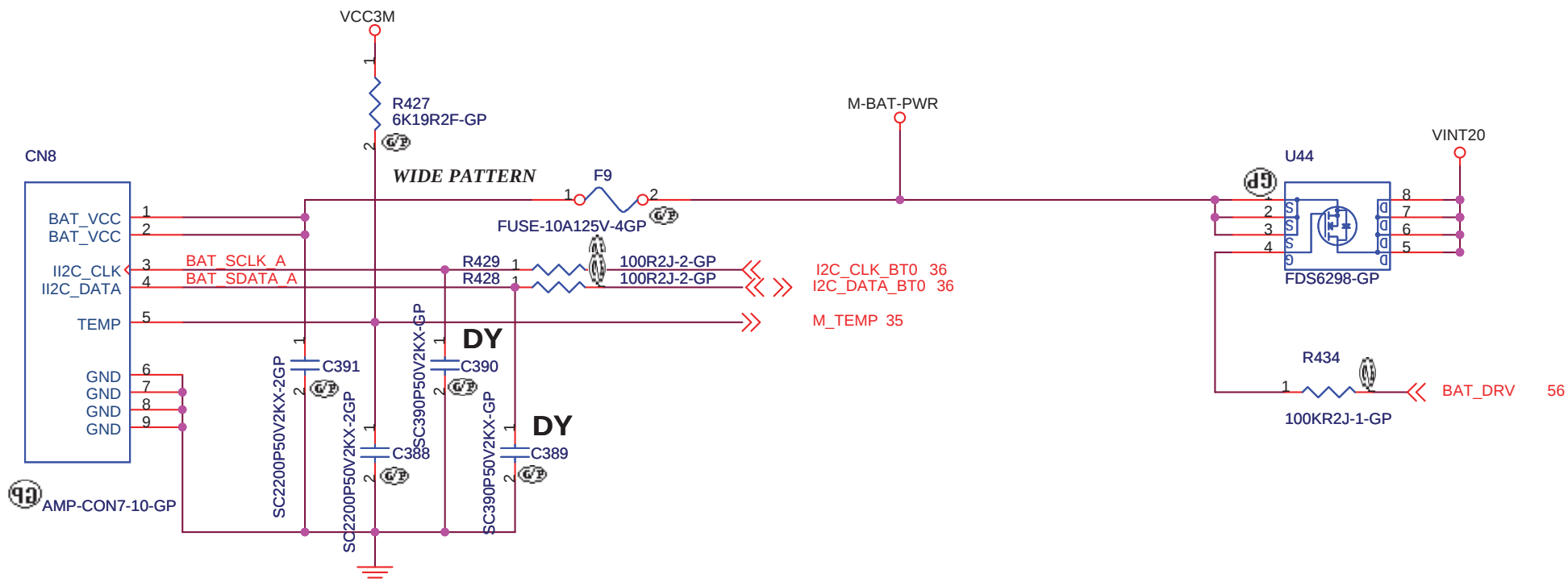
Wistron Corporation
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Title			
DC-IN AND CHARGER (2/2)			
Size	Document Number		Rev
Custom	Pecan-1		-2
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緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
CHARGER SELECT			
Size A3	Document Number Pecan-1		Rev -2
Date:	Wednesday, August 06, 2008	Sheet 44 of	61



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Taipei Hsien 221, Taiwan, R.O.C.

Title

BATTERY INPUT

Size
A

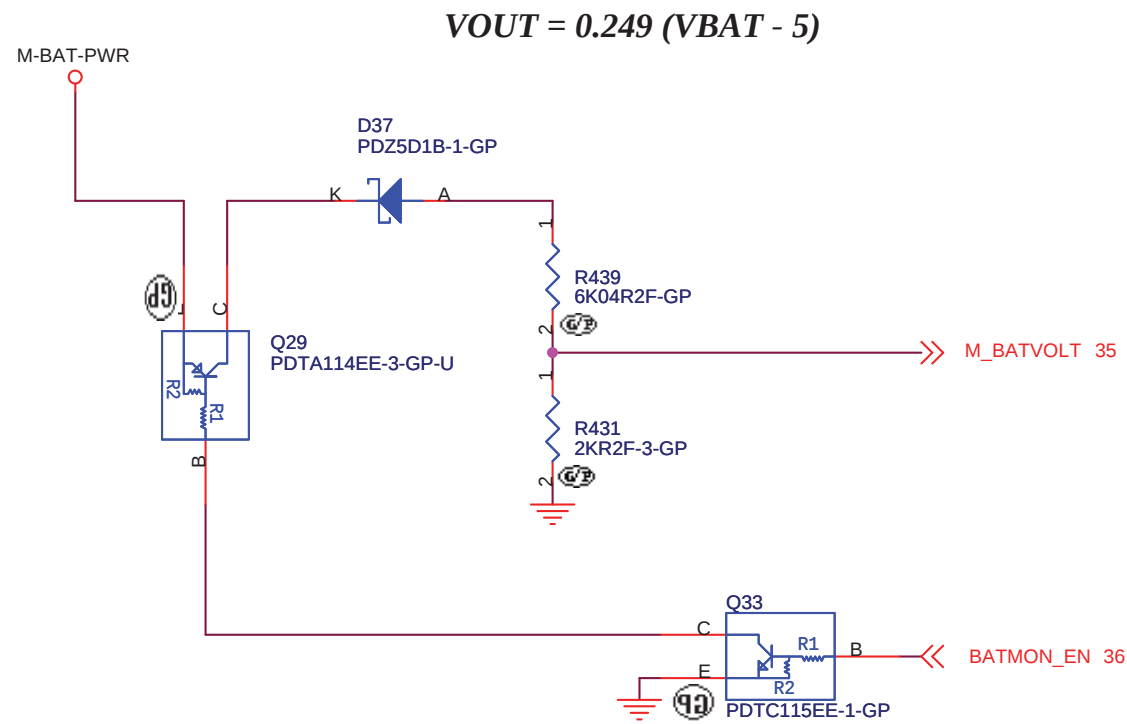
Document Number

Pecan-1

Rev
-2

Date: Wednesday, August 06, 2008

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Taipei Hsien 221, Taiwan, R.O.C.

Title

BATTERY MONITOR

Size
A

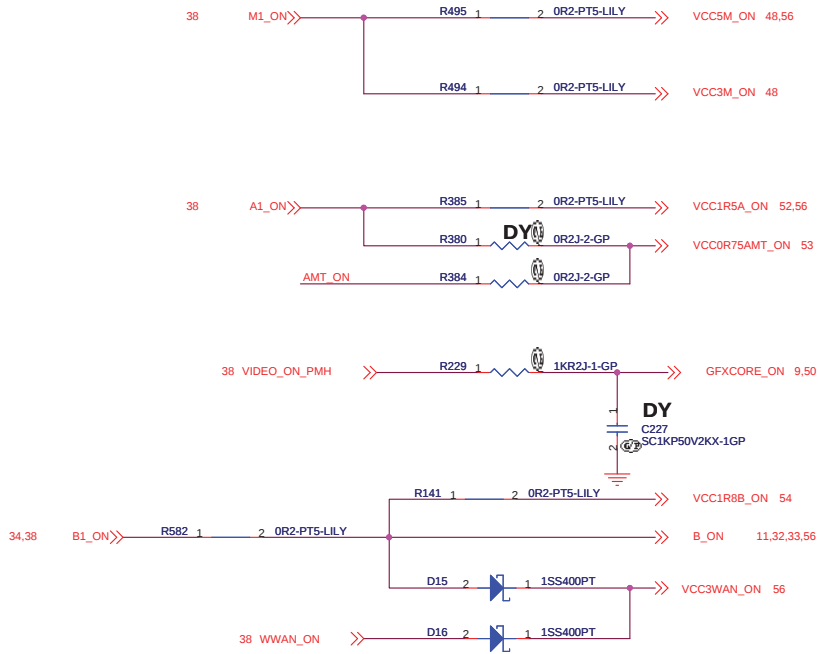
Document Number

Pecan-1

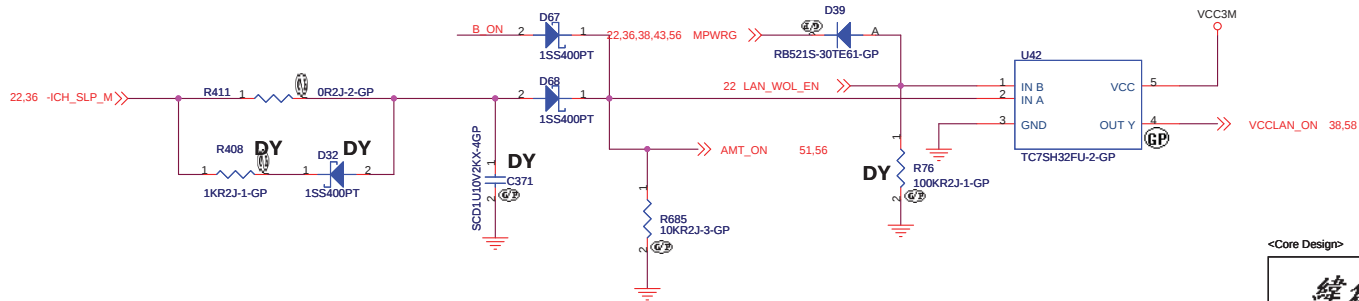
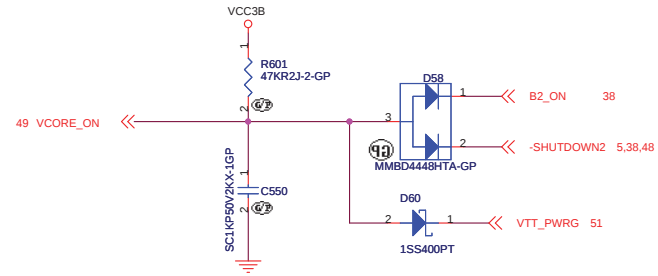
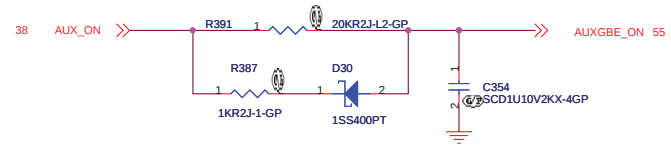
Rev
-2

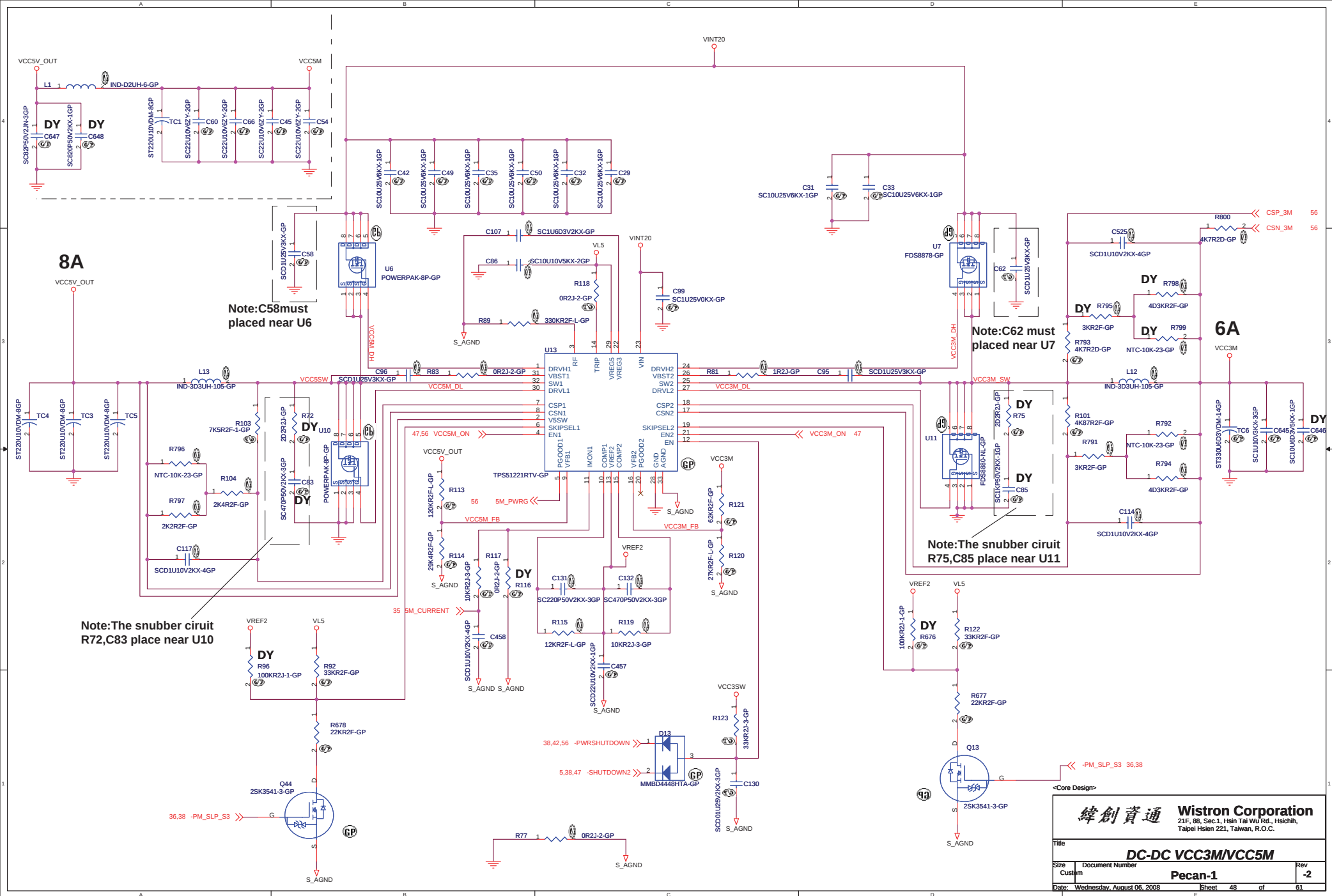
Date: Wednesday, August 06, 2008

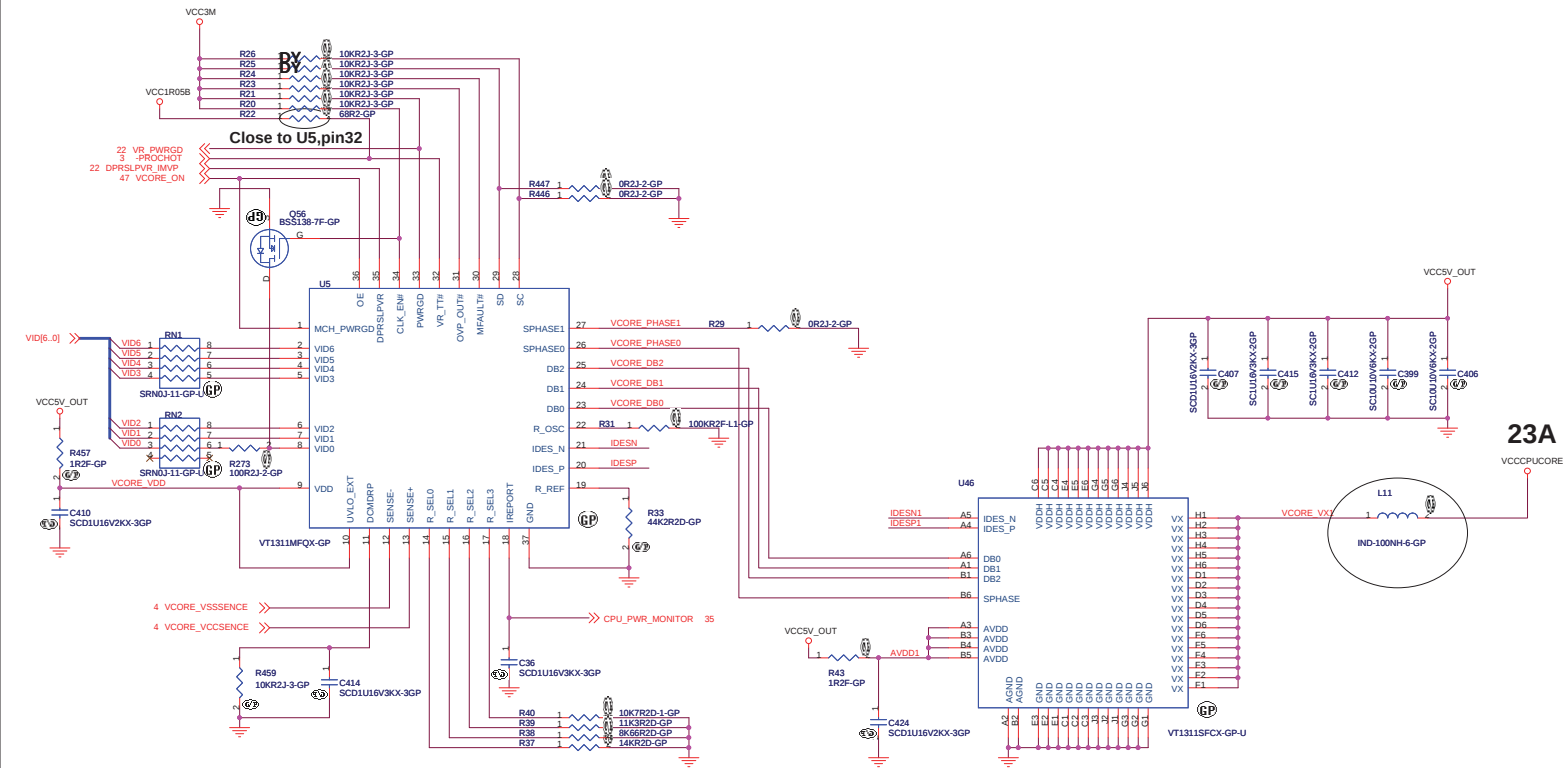
Sheet 46 of 61

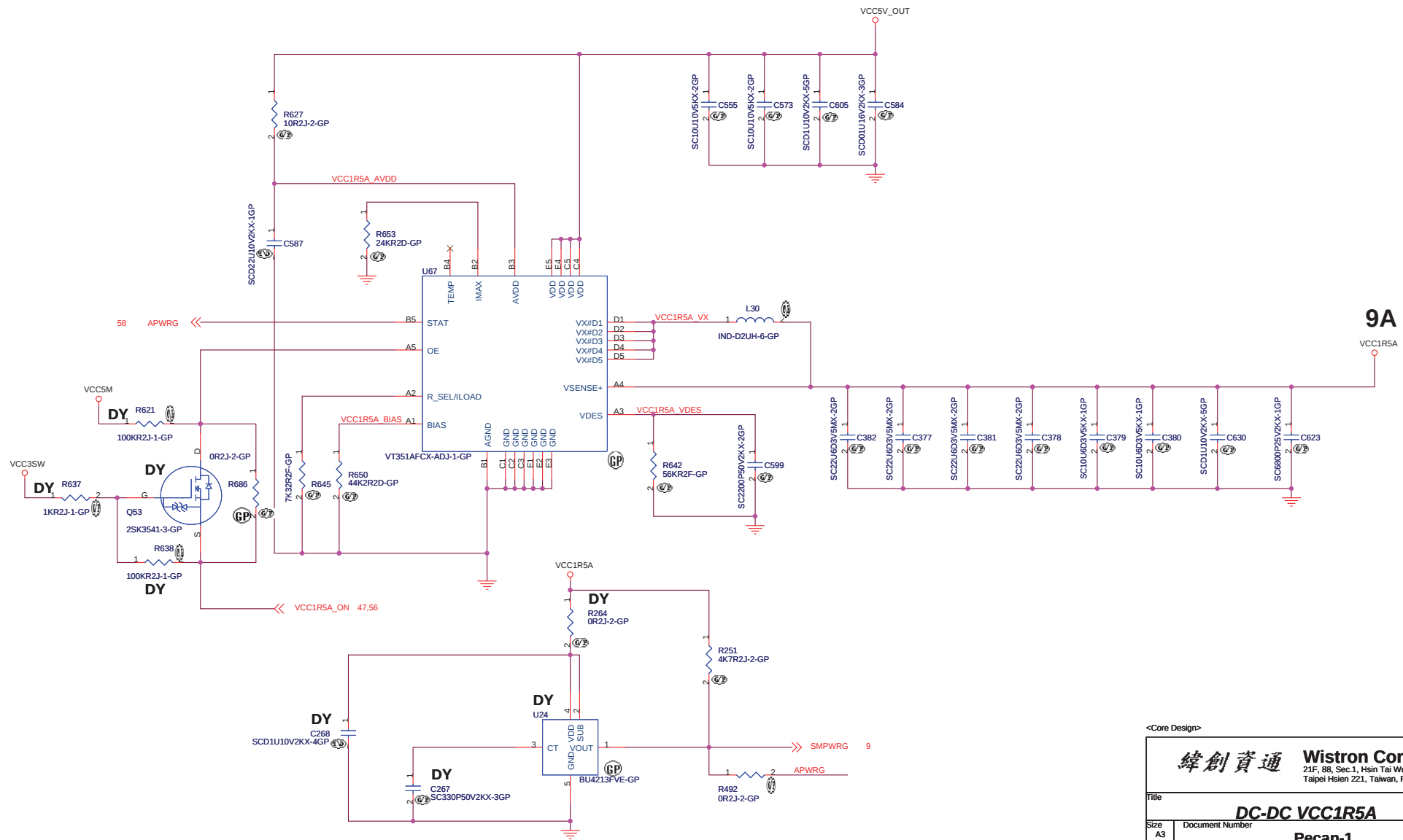


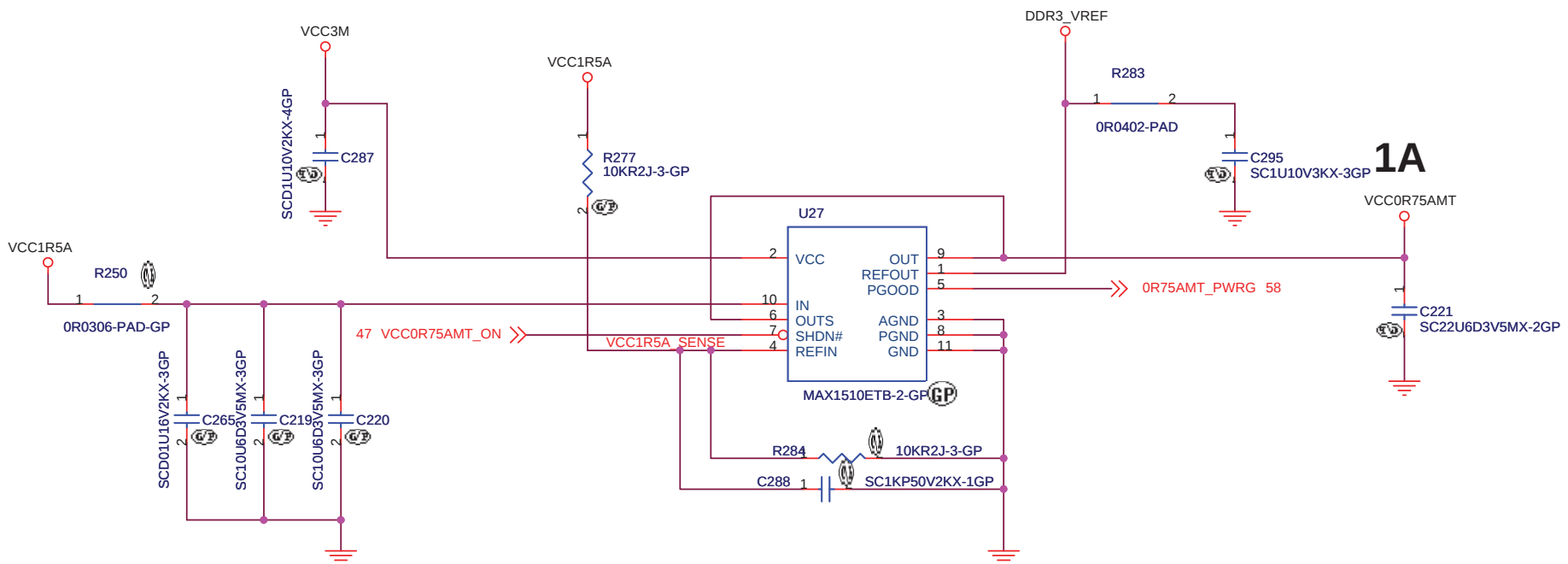
Always ON	Enable	Disable
D15	ASM	No_ASM
D16	ASM	No_ASM







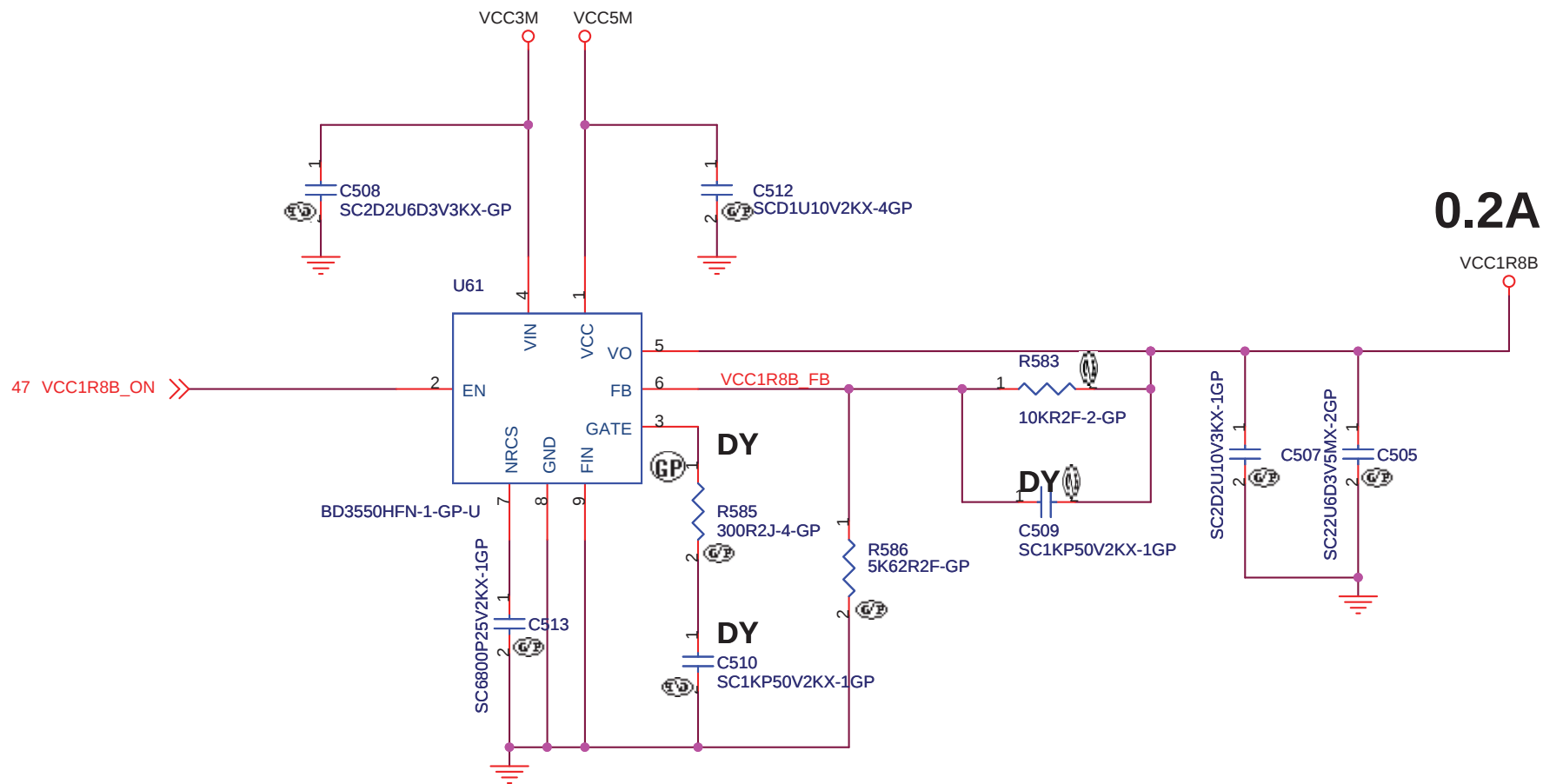




<Core Design>

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Title		
DC-DC VCC0R75AMT		
Size A4	Document Number Pecan-1	Rev -2
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Title

DC-DC VCC1R8B

Size
A

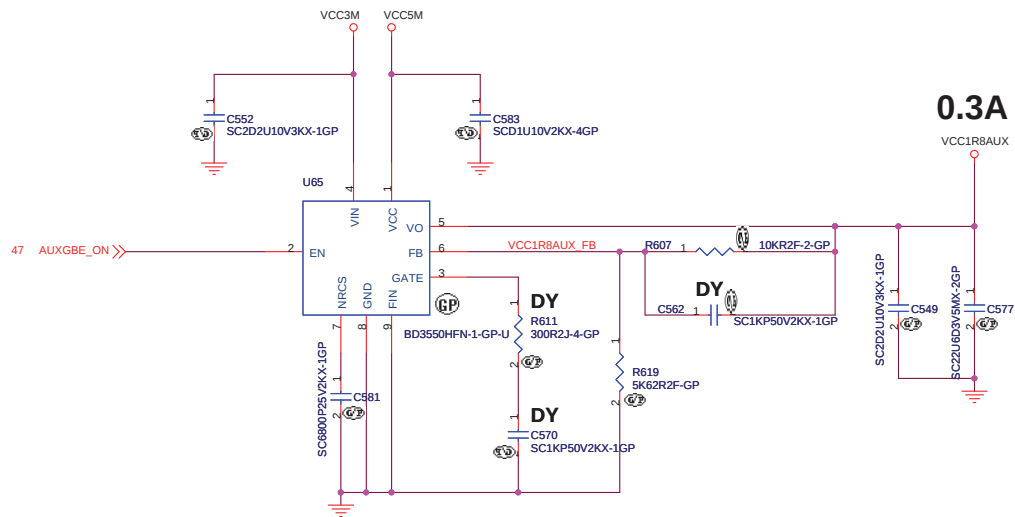
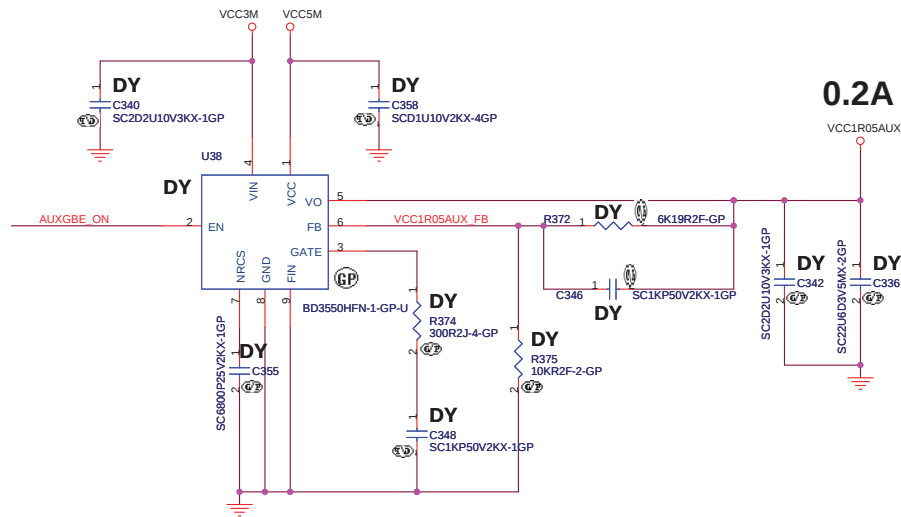
Document Number

Pecan-1

Rev
-2

Date: Wednesday, August 06, 2008

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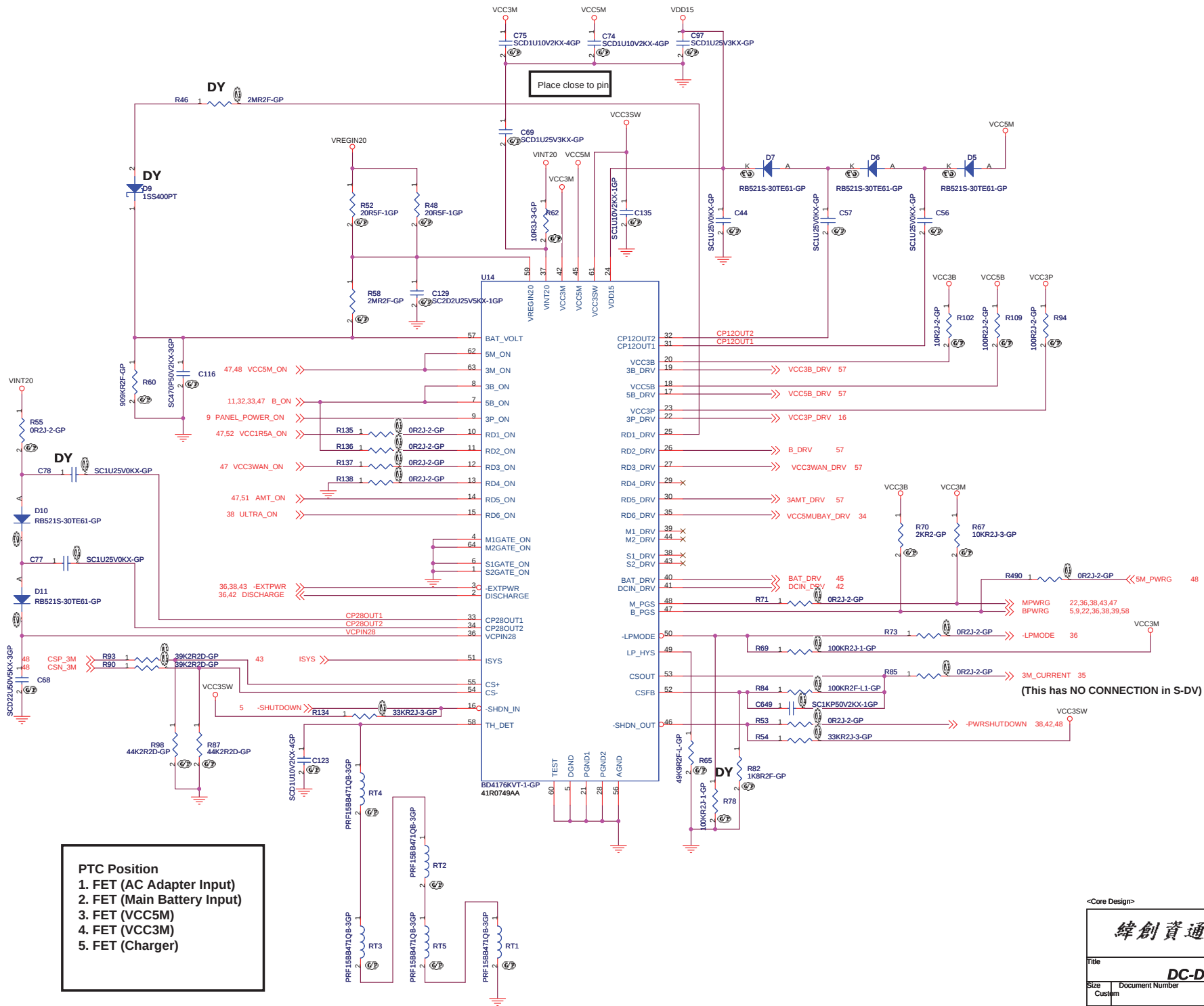


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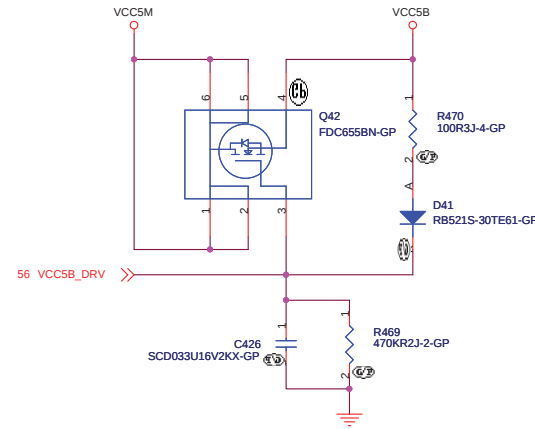
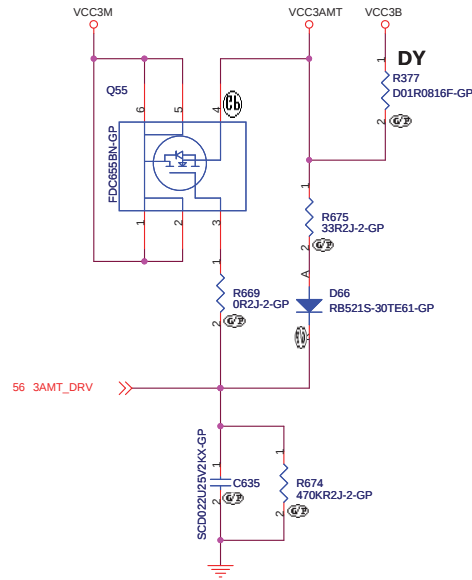
緯創資通

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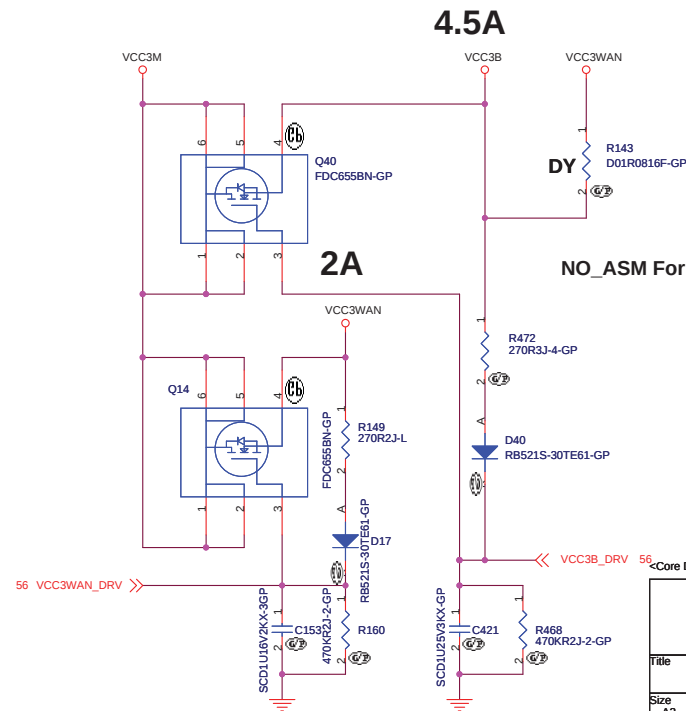
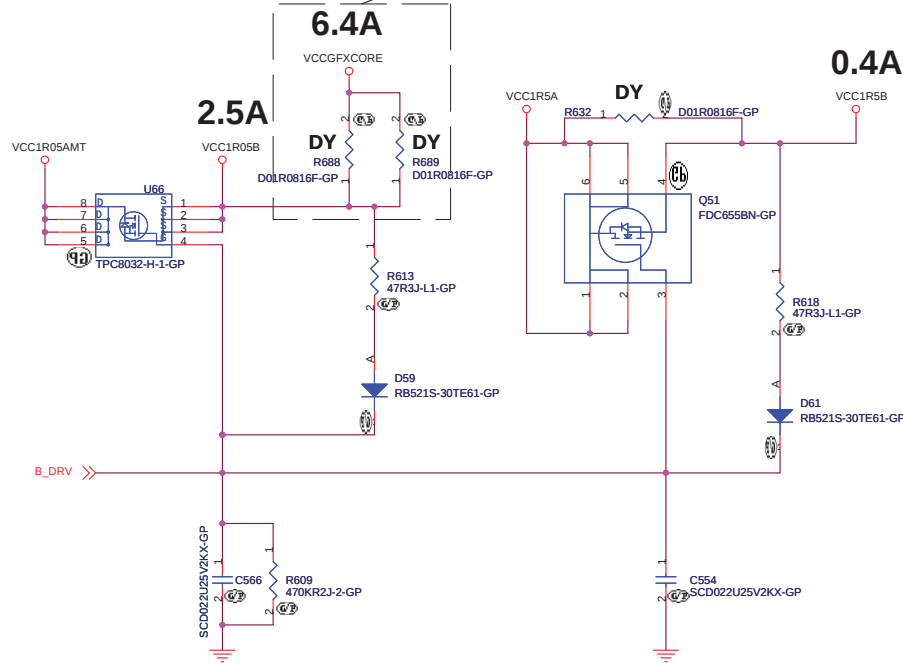
Title				
DC-DC VCC1R05AUX/VCC1R8AUX				
Size	Document Number			Rev
A3	Pecan-1			-2
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0.2A



Place near VCCGFXCORE logic

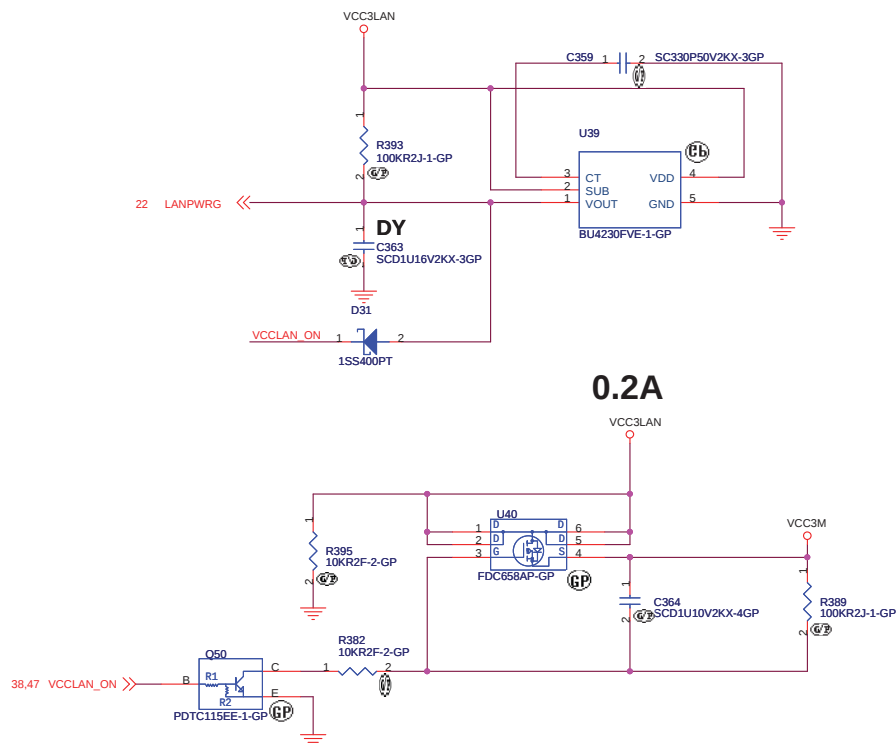


NO_ASM For No WWAN CONN. MODEL

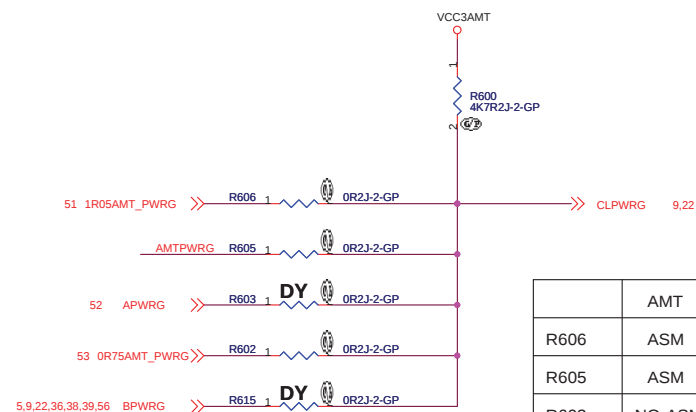
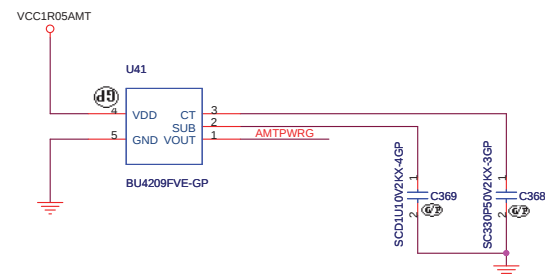
Always ON	Enable	Disable
Q14	ASM	No_ASM
R149	ASM	No_ASM
D17	ASM	No_ASM
R160	ASM	No_ASM
C153	ASM	No_ASM
R143	No_ASM	ASM

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Title		
LOAD SW 1		
Size A3	Document Number Pecan-1	Rev -2
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NOASM for NON-AMT

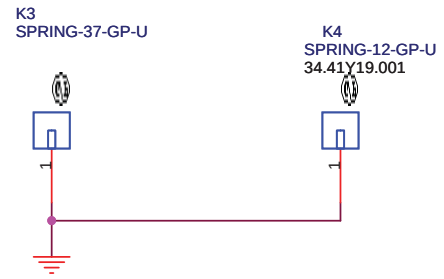
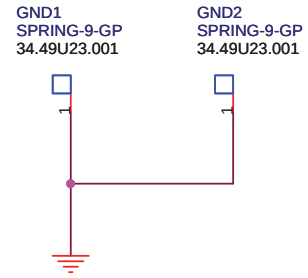
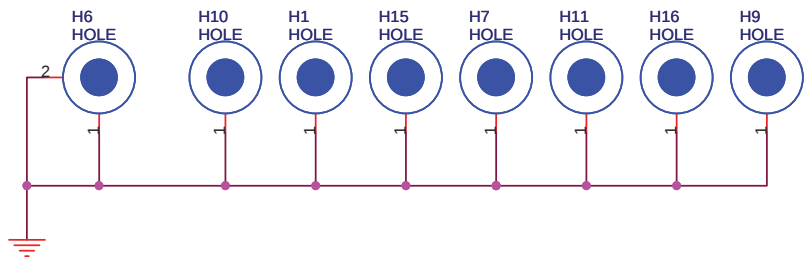
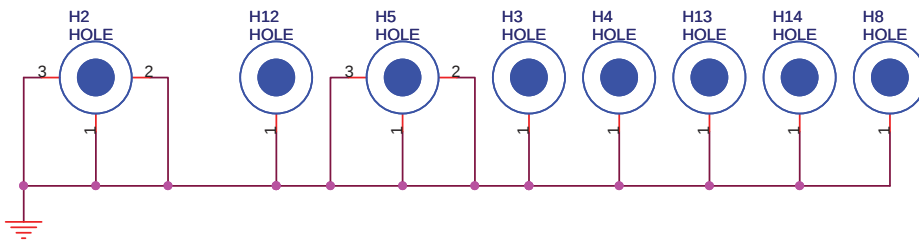


	AMT	NON-AMT
R606	ASM	NO-ASM
R605	ASM	NO-ASM
R603	NO-ASM	NO-ASM
R602	ASM	NO-ASM
R600	ASM	NO-ASM
R615	NO-ASM	ASM

<Core Design>

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Title		
LOAD SW 2		
Size A3	Document Number Pecan-1	Rev -2
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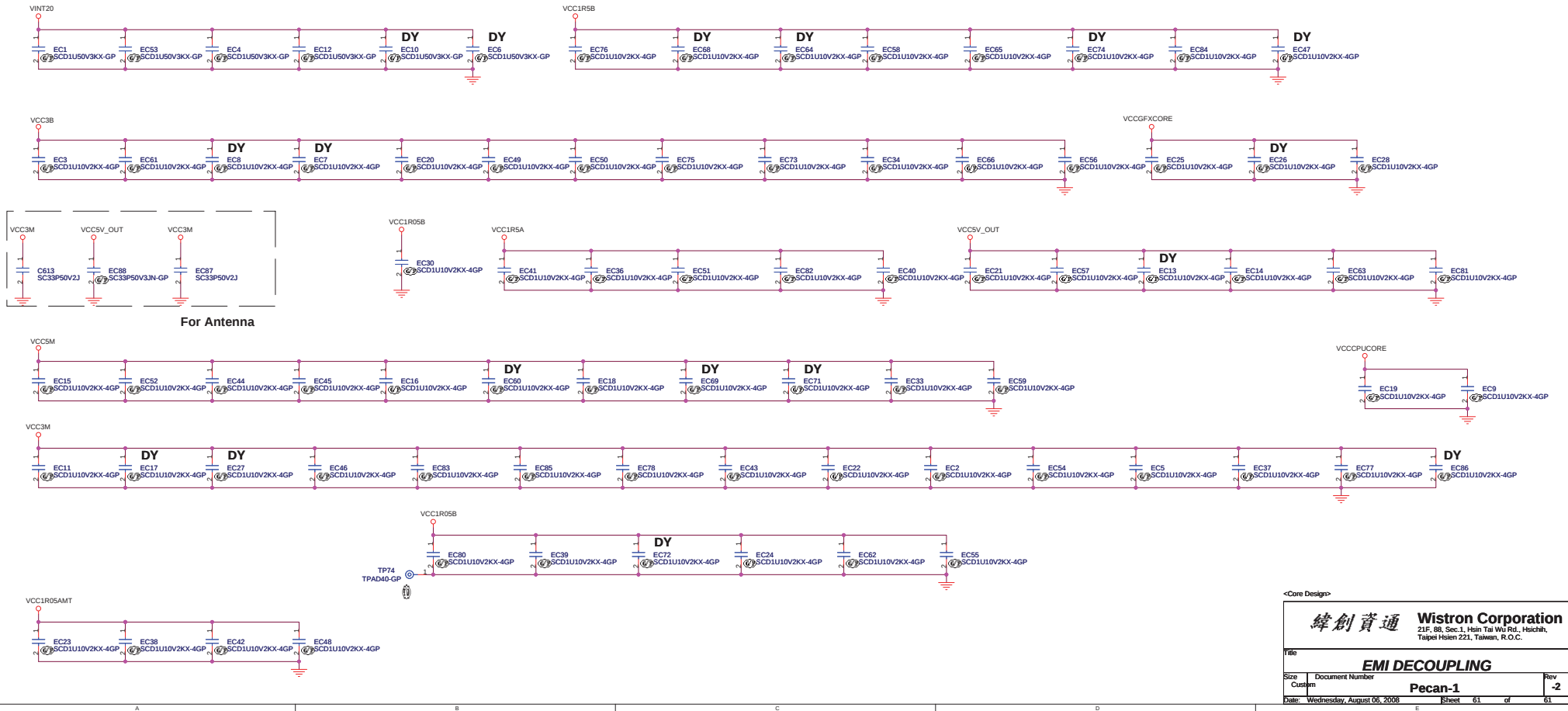
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		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title HOLES/GND/PADS			
Size A4	Document Number Pecan-1		Rev -2
Date: Wednesday, August 06, 2008		Sheet 59	of 61

Comparison chart for AMT function

Item	Page	Location Netname	AMT	NON_AMT	Modify	Note
1	22	U37	ICH9-M Enhanced	ICH9-M		
	22	R424	NO_ASM	ASM		
	24	U30	BOAZMAN 82567LM			
	29	SPI1	64Mbit			
	48	R411	ASM	NO_ASM		
	51	R636	ASM	NO_ASM		
	58	R603	ASM	NO_ASM		
		R605	ASM	NO_ASM		
		R606	ASM	NO_ASM		
	57	R377	NO_ASM	ASM		
		Q55	ASM	NO_ASM		
		R669	ASM	NO_ASM		
		R675	ASM	NO_ASM		
		D66	ASM	NO_ASM		
		C635	ASM	NO_ASM		
		R674	ASM	NO_ASM		
		U66	ASM	NO_ASM		
		R613	ASM	NO_ASM		
		D59	ASM	NO_ASM		
		C566	ASM	NO_ASM		
		R609	ASM	NO_ASM		

Long power trace EMI decoupling caps



<Core Design>			
緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsuehshih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
EMI DECOUPLING			
Size	Document Number		Rev
Custom	Pecan-1		-2
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