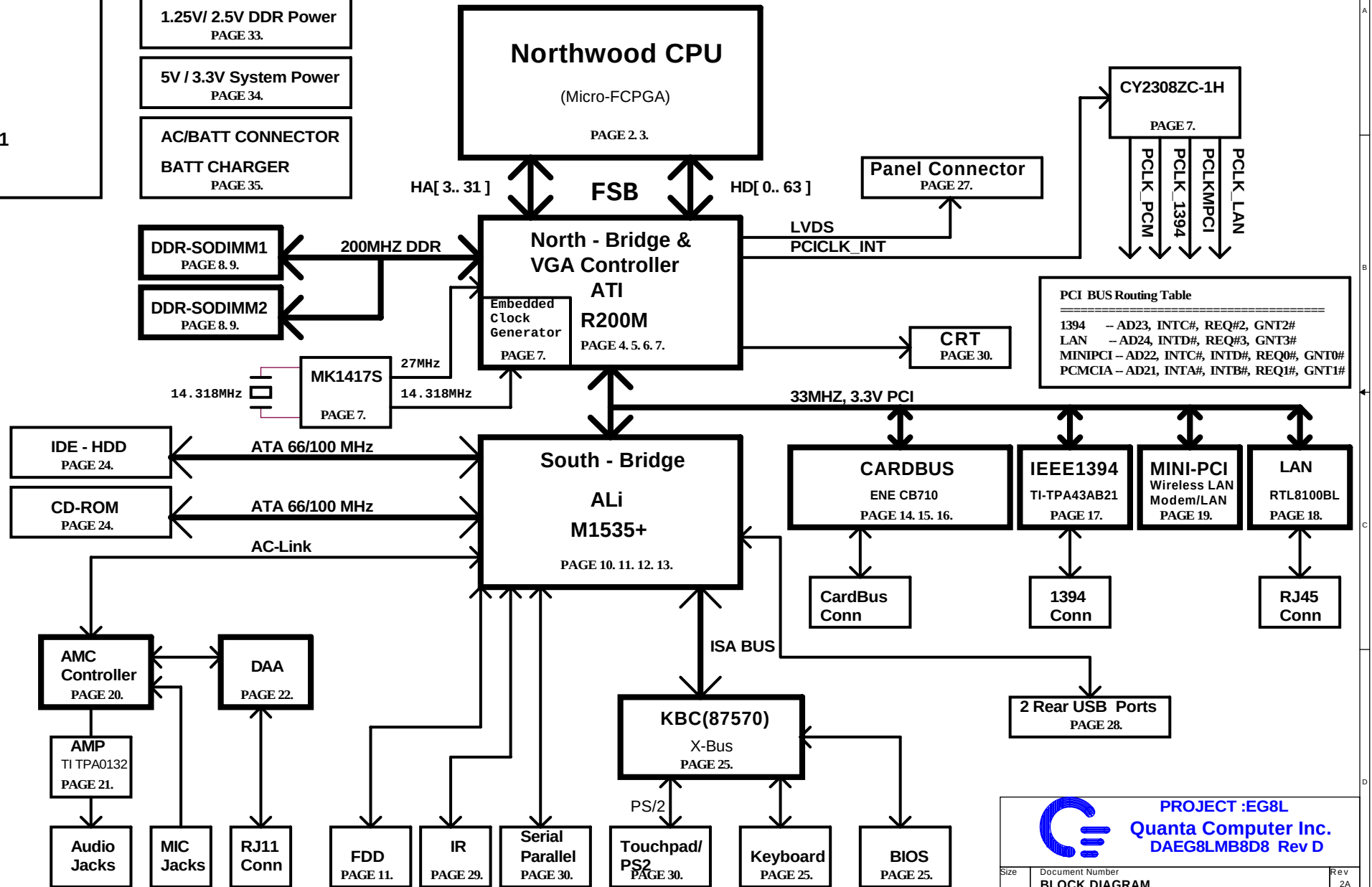
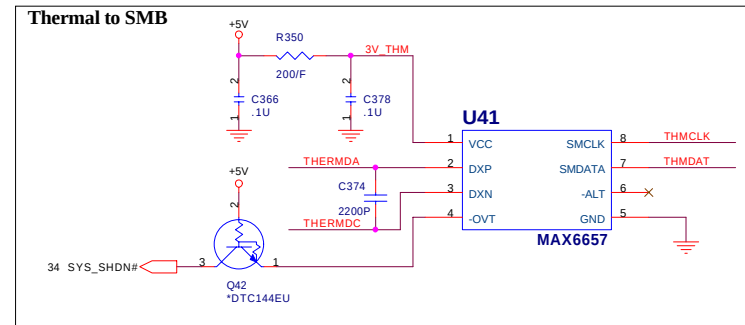
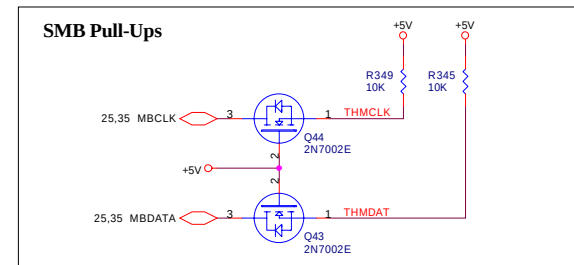
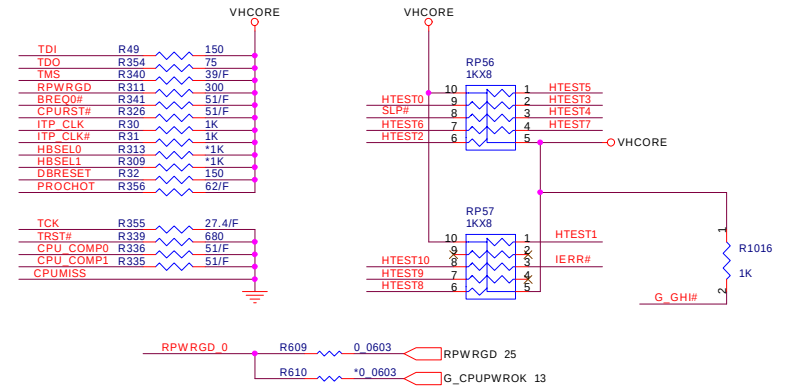


L1: TOP
L2: GND
L3: IN1
L4: VCC
L5: IN2
L6: IN3
L7: GND1
L8: BOT

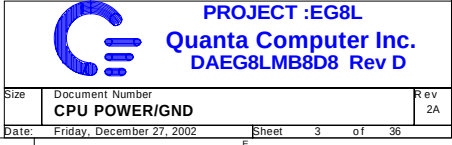
PAGE 35.



02



03

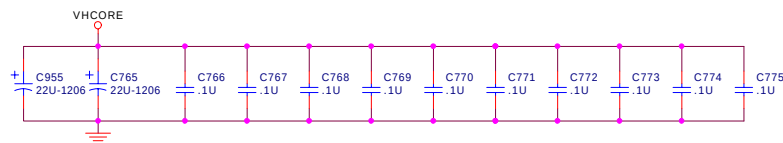
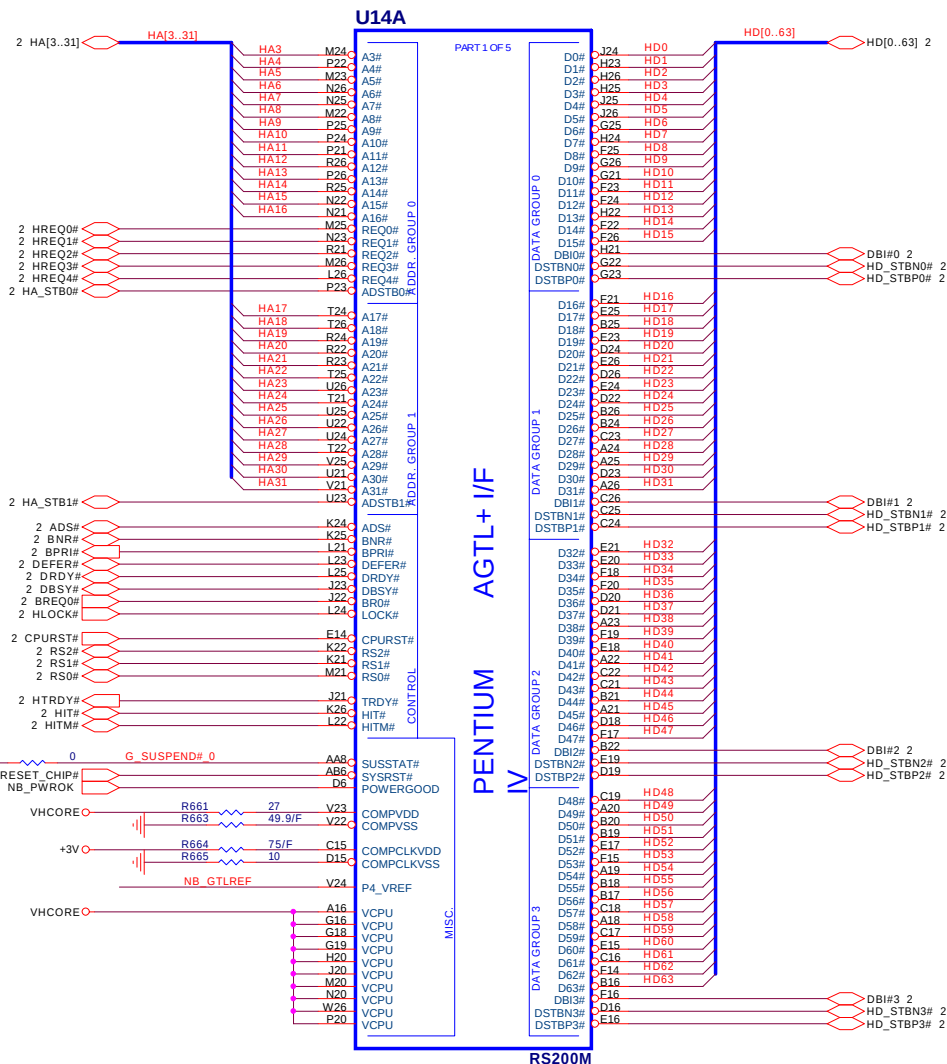
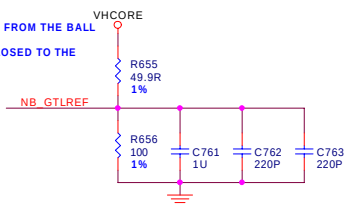


ATI_RS200M - 1 (AGTL+ Bus)

04



- 1) PLACE THOSE CIRCUITS < 1.5" FROM THE BALL
- 2) THE 220PF CAPS HAS TO BE CLOSED TO THE BALL AS POSSIBLE.



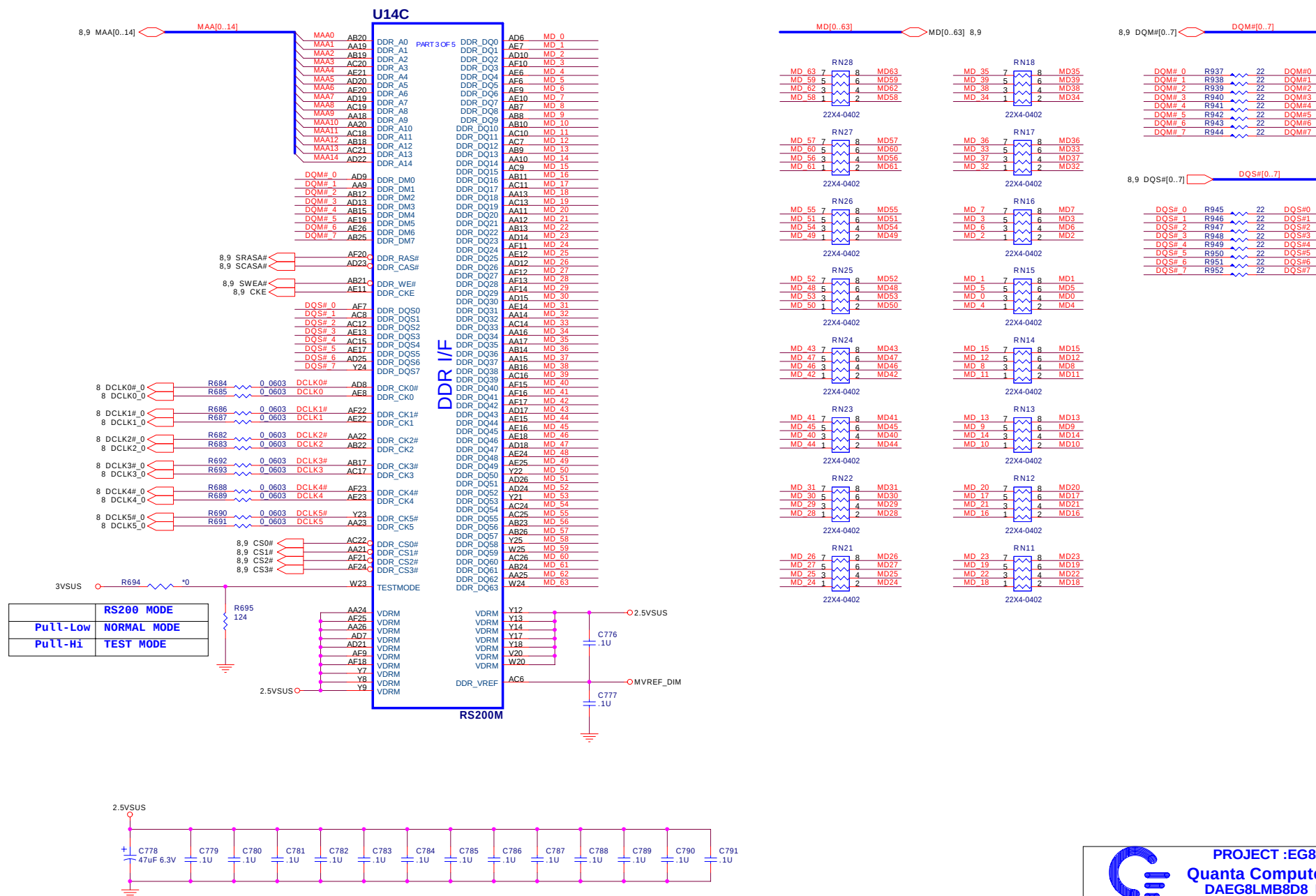
PROJECT :EG8L
Quanta Computer Inc.
DAEG8LMB8D8 Rev D

Size	Document Number	Rev
	RS200M-(AGTL+)	2A
Date:	Friday, December 27, 2002	Sheet 4 of 36

ATI_RS200M - 2 (DDR Interface)

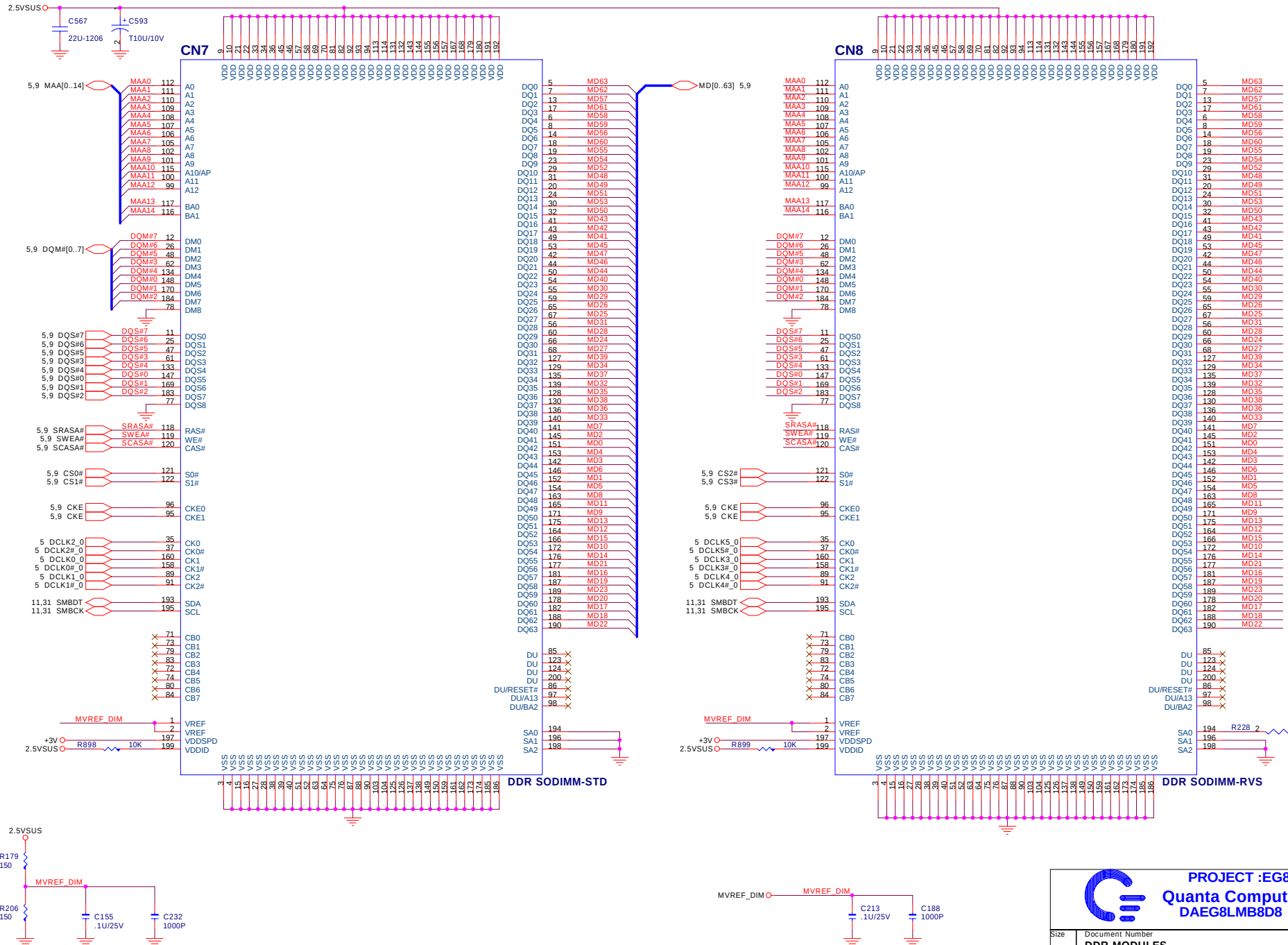
05

EC1_04/15



DDR - Modules

08

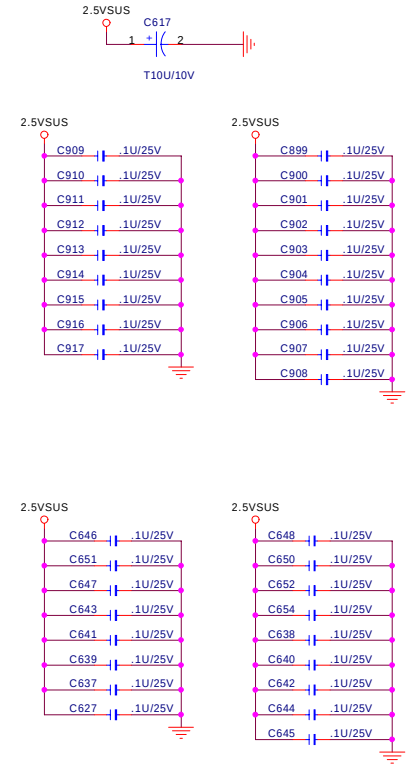
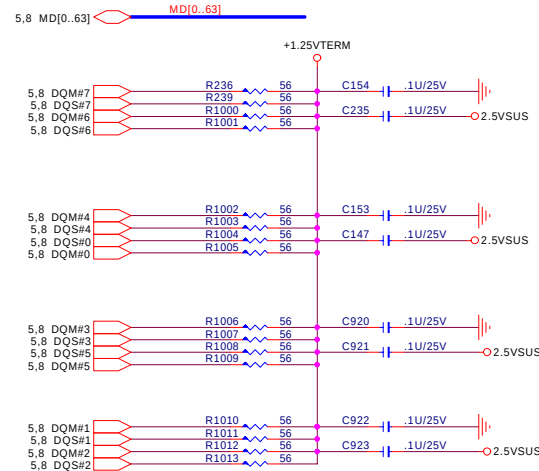
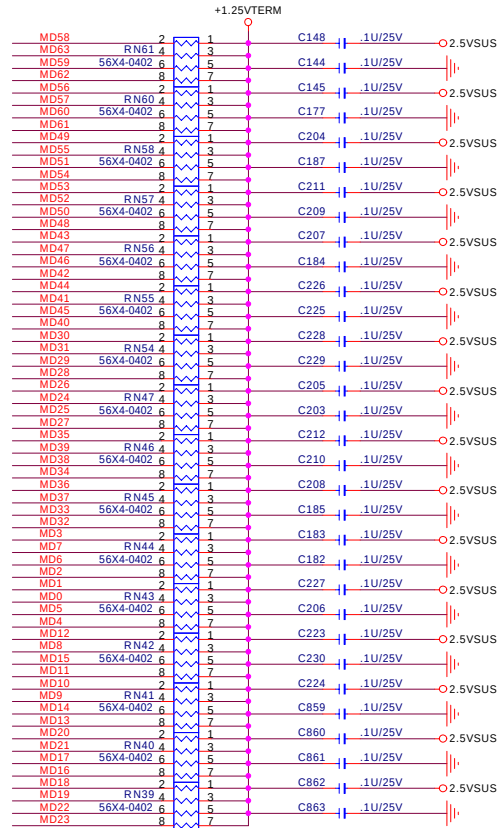


DDR TERMINATOR

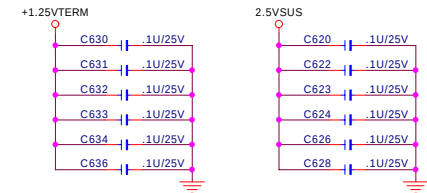
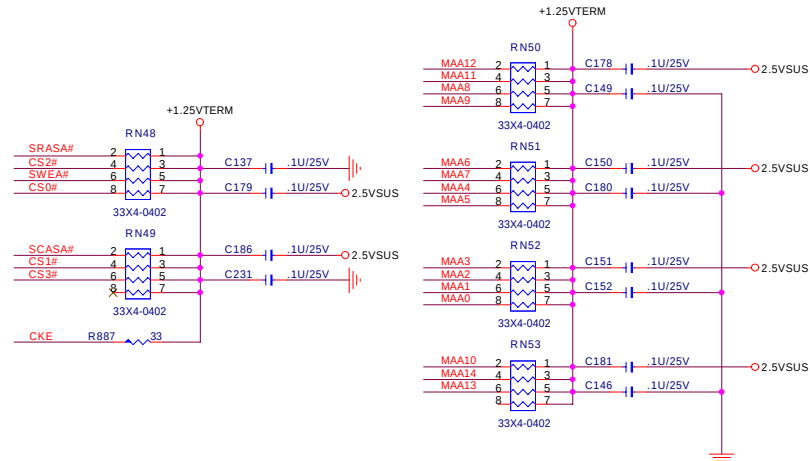
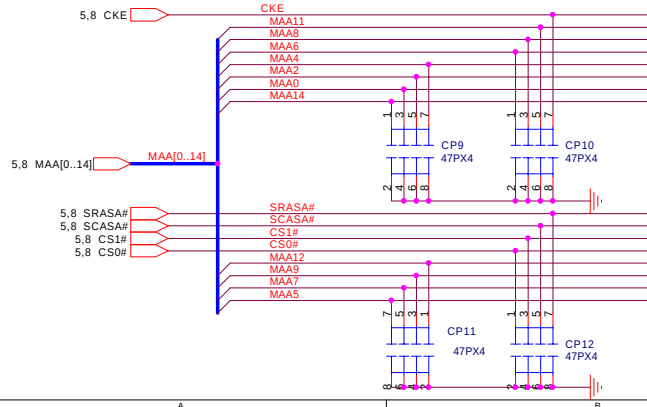
09

MD & DQM & DQS Terminator

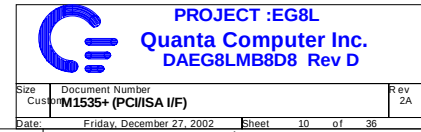
EC43_05/03



MA Terminator

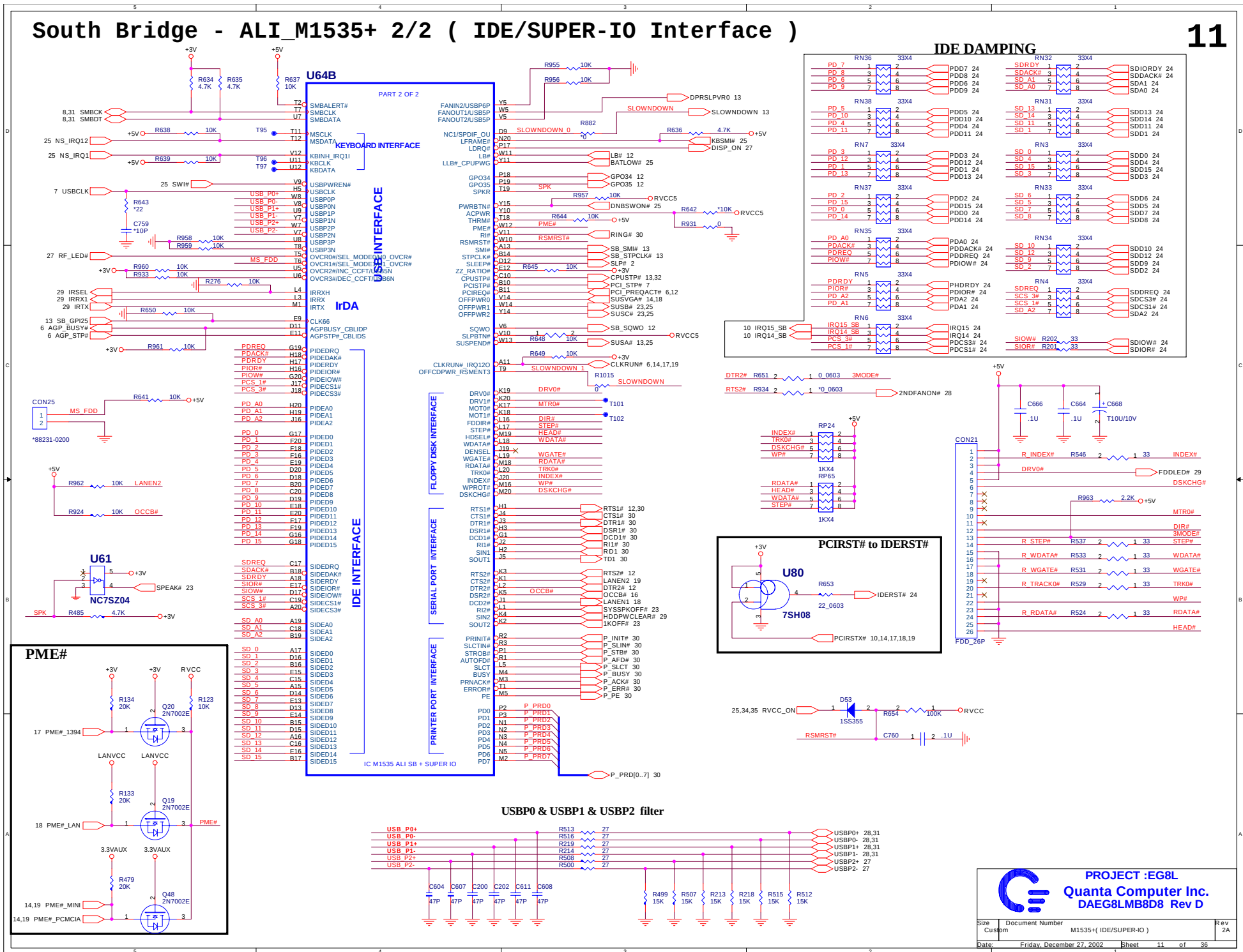


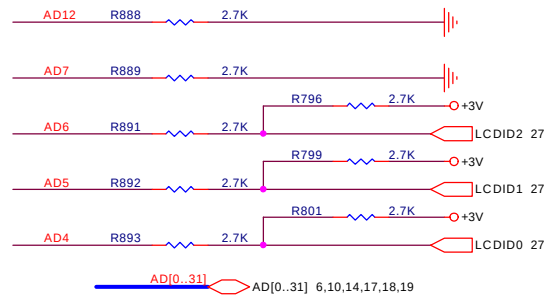
10



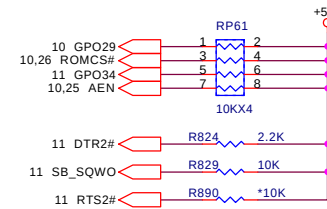
South Bridge - ALI_M1535+ 2/2 (IDE/SUPER-I/O Interface)

11





AD[30...31] : CLK SPEED DEFAULT: 00	00: 100 MHz 10: 166MHZ 01: 133 MHZ 11: 66 MHZ	AD15 : RESERVED	
AD29: RS200M FULL CONFIGURATION DEFAULT: 0	0: FULL CONFIGURATION 1: USES DEFAULT VALUES	AD14 : AGTL+ VDDQ DEFAULT: 0	0: 1.2V MOBILE PART 1: 1.7V DESKTOP PART
AD28: SPREAD SPECTRUM ENABLE DEFAULT: 1	0: DISABLE 1: ENABLE	AD13: RESERVED	
AD27: MCERR OBSERVATION DEFAULT: 0	0: DISABLE 1: ENABLE	AD[12] : FLAT PANEL ID MSB	
AD26: QUICK START SELECT DEFAULT : 0	0: DISABLE 1: ENABLE	AD10 : RESERVED	
AD25: INIT OBSERVATION DEFAULT : 0	0: DISABLE 1: ENABLE	AD09 : RESERVED	
AD24: IOQ ENABLE DEFAULT : 1	0: DISABLE 1: ENABLE	AD[7..4] : FLAT PANEL ID	
AD23 : RESERVED		AD3: PCI66 MODE DEFAULT: 0	0: 33MHz 1: 66MHz
AD22: 27MHz/33 MHz CLOCK SELECTION DEFAULT : 1	0: 33 MHz CLOCK OUTPUT 1: 27 MHz CLOCK INPUT	AD2: CAL DEFAULTS FOR CPU DEFAULT: 0	0: DISABLE 1: ENABLE
AD21 : RESERVED	0: PLL Bypass mode disable(Normal mode) 1: PLL Bypass mode enable(Debug only)	AD1: FrcShortReset DEFAULT: 0	0: DISABLE 1: ENABLE
AD20 : PCICLK EXPANSION DEFAULT : 0	1: PCI_REQ#3, PCI_GNT#3 USED AS PCICLK 0: PCI_REQ#3, PCI_GNT#3 USED AS REQ/GNT	AD0: PCI/AGP CONFIG. DEFAULT : 0	0: 33 MHz 1: 66 MHz
CBE#3: PROD TEST DEFAULT : 1	0: SHORT TIMERS 1: NORMAL OPERATION	PCI_PREQACT#: INTERNAL CLOCK GEN. DEFAULT : 1	0: DISABLE 1: ENABLE
CBE#0: Reserve DEFAULT : 0			

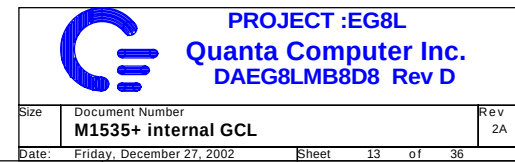


	DTR2#	RTS2#	RTS1#	CLK320	SPLED	SQWO
PULL HIGH	INTERNAL KEYBOARD PS2 MODE DEFAULT	ENABLE INTERNAL KEYBOARD	SIO BASE ADDRESS 0X3F0h	32K CLOCK TEST MODE ENABLE	AT POWER MODE	PENTIUM II MODE DEFAULT
PULL LOW	INTERNAL KEYBOARD AT MODE DEFAULT	DISABLE INTERNAL KEYBOARD DEFAULT	SIO BASE ADDRESS 0X370h DEFAULT	32K CLOCK TEST MODE DISABLE DEFAULT	ATX POWER MODE DEFAULT	PENTIUM / K7 MODE

PCS1#	PCS0#	GPO34	LB#	GPO30[AEN]	GPO29	ROMCS#	GPO35
GPI24 & GPO30 FUNCTIONS	DISABLE 4Mb FLASH ROM MODE	CHIP TEST MODE DISABLE DEFAULT	ACTIVE HIGH TO POWER OFF	L, L - USB TEST MODE L, H - AUDIO/MODEM TEST MODE H, L - SUPER IO TEST MODE H, H - NORMAL OPERATION		FLASH ROM MODE SELECTION L, L - RESERVED L, H - ENABLE 8Mb Flash ROM SIZE H, L - DISABLE ALL FUNC. IN THIS CONFIG. H, H - ENABLE LPC FLASH ROM	
IOCHRDY & AEN FUNCTIONS	ENABLE 4Mb FLASH ROM MODE	CHIP TEST MODE ENABLE	ACTIVE LOW TO POWER OFF				
DEFAULT	DEFAULT		DEFAULT	DEFAULT : H, H		DEFAULT : H, L	

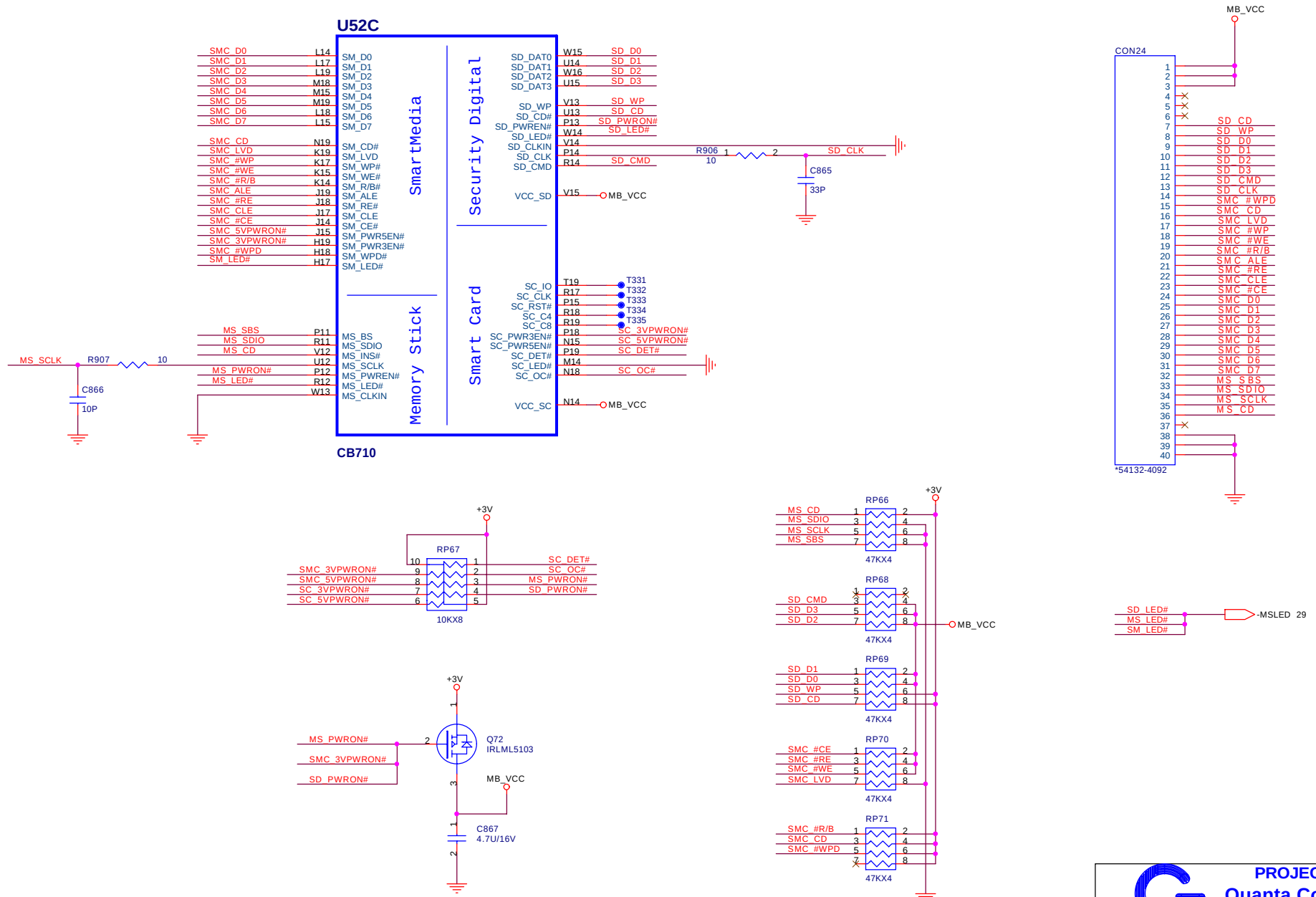
 PROJECT :EG8L Quanta Computer Inc. DAEG8LMB8D8 Rev D		
Size	Document Number	Rev
SYSTEM CONFIG. & S/B H/W STRAPPING		2A
Date:	Friday, December 27, 2002	Sheet 12 of 36

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CARDBUS CONTROLLER - CB710_ 2/2 (Memorystick I/F)

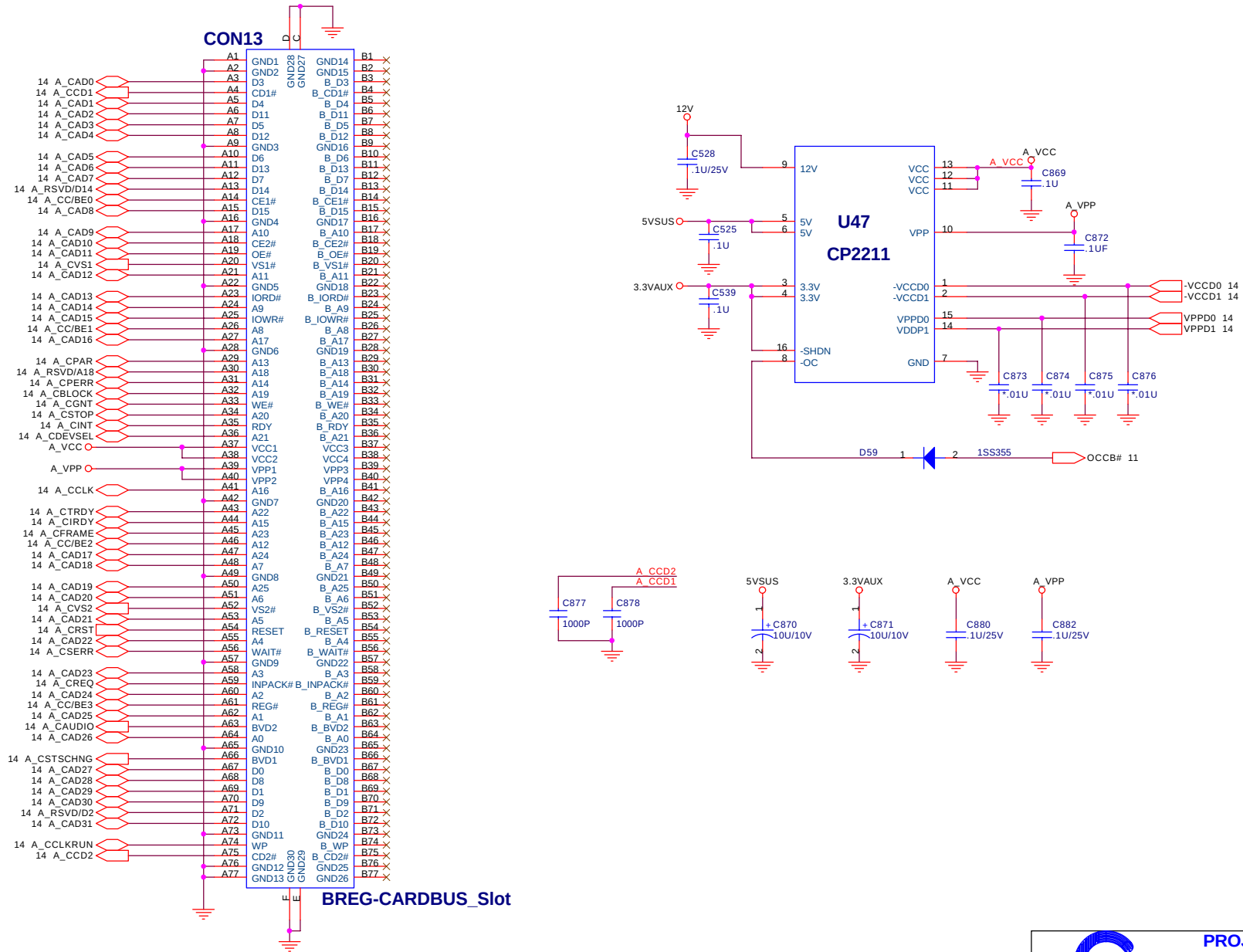
15



PROJECT :EG8L
Quanta Computer Inc.
DAEG8LMB8D8 Rev D

CARDBUS Slot

16



CARDBUS PCMCIA SOCKET

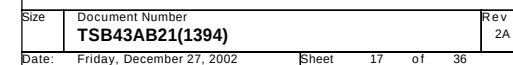
PROJECT :EG8L

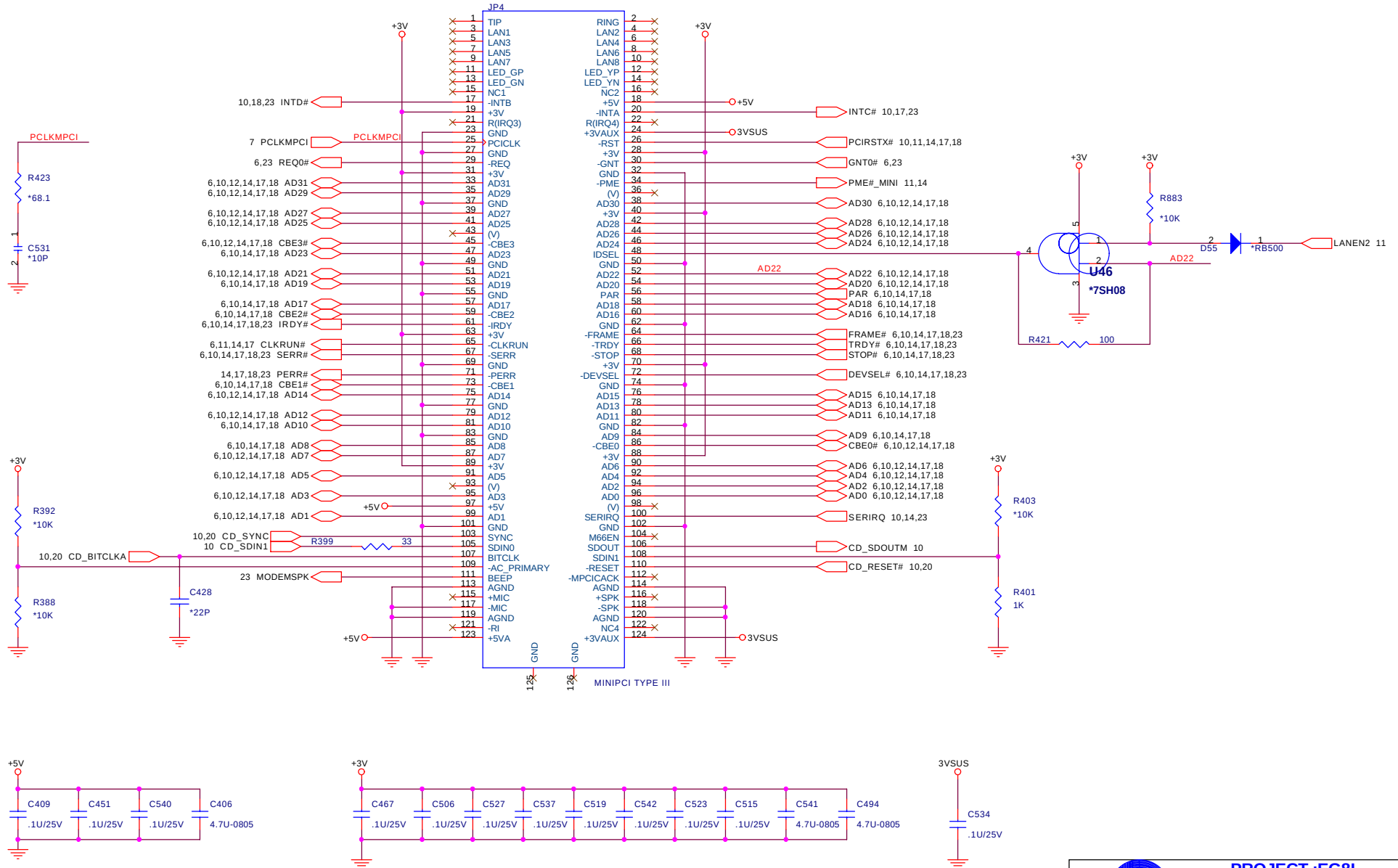
Quanta Computer Inc.

DAEG8LMB8D8 Rev D

Size	Document Number	Rev
	CARDBUS SLOT	2A
Date:	Friday, December 27, 2002	Sheet 16 of 36

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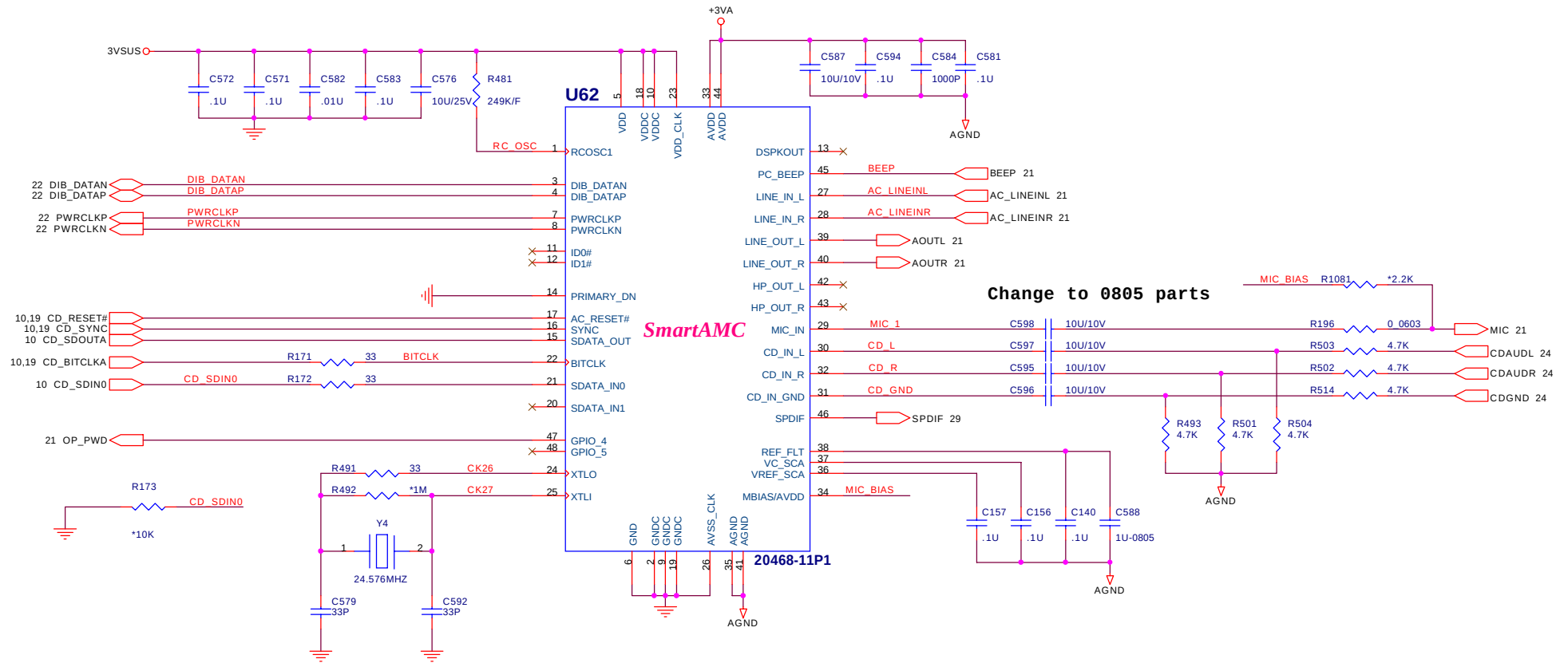




PROJECT :EG8L
Quanta Computer Inc.
DAEG8LMB8D8 Rev D

Size	Document Number	Rev
	MINI-PCI	2A
Date:	Friday, December 27, 2002	Sheet 19 of 36

The AMC20463-001 modem is used for mother board family MBAMC20463-005.



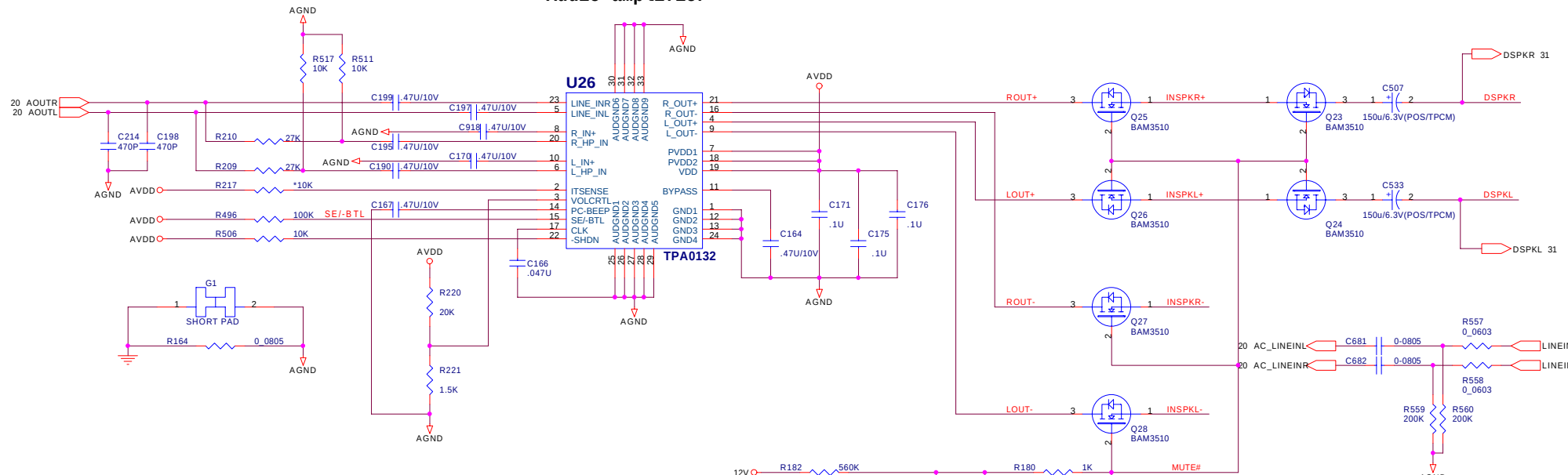
PROJECT :EG8L
Quanta Computer Inc.
DAEG8LMB8D8 Rev D

Size	Document Number	Rev
	AMC97- Controller	2A
Date:	Friday, December 27, 2002	Sheet 20 of 36

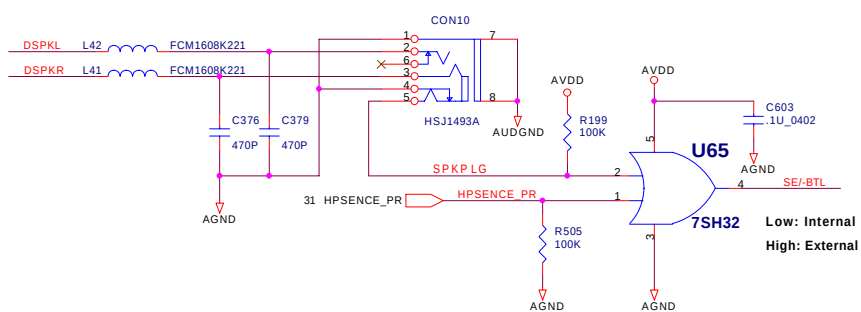
AUDIO AMPLIFIER - TI_TPA0132

21

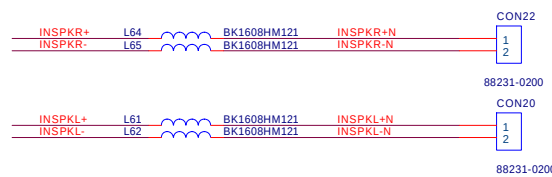
Audio amplifier



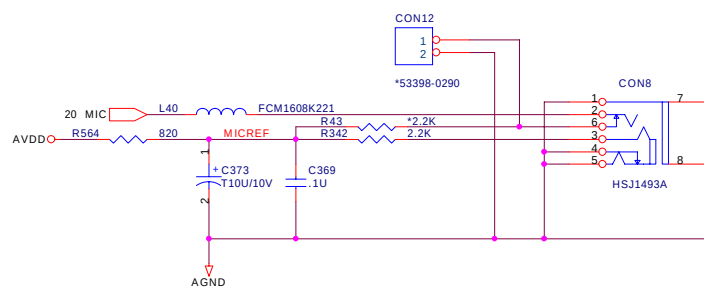
EXTERNAL SPEAKER



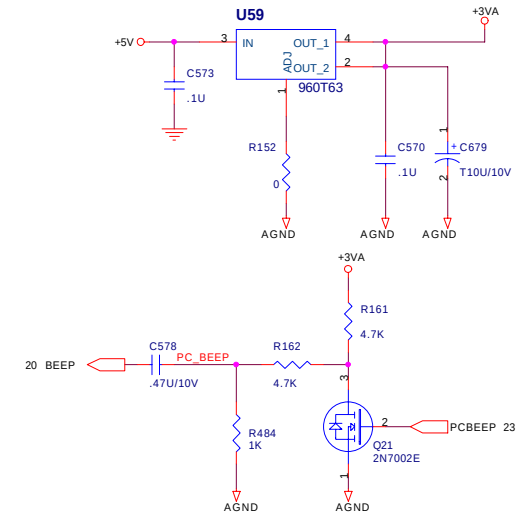
SPEAKER CONNECTOR



MICPHONE CONNECTOR



5V --> +3VA



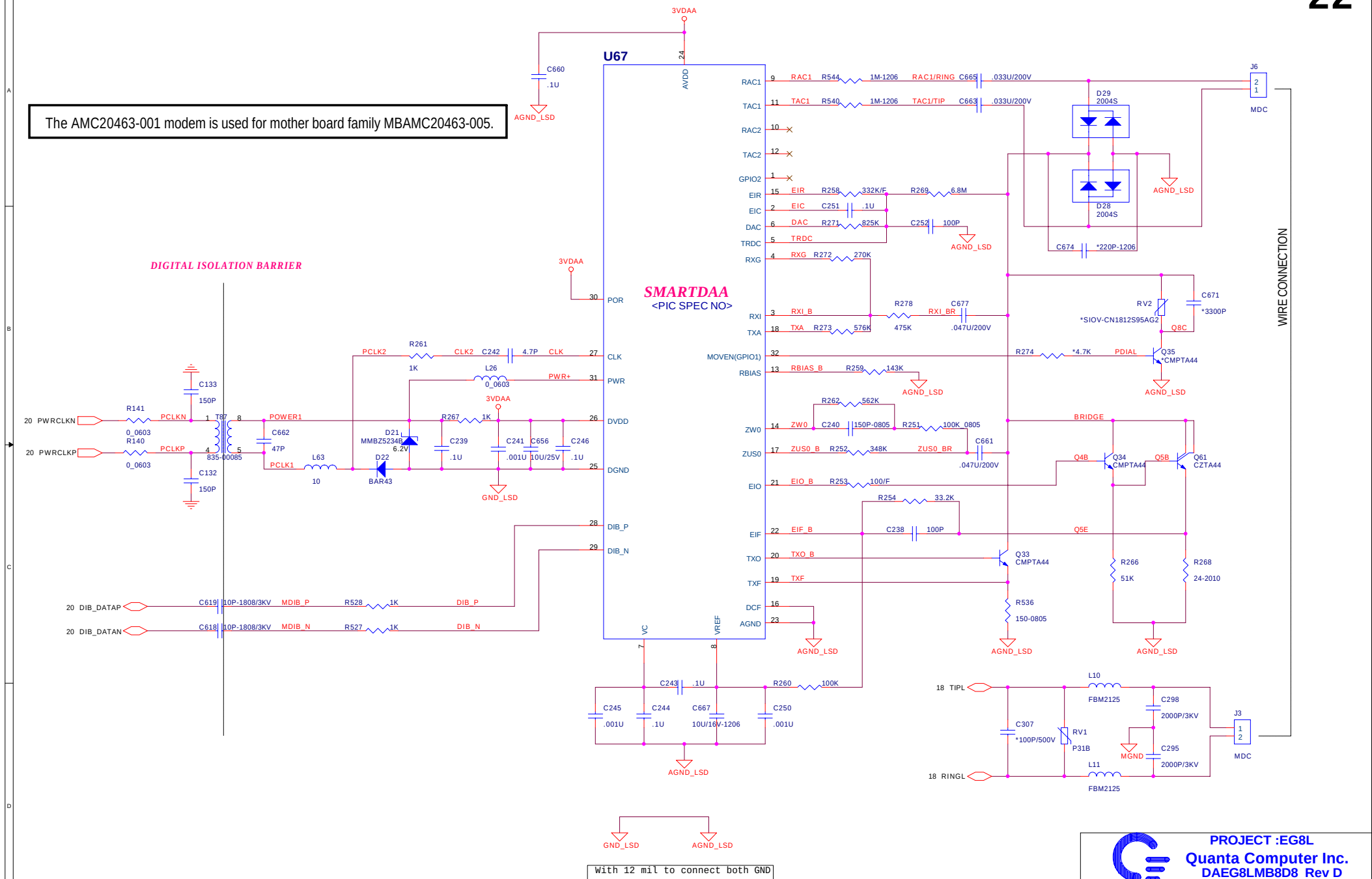
PROJECT :EG8L
Quanta Computer Inc.
DAEG8LMB8D8 Rev D

Size	Document Number	Rev
	AMPLIFIER	2A
Date:	Friday, December 27, 2002	Sheet 21 of 36

MODEM DAA

22

The AMC20463-001 modem is used for mother board family MBAMC20463-005.

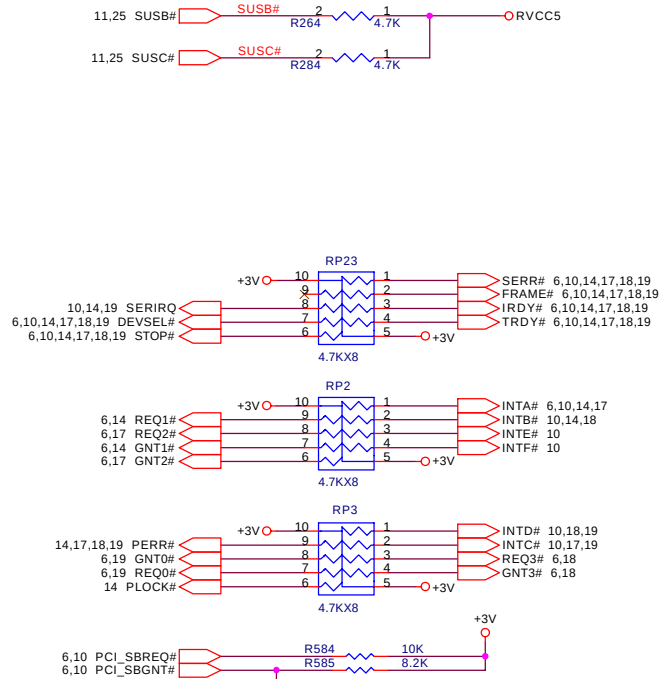


With 12 mil to connect both GND

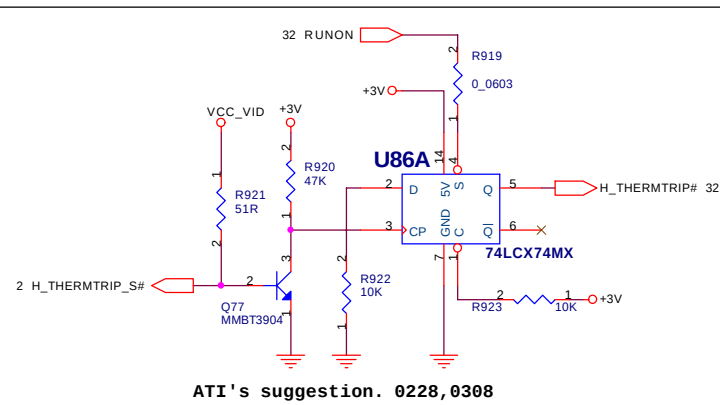
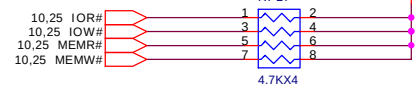
PULL UP Resistor , SPEAKER OUT Logic

23

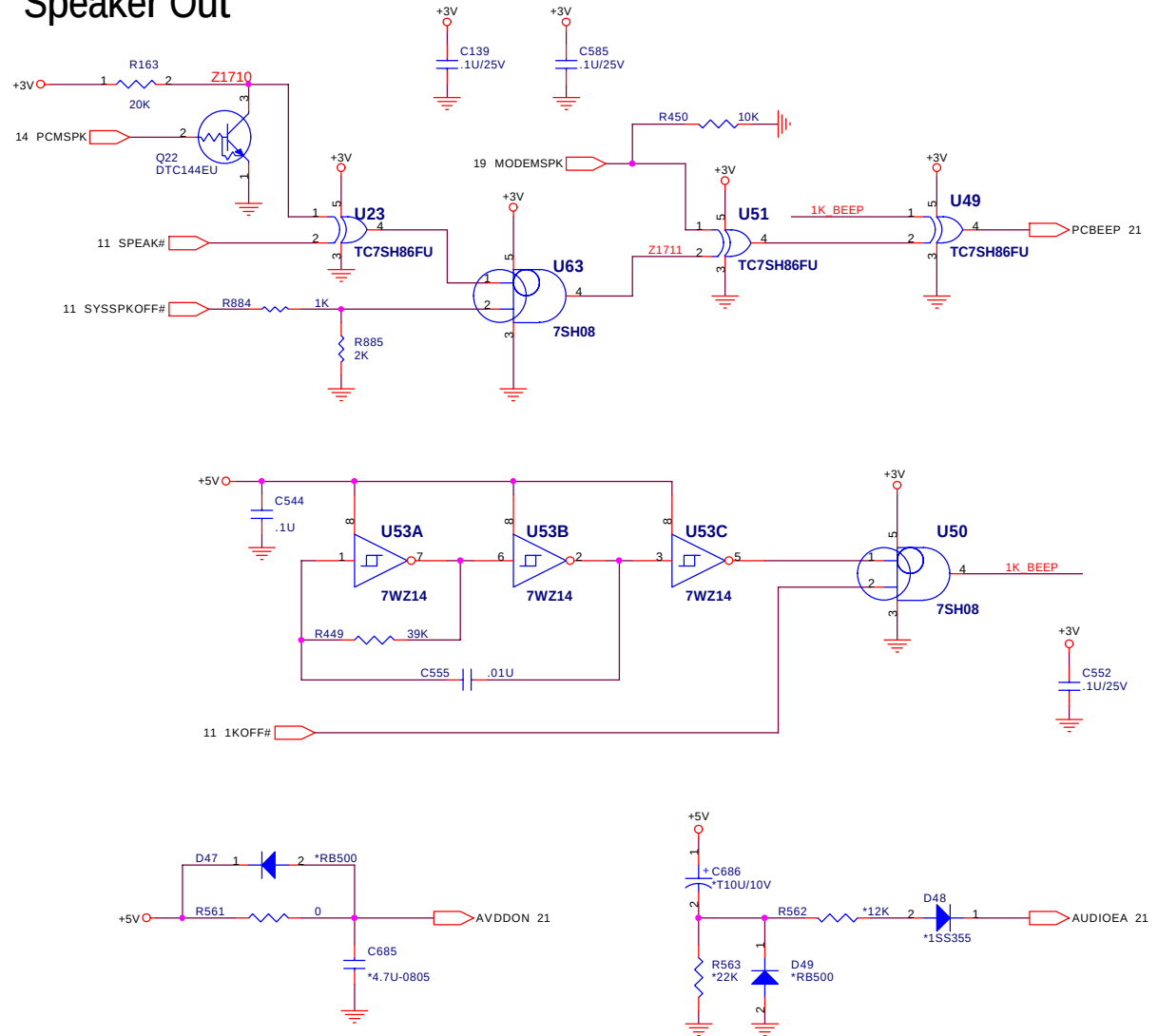
PCI PULL UP



ISA PULL UP



Speaker Out

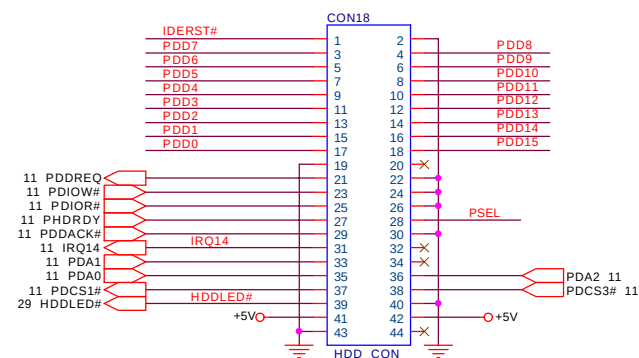
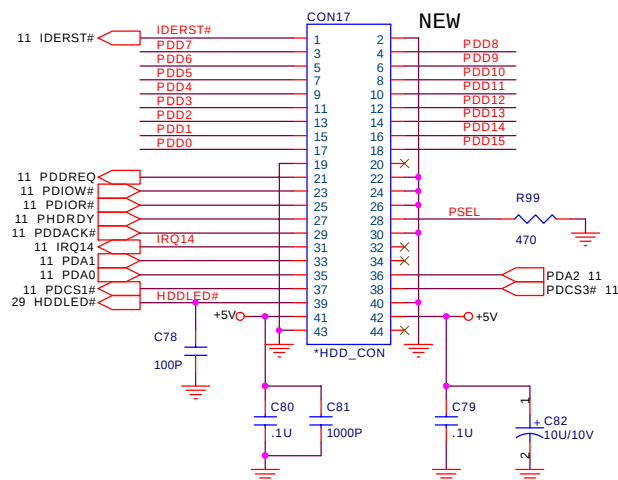
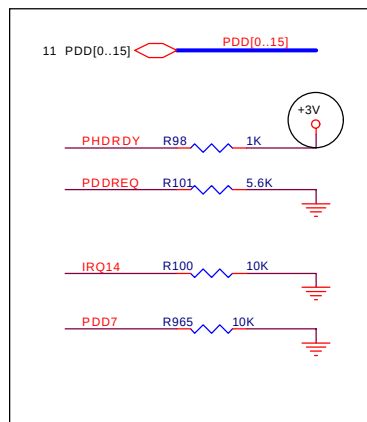


PROJECT :EG8L
Quanta Computer Inc.
DAEG8LMB8D8 Rev D

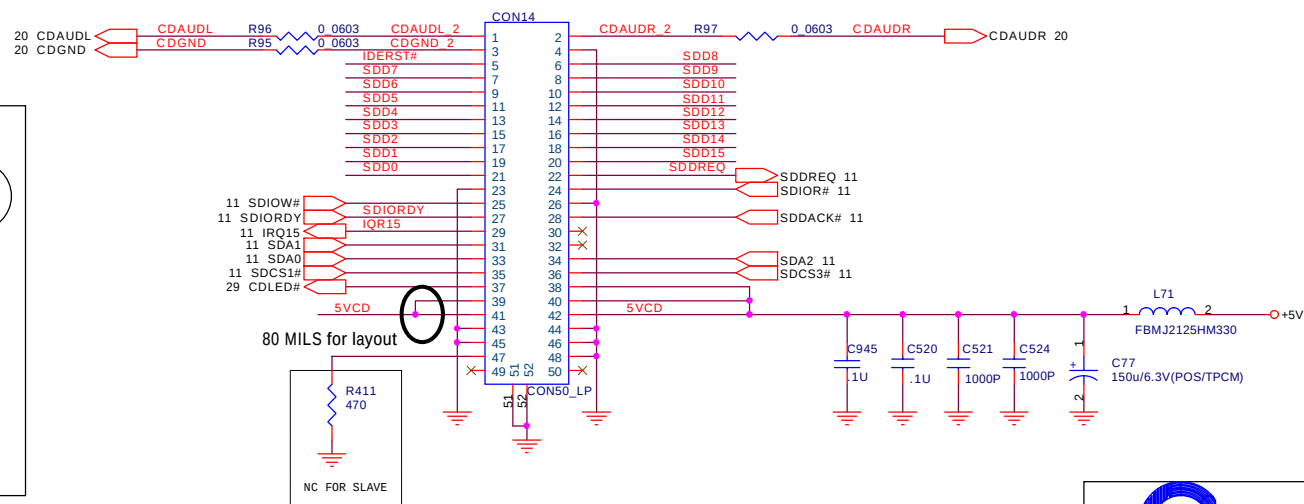
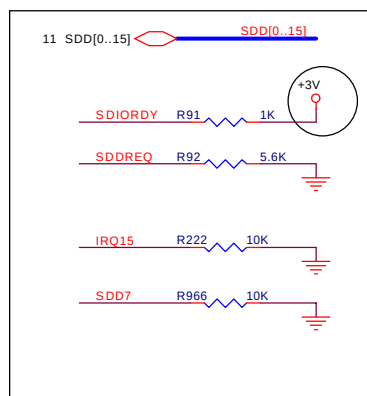
Size	Document Number	Rev
	PULL UP,SPEAKER	2A
Date:	Friday, December 27, 2002	Sheet 23 of 36

HDD connector

24



CD-ROM connector

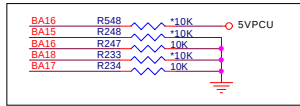


PROJECT :EG8L
Quanta Computer Inc.
DAEG8LMB8D8 Rev D

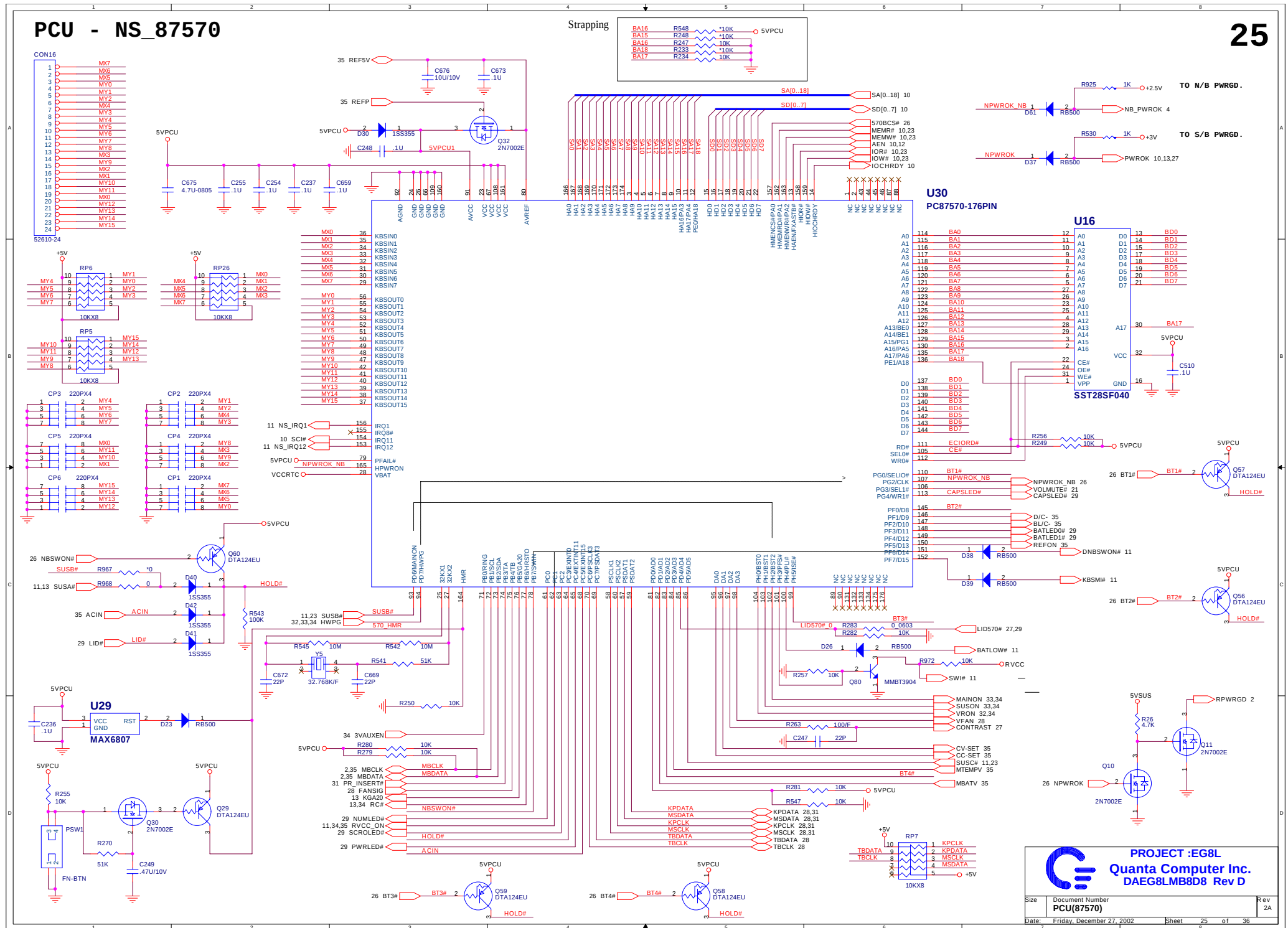
Size	Document Number HDD,CD-ROM	Rev 2A
Date:	Friday, December 27, 2002	Sheet 24 of 36

PCU - NS_87570

Strapping

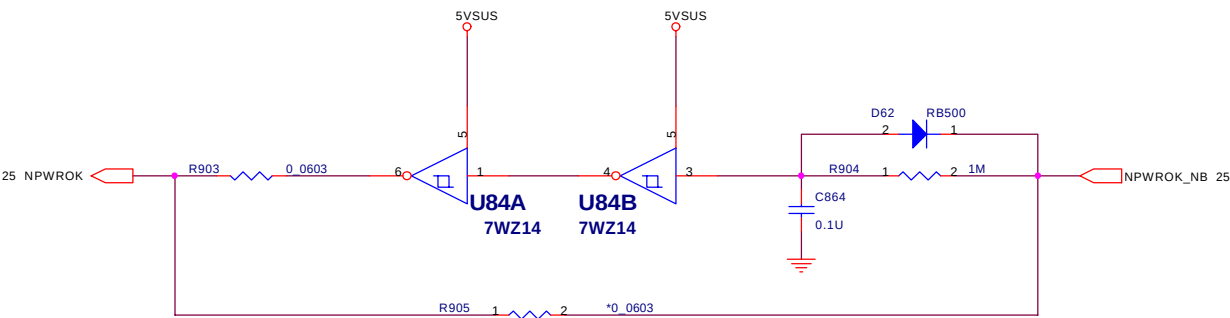


25

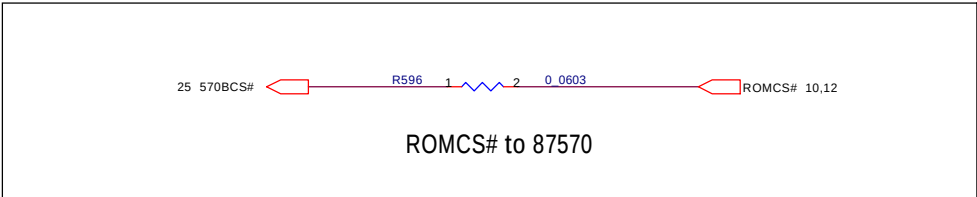


PROJECT :EG8L
Quanta Computer Inc.
DAEG8LMB8D8 Rev D

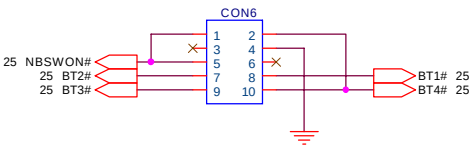
Size	Document Number	Rev
PCU(87570)		2A
Date	Friday, December 27, 2002	Sheet 25 of 36



For_ATI_PWG function



ROMCS# to 87570



UPPER CONNECT

A

1

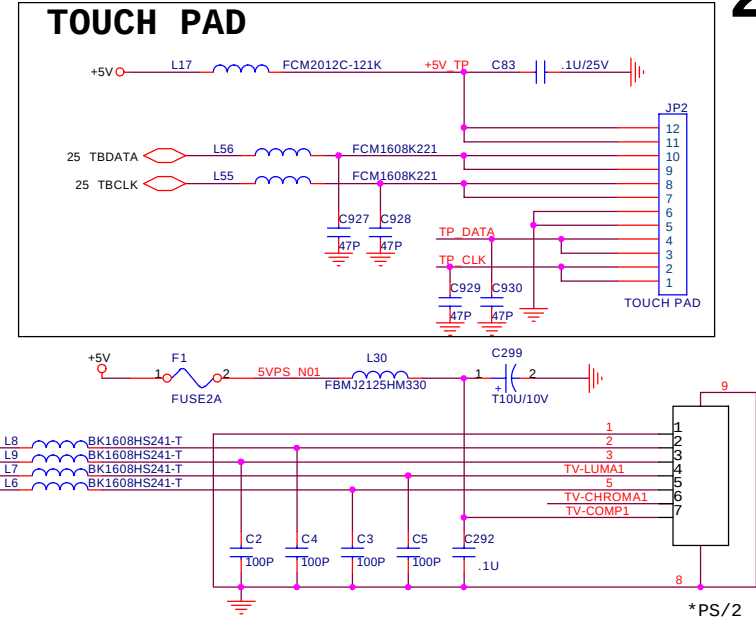
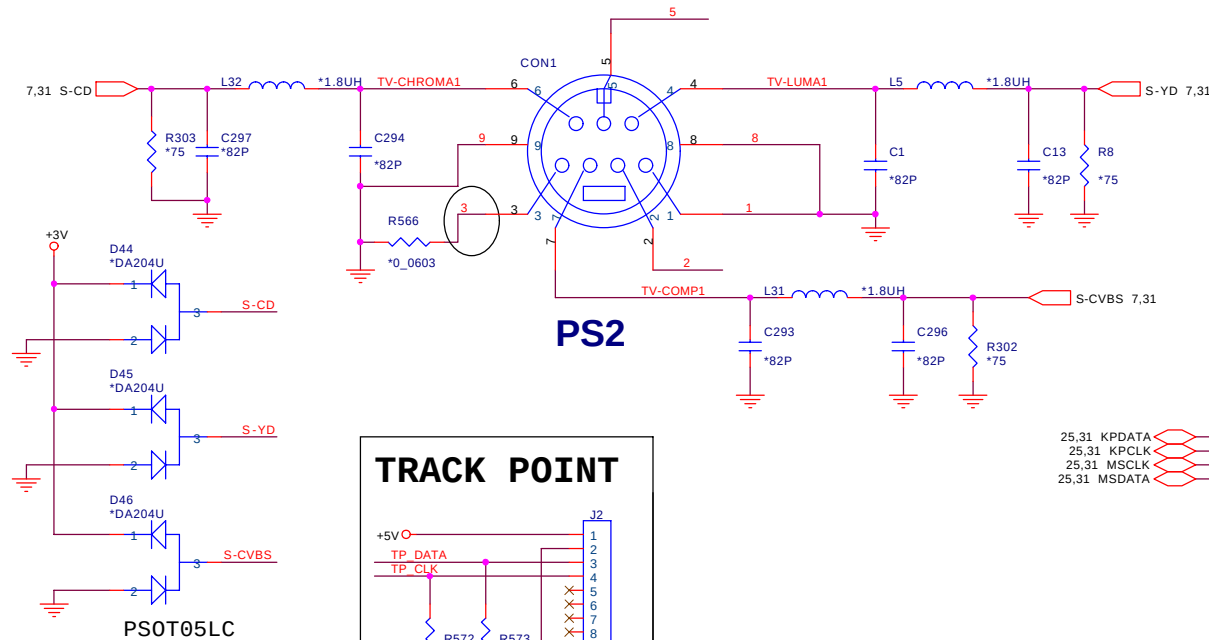


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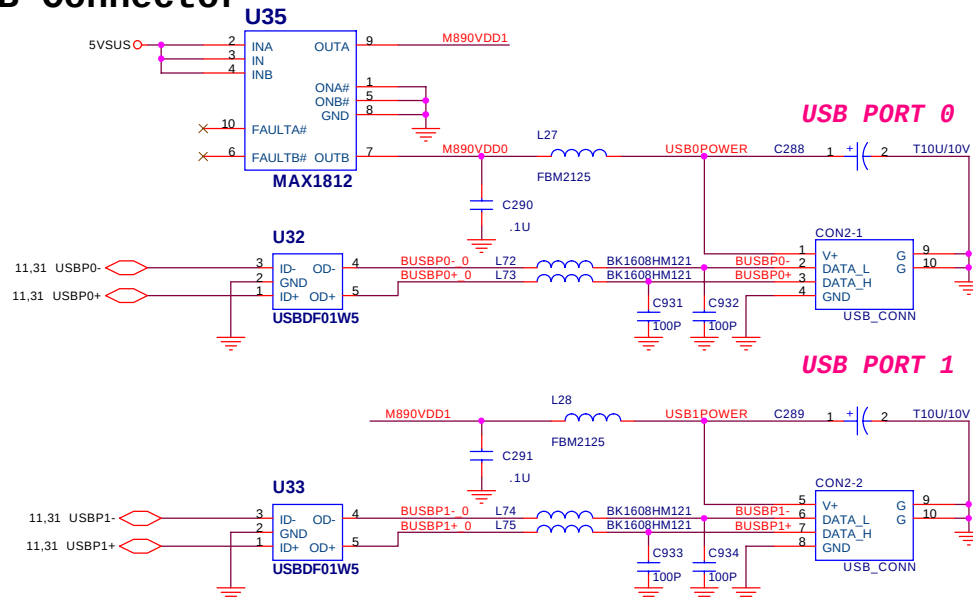
1

S-VIDEO, USB , FAN, PS/2 , T/P Connector

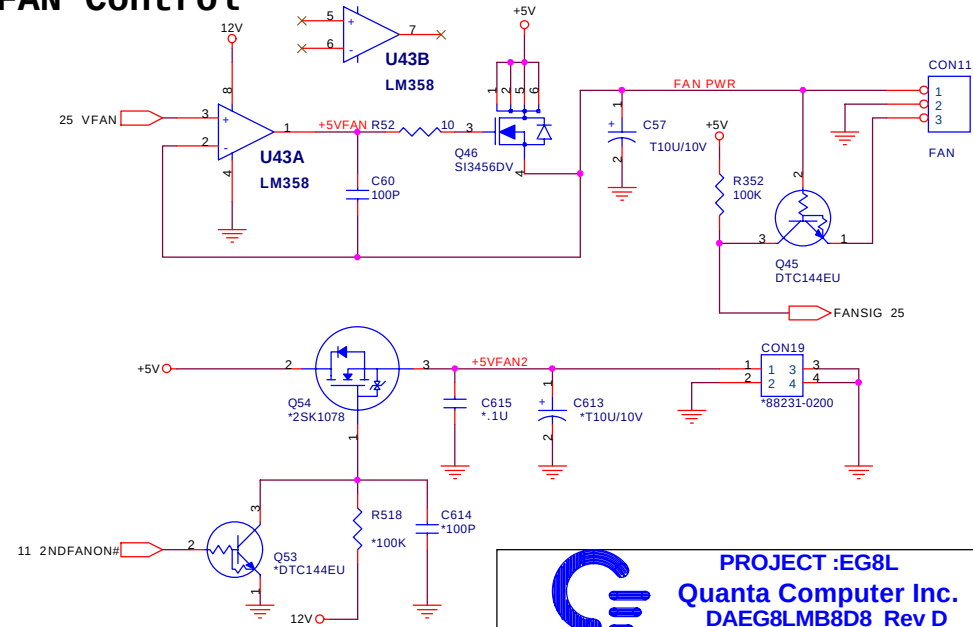
28



USB Connector



FAN Control

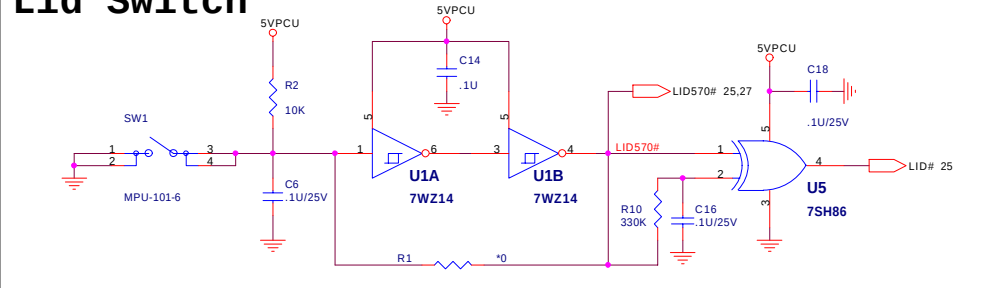


PROJECT :EG8L
Quanta Computer Inc.
DAEG8LMB8D8 Rev D

Size	Document Number	Rev
	S-VIDEO,FAN,PS/2,USB,T/P	2A
Date:	Friday, December 27, 2002	Sheet 28 of 36

LED, RTC, LID, IR, SPDIF Control Logic

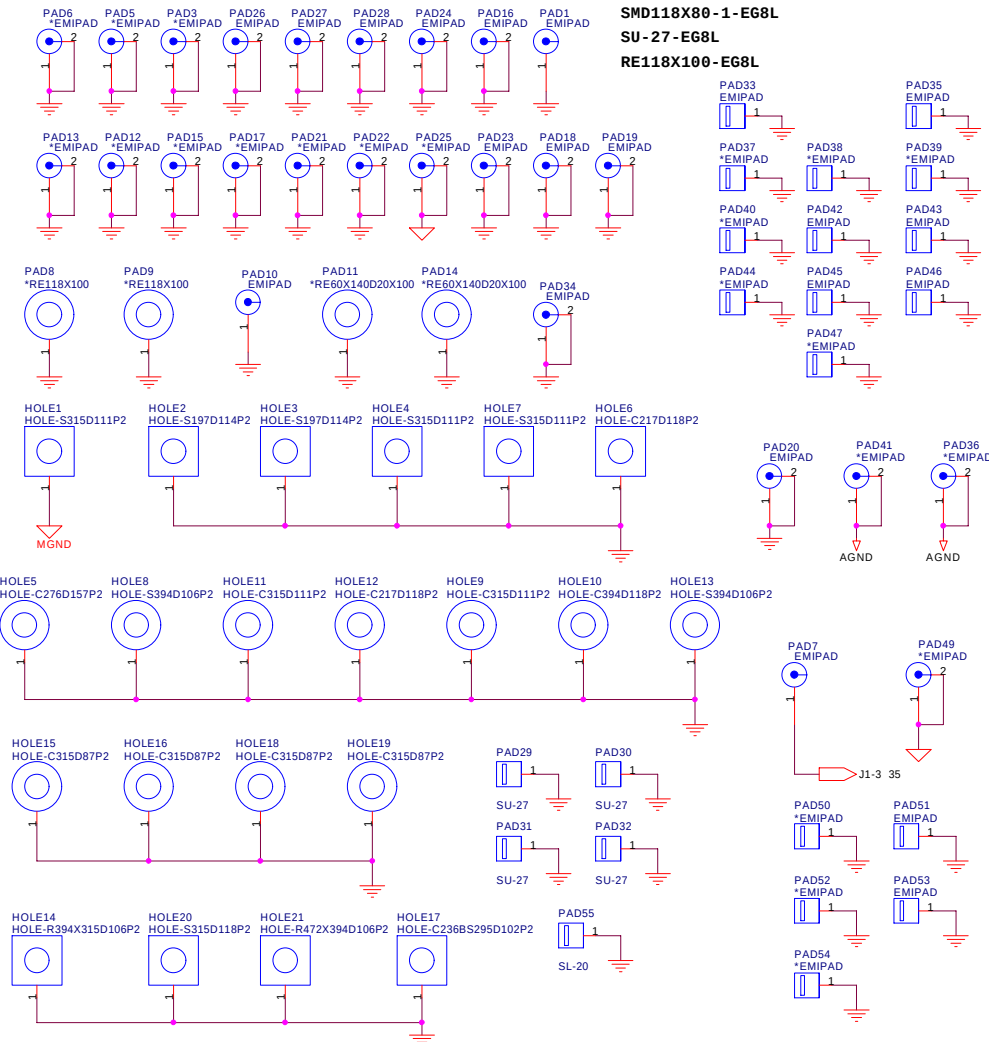
Lid Switch



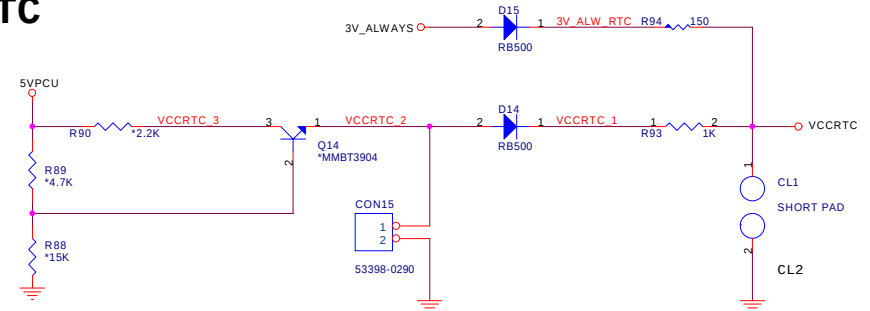
SMD118X80-1-EG8L

SU-27-EG8L

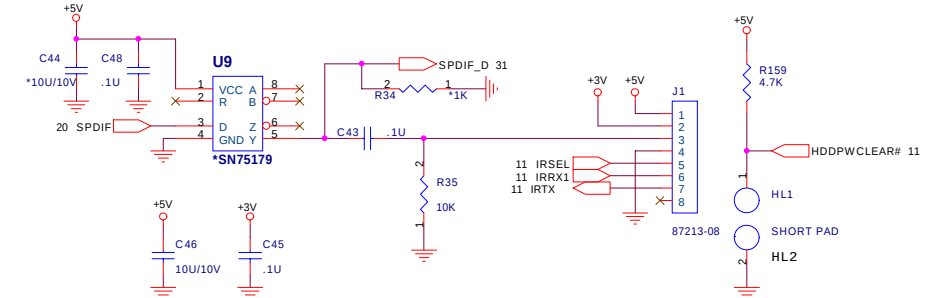
RE118X100-EG8L



RTC



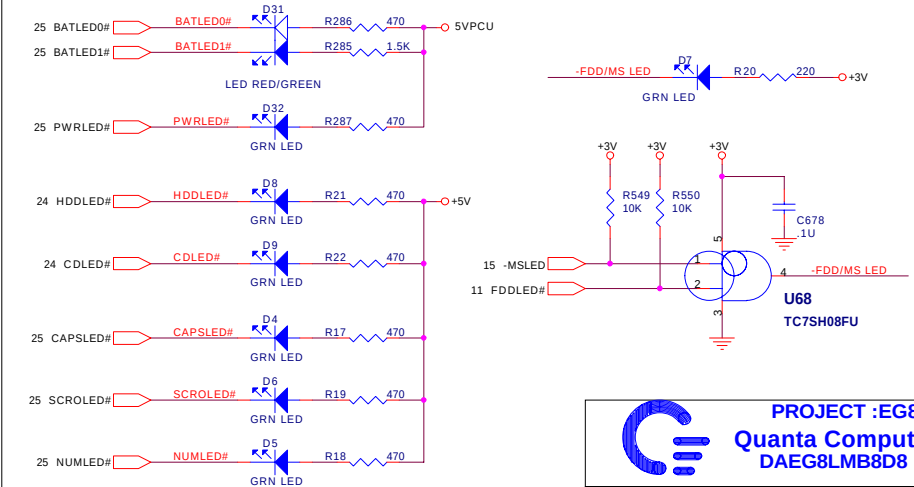
IR Conn.



LED

INDICATOR

CAPS-LOCK NUM-LOCK SCROLL FDD HDD CD-ROM

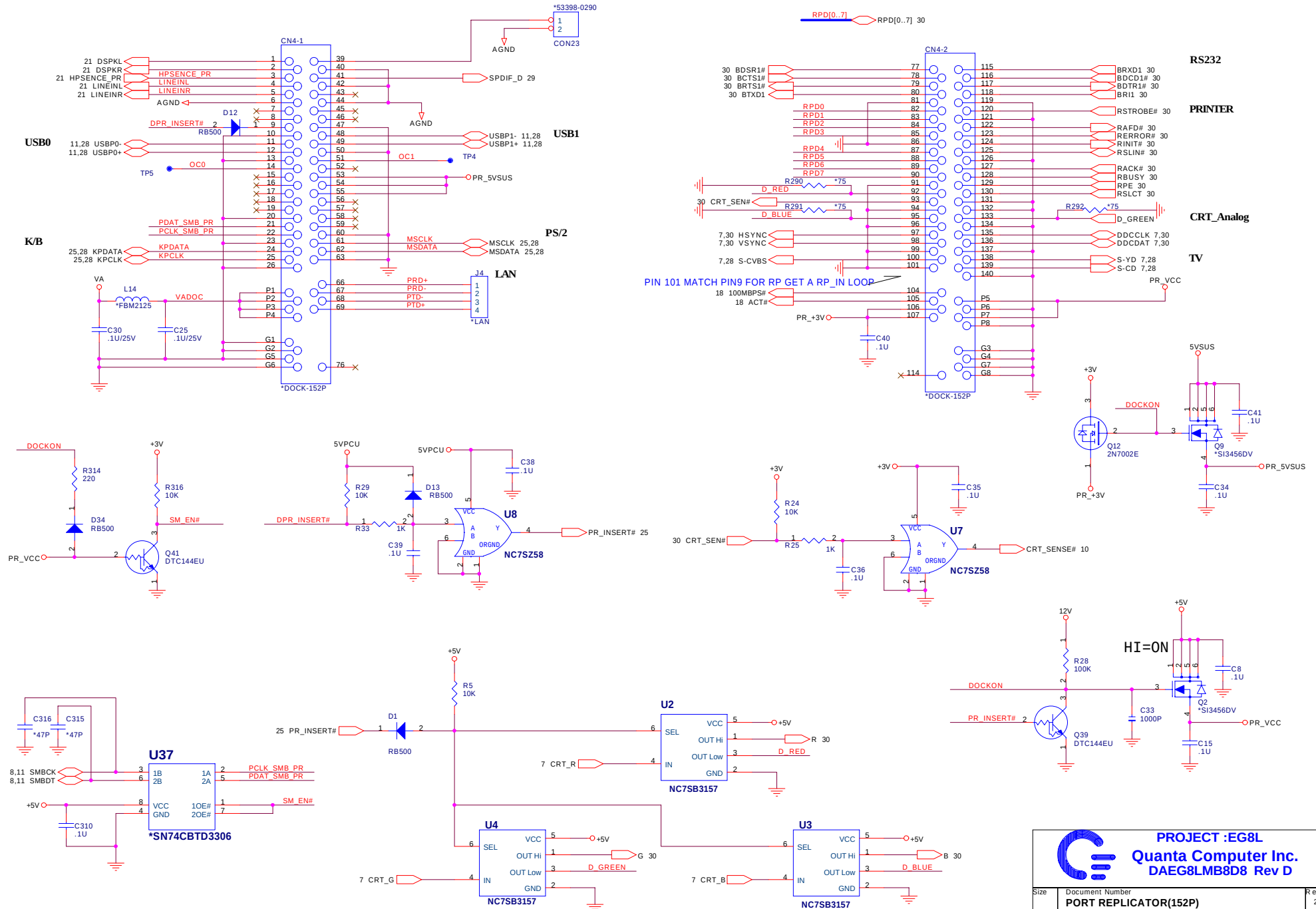


PROJECT :EG8L

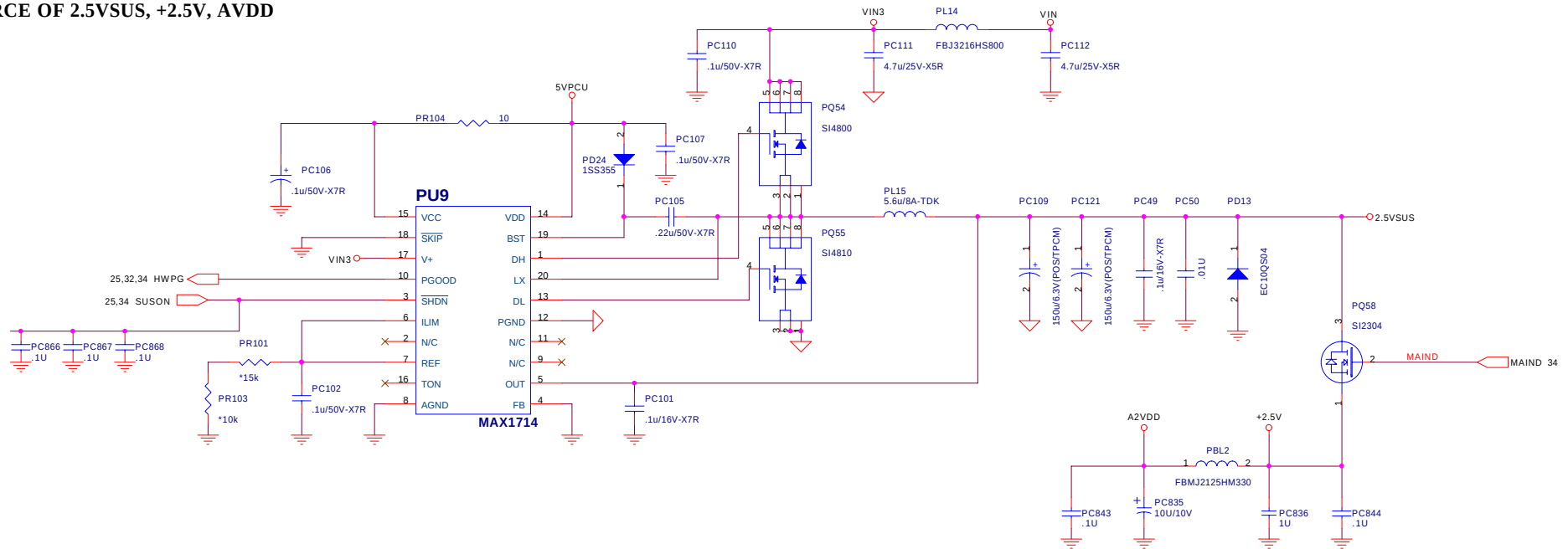
Quanta Computer Inc.
DAEG8LMB8D8 Rev D

PORT REPLICATOR (152 Pin)

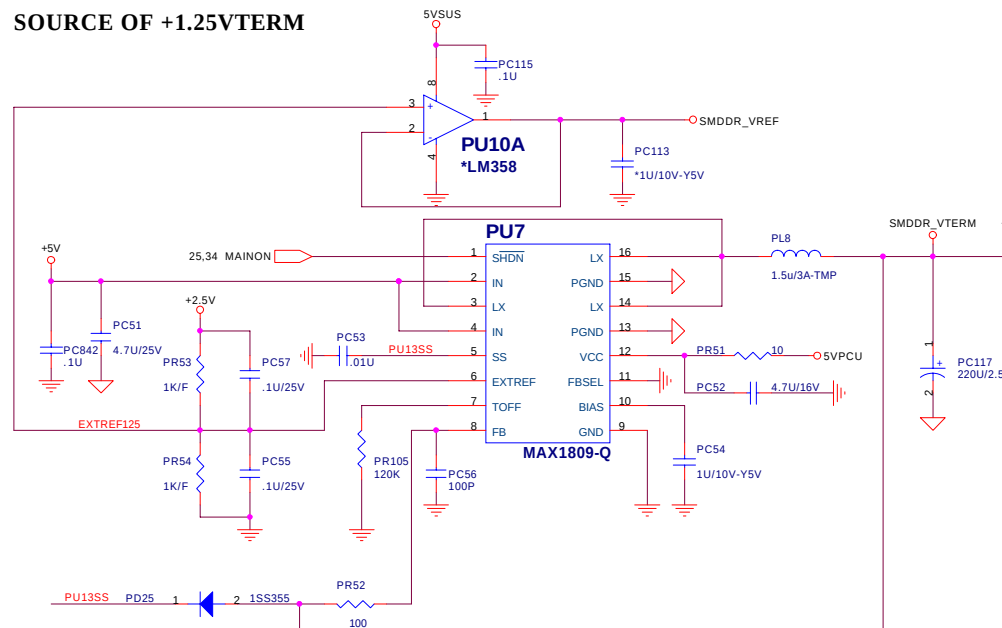
31



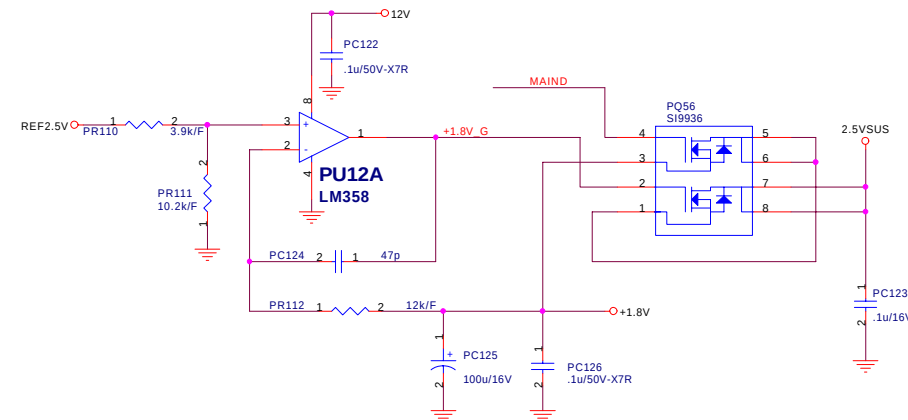
SOURCE OF 2.5VSUS, +2.5V, AVDD



SOURCE OF +1.25VTERM



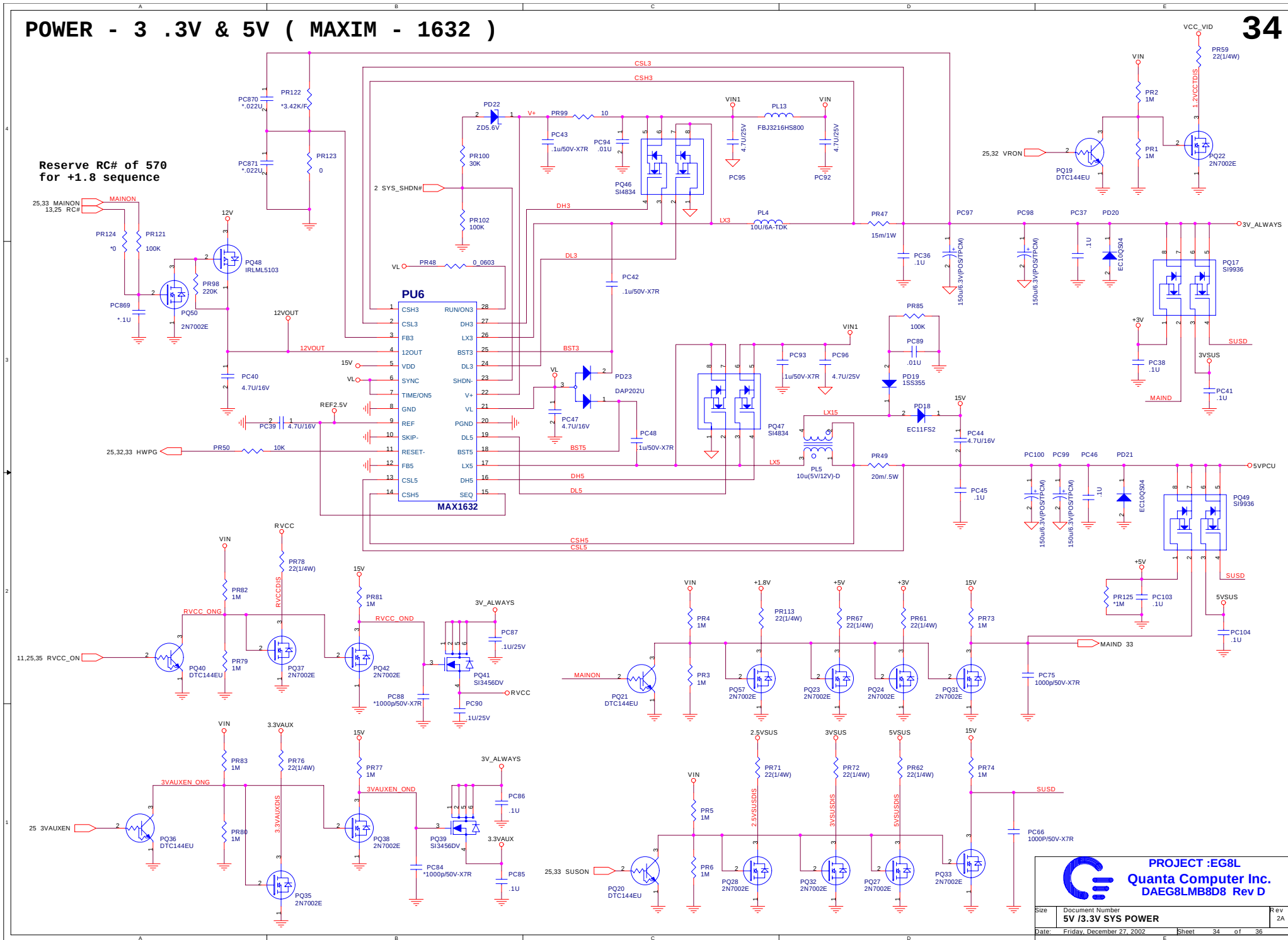
SOURCE OF 1.8V



PROJECT :EG8L
Quanta Computer Inc.
DAEG8LMB8D8 Rev D

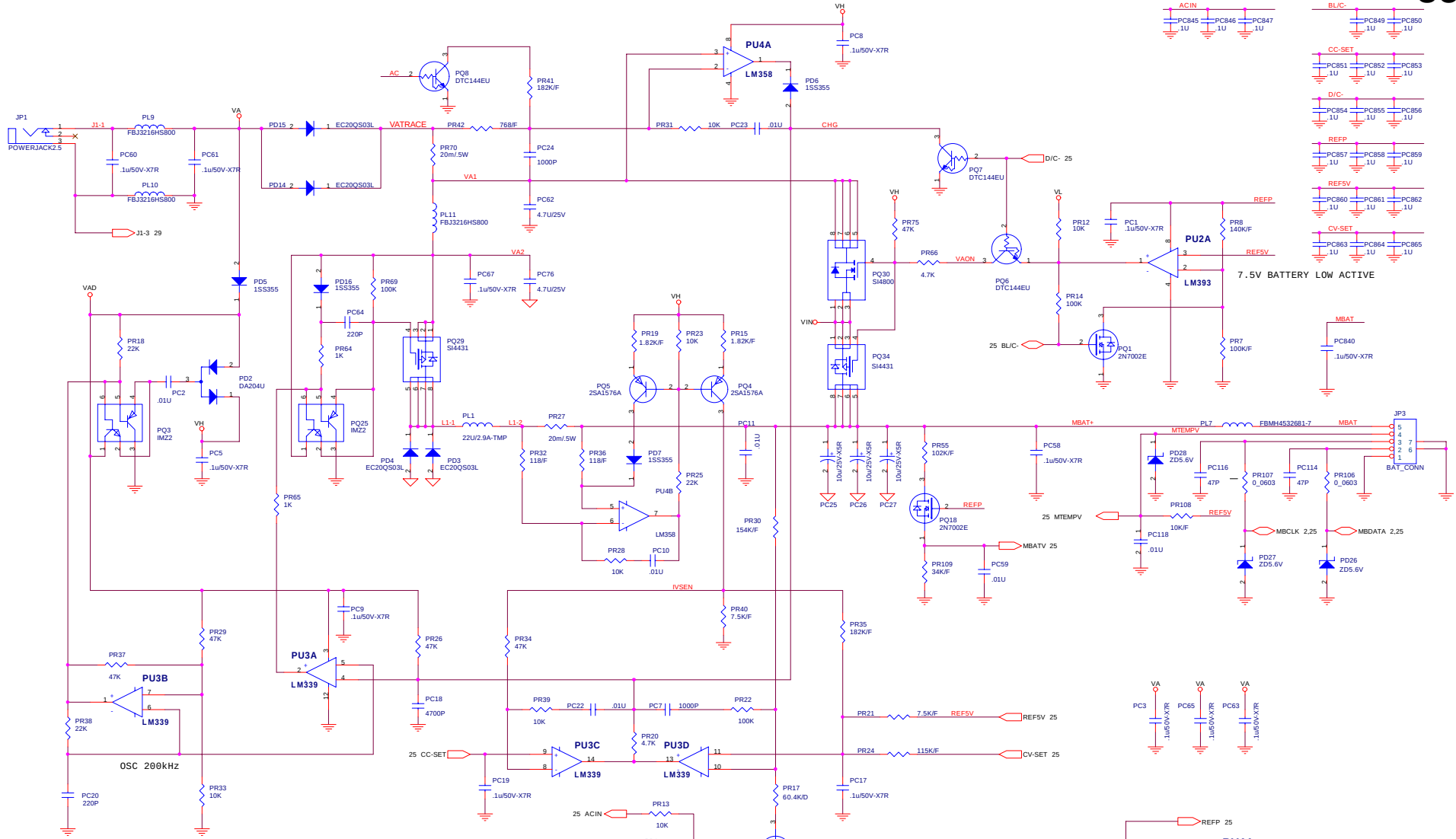
POWER - 3 .3V & 5V (MAXIM - 1632)

34

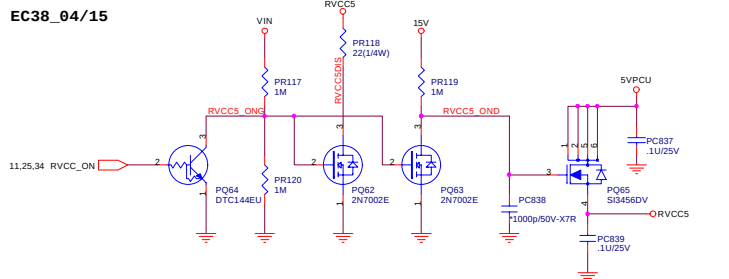


POWER - 4 BATTERY CHARGER

35



EC38_04/15



PROJECT :EG8L
Quanta Computer Inc.
DAEG8LMB8D Rev D

Size: Document Number: **BATTERY CHARGER** Rev: 2A
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