

BW1 Block Diagram

Notation for Resistor

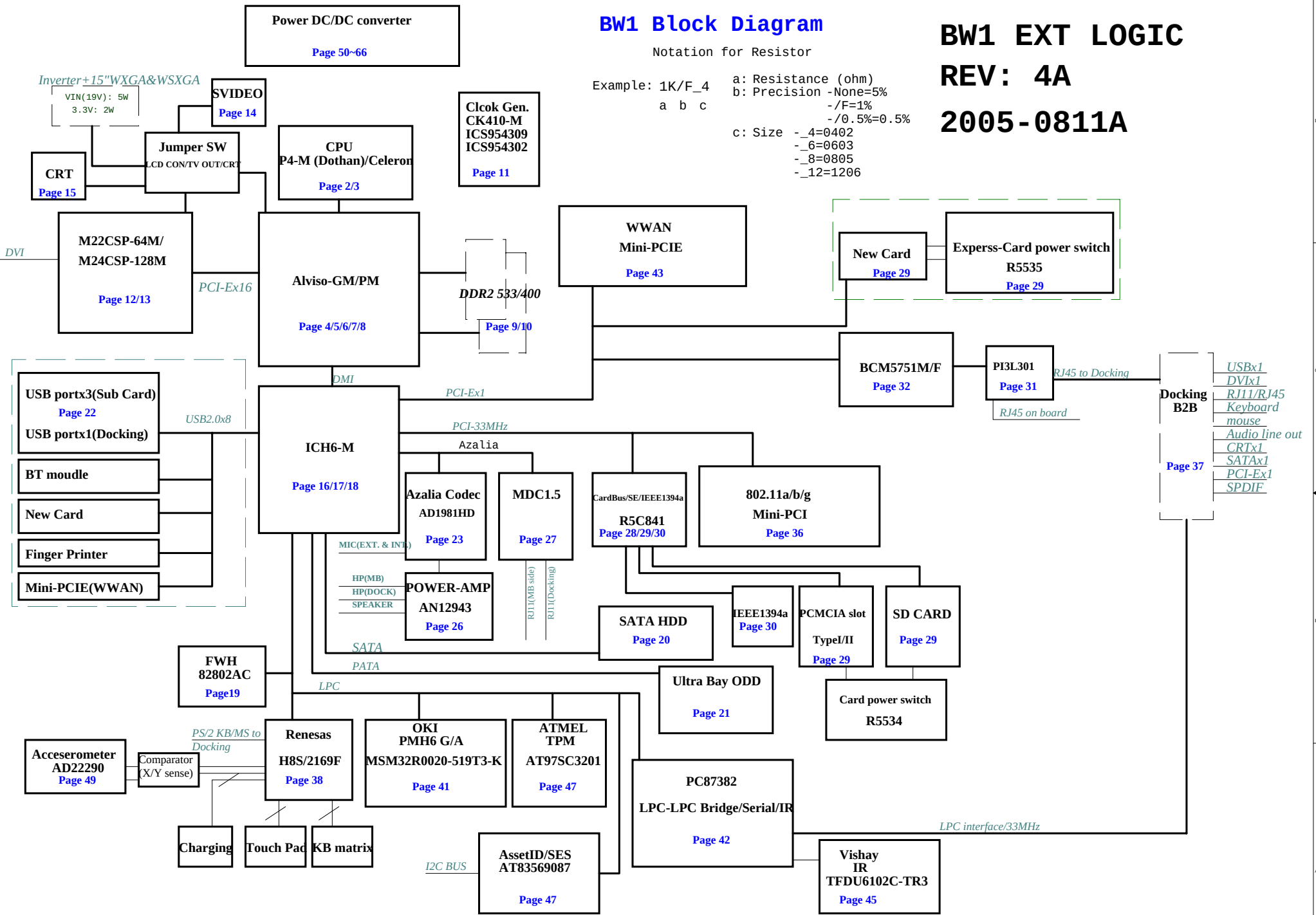
Example: 1K/F_4
a b c

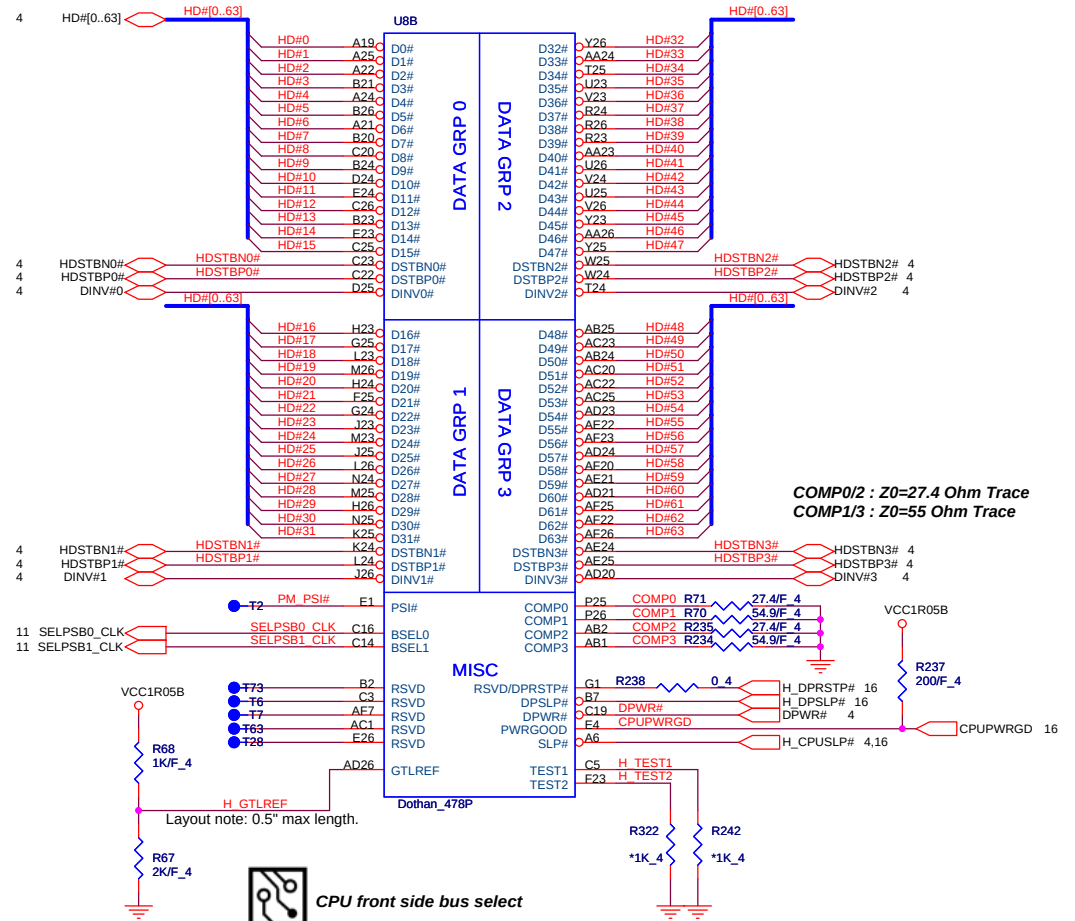
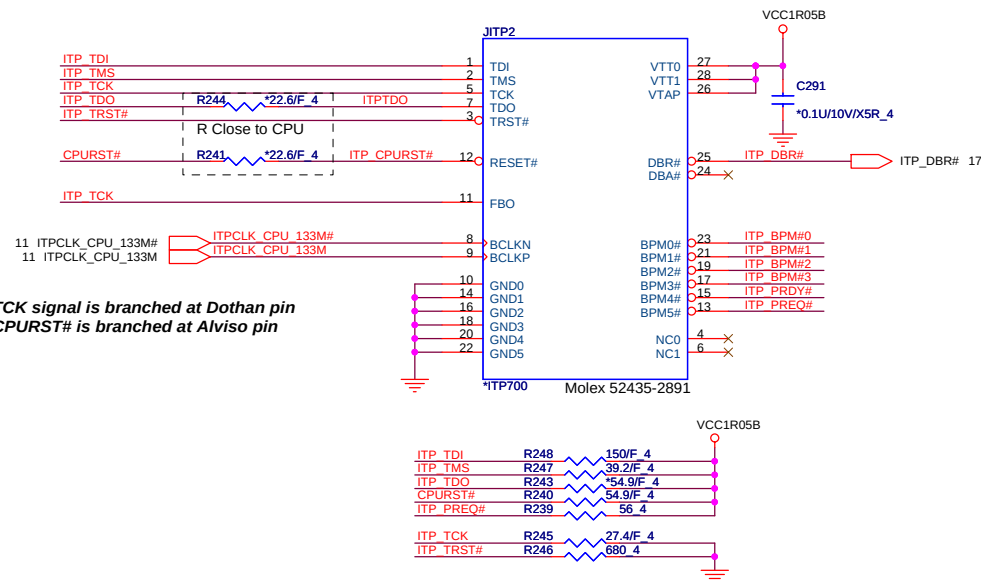
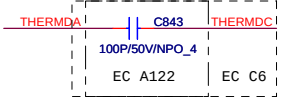
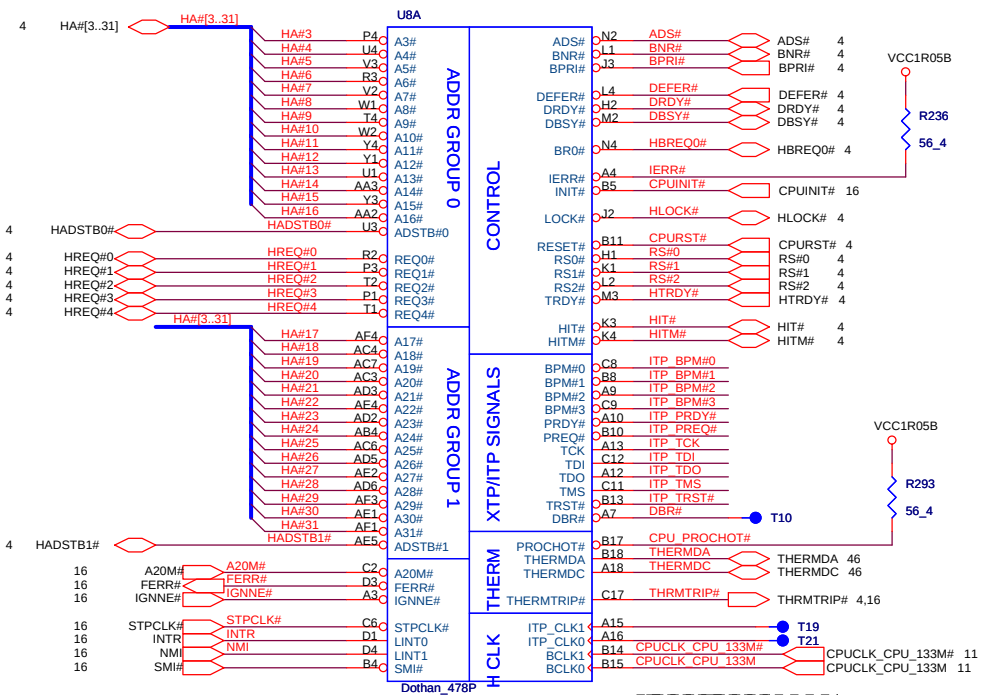
a: Resistance (ohm)
b: Precision -None=5%
- /F=1%
- /0.5%=0.5%
c: Size -_4=0402
-_6=0603
-_8=0805
-_12=1206

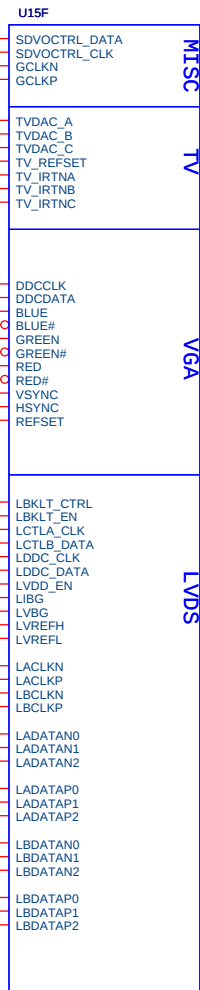
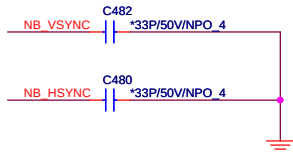
BW1 EXT LOGIC

REV: 4A

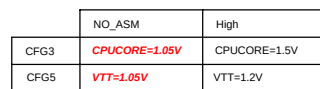
2005-0811A



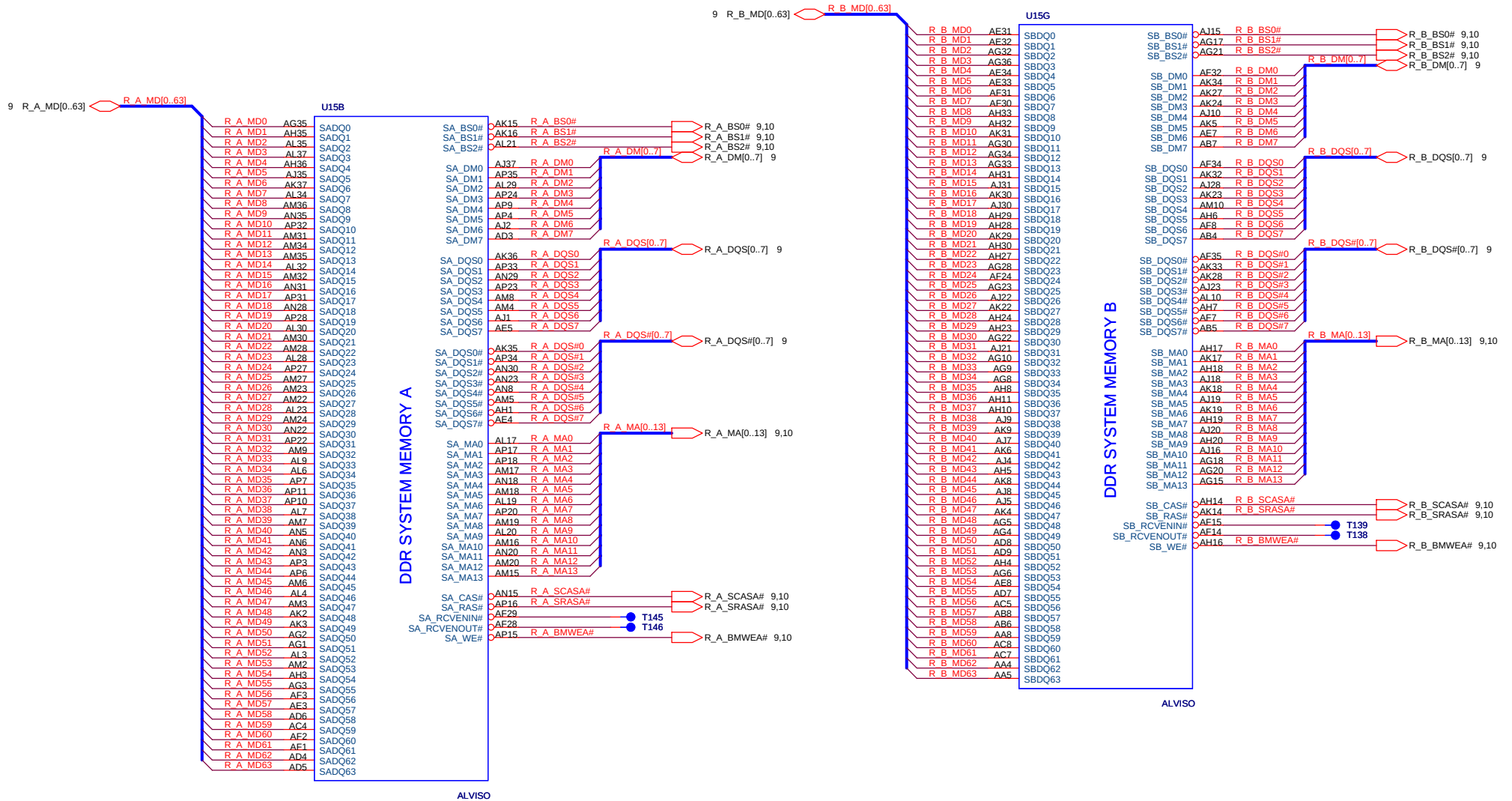


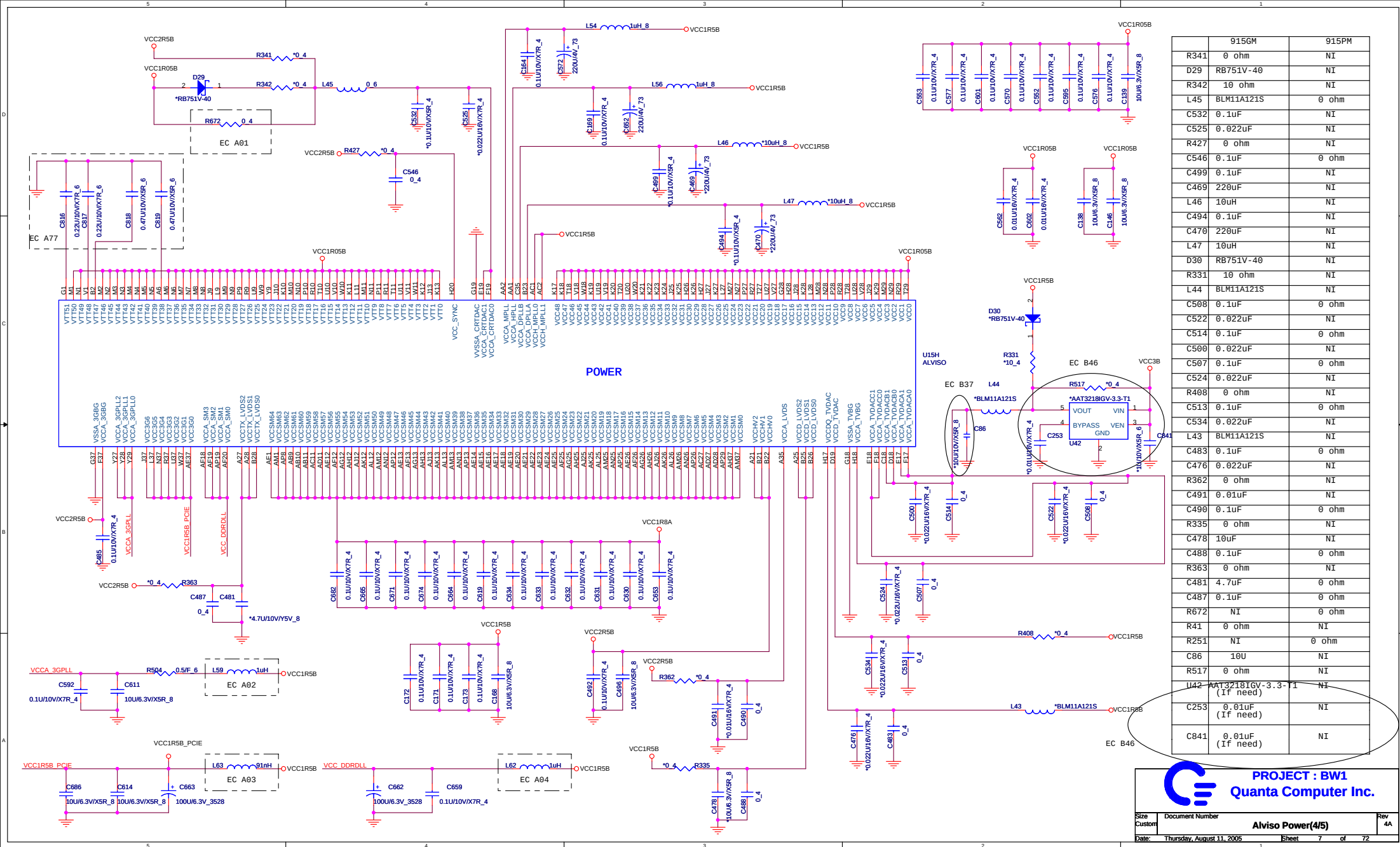


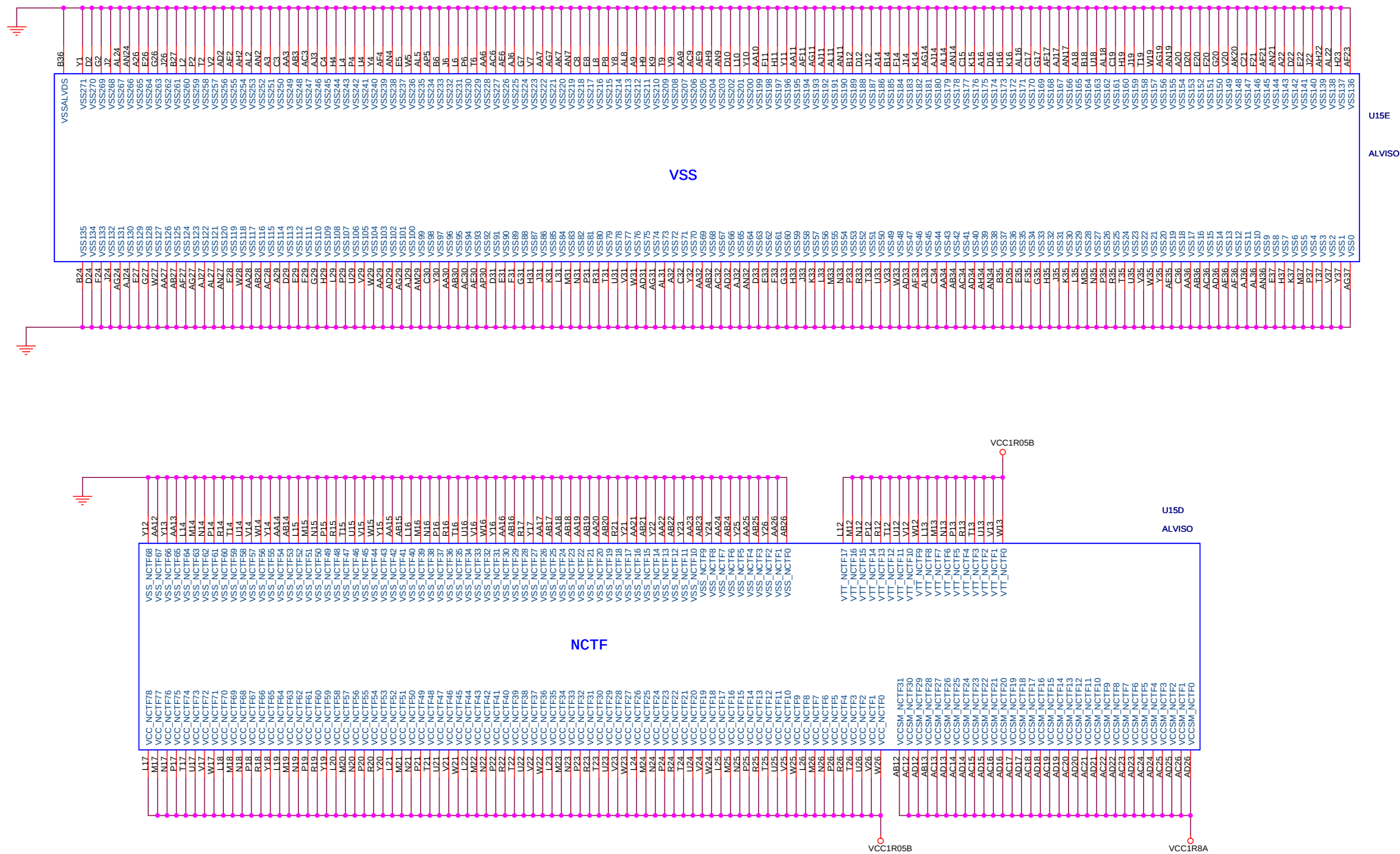
EC A66

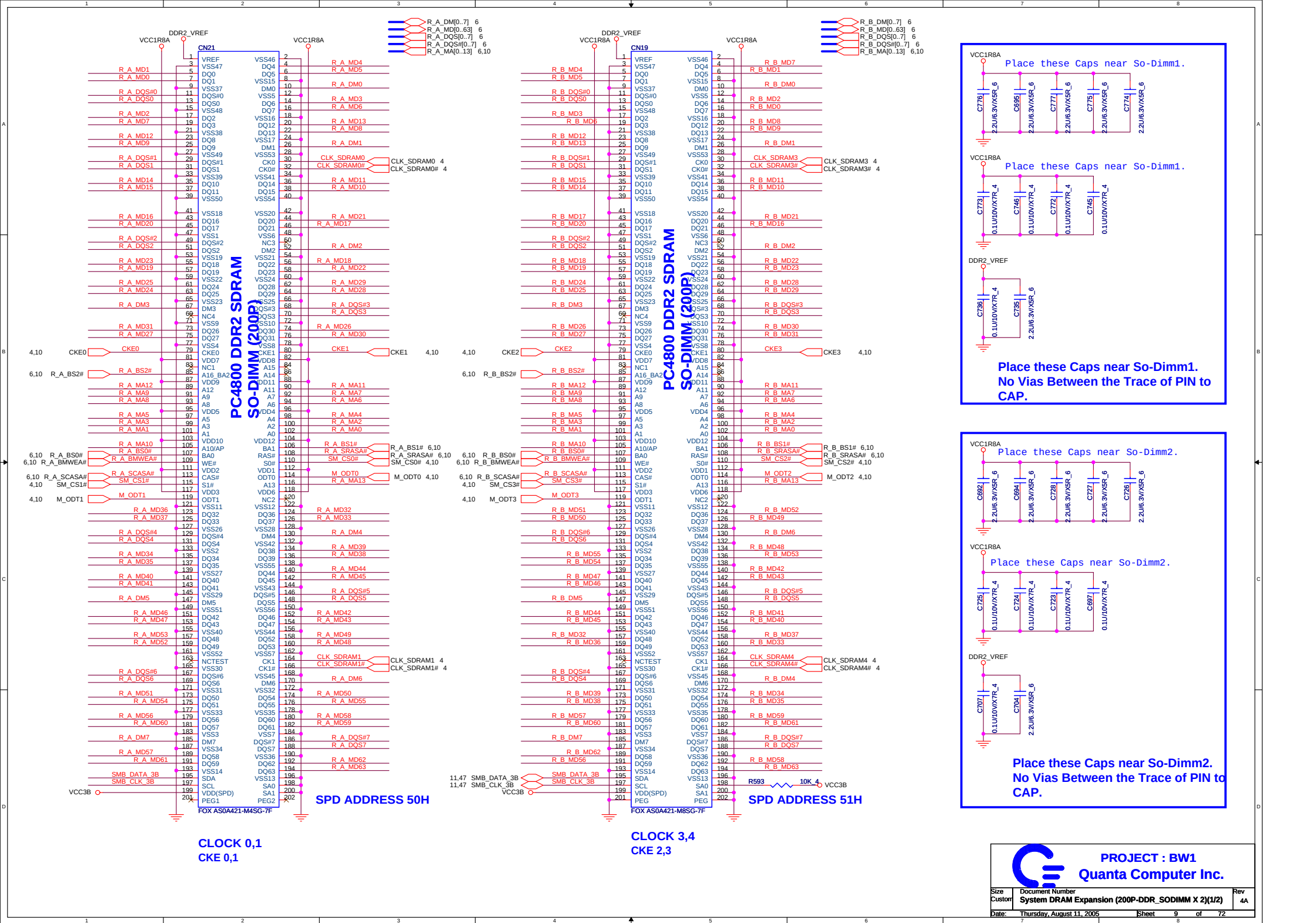


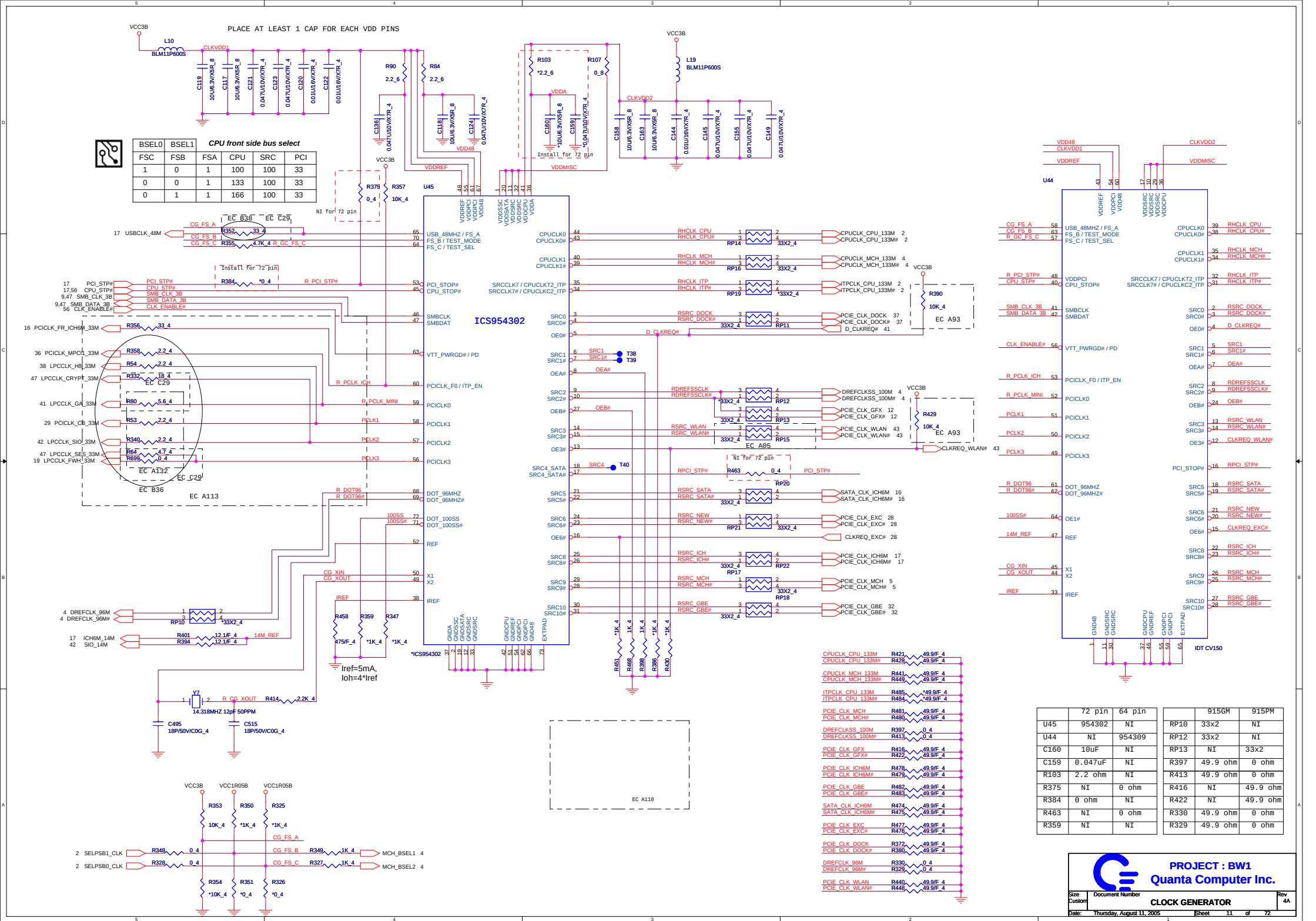
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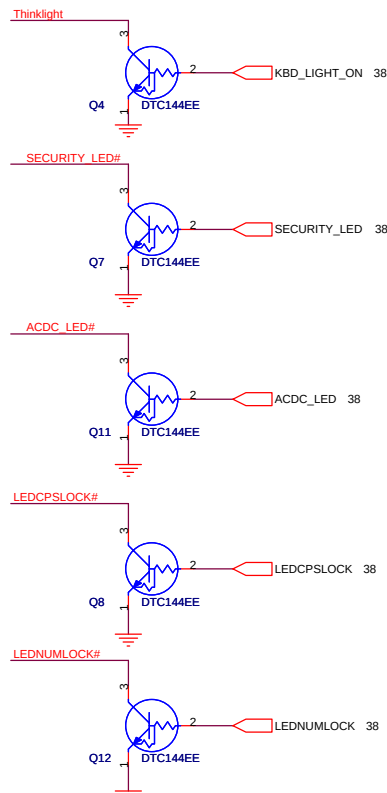
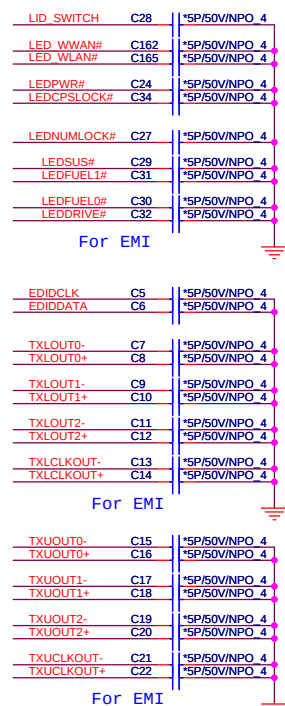
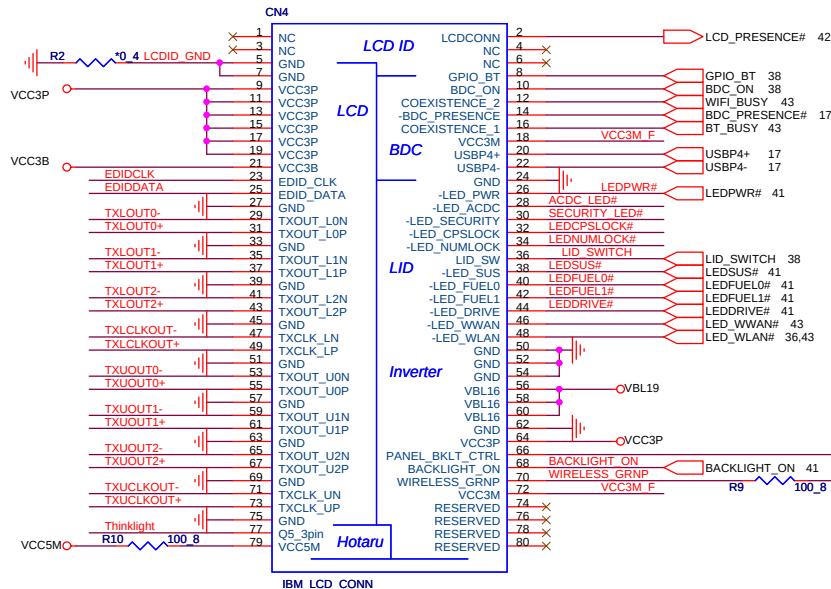
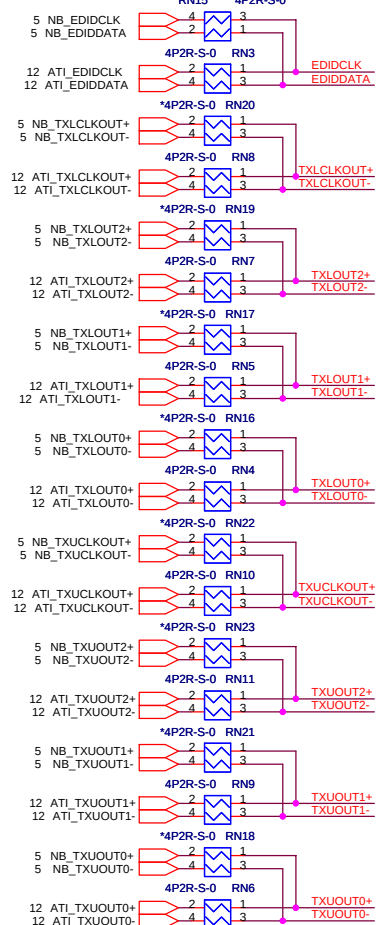




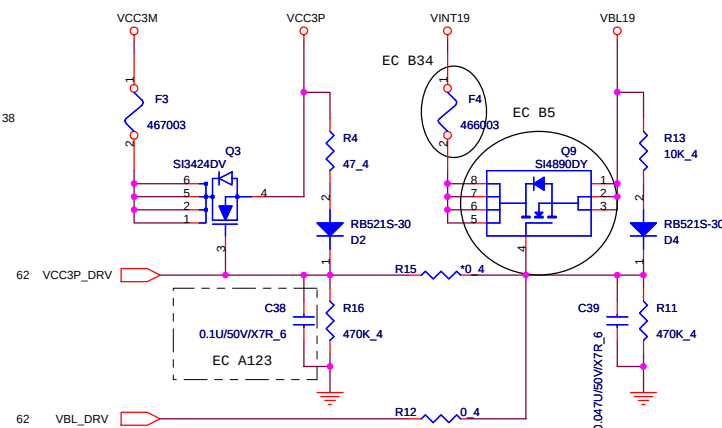
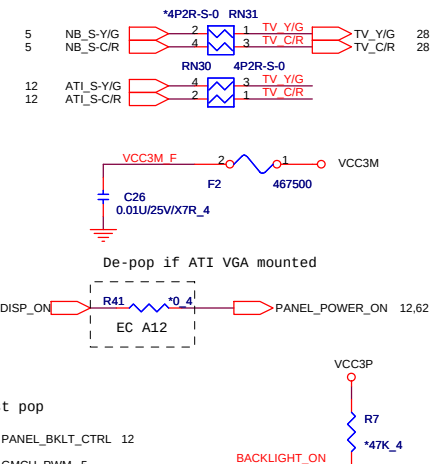








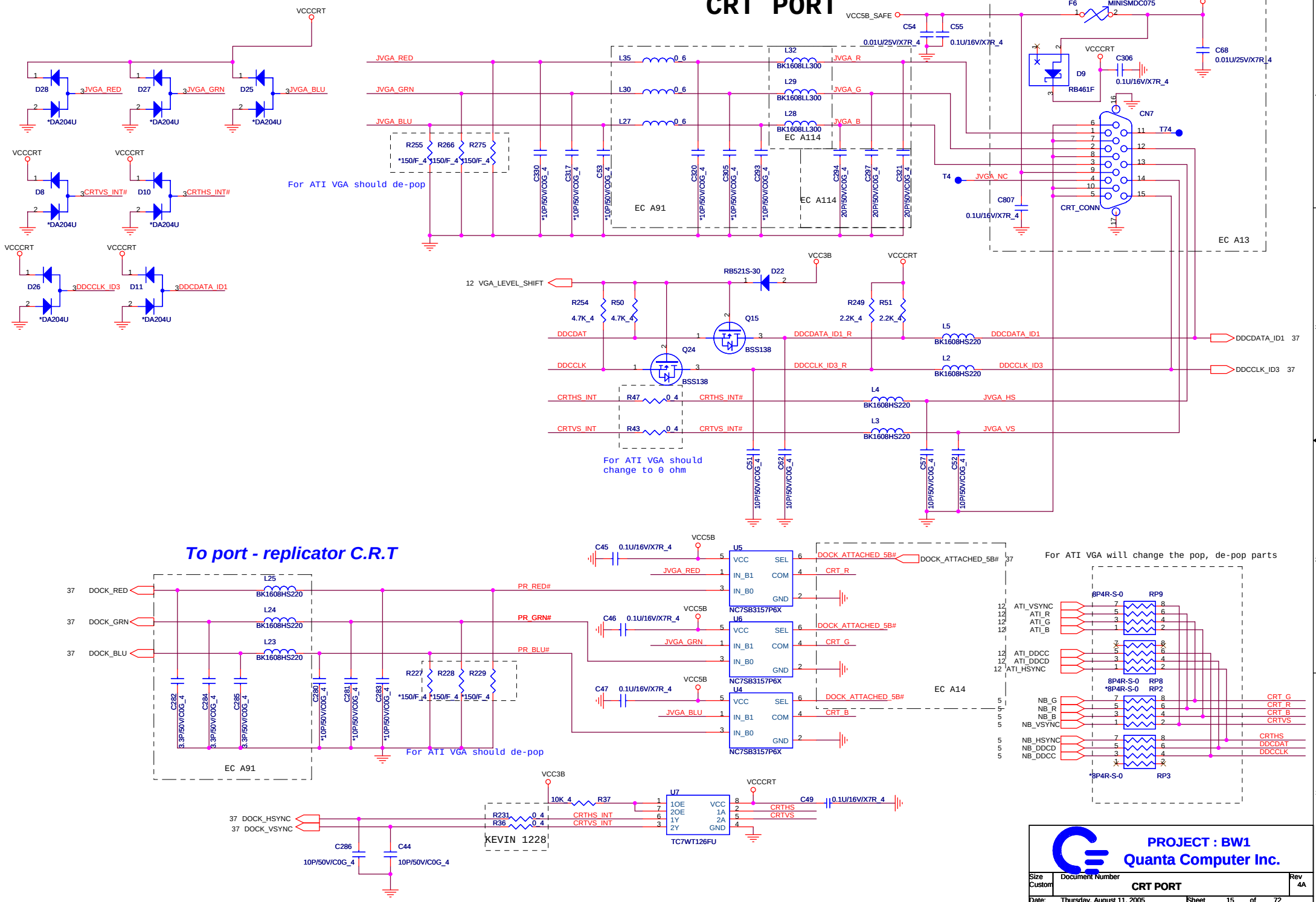
S-VIDEO

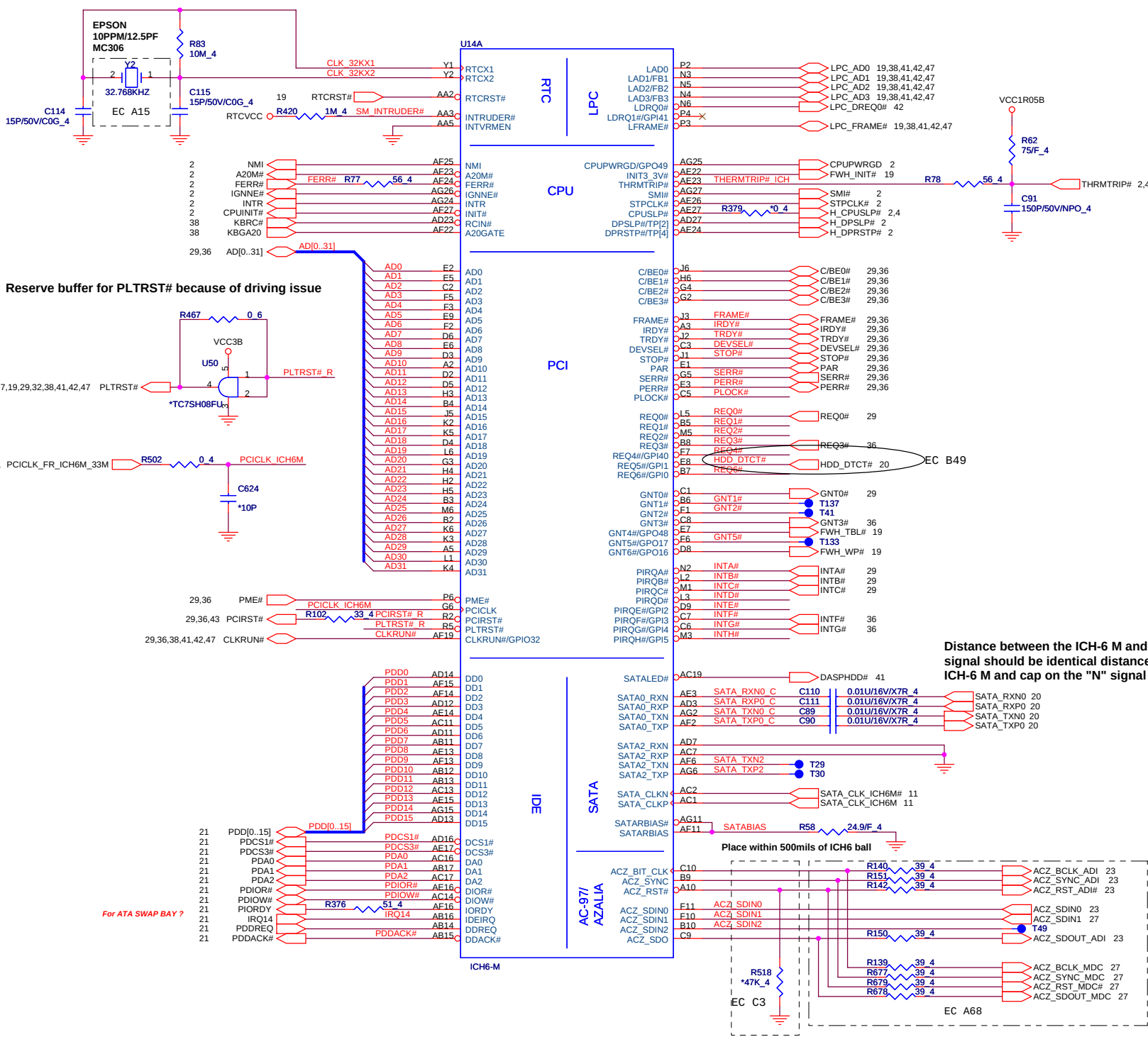


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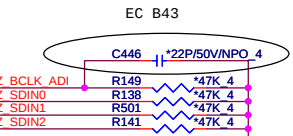
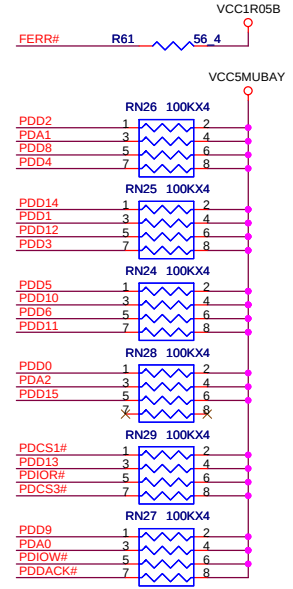
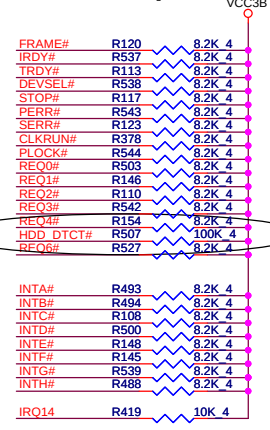
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CRT PORT





PCI Pullups



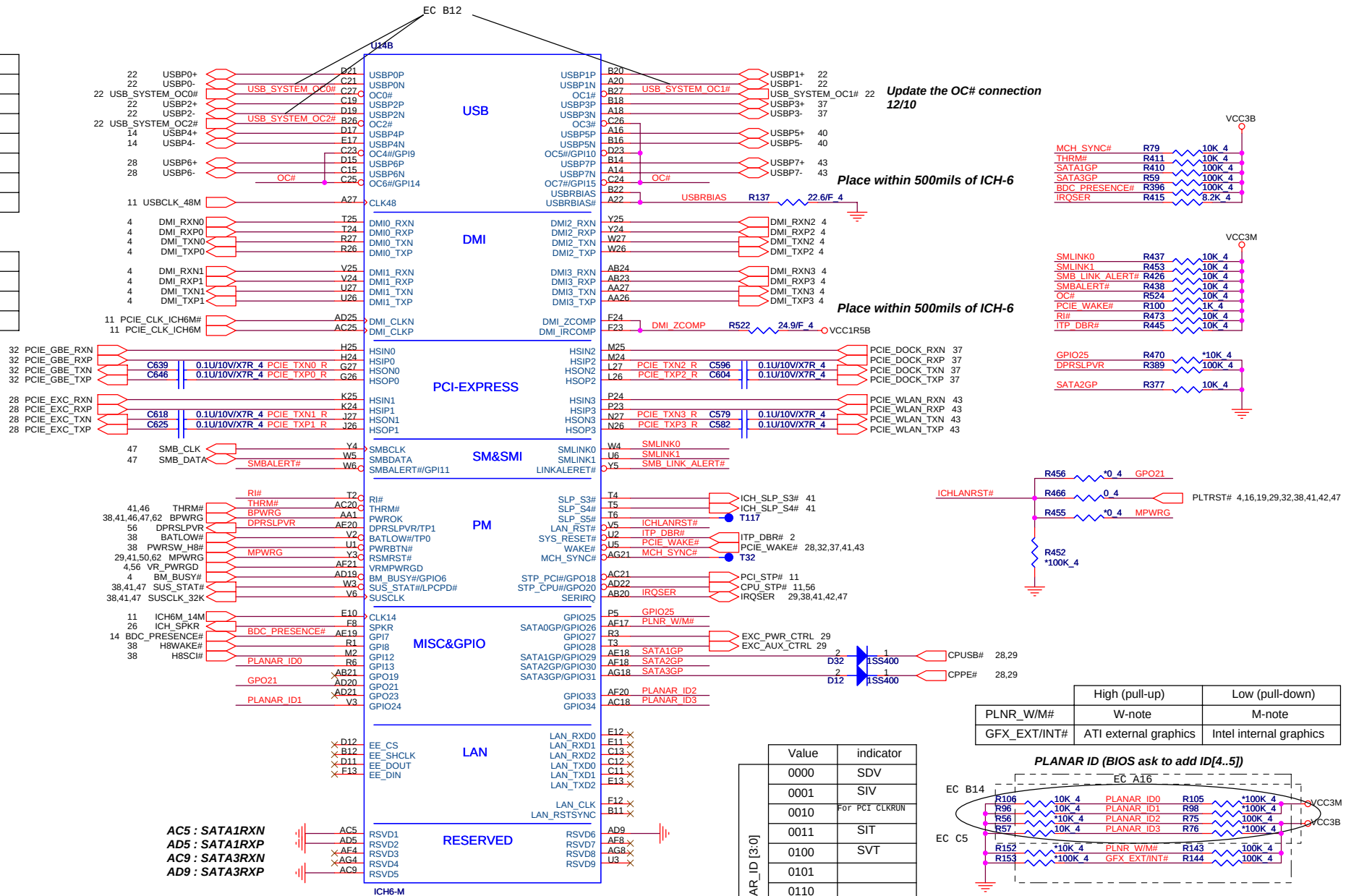
Distance between the ICH-6 M and cap on the "P" signal should be identical distance between the ICH-6 M and cap on the "N" signal for same pair.



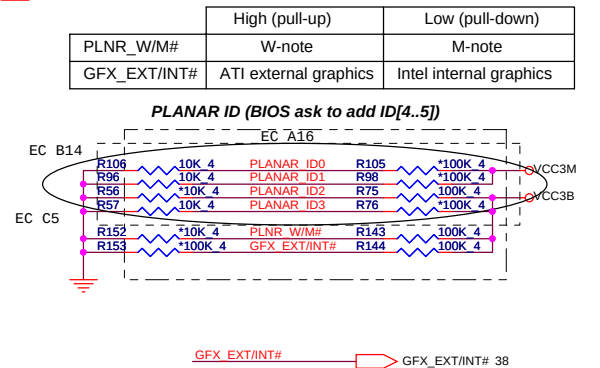
USB0	USB Port 1
USB1	USB Port 2
USB2	USB Port 3
USB3	Docking
USB4	Bluetooth
USB5	Finger print
USB6	EXPRESS CARD
USB7	MINI PCI-E CARD

PCIE distribution

PCIE0	Enthernet Controller
PCIE1	EXPRESS CARD
PCIE2	Docking
PCIE3	PCIE WLAN

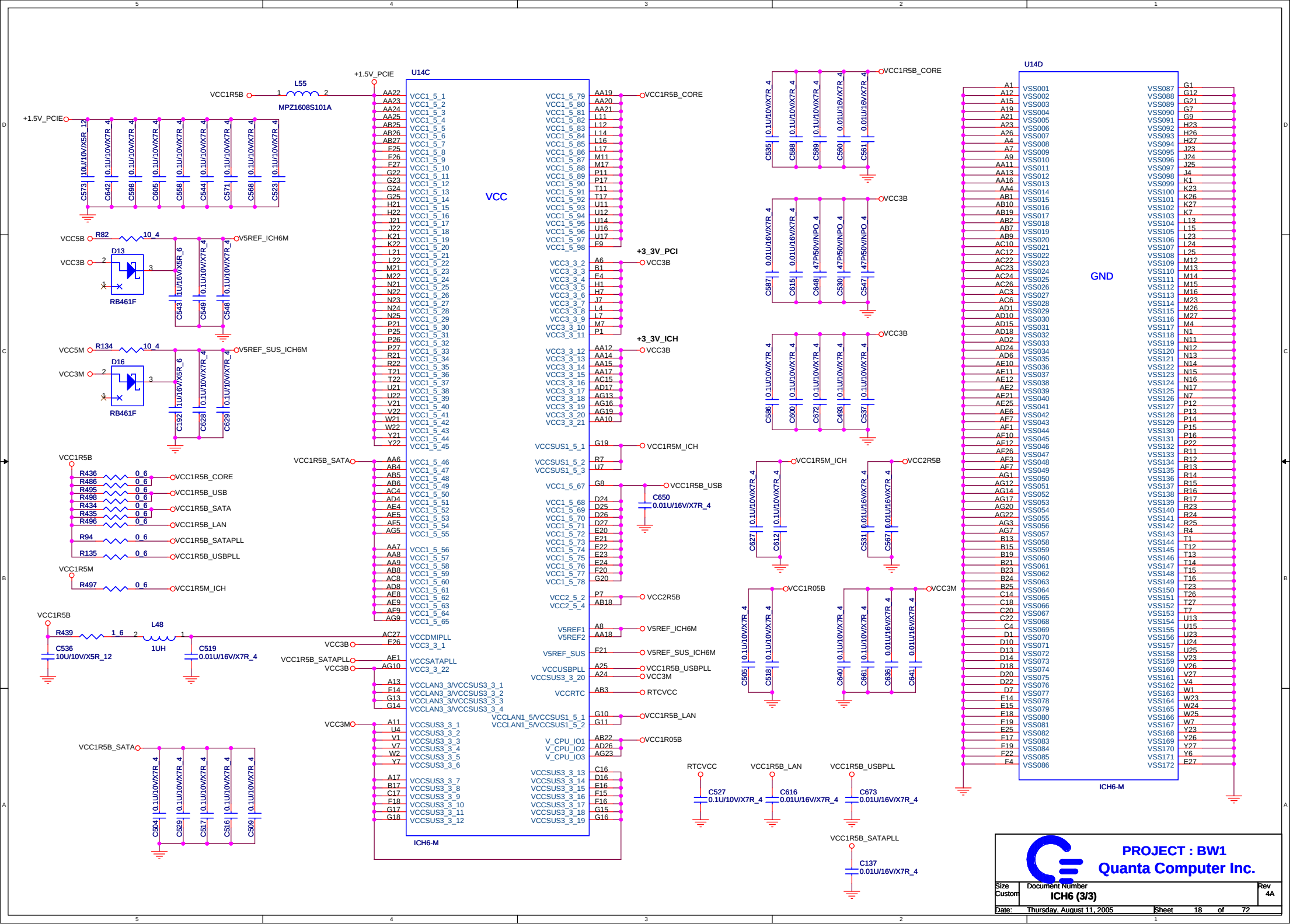


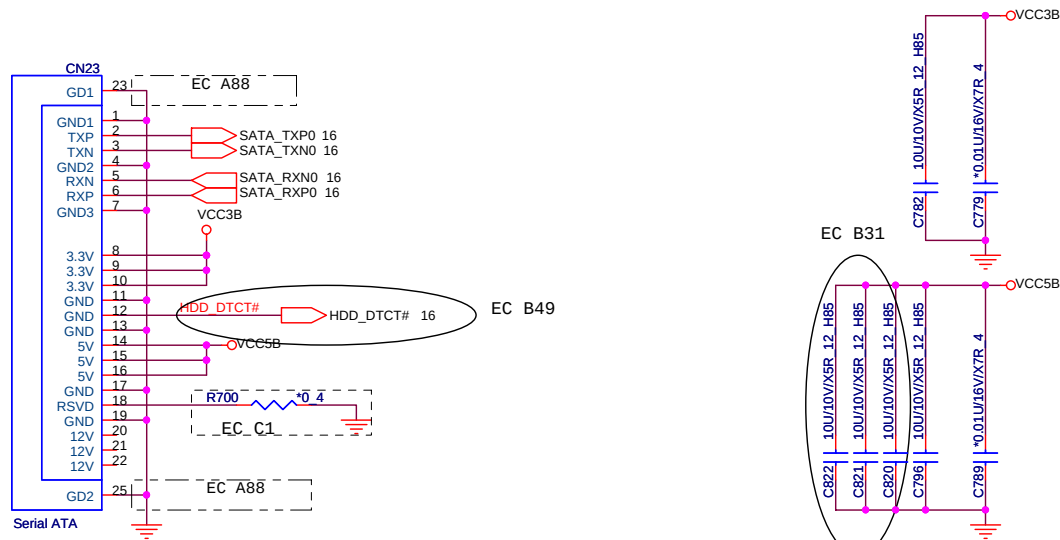
PLANAR_ID [3:0]	Value	indicator
	0000	SDV
	0001	SIV
	0010	For PCI CLKRUN
	0011	SIT
	0100	SVT
	0101	
	0110	
	0111	
	1000	
	1001	
	1010	
	1011	
	1100	
	1101	
	1110	
1111		



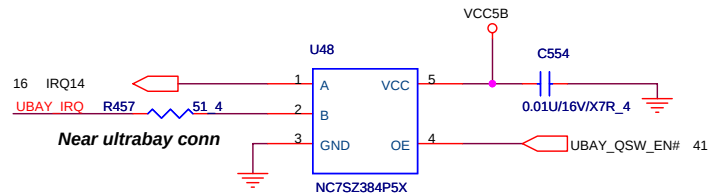
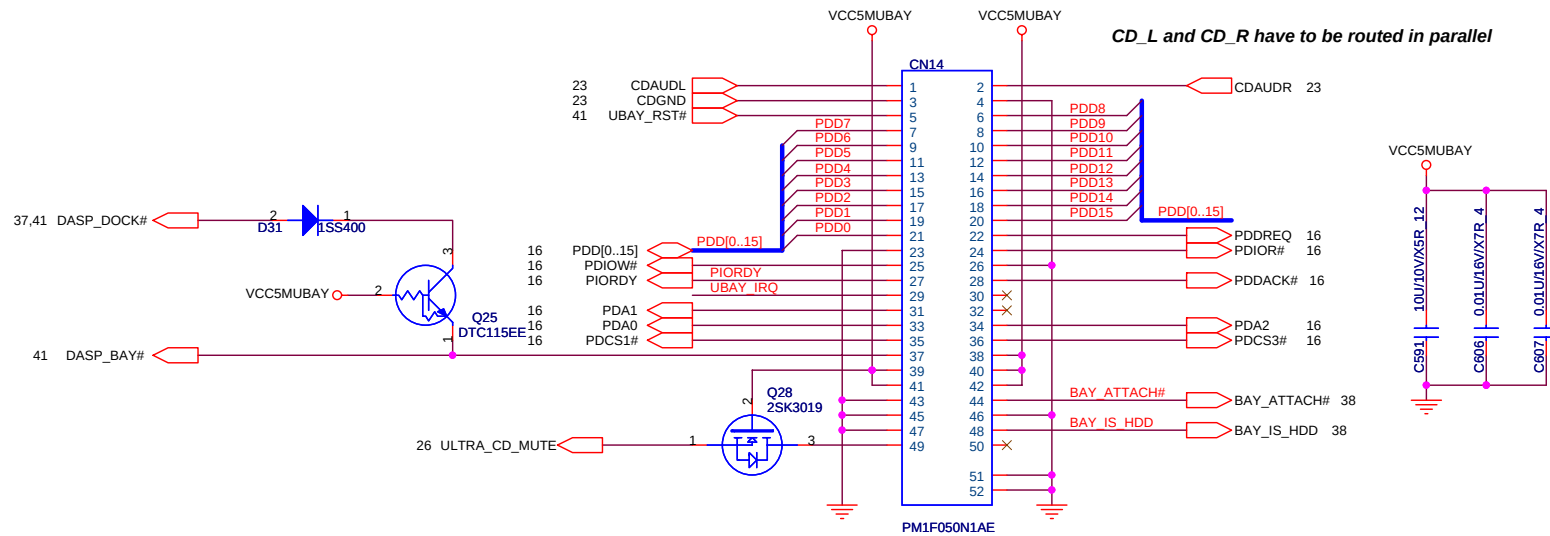
PROJECT : BW1
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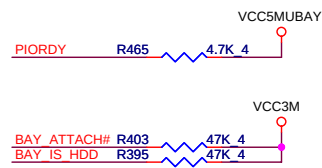




ODD CONNECTOR

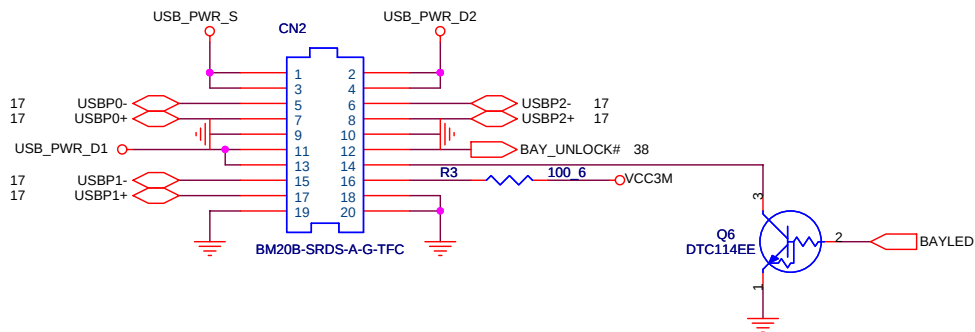


	BAY_ATTACH#	BAY_IS_HDD
NO_DEVICE	H	Don't care
OPTICAL	L	L
HDD	L	H

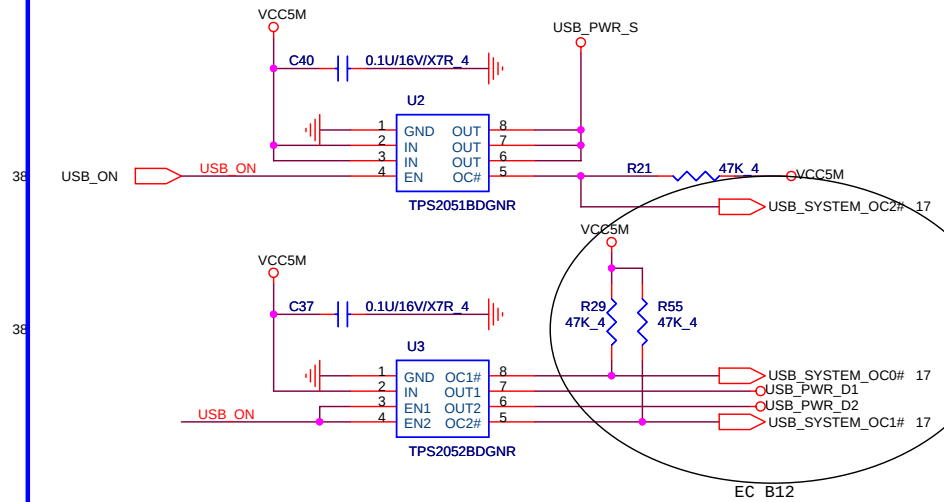


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Change to 20pin board to board connector
2004/11/11 Thunder

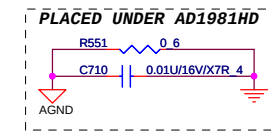
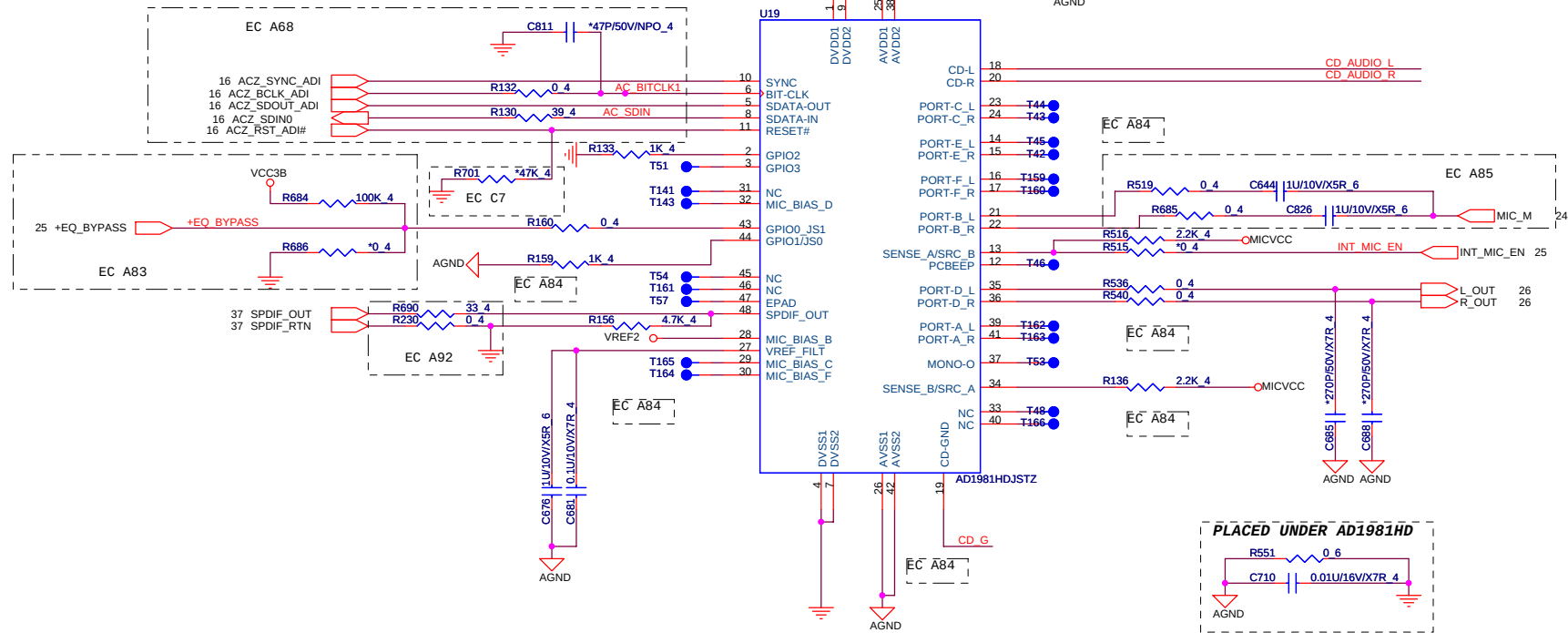
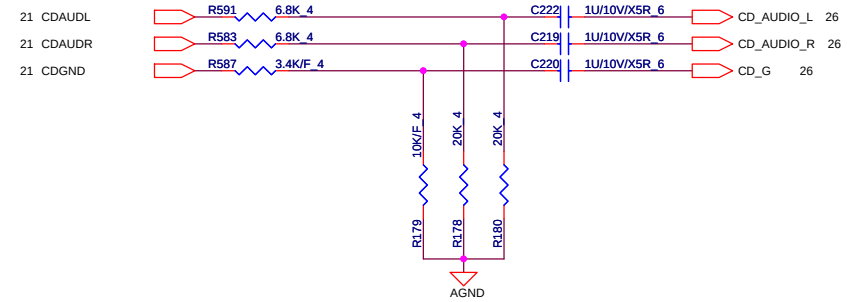
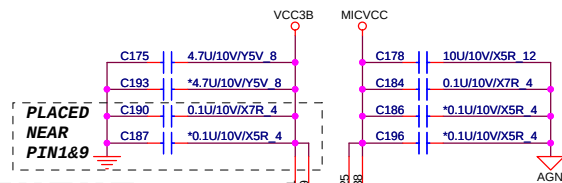
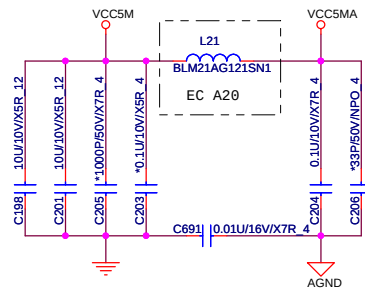
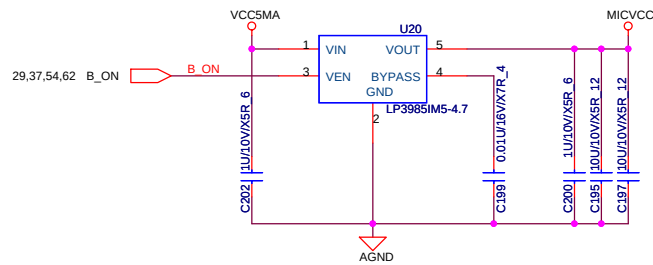


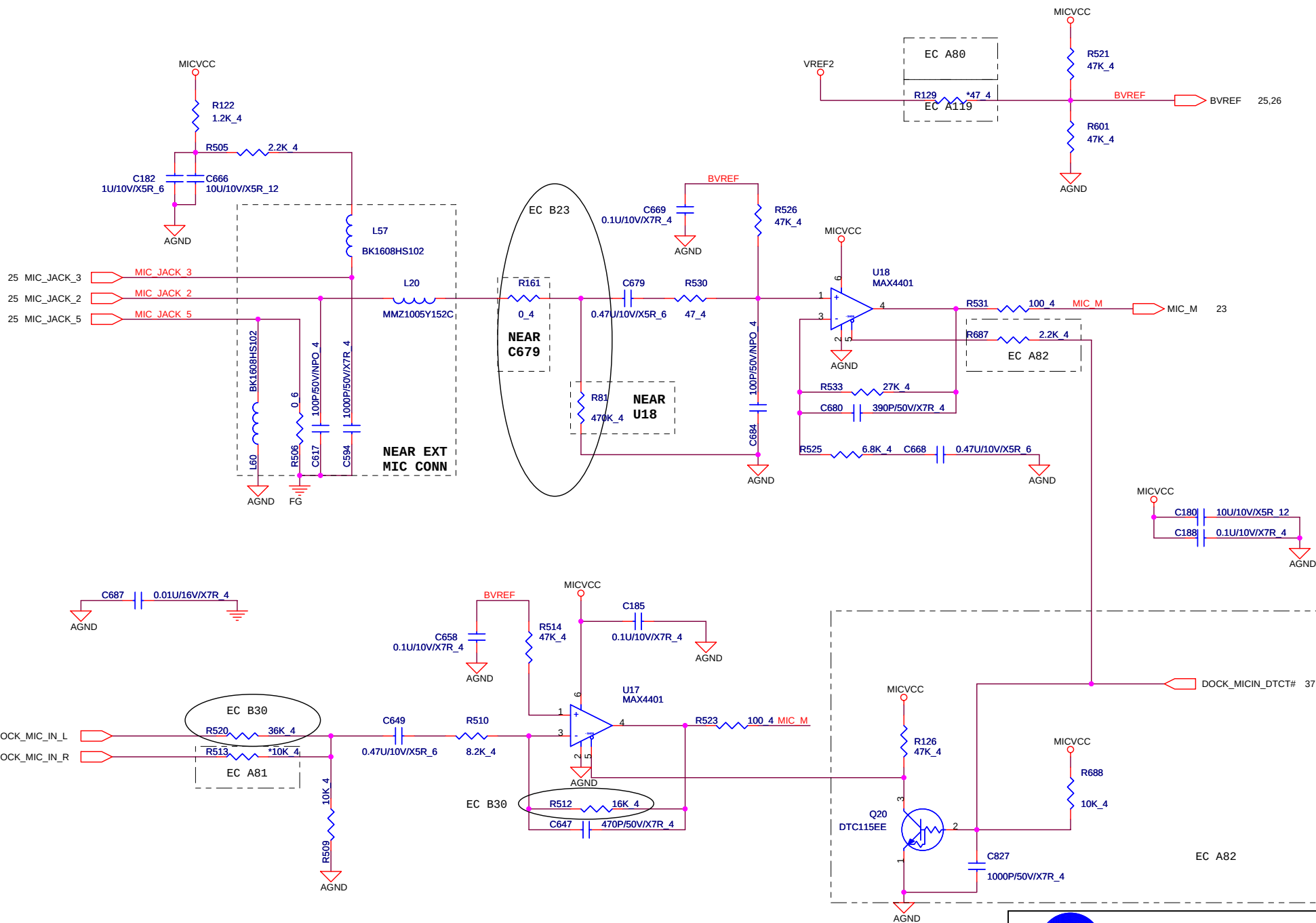
Change to TI solution (TPS2051 + TPS2052 power switch)
2004/12/09 Thunder



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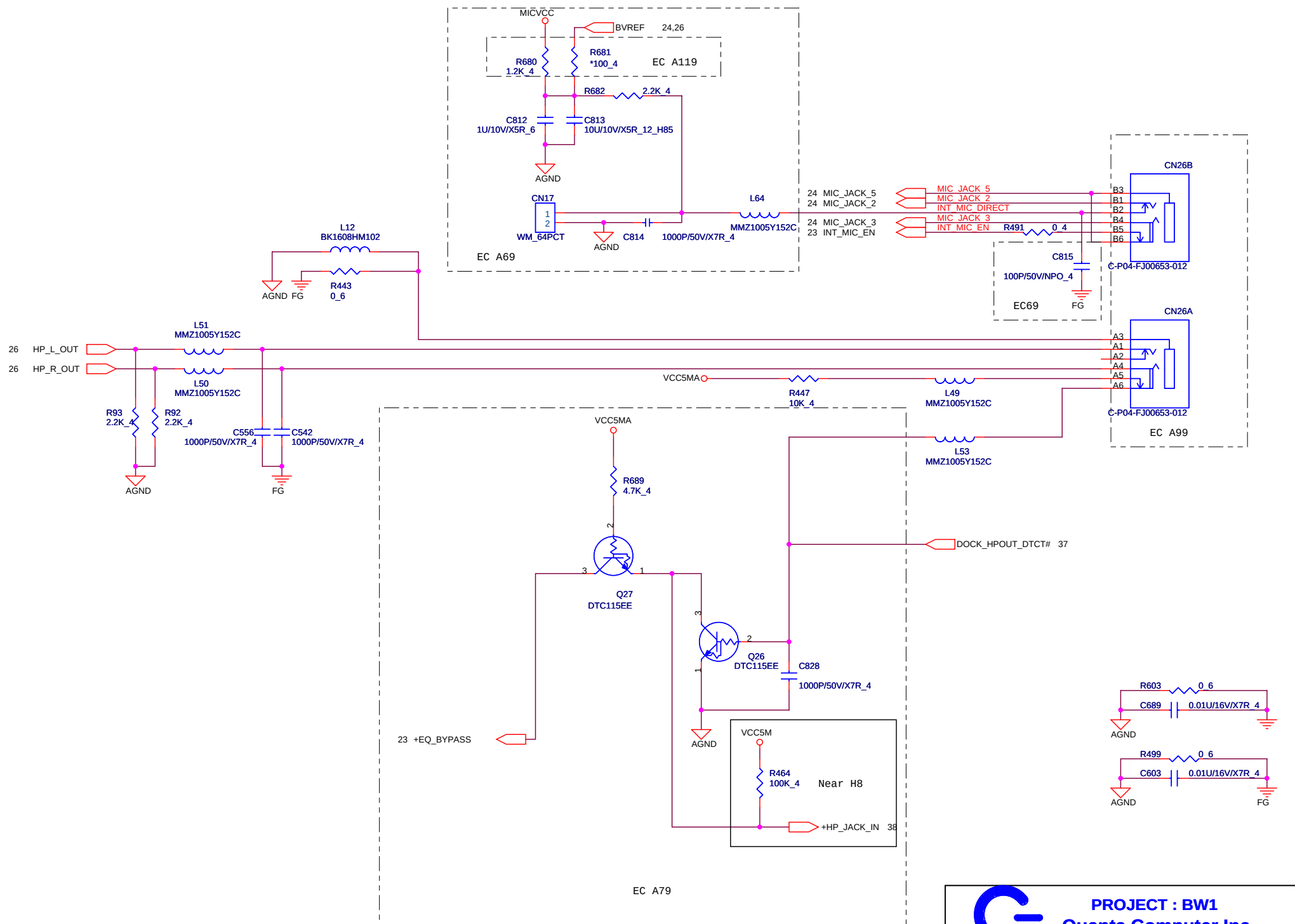
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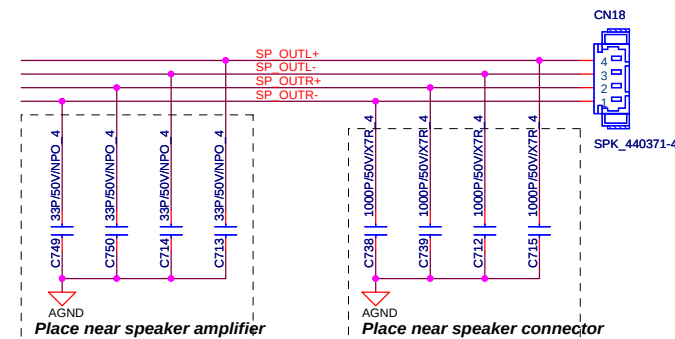
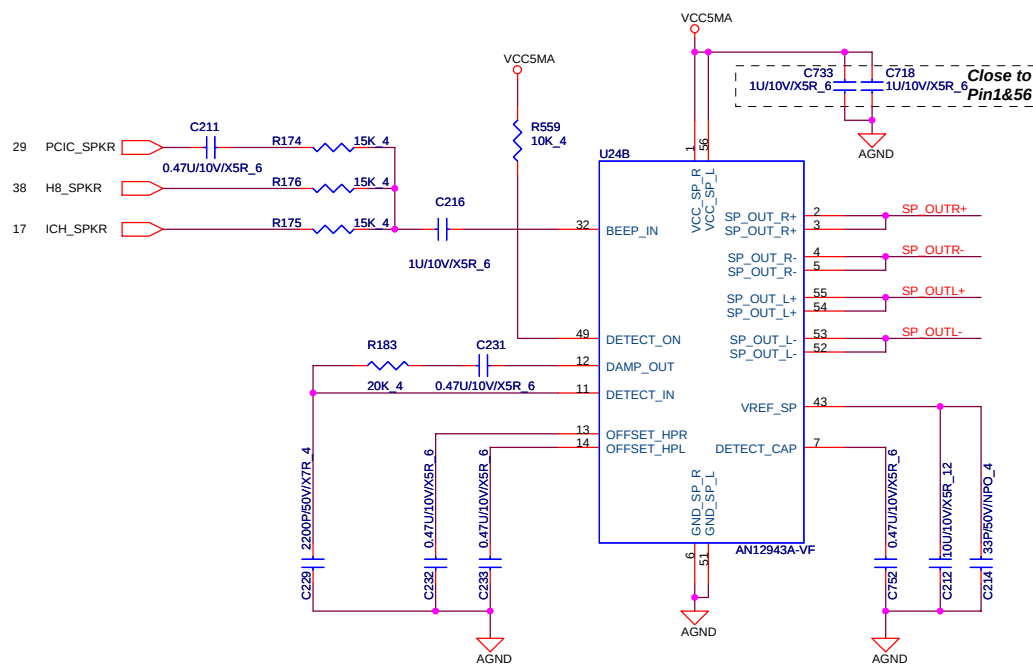
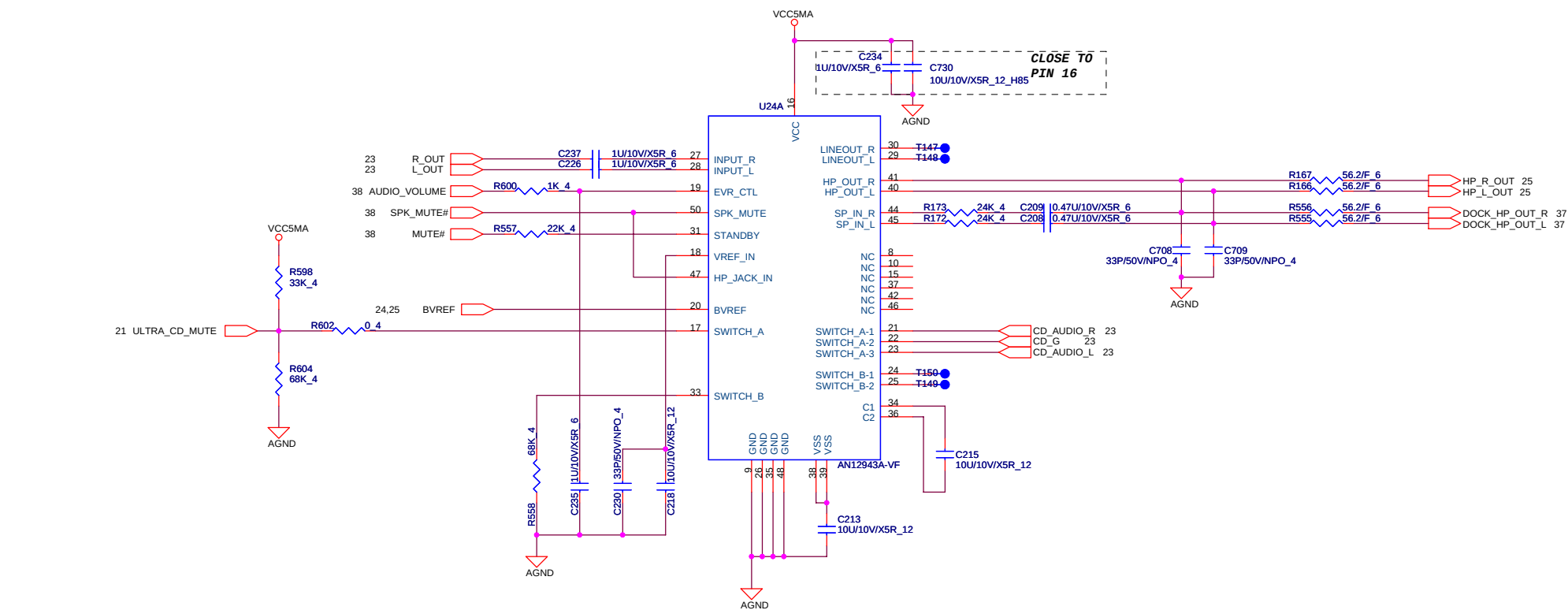


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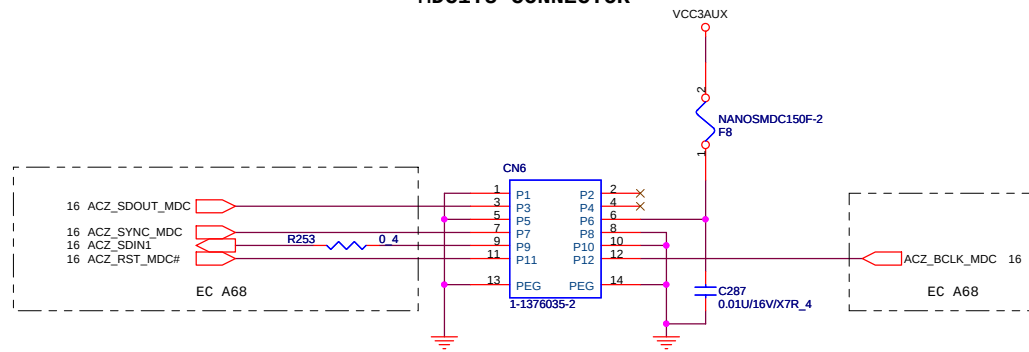
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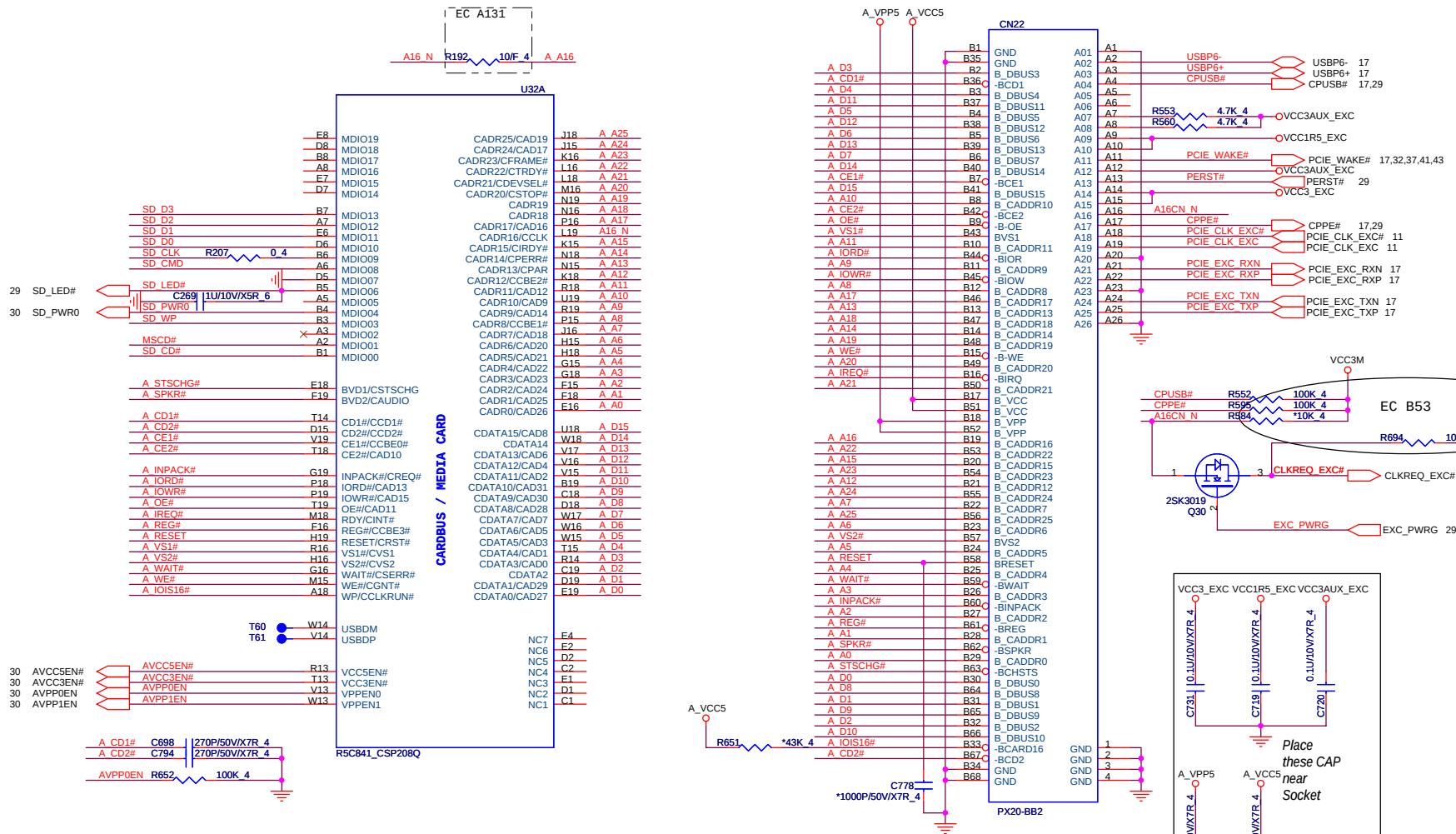


MDC1.5 CONNECTOR

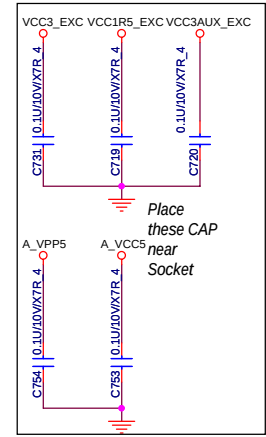
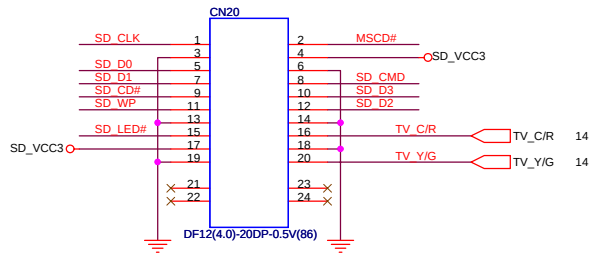
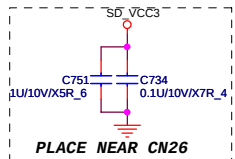


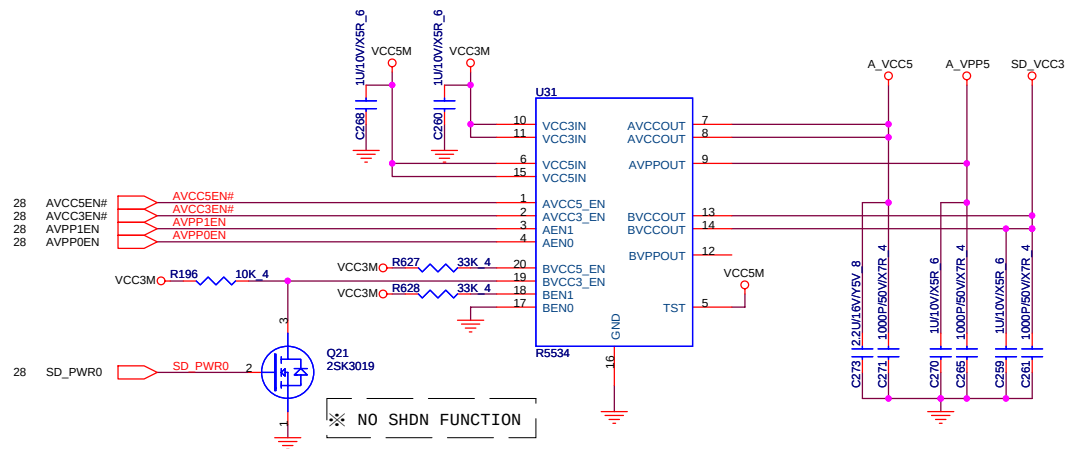
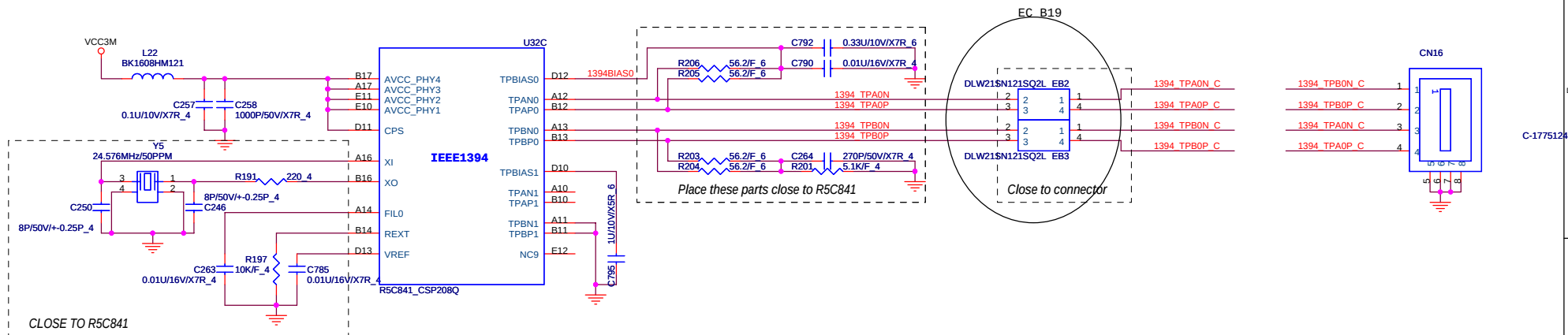
PROJECT : BW1
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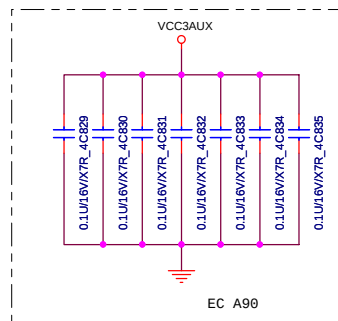
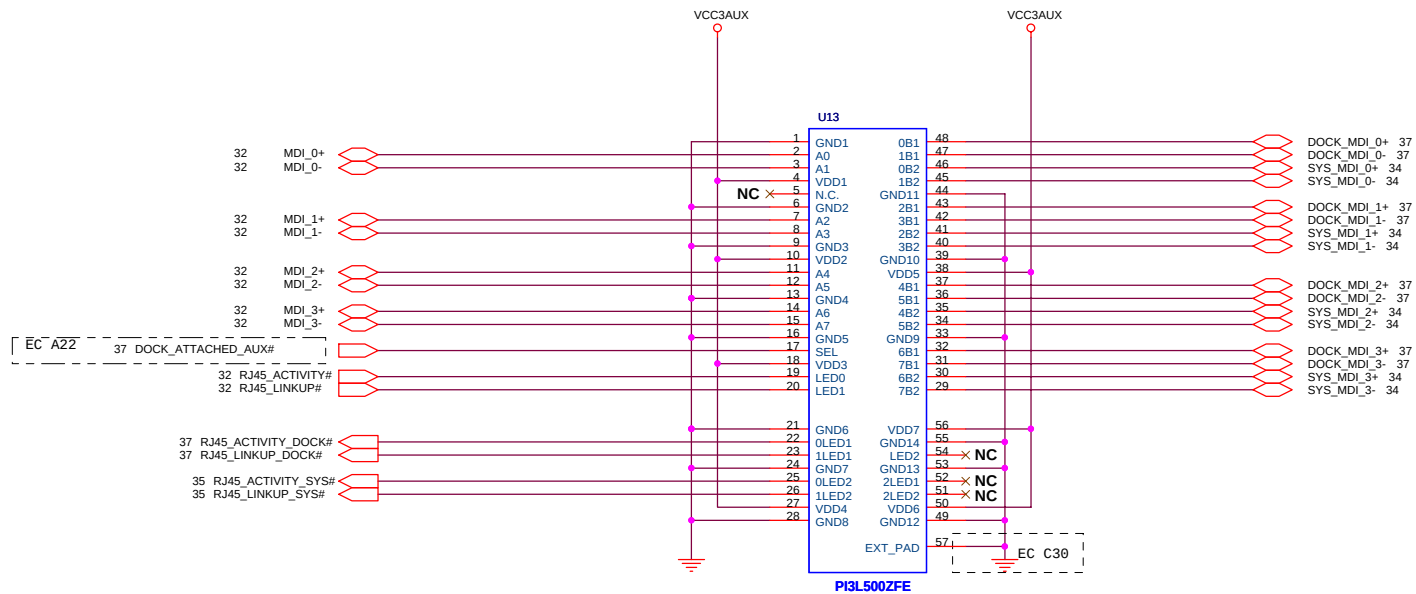
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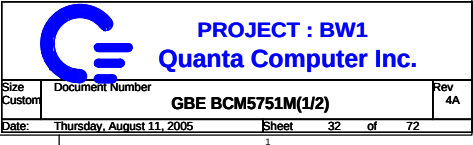


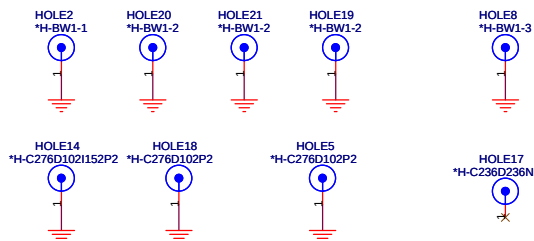
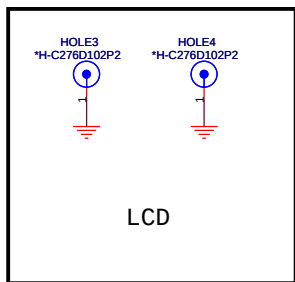
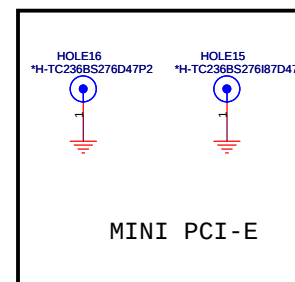
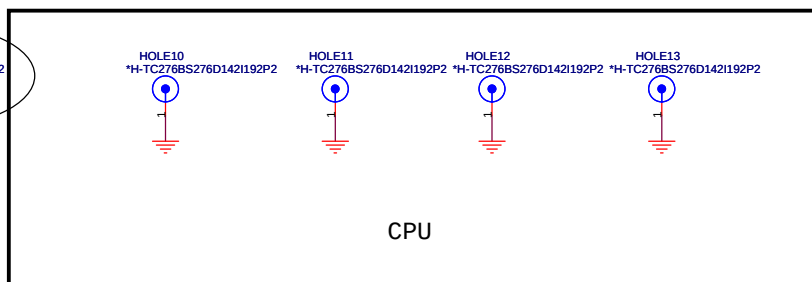
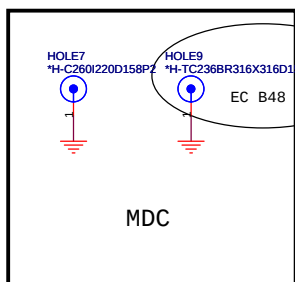
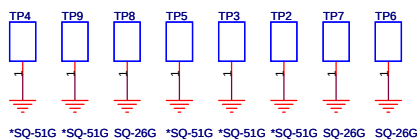
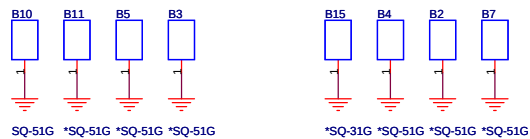
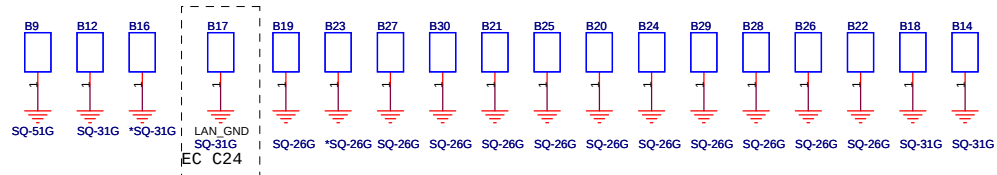
SDIO SLOT

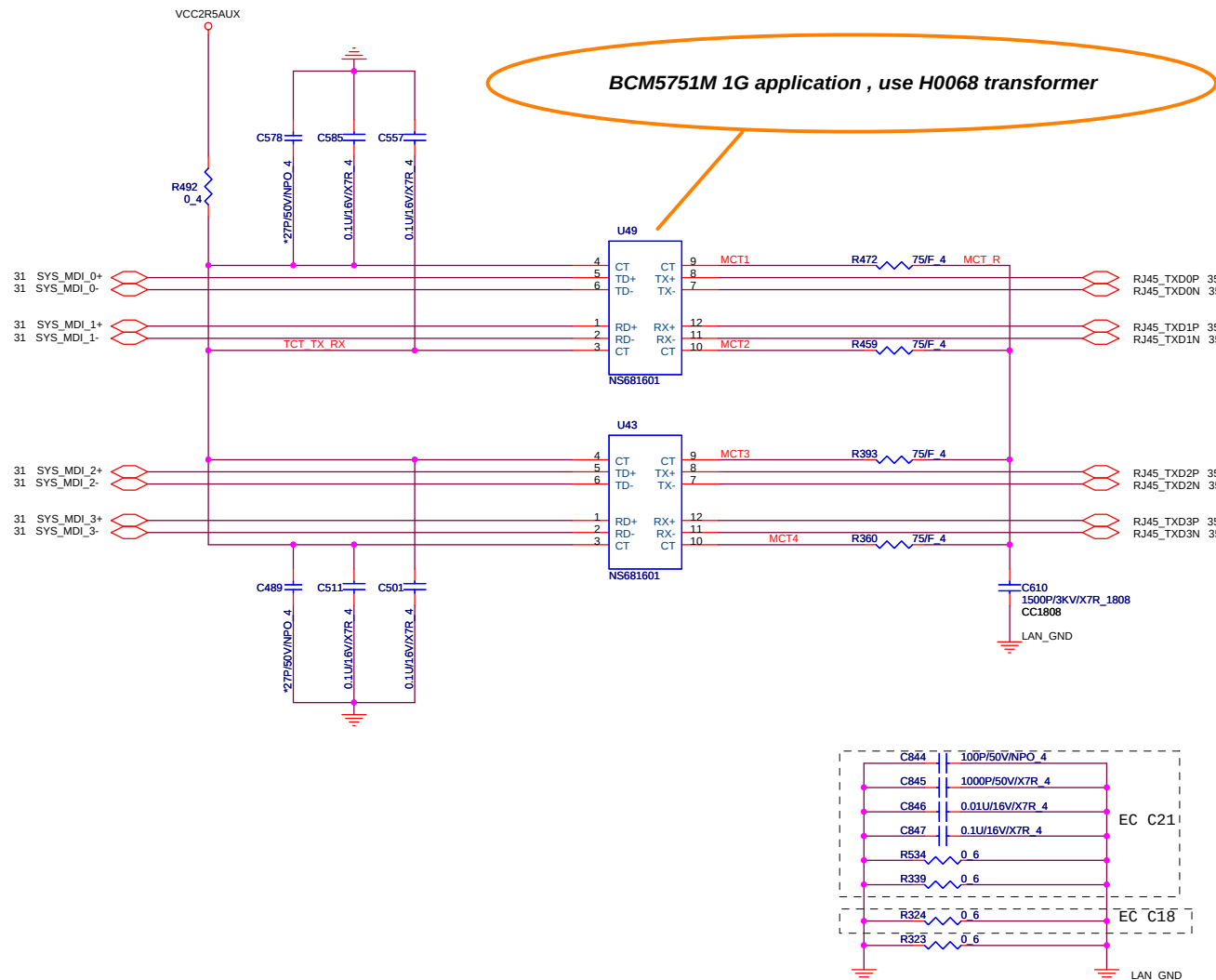


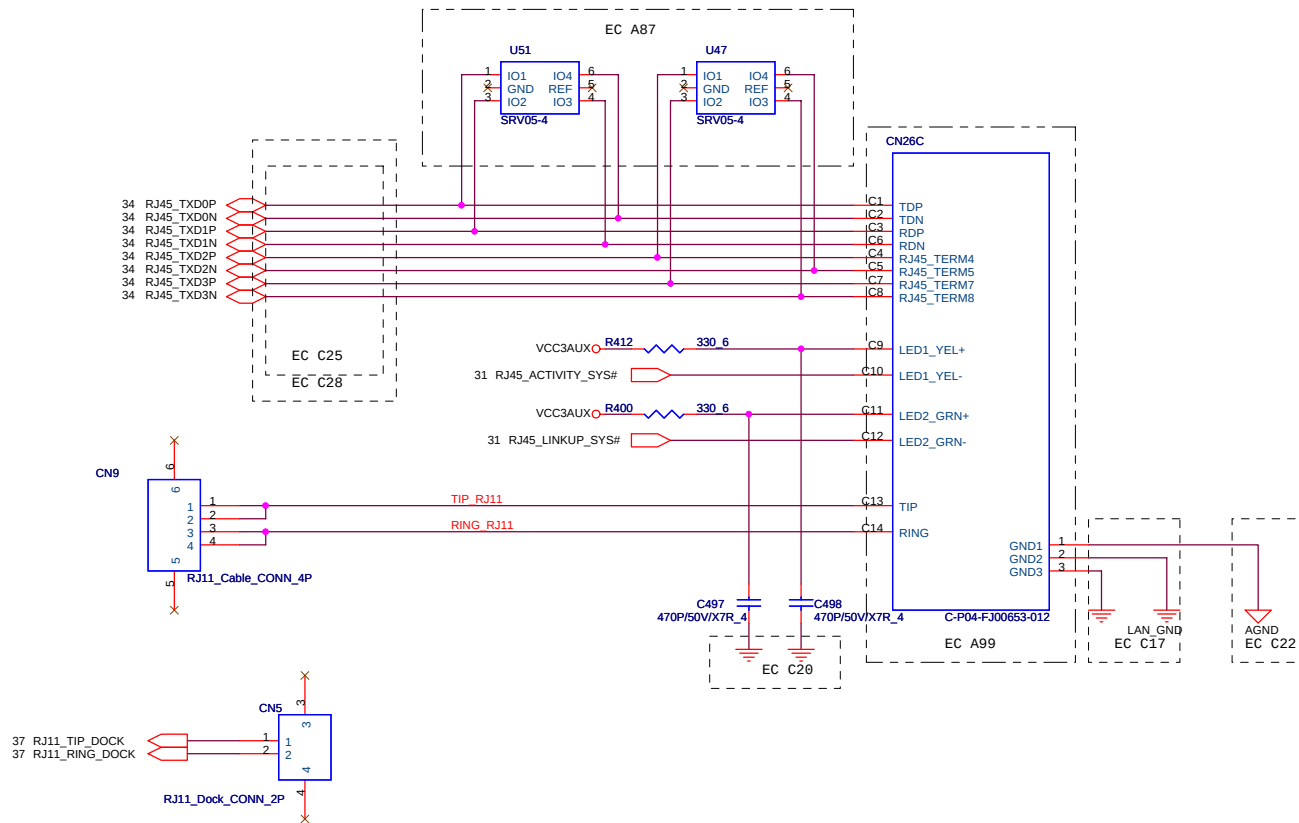




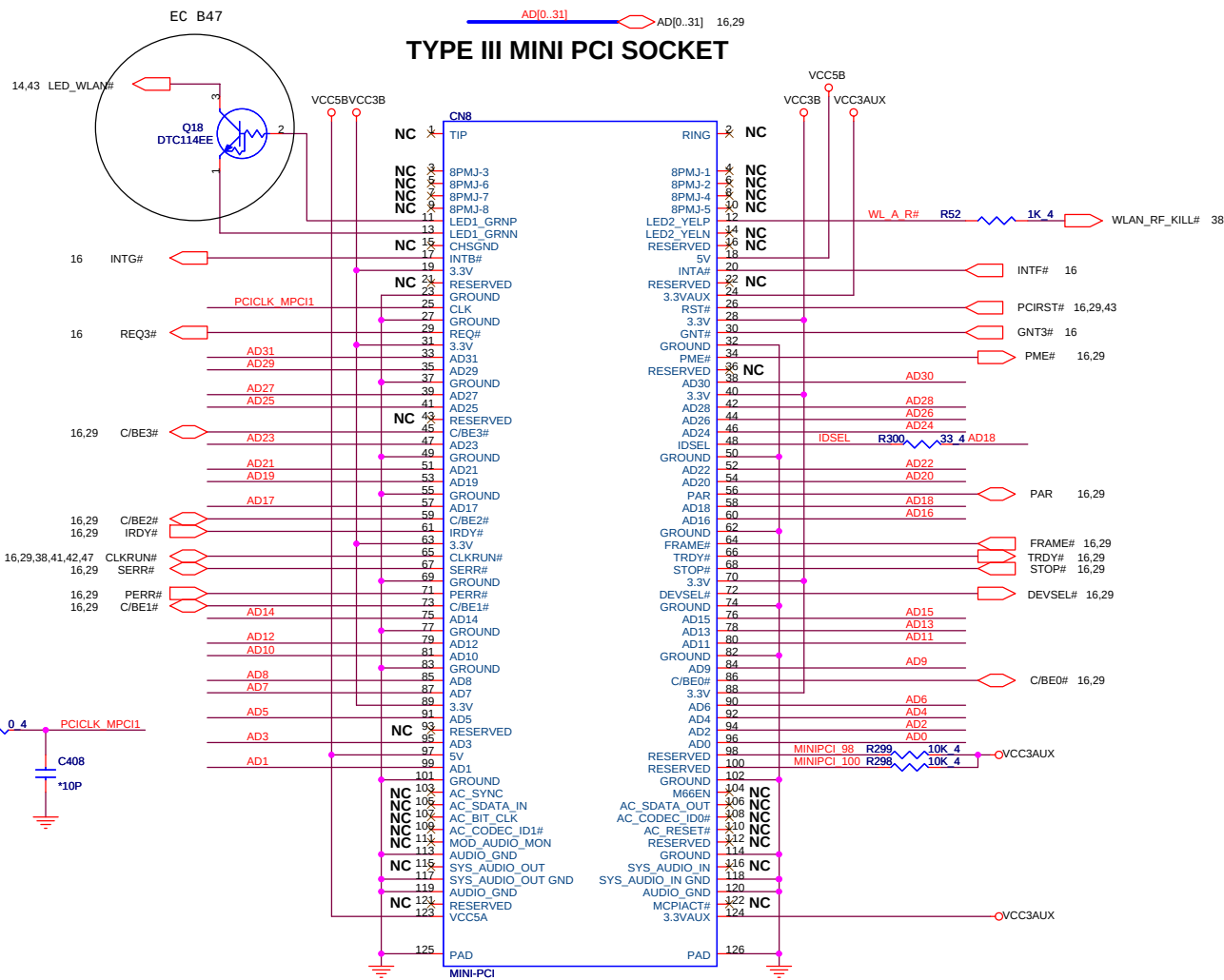


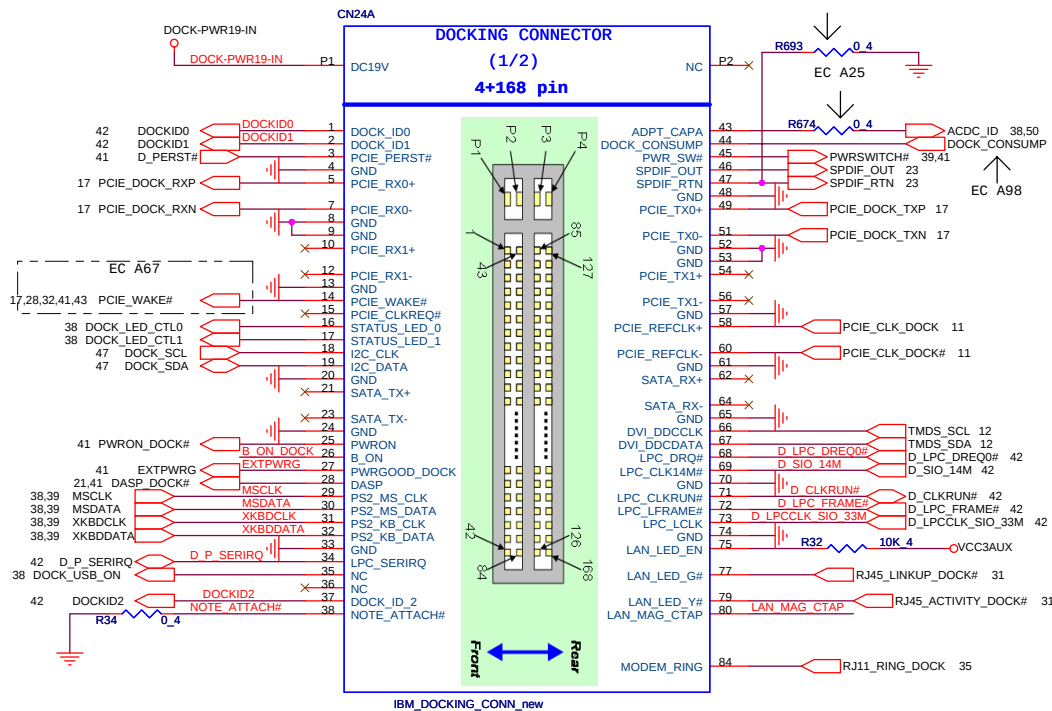
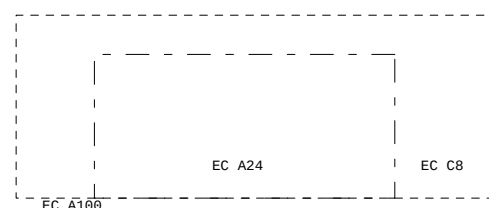
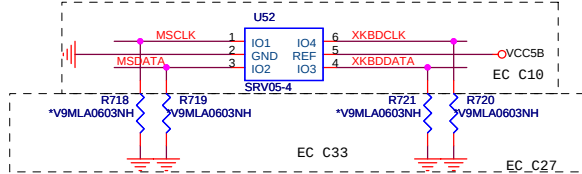


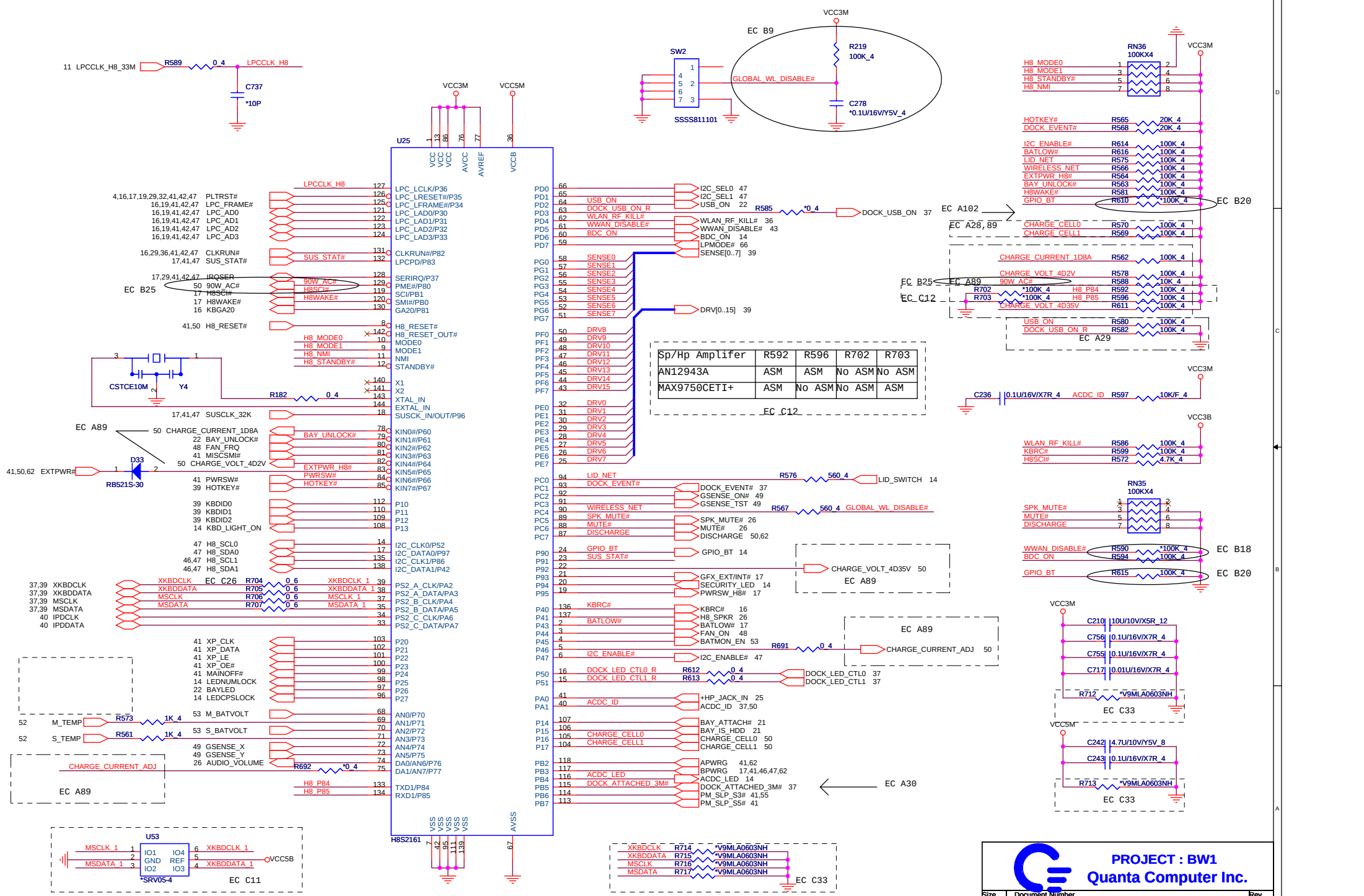


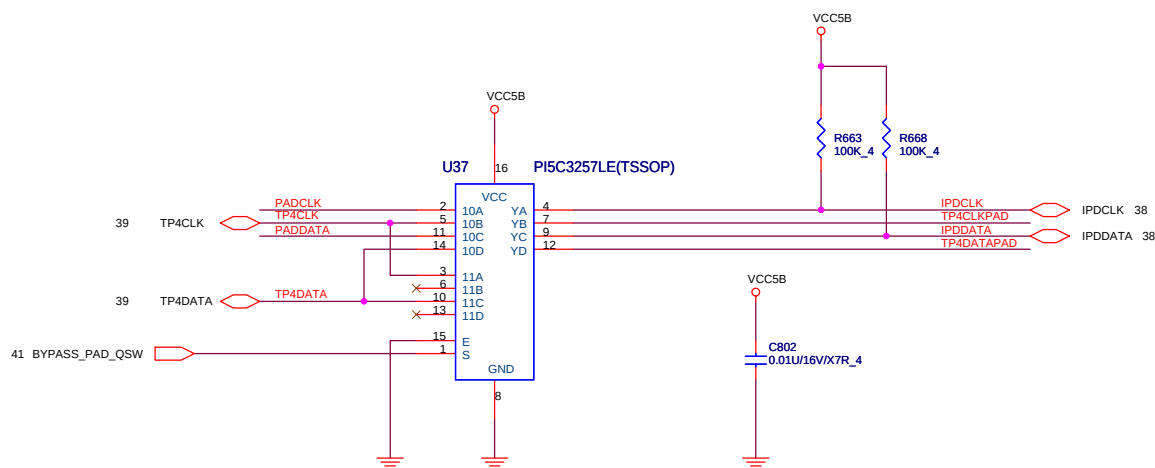
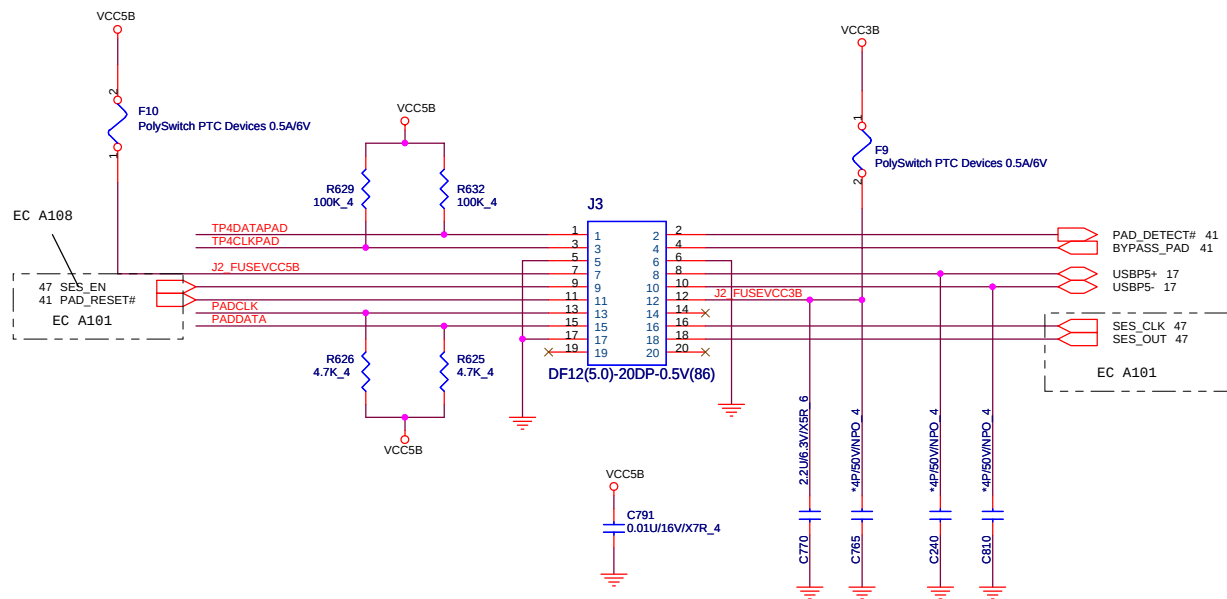


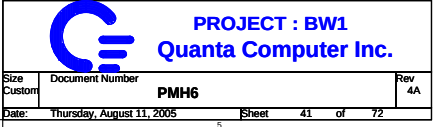
Close to Dock

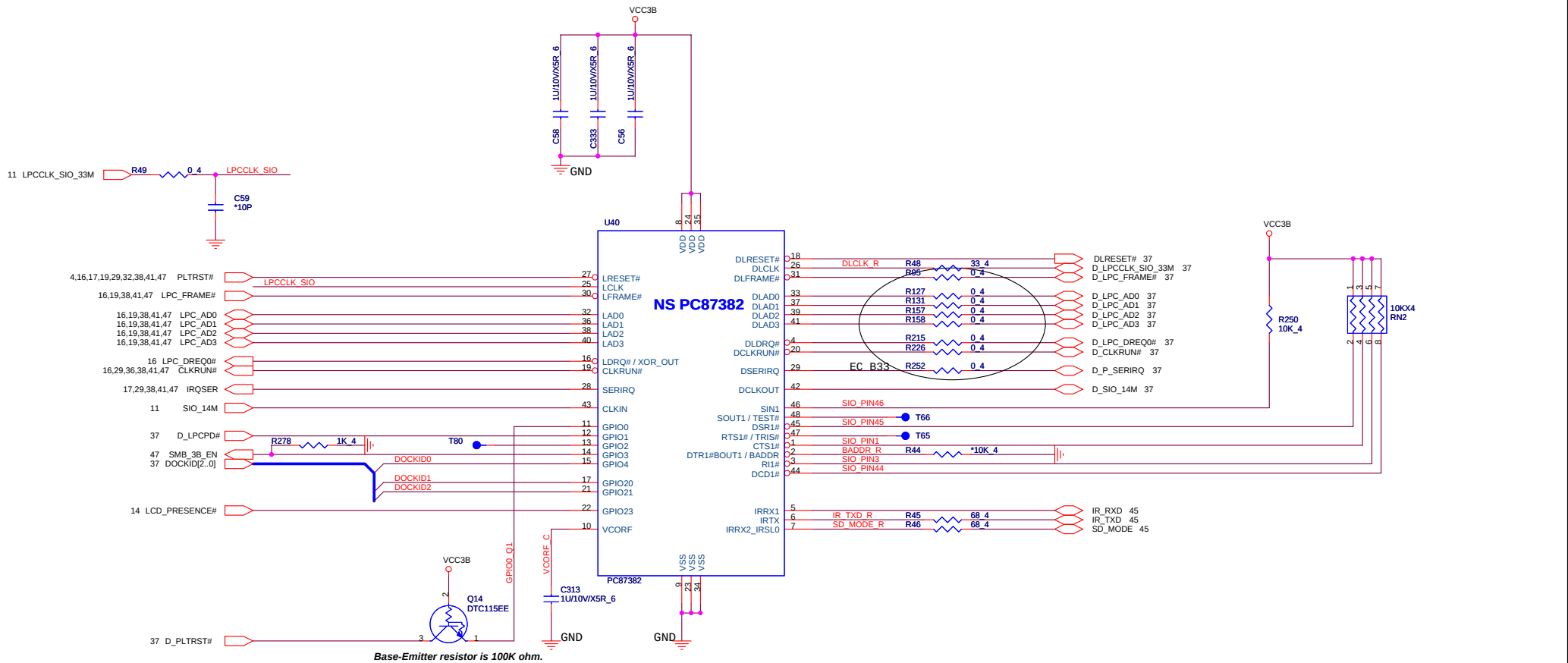




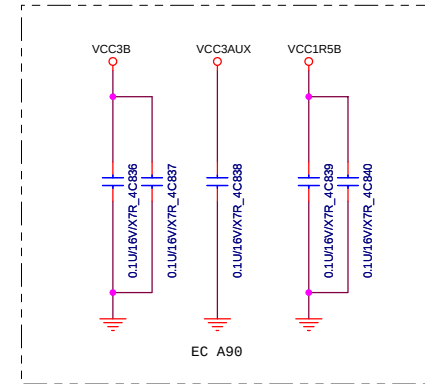
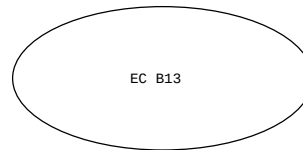
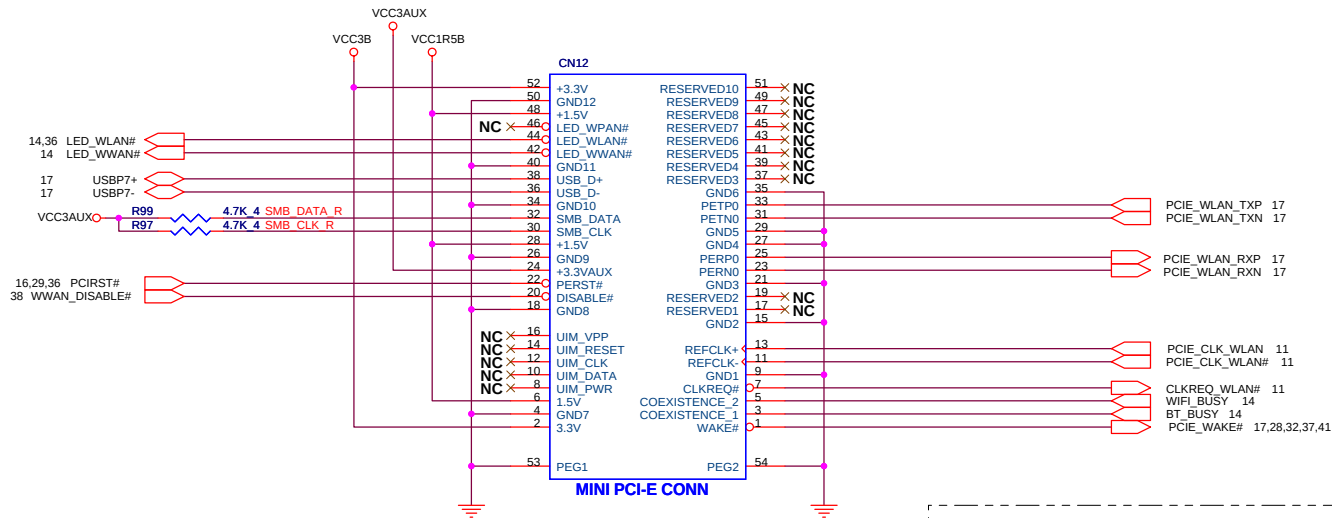






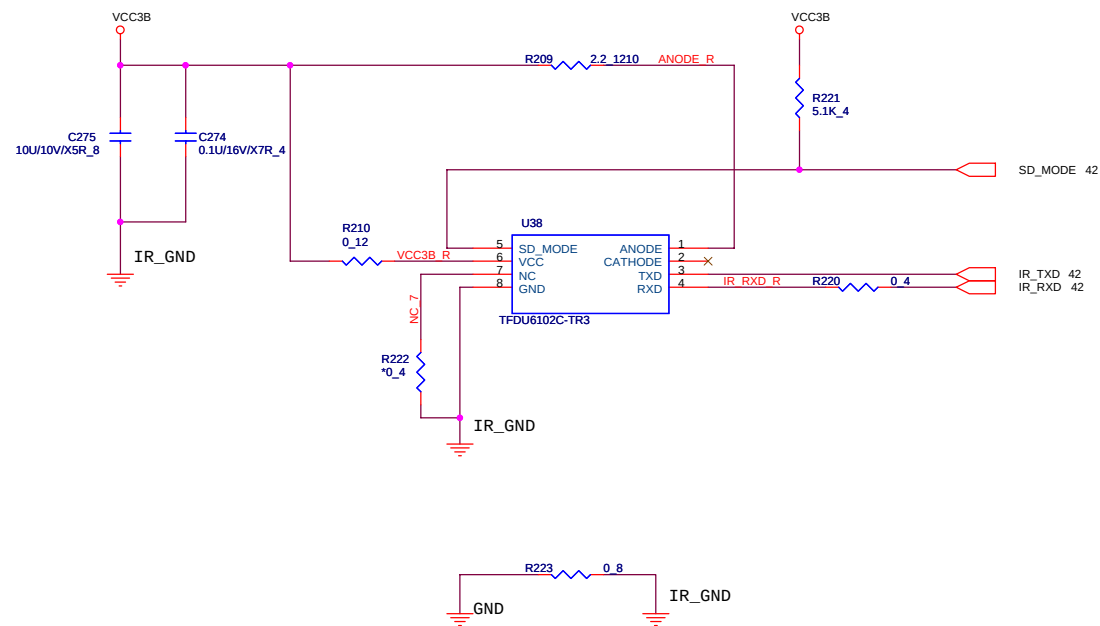


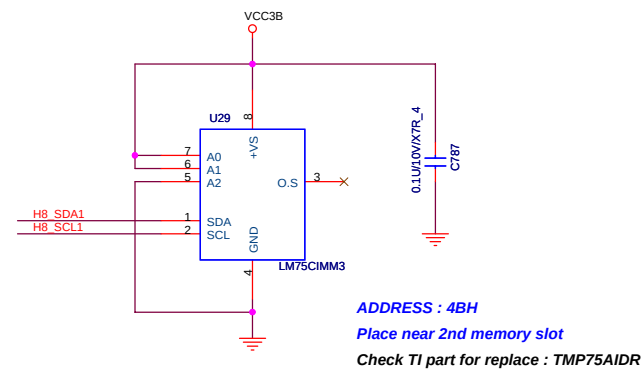
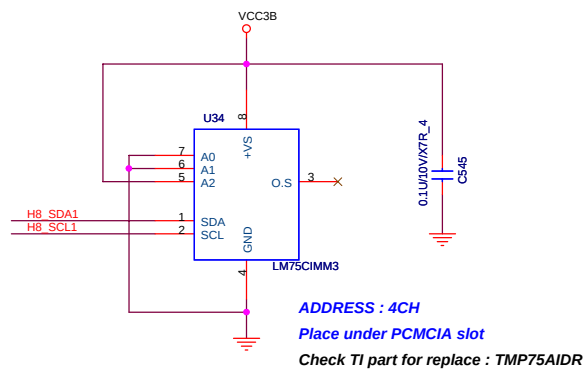
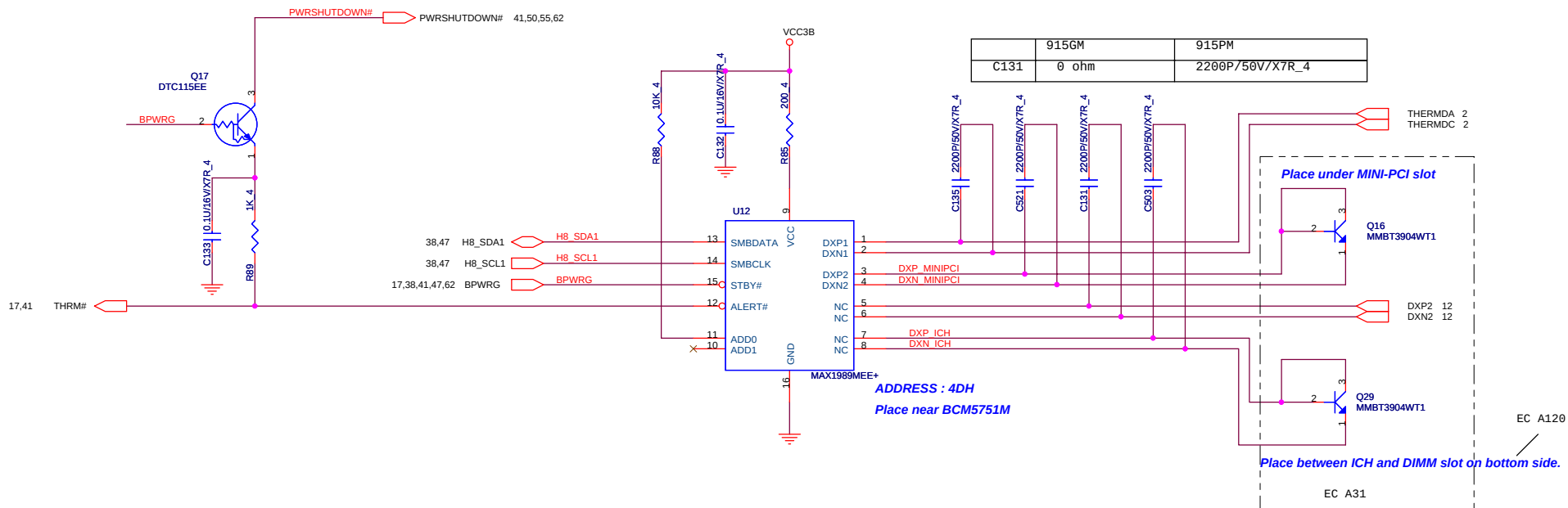
MINI PCIE CONN-1 FOR WWAN

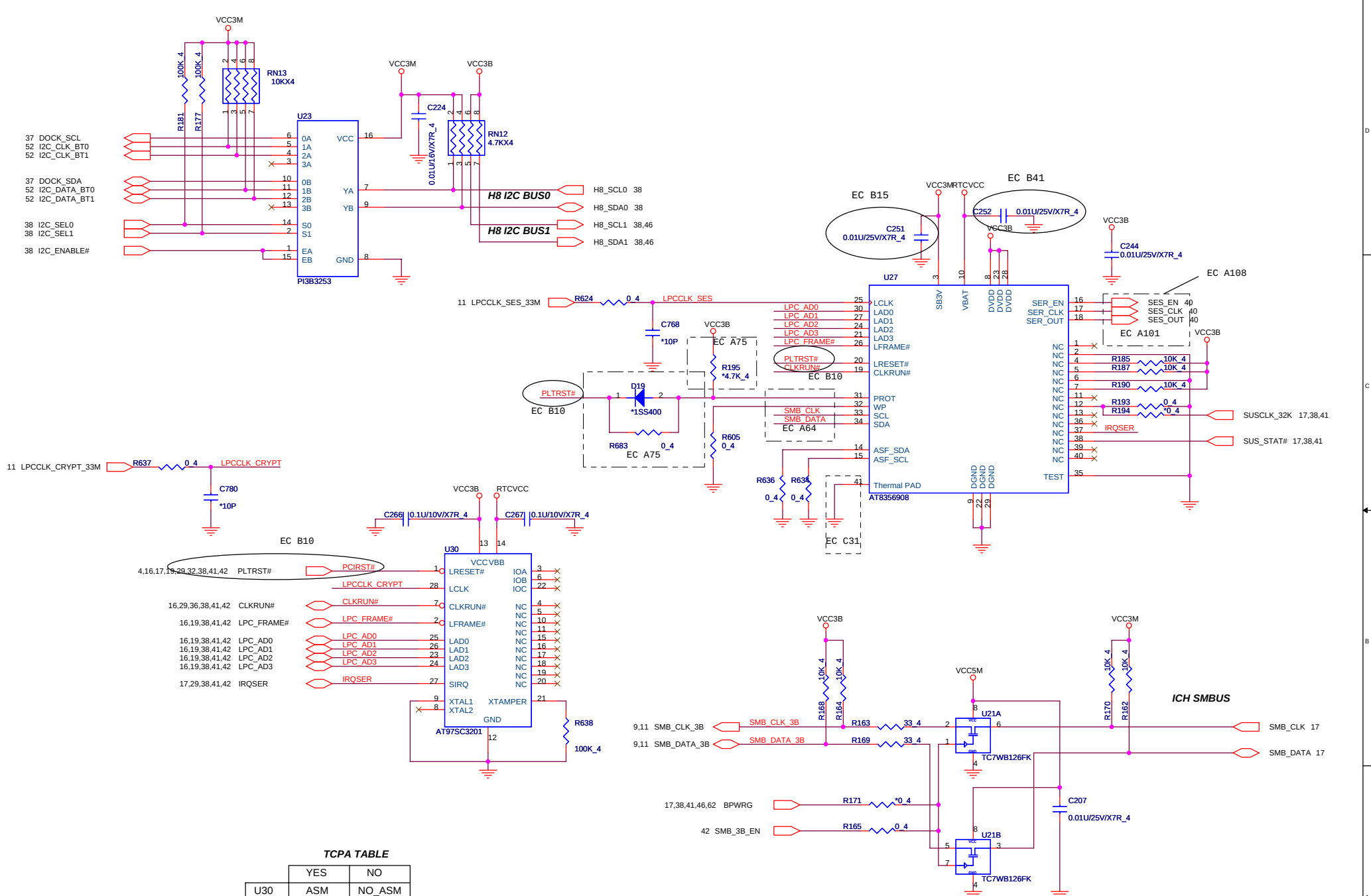


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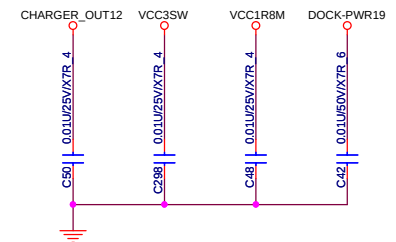
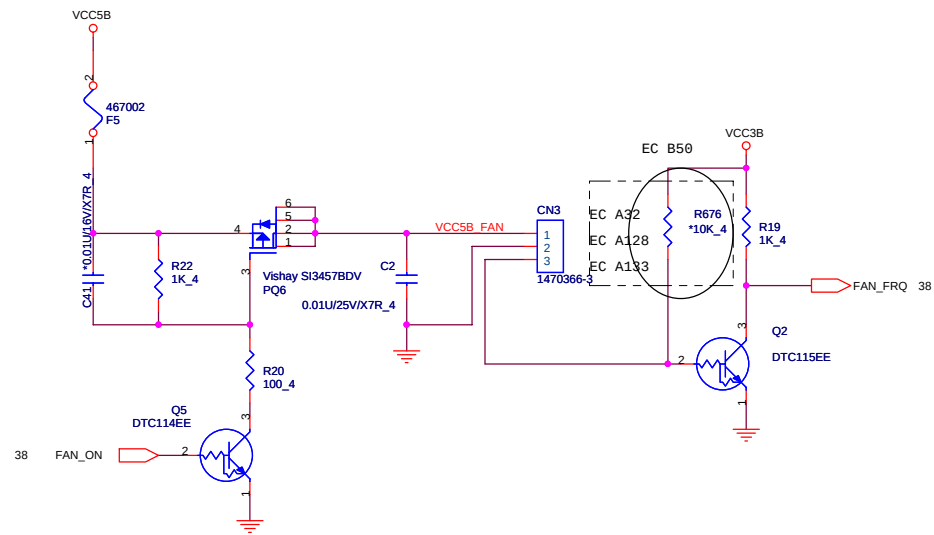


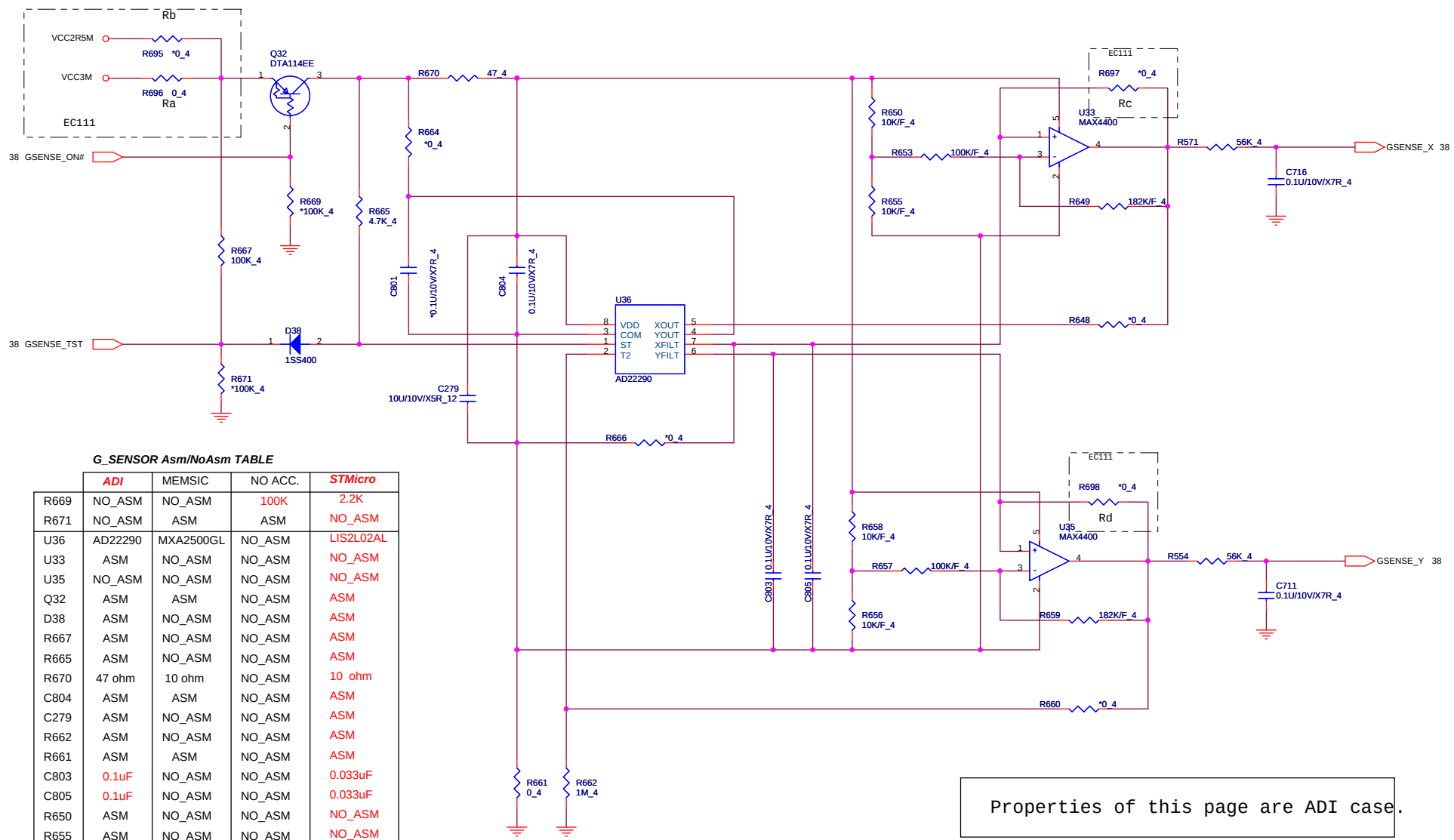




TCPA TABLE

	YES	NO
U30	ASM	NO_ASM
C266	ASM	NO_ASM
C267	ASM	NO_ASM
R545	ASM	NO_ASM
R546	NO_ASM	ASM



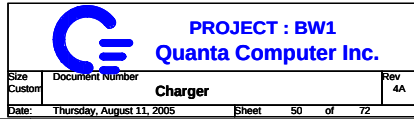


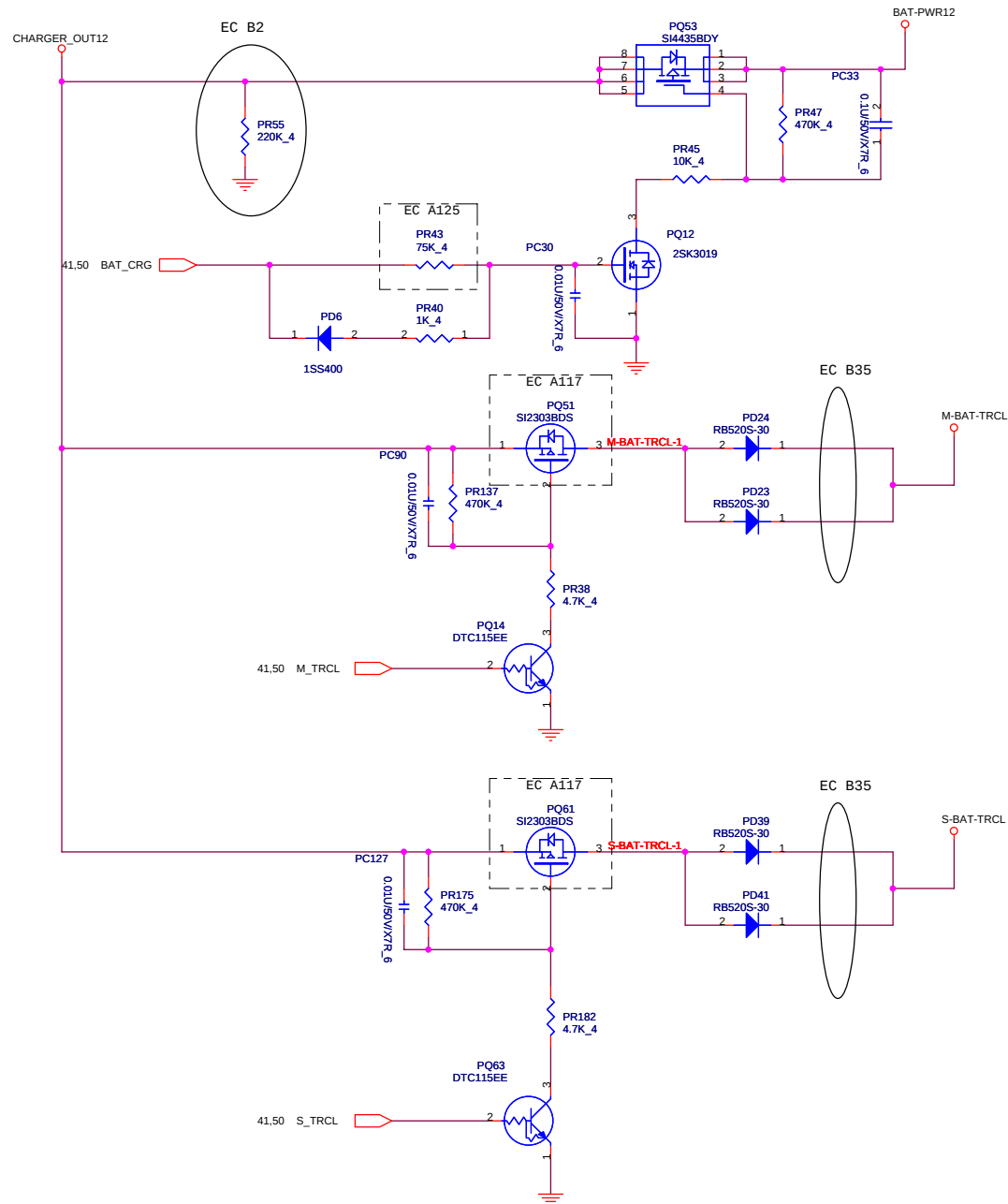
G_SENSOR Asm/NoAsm TABLE

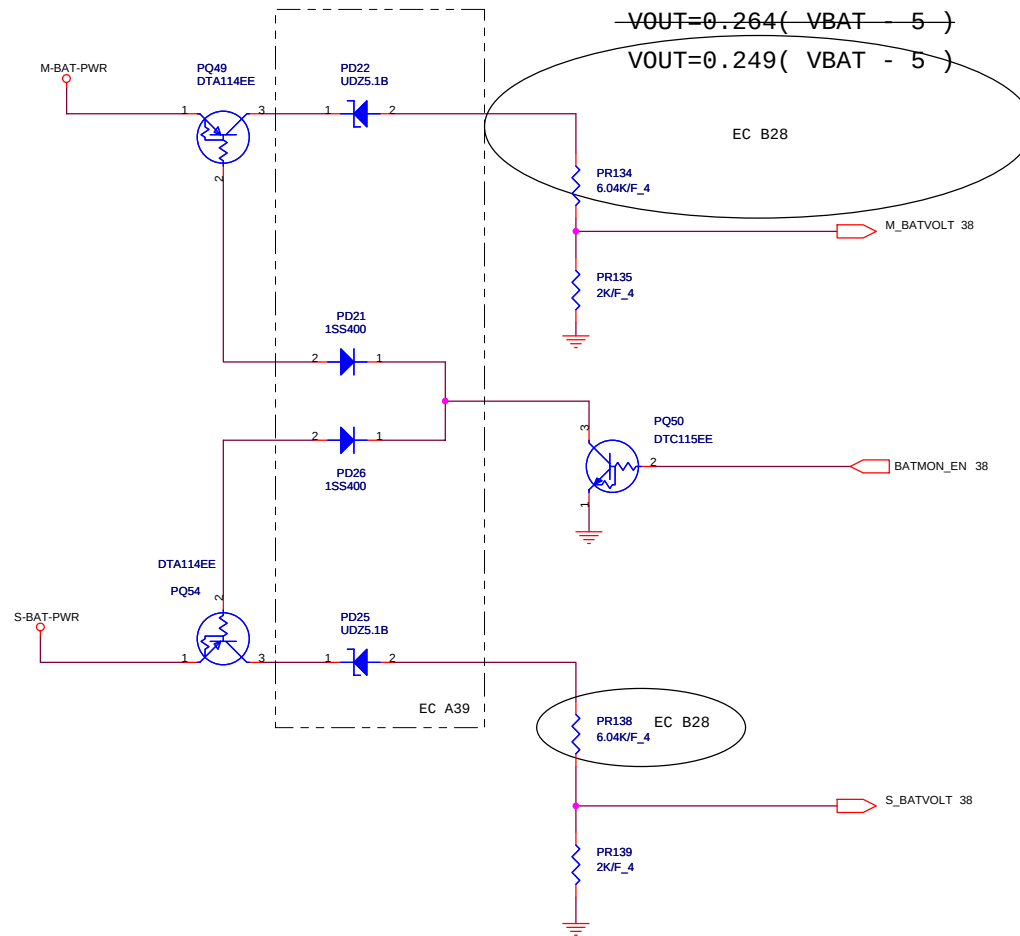
	ADI	MEMSIC	NO ACC.	STMicro
R669	NO_ASM	NO_ASM	100K	2.2K
R671	NO_ASM	ASM	ASM	NO_ASM
U36	AD22290	MXA2500GL	NO_ASM	LIS2L02AL
U33	ASM	NO_ASM	NO_ASM	NO_ASM
U35	NO_ASM	NO_ASM	NO_ASM	NO_ASM
Q32	ASM	ASM	NO_ASM	ASM
D38	ASM	NO_ASM	NO_ASM	ASM
R667	ASM	NO_ASM	NO_ASM	ASM
R665	ASM	NO_ASM	NO_ASM	ASM
R670	47 ohm	10 ohm	NO_ASM	10 ohm
C804	ASM	ASM	NO_ASM	ASM
C279	ASM	NO_ASM	NO_ASM	ASM
R662	ASM	NO_ASM	NO_ASM	ASM
R661	ASM	ASM	NO_ASM	ASM
C803	0.1uF	NO_ASM	NO_ASM	0.033uF
C805	0.1uF	NO_ASM	NO_ASM	0.033uF
R650	ASM	NO_ASM	NO_ASM	NO_ASM
R655	ASM	NO_ASM	NO_ASM	NO_ASM
R653	ASM	NO_ASM	NO_ASM	NO_ASM
R649	ASM	NO_ASM	NO_ASM	NO_ASM
R658	ASM	NO_ASM	NO_ASM	NO_ASM
R656	ASM	NO_ASM	NO_ASM	NO_ASM
R657	ASM	NO_ASM	NO_ASM	NO_ASM
R659	ASM	NO_ASM	NO_ASM	NO_ASM
R571	56K	8.2K	NO_ASM	56K
C716	ASM	ASM	NO_ASM	ASM
R554	56K	8.2K	NO_ASM	56K
R64	ASM	ASM	NO_ASM	ASM
R664	NO_ASM	ASM	NO_ASM	NO_ASM
C801	NO_ASM	ASM	NO_ASM	NO_ASM
R666	NO_ASM	ASM	NO_ASM	NO_ASM
R648	NO_ASM	ASM	NO_ASM	NO_ASM
R660	NO_ASM	ASM	NO_ASM	NO_ASM

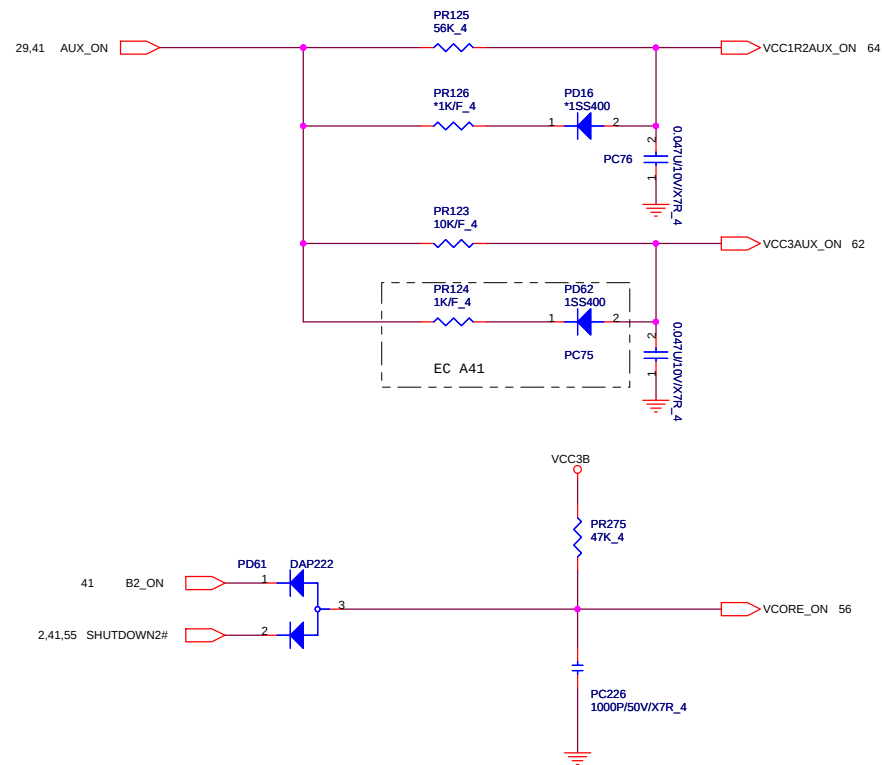
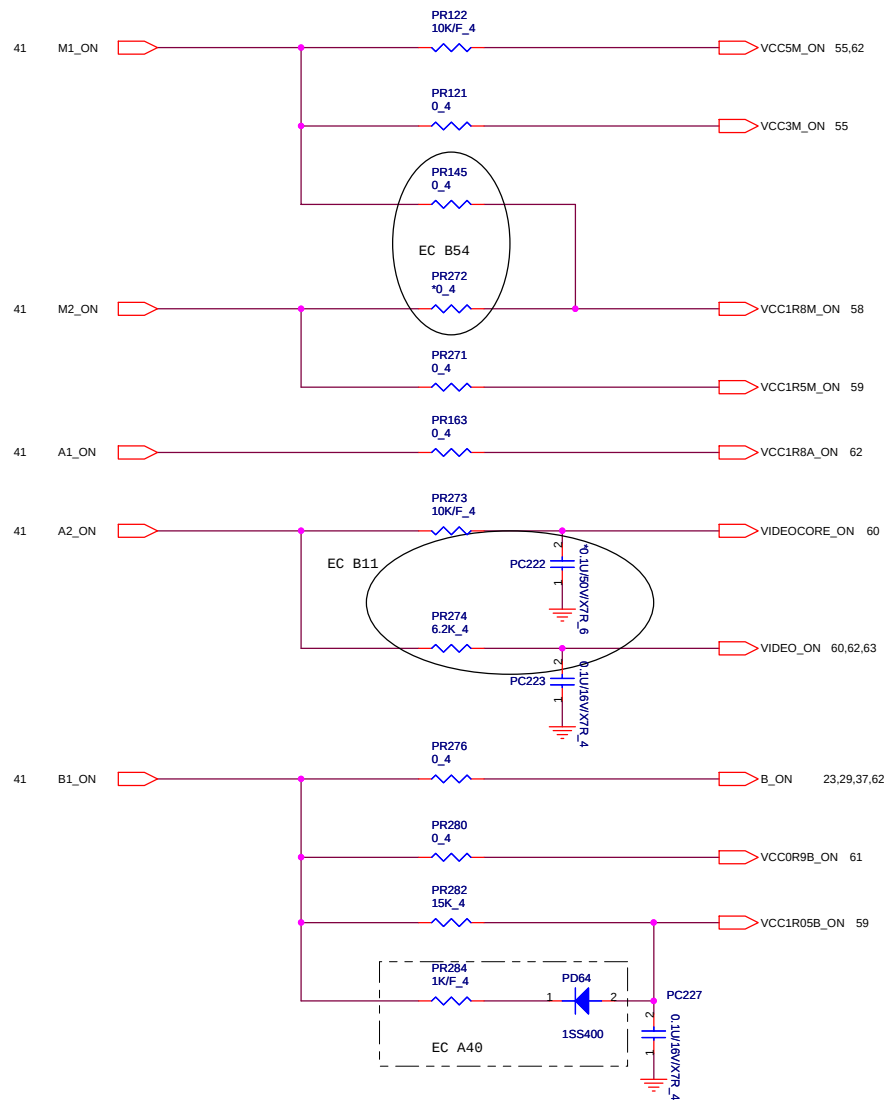
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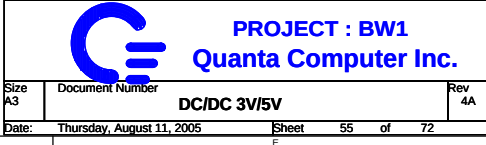
	ADI	MEMSIC	STMicro	NO ACC.
Ra	ASM	ASM	ASM	NO_ASM
Rb	NO_ASM	NO_ASM	NO_ASM	NO_ASM
Rc	NO_ASM	NO_ASM	ASM	NO_ASM
Rd	NO_ASM	NO_ASM	ASM	NO_ASM





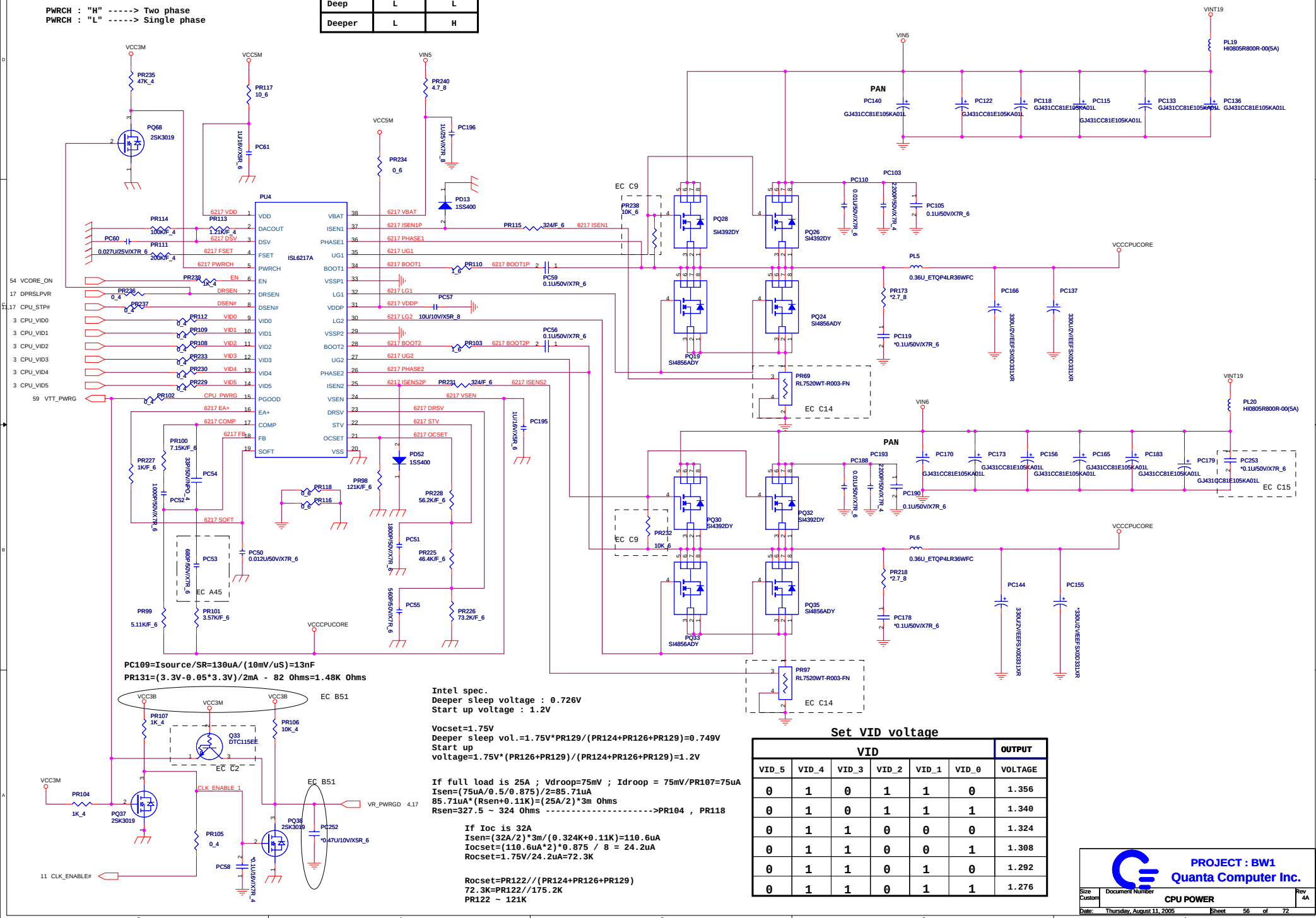




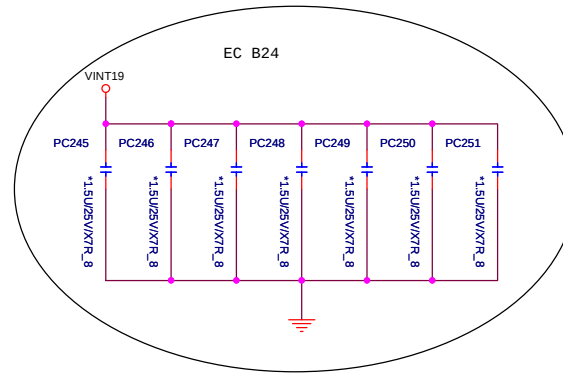
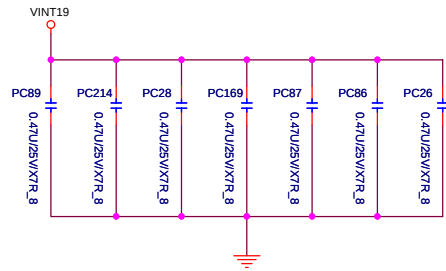


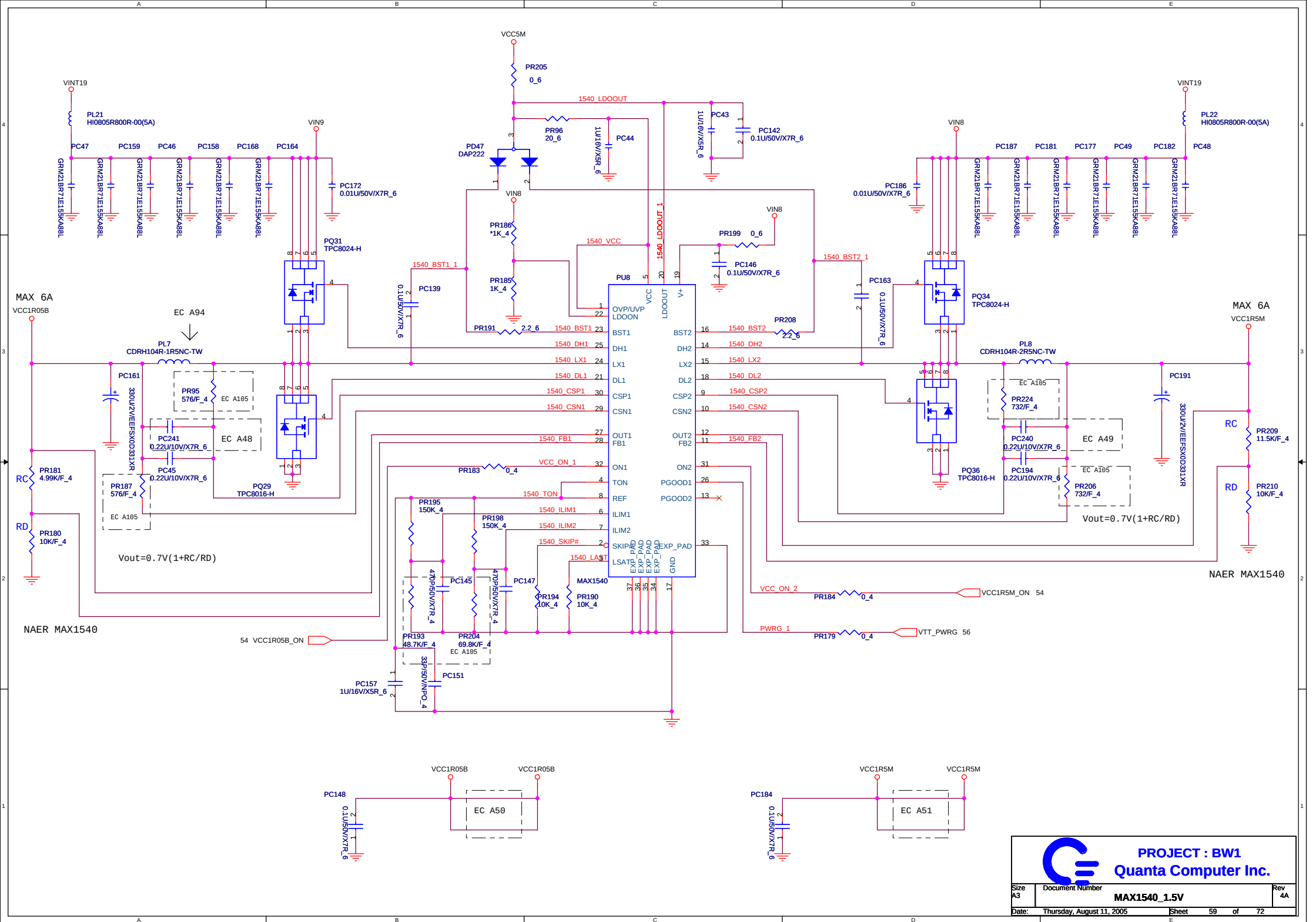

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PWRCH : "H" -----> Two phase
PWRCH : "L" -----> Single phase
```

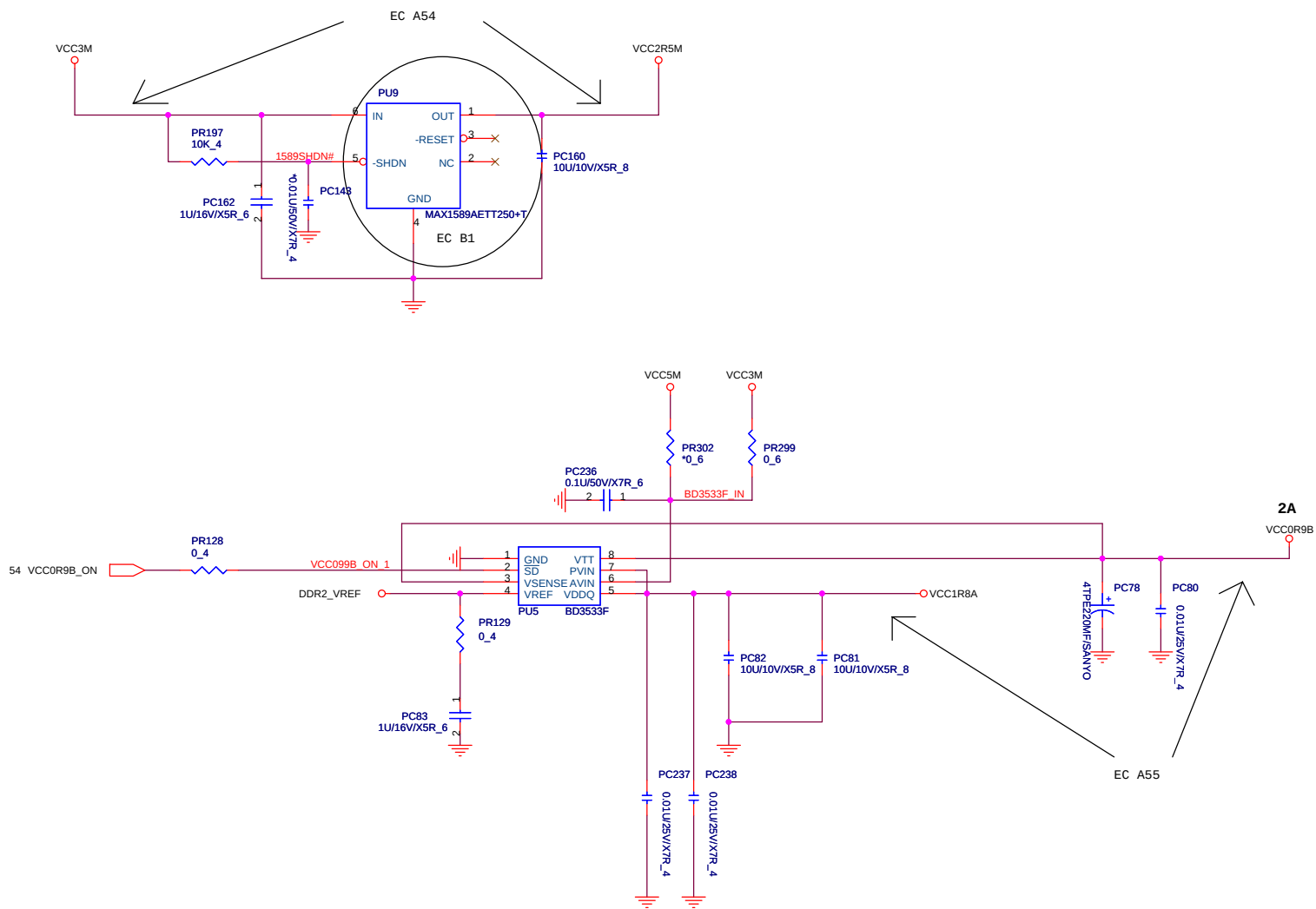
	DSEN#	DRSEN
Active	H	L
Deep	L	L
Deeper	L	H

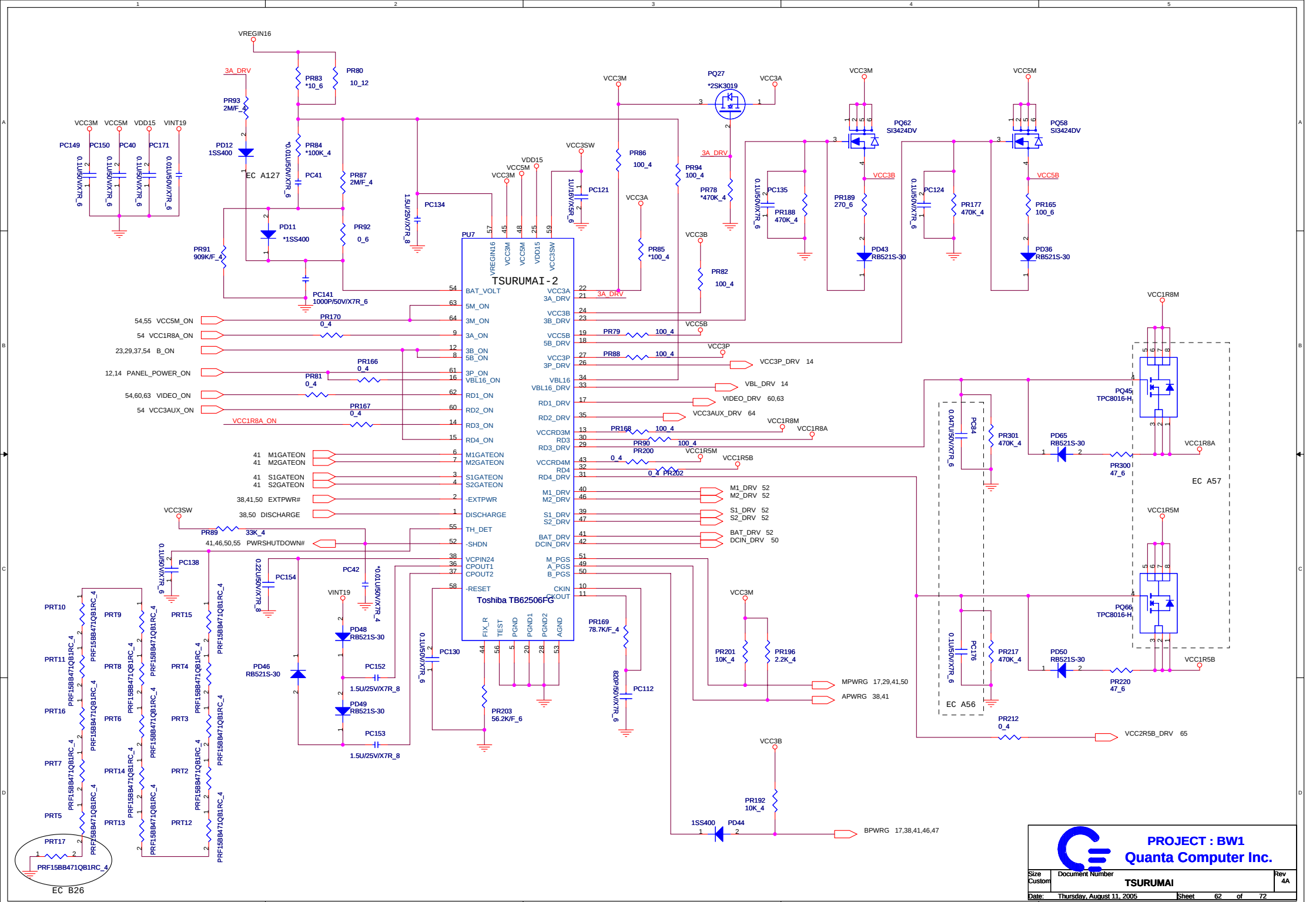


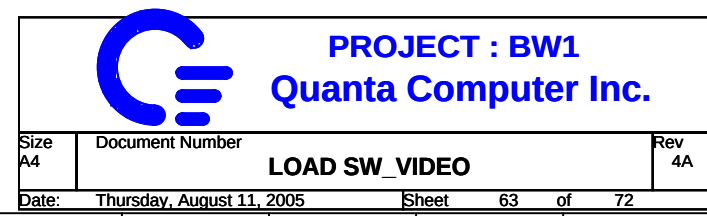
Set VID voltage						OUTPUT
VID						VOLTAGE
VID_5	VID_4	VID_3	VID_2	VID_1	VID_0	
0	1	0	1	1	0	1.356
0	1	0	1	1	1	1.340
0	1	1	0	0	0	1.324
0	1	1	0	0	1	1.308
0	1	1	0	1	0	1.292
0	1	1	0	1	1	1.276

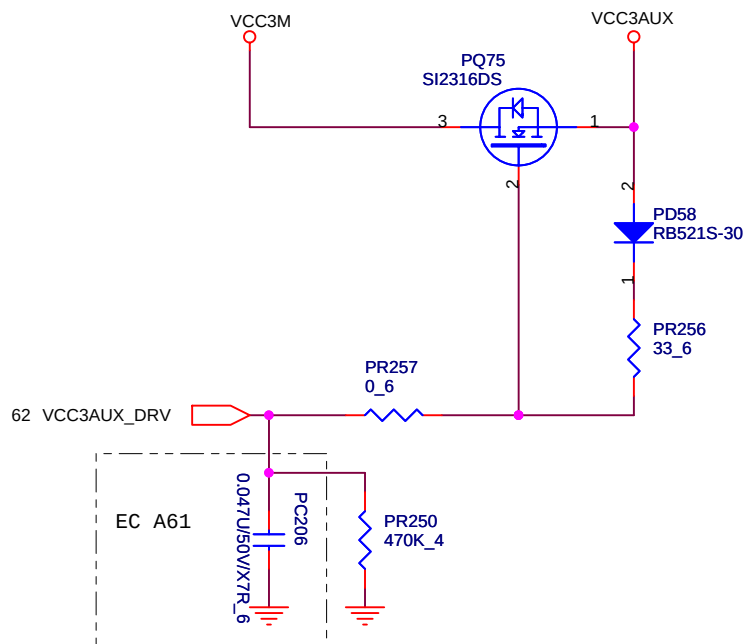
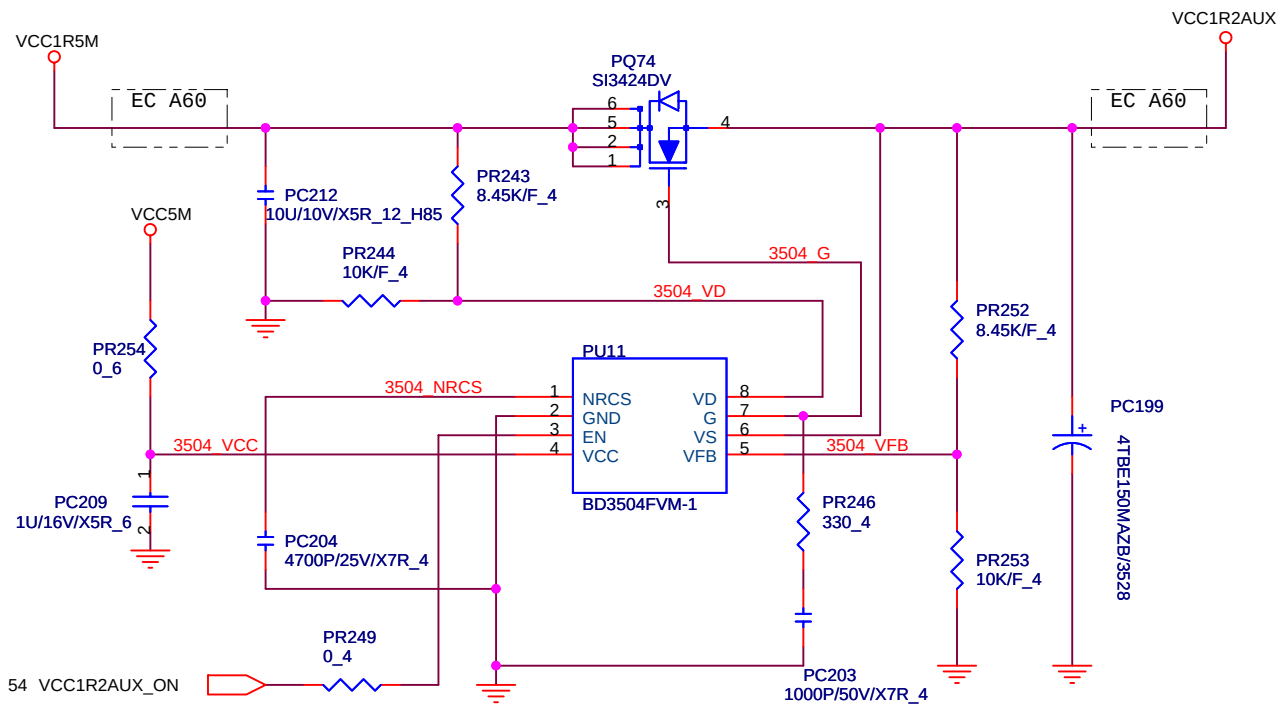


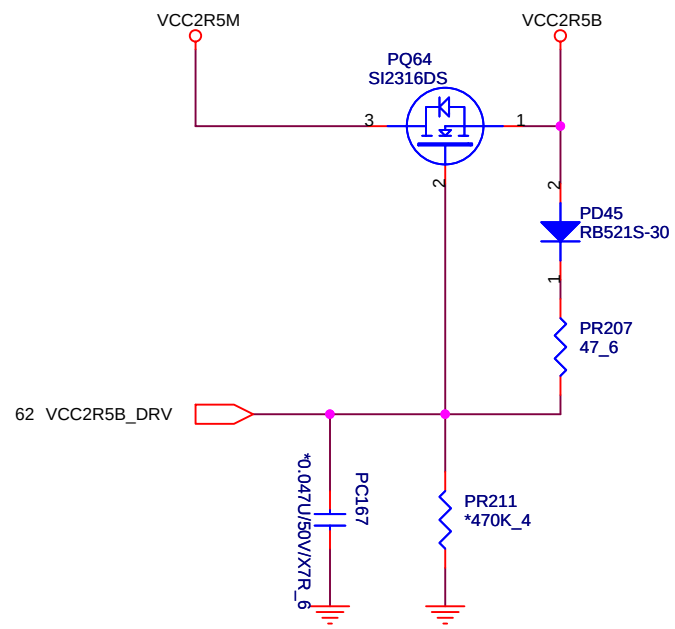
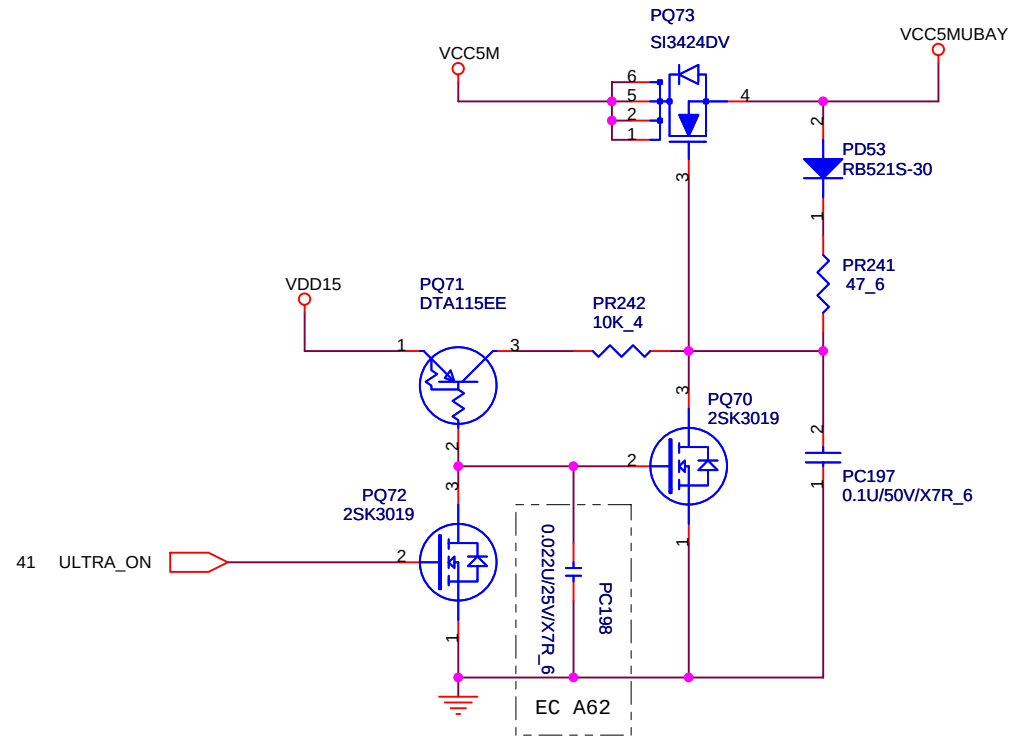








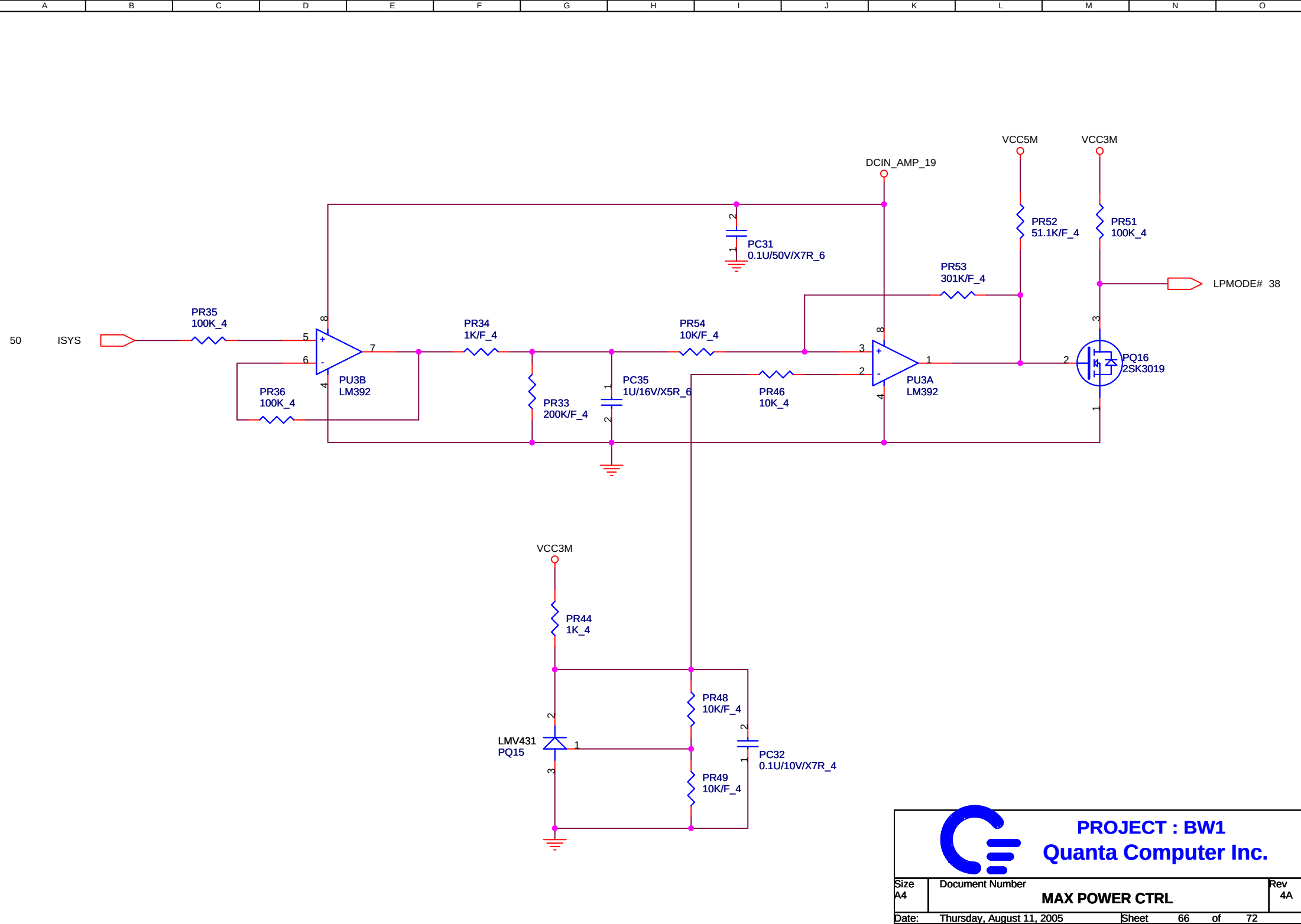






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Quanta Computer Inc.

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Size A4	Document Number MAX POWER CTRL	Rev 4A
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Page		Model-1(Integrated 915GM only)	Model-2(M22CSP64)	Model-3(M24CSP128)
5	POP	R423=4.99K, R337, R336, R432, R404, R405, R406, R382, R388, R402, R345, R366, PQ69, PQ67, R373, R385, R409, R368, R364=75, R370=150, R369=150, U15=915GM		
	De-Pop	R371, R407, R387, R367, R431, R424, R418		
7	POP	R341, D29=RB751V-40, R342=10, L45=BLM11A121S, C532, C525, R427, C546=0.1uF, C499, C469, L46, C494, C470, L47, D30, R331, L44, C508=0.1uF, C522, C514=0.1uF, C500, C507=0.1uF, C524, R408, C513=0.1uF, C534, L43, C483=0.1uF, C476, R362, C491, C490=0.1uF, R335, C478, C488=0.1uF, R363, C481=4.7uF, C487=0.1uF, C86= 10uF, R517 = 00hm.		
	De-Pop	R672		
11	POP	RP10, RP12, R397=49.9, R413=49.9, R330=49.9, R329=49.9		
	De-Pop	RP13, R416, R422		
12	POP			
	De-Pop	ALL		
13	POP			
	De-Pop	ALL		
14	POP	RN15, RN20, RN19, RN17, RN16, RN22, RN23, RN21, RN18, RN31, R41, R6		
	De-Pop	RN3, RN8, RN7, RN5, RN4, RN10, RN11, RN9, RN6, RN30, R5		
15	POP	R255, R266, R275, R227, R228, R229, RP2, RP3, R43=R47= 390hm	R43=R47= 00hm	R43=R47= 00hm
	De-Pop	RP9, RP8		
17	POP	R153		
	De-Pop	R144		
46	POP	C131=0ohm		
	De-Pop			
54	POP			
	De-Pop	PR273, PR274, PC222, PC223		
60	POP		PR28=35.7K 1%	PR28=49.9K 1%
	De-Pop	ALL		
63	POP			
	De-Pop	ALL		

Revision History

Rev.	Date	Phase	Change List
1A	2005/1/4	DV	Initial release
2A	2005/2/5	SIV	See page 69 Record A
3A	2005/4/25	SIT	See page 71 Record B
4A	2005/7/5	SVT	See page 72 Record C

EC #/Page/Description/Part Affected/CMVC #

EC A00/04/Install R511,R508 for Alviso REV:C.
EC A01/07/Add R672 for ext/int VGA option.
EC A02/07/Change L59 from BLM18PG181SN1 to 1uH(TDK NLFV25T-1R0M-PF) to meet Intel spec.
EC A03/07/Change L63 from BLM18PG181SN1 to 91nH(TDK NLCF3225T-091M-GPF) to meet Intel spec and reduce voltage drop.
EC A04/07/Change L62 from 10uH to 1uH(TDK NLFV25T-1R0M-PF) to meet Intel spec.
EC A05/11/Install RP15 for mini PCI-E.
EC A06/12/Correct Y6 pin2 to pin3.
EC A07/12/Add R673 for S-VIDEO termination.
EC A08/12/Delete D24 and change GPIO0 connection.
EC A09/13/Delete redundant R252.
EC A10/13/Add C806 to solve blue screen issue.
EC A11/13/Change channel B GND pins to NC.
EC A12/14/Change R41 from 0ohm to NI for M22/M24.
EC A13/15/Correct VCCCRT source per IBM EC.
EC A14/15/Change DOCK_ATTACHED# to DOCK_ATTACHED_5B# per IBM EC.
EC A15/16/Correct Y2 pin4 to pin2.
EC A16/17/Change R106 from 10Kohm to NI, R105 from NI to 100Kohm to differ board ID per IBM EC.
EC A17/18/Correct D14,D15 footprint to SC79.
EC A18/18/Change R155 to 20K, C194 to 1uF per Intel sighting alert.
EC A19/18/Delete U52 32pin PLCC BIOS ROM.
EC A20/23/Correct L21 footprint to RC0805.
EC A21/29/Delete R215 MPWRG pull-high.(Double pull-high).
EC A22/31/Change LAN_DOCK_ATTACHED# to DOCK_ATTACHED_AUX# per IBM EC.
EC A23/32/Correct Y3 pin2 to pin3.
EC A24/37/Change CN25 debug conn. to NI.
EC A25/37/Add R674 for ACDC_ID connection per IBM EC.
EC A26/37/Change DOCK_ATTACHED# connection per IBM EC.
EC A27/37/Add C808,C809 per IBM EC.
EC A28/38/Delete R569,R570,R574,R562.(Double pull-high)
EC A29/38/Change R580,R582 to pull-low per IBM EC.
EC A30/38/Change DOCK_ATTACH# to DOCK_ATTACHED_3M# per IBM EC.
EC A31/46/Correct Q16,Q29 footprint to SOT23.
EC A32/48/Add R676 10Kohm for FAN output pull-high to solve abnormal fan speed.
EC A33/50/Change PC88 from 0.1uF to 0.01uF per power team request.
EC A34/50/Change connection from VCC3M to VCC3SW per power team request.
EC A35/50/Change connection from VCC3M to VCC3SW per power team request.
EC A36/50/Add PR303 connect GND and 8724_AGND.
EC A37/52/Change PC16,PC17 to NI for IIC signal quality.
EC A38/52/Change PC93,PC95 to NI for IIC signal quality.
EC A39/53/Correct PD22,PD25 footprint to DSM.
EC A40/54/Install PR284,PD64 to meet power seq. requirement.
EC A41/54/Install PR124,PD62 to meet power seq. requirement.
EC A42/55/Delete JP7,JP8 short pad.
EC A43/55/Correct connection of PL9.
EC A44/55/Delete JP6,JP5 short pad.
EC A45/56/Change PC53 from 330pF to 680pF per power team request.
EC A46/58/Add PC239 per power team request.
EC A47/58/Delete JP10,JP11 short pad.
EC A48/59/Add PC241 per power team request.
EC A49/59/Add PC240 per power team request.
EC A50/59/Delete JP14,JP15 short pad.
EC A51/59/Delete JP17,JP18 short pad.
EC A52/60/Add PC242 per power team request..
EC A53/60/Delete JP2,JP3,JP4 short pad.
EC A54/61/Delete JP12,JP13 short pad.
EC A55/61/Delete JP9,JP22 short pad.
EC A56/62/Change PC84 to 50V rating, PC176 from 0.047uF/16V to 0.1uF/50V for power rating and sequence.
EC A57/62/Change PQ45,PQ66 from SI4890DY to TCP8016H per power team request.
EC A58/63/Delete JP16,JP19 short pad.
EC A59/63/Change PC39 to 50V rating.
EC A60/64/Delete JP20,JP21 short pad.
EC A61/64/Change PC206 to 50V rating.
EC A62/65/Change PC198 to 50V rating.
EC A63/50/Install PC99 for charger accuracy.
EC A64/47/Change U27 SMB_CLK_3B and SMB_DATA_3B to SMB_CLK and SMB_DATA.

EC A65/37/Size of resister R33 on LAN_MAG_TAP changes to 1608 size.(IBM EC)
EC A66/5/Change termination resister R364 for Alviso composite TVout from 150ohm to 75ohm.(IBM EC)
EC A67/37/Delete a gate logic from docking PCIE_WAKE# signal.(IBM EC)
EC A68/16,23,27/change value of resistors and signal name on Azalia interface.(IBM EC)
EC A69/25/Add internal MIC Bias logic.(IBM EC)
EC A70/12/Change diode to cheaper one.(IBM EC)
EC A71/50/PJ2(DC-IN connector) change to right-angle type.
EC A72/52/PJP2,PJP3(Battery conn.) change to 7-pin. type.
EC A73/55/PL12 change to GANSHIN(CDRH104RNP-5R2NC-EP).
EC A74/12/ATI TV-OUT termination R304,R308 change to 150 ohm.
EC A75/12/Add R683=0ohm, change R195,D19 to NI per Atmel recommend.
EC A76/32/R109 change to 1K,R115 change to 1.2K,C156 and C166 change to 12pF to pass IEEE test.
EC A77/7/Add C816-C819 edge capacitors to meet Intel design guide.
~~EC A78/11/Install U10,U11,U42 and change to TC7SZ08.Change damping resistors' value to solve PCI clock slew rate too slow issue.~~
EC A79/25/Change DOCK_HPOUT_DTCT# and +HP_JACK_IN connection.(IBM Nakagawa-san).
EC A80/24/Delete R555.(IBM Nakagawa-san).
EC A81/24/Change R513 to NI.(IBM Nakagawa-san).
EC A82/24/Change DOCK_MICIN_DTCT# connection.(IBM Nakagawa-san).
EC A83/23/Change +EQ_BYPASS connection.(IBM Nakagawa-san).
EC A84/23/Delete R161,C683,C189,R158,R157,R535,R541,R517,R518.(IBM Nakagawa-san).
EC A85/23/Add R685,C826.(IBM Nakagawa-san).
EC A86/37/Change DOCK_HPOUT_DTCT and DOCK_MICIN_DTCT to DOCK_HPOUT_DTCT# and DOCK_MICIN_DTCT#.(IBM Nakagawa-san).
EC A87/31,35/Change location of TVD diode (U52,U47) and change to SRV05-4. (IBM EC SIV-IBM-28)
EC A88/20/Alignment guide pin of SATA HDD connector should be soldered to GND pad. (IBM EC SIV-IBM-30)
EC A89/38/DC/DC charger EC. (IBM EC SIV-IBM-31)
EC A90/31,43/Add capacitors on power line at : GbE LAN switch and miniPCie slot. (IBM EC SIV-IBM-32)
EC A91/15/Change pi-filter components' value. (IBM EC SIV-IBM-34)
EC A92/23/Add dumper resister 33ohm on SPDIF_OUT. (IBM EC SIV-IBM-35)
EC A93/11/Change R390 and R429 from 100K to 10K(IBM EC SIV-IBM-36)
EC A94/59/Change PL7 from CDRH104R_2R5UH_7R5A to CDRH104R_1R5UH_10A.(IBM EC)
EC A95/50/Make "8724_LDO_3D3V", and connect to pin_REFIN.(IBM EC 20050217_MW-Note_Charger_EC.xls - EC2)
EC A96/50/Change pin_VCTL control method.(IBM EC 20050217_MW-Note_Charger_EC.xls - EC3)
EC A97/50/Modify pin_ICTL circuit.(IBM EC 20050217_MW-Note_Charger_EC.xls - EC4)
EC A98/37,50/Modify pin_CSSN to support Docking System.(IBM EC 20050217_MW-Note_Charger_EC.xls - EC5)
EC A99/25,35/RJ45+RJ11 and audio jacks change to combo type.
EC A100/37/Add 0 ohm resistor for SPDIF_RTN(IBM EC SIV-IBM-41,42).
EC A101/40,47/Connect TATER signals from ATMEI to TouchPad connector.(IBM EC SIV-IBM-40)
EC A102/38/Delete R577.(IBM EC SIV-IBM-39)
EC A103/50/PQ60 change to SI4804BDY(QCI power team).
EC A104/58/PR127 and PR283 change to 732ohm.PR296 change to 69.8K.(QCI power team)
EC A105/59/PR187,PR95 change to 576 ohm.PR193 change to 48.7K.PR204 change to 69.8K. PR224,PR206 change to 732ohm(QCI power team)
EC A106/60/PR21 change to 59K. PR8,PR22 change to 649 ohm.(QCI power team)
EC A107/41/PR631 change to 100K.
EC A108/50,55/PC131,PC132,PC108,PC101,PC94,PC65,PC66,PC67,PC68,PC201 change to TMK325BJ475KN-H.
EC A109/55/Add reserve PC244 220uF/6.3V.(IBM EC)
EC A110/11/Delete unused C446,C447,C87,C86,C113,C106,C484,C477.(IBM EC SIV-IBM-37)
EC A111/49/Prepare to support new STMicro G-sensor.(IBM EC SIV-IBM-43)
EC A112/50/PR74 : change to 11.3kohm.PR176 : change to 19.1kohm.PR64 : change to 100kohm/0.5%.PR65 : change to 12.4kohm/0.5%.(IBM EC SIV-IBM-44)
EC A113/11/Change PCI clock buffer U52 to low delay buffer. Separate clock for FWH and PMH6.
EC A114/15/Change pi-filters' value based on Kevinesss' experiment..
EC A115/50/PR304 change to 10ohm/1%. Power Supply for MAX8724 pin_17 change to "8724_LDO_3D3V".Delete PR67,PR77,PR309,PR311,PR58. Add description.
EC A116/50/PR65 change to 12.7K/0.5%. Description change to "AC adapter detection = 18.17V".
EC A117/51/PQ51,PQ61change from SI2301 to SI2303.
EC A118/40,47/Change net name SES_EN# to SES_EN.(IBM EC SIV-IBM-52)
EC A119/24,25/R129,R681 change to NOASM. R680 change to ASM.(IBM EC SIV-IBM-51)
EC A120/46/Change themal diodes' description.(IBM EC SIV-IBM-53)
EC A121/50/PC99 change to 1000pF/X7R. PC243 change to 0.01uF/X7R .(IBM EC SIV-IBM-55)
EC A122/2/Reserve 100pF capor THERMDA and THERMDC.
EC A123/14/Change C38 from 0.047uF to 0.1uF.
EC A124/50/Change charger CCS cap to 1000pF.
EC A125/51/Charger Reverse FET control PR43 change to 75K.
EC A126/58/Add description for I out and ILIM.
EC A127/62/PC41 change to NC.(IBM EC SIV-IBM-57)
EC A128/48/FAN pull-up R676 change to NC.(IBM EC SIV-IBM-58)
EC A129/29/duplicate pull-up resister R212 on CARDBUSPDN# change to NC.(IBM EC SIV-IBM-59)
EC A130/37/F7 change to PolyFuse Raychem NANOSMDC050F.
EC A131/28/R192 change from 75ohm to 10ohm to improve CCLK slew rate.
EC A132/11/R340,R80,R53,R64,R690 change from 33ohm to 12.1ohm to increase slew rate.
EC A133/48/R676 change to install for fan pull-up.

EC #/Page/Description/Part Affected/CMVC #

EC B1/61/Change P/N of PU9 to MAX1589AETT250+T (IBM EC)
EC B2/51/Add PR55=220K for charge inrush (IBM EC)
EC B3/50/Add PR162=1K, Change PR155 to 00hm (IBM EC)
EC B4/50/Change PC126 to 0.01U/50V/X7R_6 (IBM EC)
EC B5/14/Change Q9 to SI4890DY (IBM EC)
EC B6/50/Delete PR39, Add PD66 ,Change PQ11 to DTC115EE and connection, Change PQ13 to SI4840DY(IBM EC)
EC B7/60/Change PR21 to 100K 1%
EC B8/60/Change PR14 to 00hm
EC B9/38/Change connection of SW2 (IBM EC)
EC B10/47/Reset signal change for TPM/TATER chip (IBM EC)
EC B11/54/Change PR274 to 6.2K, Change PC222 to No ASM (IBM EC)
EC B12/17,22/Divide USB OC# signal: Add R29, R55 (IBM EC)
EC B13/43/Delete CN11:MiniPCIE Latch (IBM EC)
EC B14/17/Change Planner ID to 0011: R96 to No ASM, R98 to ASM (IBM EC)
EC B15/47/Add C251 for VCC3M of AT8356908 (IBM EC)
EC B16/39/Change R399 to No ASM (IBM EC)
EC B17/29/Pull up on PCMACT(R208) from VCC3M to VCC3B (IBM EC)
EC B18/38/Change R590 to No ASM (IBM EC)
EC B19/30/Change EB2/EB3 to DLW21SN121SQ2L(IBM EC)
EC B20/38/Change R610 to No ASM, Add R615(for GPIO_BT) to Pull down (IBM EC)
EC B21/63/Change PQ20 to SI4890DY for VCC1R8VEDIO Drop issue.
EC B22/37/Add Docking connector shell tied to GND.(IBM EC)
EC B23/24/Add R81, R161 for MicTHDN improve.(IBM EC)
EC B24/57/Add PC245,PC246,PC247,PC248,PC249,PC250,PC251 but No ASM for VINT19.(IBM EC)
EC B25/38,50/Add PR77, PQ55 and connection for 65W Adapter.(IBM EC)
EC B26/62/Add PRT17 for PQ46(BAT_DRV FET). (IBM EC)
EC B27/52/Add PD67 but No ASM for PQ46 (IBM EC)
EC B28/53/Change PR134,PR138 to 6.04K 1% (IBM EC)
EC B29/50/Change PR30 to 220K 5% for Rush Protection.(IBM EC)
EC B30/24/Change R520 to 36K 5%, R512 to 16K 5% for DockMICInTHDH Improve.(IBM EC)
EC B31/20/Add C820,C821,C822 for VCC5B of HDD.(IBM EC)
EC B32/55/Add PR39 100K 5% but No ASM for PWM preparation to Skip mode.(IBM EC)
EC B33/42/Add R95,R127,R131,R157,R158,R215,R226,R252 of 00hm.(IBM EC)
EC B34/14/Change F4 to 0466003(0603 to 1206).
EC B35/51/According to minimum Trickle Charge condition spec. will be designed to "3.9V", So delete PD19,PD20,PD37,PD35 (IBM EC)
EC B36/11/Delete clock buffer and change damping resistor to 2.2 Ohm.
EC B37/07/Add C86=10U 10V for S-vedio flicker.
EC B38/11/Change R352 to 750hm for slew rate.
EC B39/49/Change table of G-sener.(IBM EC)
EC B40/19/Remove G2 short pad.(IBM EC)
EC B41/47/Add C252=0.01U for SES chip of RTCVCC.(IBM EC)
EC B42/50/Change PU6 From MAX8724 to MAX8724A.(IBM EC)
EC B43/16/Add C446=22pF for EMI 98.85Mhz and 196.72Mhz issue.
EC B44/05/Change R432 form 2550hm to 2490hm for REFSET of Alviso chip.(IBM EC)
EC B45/12/Delete U41 and related circuit.
EC B46/07/Add U42 for TVDAC voltage(Intel recommend circuit).
EC B47/36/WLAN LED EC for CX-2. (IBM EC)
EC B48/33/Change size of hole9 (IBM EC)
EC B49/16,20/Change net name and change R507 to 100K (IBM EC)
EC B50/48/Change R676 to NOASM for "not open collector" FAN.(IBM EC)
EC B51/56/Add PC252=0.47U for delay.
EC B52/32/Add Test point for ICT.
EC B53/28/Delete R584 and add R694 for leakage current (IBM EC).
EC B54/54/Delete PR272 and add PR145 for power sequence (IBM EC).



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Quanta Computer Inc.

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EC #/Page/Description/Part Affected/CMVC #

EC C1/20/Add 00hm but No ASM at pin 18 (IBM EC)
EC C2/56/Add DTC115EE for neck voltage.(IBM EC)
EC C3/16/Add R518 but No ASM for leakage voltage.(IBM EC)
EC C4/50/Change PQ13 form SI4840DY to SI4890DY(IBM EC)
EC C5/17/Change Planner ID to "0100".(IBM EC)
EC C6/02/ASM C843.(IBM EC)
EC C7/23/Add R701 but NO ASM for leakage voltage.(IBM EC)
EC C8/37/Delete CN25.
EC C9/56/Add PR232,PR238 to 10K for Intersil's recommand.
EC C10/37/Add U52 for ESD.
EC C11/38/Add U53 but NO ASM for ESD.
EC C12/38/Add R702,R703 for Amplifer select.(IBM EC)
EC C13/50/Change PR174 form 00hm to 100hm.(IBM EC)
EC C14/56/Modify footprint per SMT require cause has poor solder issue.
EC C15/56/Add PC253 but No ASM for EMI reserve.
EC C16/33/Add B31,B32,B33,B34 but No ASM for EMI reserve.
EC C17/35/Change Lan GND from pin3 to pin2..
EC C18/34/Add R324 for connect LAN GND and GND
EC C19/55/Add PD68 for VCC5M.
EC C20/35/Change Lan GND to GND for C497,C498.
EC C21/34/Add R339,R534,C844,C845,C846,C847 for LAN GND to GND.
EC C22/35/Change GND to AGND for CN26.1.
EC C23/33/Add B35,B36,B37,B38 for EMI.
EC C24/33/Change AGND to LED_GND for B17.
EC C25/35/Add C848,C849,C850,C851,C852,C853,C854,C855 for EMI reserve.
EC C26/38/Add R704,R705,R706,R707 for PS2 ESD issue.
EC C27/37/Add C856,C857,C858,C859 but NO ASM for PS2 ESD issue.
EC C28/35/Delete C848,C849,C850,C851,C852,C853,C854,C855.
EC C29/11/Change damping resistor value of 33M and 48M CLK.
EC C30/31/Connect thermal PAD of U13 to GND.
EC C31/47/Connect thermal PAD of U27 to GND.
EC C32/13/Delete R258(CMVC67933).
EC C33/37,38,39/Add R708-R720(varistor) but No ASM and delete C856-C859 for PS2 ESD issue.
EC C34/39/Delete R709,R710,R711 and change R708 connection to GND