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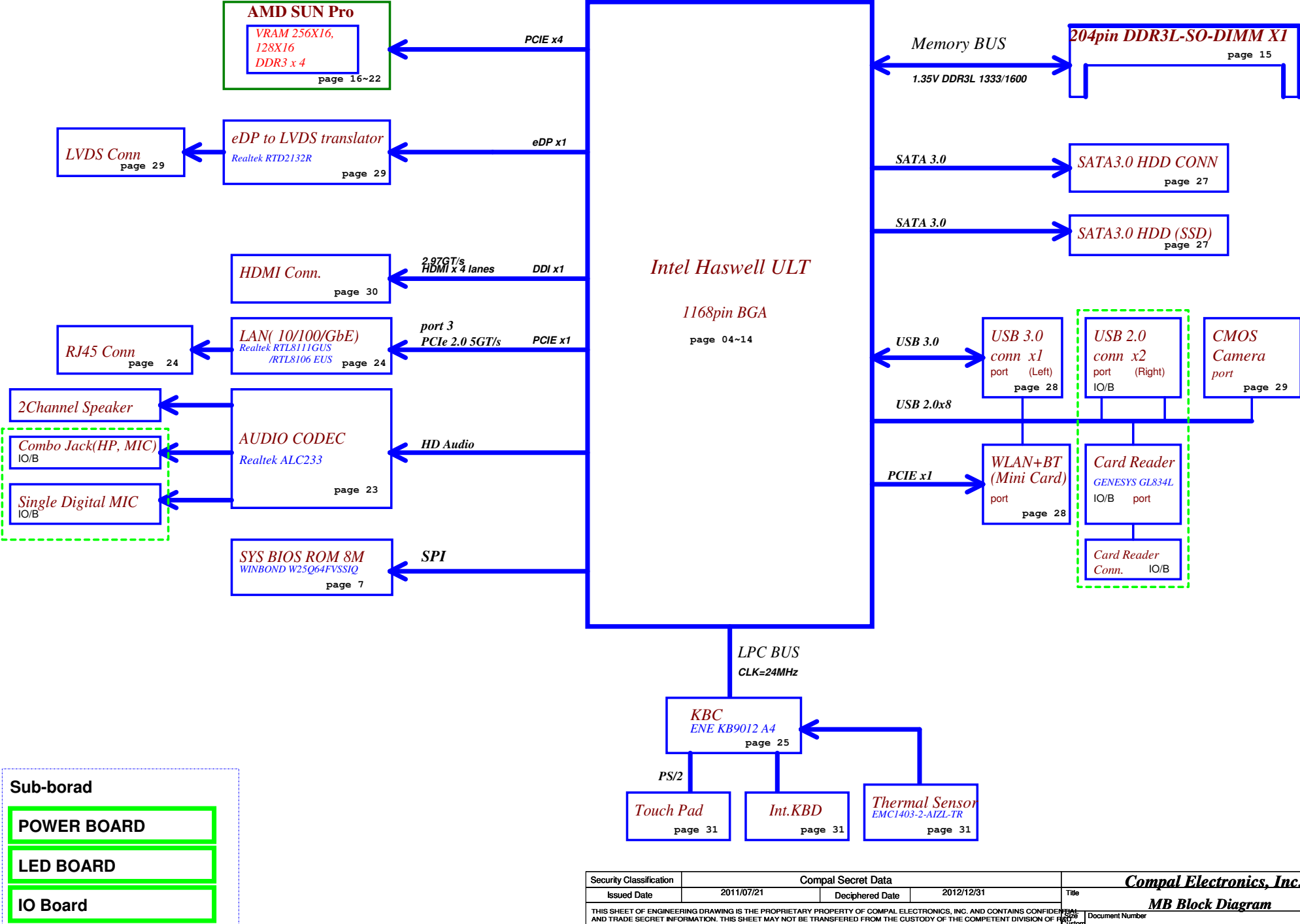
Model Name : ZIUS6/S7
File Name : LA-A321PR01
BOM P/N:4319OP38L5x -- ZIUS7
4319OP38L0x -- ZIUS6

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ZIUS6/S7 M/B Schematics Document
Intel SharyBay ULT Processor + AMD SUN Pro

2013-03-14
REV: 0.2

Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/06/24	Deciphered Date	2012/07/12	Title Cover Page
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			Date: Thursday, March 14, 2013	Sheet 1 of 46



Voltage Rails

power plane State	B+	+5VALW +3VALW	+1.35V	+5VS +3VS +1.5VS +1.05VS_VTT +CPU_CORE +0.675VS +VGA_CORE (PX) +0.95VS_VGA (PX) +3.3VS_VGA (PX) +1.8VS_VGA (PX) +1.5VS_VGA (PX)
S0	O	O	O	O
S3	O	O	O	X
S5 S4/AC	O	O	X	X
S5 S4/ Battery only	X	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X

STATE	SIGNAL	SLP_S0#	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON		HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON
S3 (Suspend to RAM)		LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)		LOW	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)		LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF

BOARD ID Table

Board ID	PCB Revision
0	
1	
2	0.2
3	0.1
4	
5	
6	
7	

BOM Structure Table

BTO Item	BOM Structure
Connector	ME@
45 LEVEL	45@
Unpop	@
CPU OPTION	CPU1@
INTEL UMA	UMA@
AMD SUN Pro	PX@
VRAM Option	S1G@ M1G@ H1G@ S2G@ M2G@
LAN RTL8111GUS	8111@ SWR@
LAN RTL8106EUS	8106@ SWR@
USB3.0	USB3@
EMI PART	EMI@
ESD PART	ESD@

EC SM Bus1 address

Device	Address
Smart Battery Charger	

EC SM Bus2 address

Device	Address
Thermal Sensor EMC1403-2-AI2L-TR Panel - eDP to LVDS translator SharkBay ULT AMD SUN Pro	1001_101xb

CPU SM Bus address

Device	Address
DDR DIMM0 WALN Touch Pad	0XA0

CPU SML0 Bus address

Device	Address
--------	---------

USB 2.0 Port Table

Port	3 External USB Port
0	USB 2.0 Port (I/O Board)
1	USB 3.0/2.0 Port (MB)
2	USB 2.0 Port (I/O Board)
3	Card Reader
4	Touch Screen (reserve)
5	Camera
6	Mini Card (WLAN/BT)
7	

USB 3.0 Port Table

Port	
1	
2	USB 3.0 Port (MB)
3	
4	

PCIe Port Table

Port	Lane	
1		
2		LAN
3		WLAN
4		
5	0	
	1	
	2	GPU
	3	
6	0	
	1	
	2	
	3	

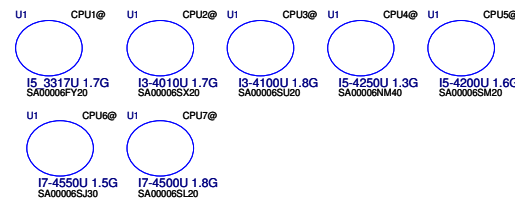
SATA Port Table

Port	
3	
2	
1	mSATA SSD
0	HDD

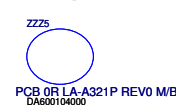
SMBUS Control Table

	HOST	VGA	BATT	KB9012	SODIMM	WLAN	PANEL sensor ADM1032	Thermal sensor EMC1403	VGA sensor ADM1032	CPU Sensor	Touch Pad	Security ROM	NFC
EC_SMB_CK1 EC_SMB_DA1	KB9012 +3VLP	X	+3VLP	X	X	X	X	X	X	X	X	X	X
EC_SMB_CK2 EC_SMB_DA2	KB9012 +3VS	+3VS	X	X	X	X	+3VS	+3VS	+3VS	+3VALW	X	X	X
SMBCLK SMBDATA	CPU +3VALW	X	X	X	+3VS	+3VS	X	X	X	X	+3VS	+3VS	+3VS
SMLCLK SML0DATA	CPU +3VALW	X	X	X	X	X	X	X	X	X	X	X	X
SML1CLK SML1DATA	KB9012 +3VS	X	X	+3VS	X	X	X	X	X	+3VALW	X	X	X

CPU part



PCB part

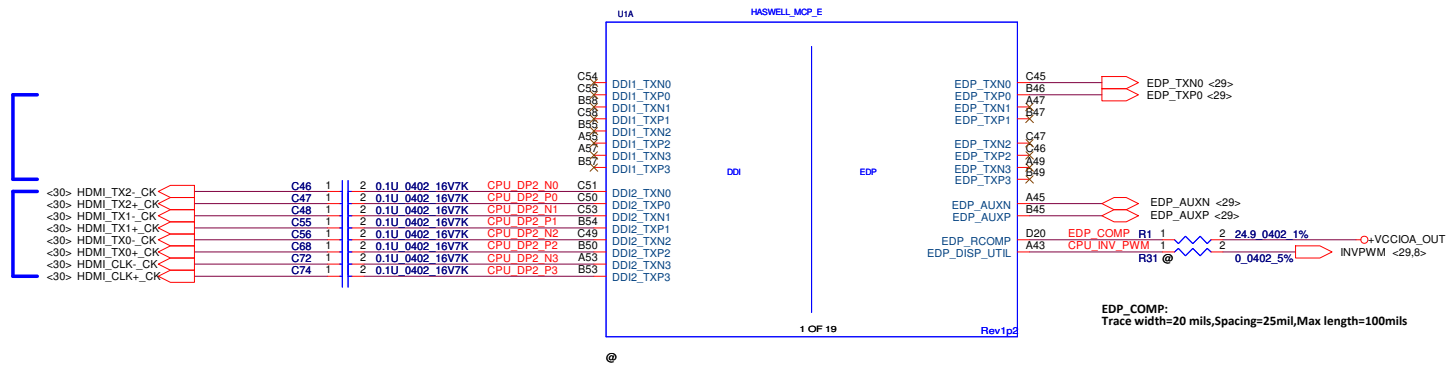


VRAM

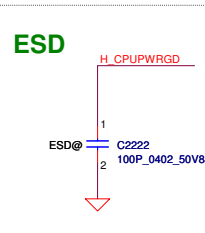
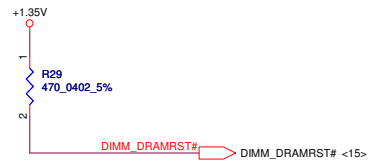
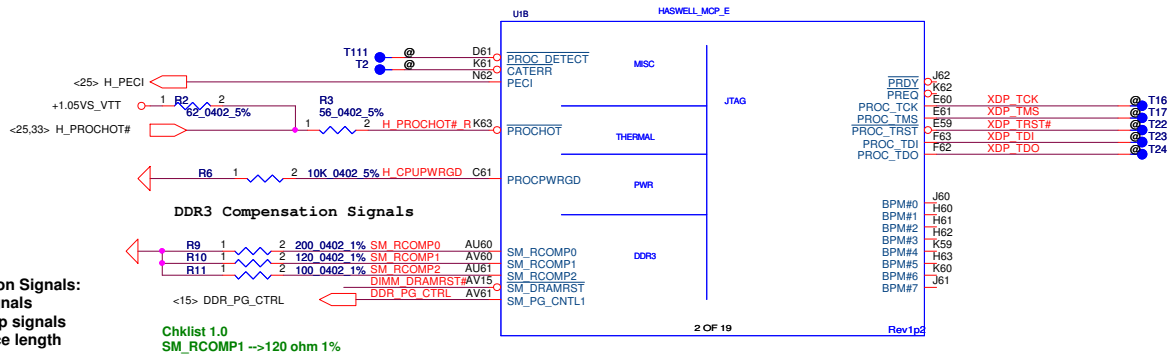


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				Date: Thursday, March 14, 2013	LA-A321P R02
				Sheet 3 of 46	Rev 0.2

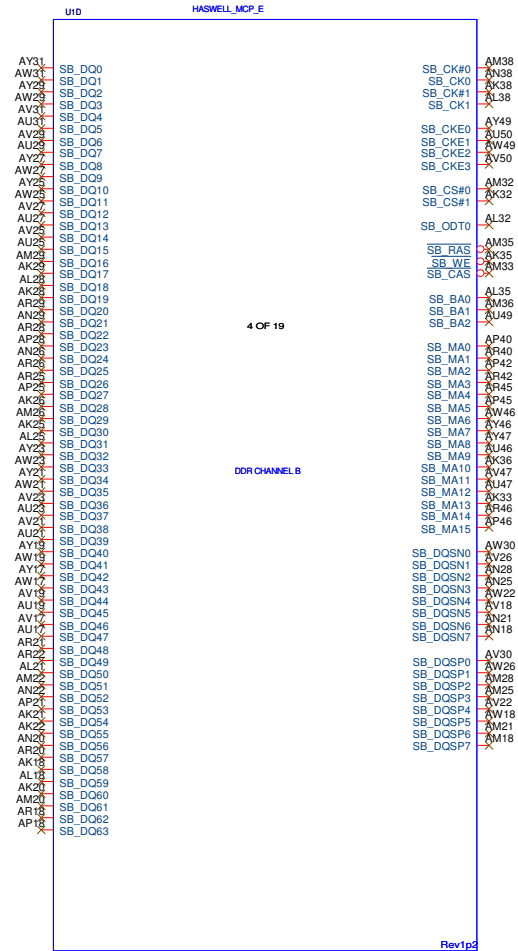
CRT
HDMI

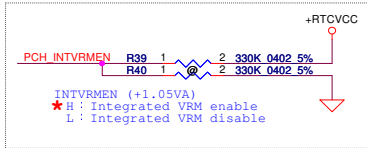
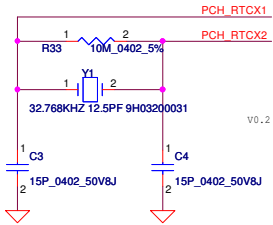


DDR3 Compensation Signals:
20mils to comp signals
25mils to non-comp signals
500mil for Max trace length

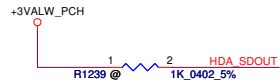
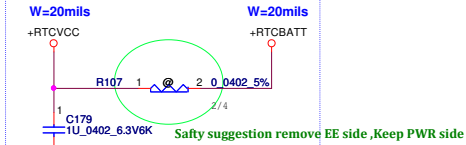


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						LA-A321PR02		0.2			
						Date: Thursday, March 14, 2013		Sheet 4 of 46			



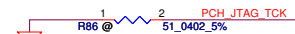


RTC Battery

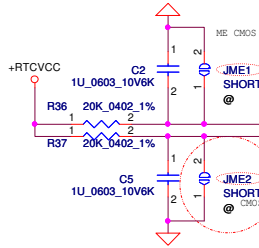


HDA_SDOUT
ME debug mode, this signal has a weak internal PD
* Low = Disabled (Default)
High = Enabled [Flash Descriptor Security Override]

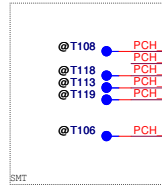
9/28



JME2 Short PAD placement to Bottom side.



<23> HDA_SDIN0

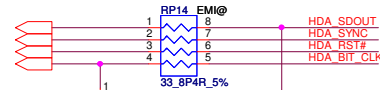


<23> HDA_SDOUT_AUDIO
<23> HDA_SYNC_AUDIO
<23> HDA_RST_AUDIO#
<23> HDA_BITCLK_AUDIO

RF

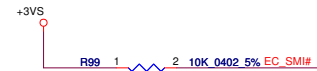
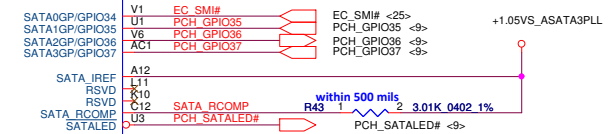
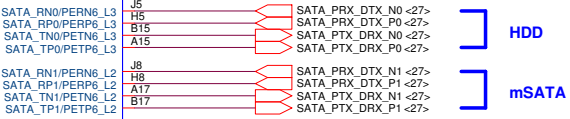
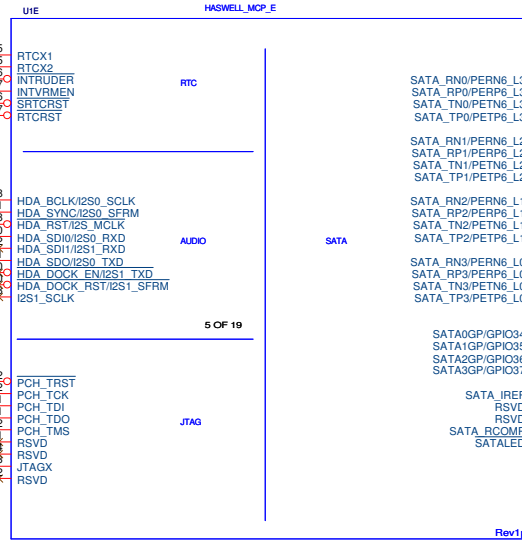
<25> ME_FLASH

EMI



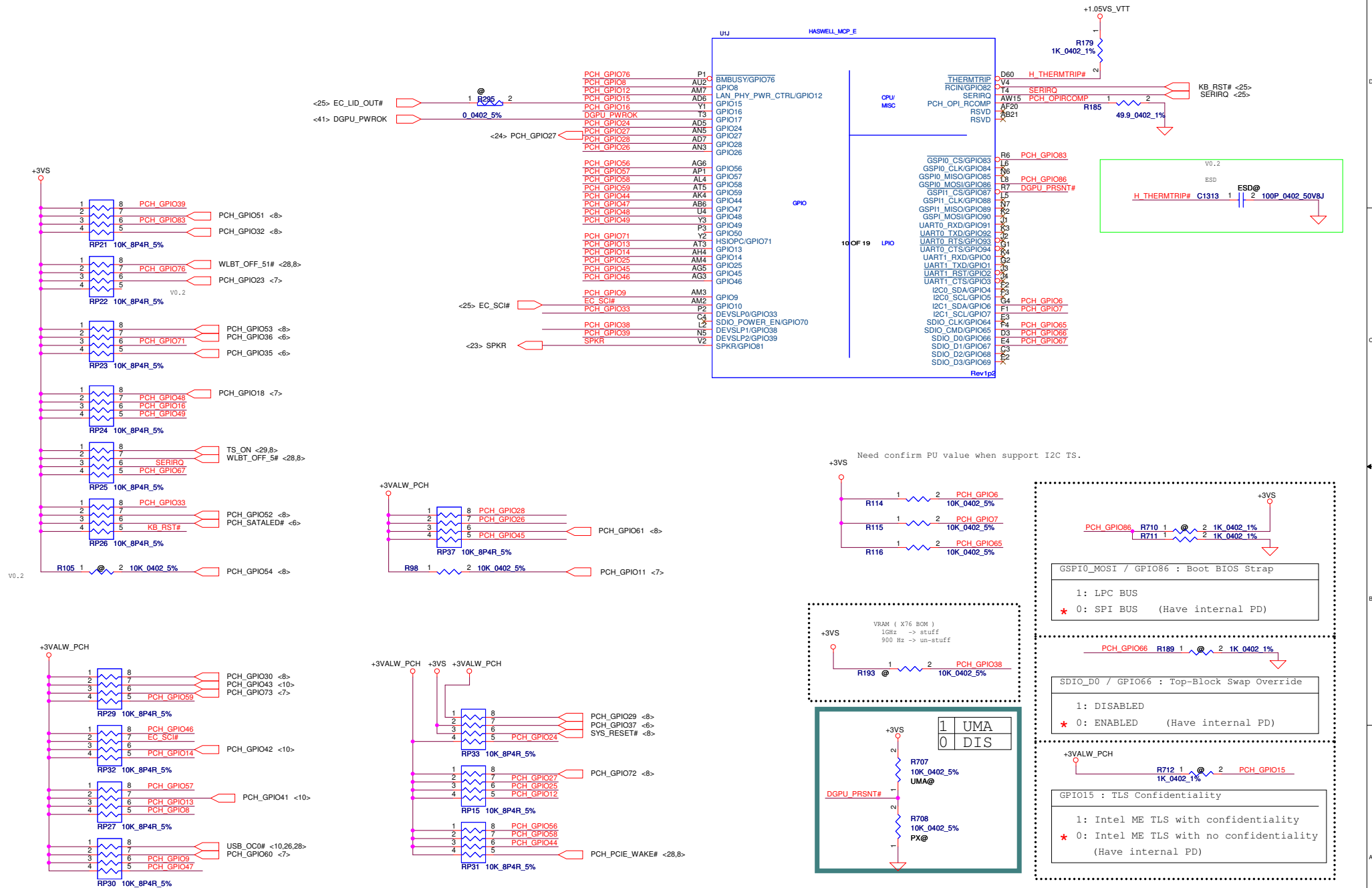
RF

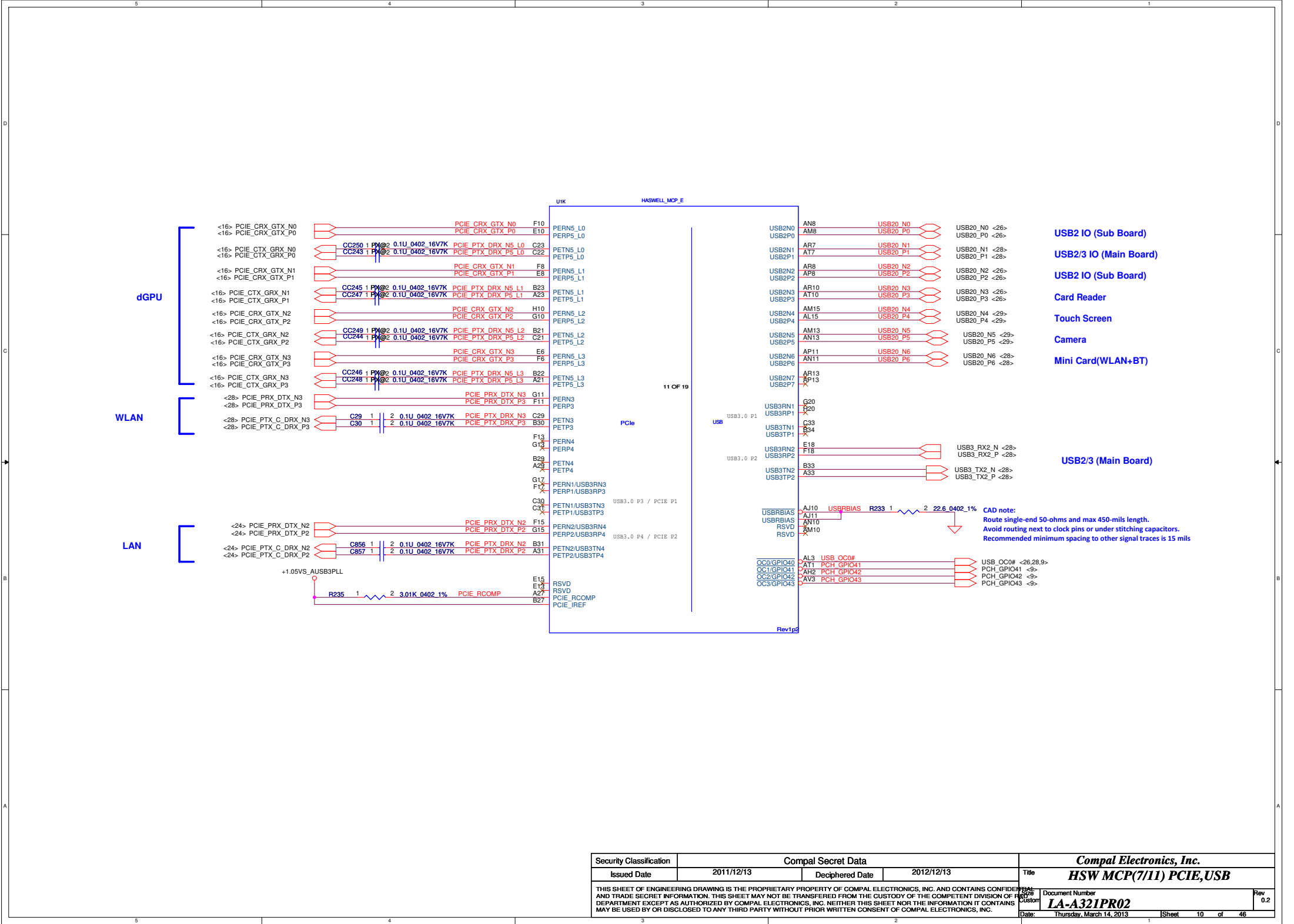
<25> ME_FLASH



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										Customer		LA-A321PR02		0.2	
										Date:		Thursday, March 14, 2013		Sheet 6 of 46	

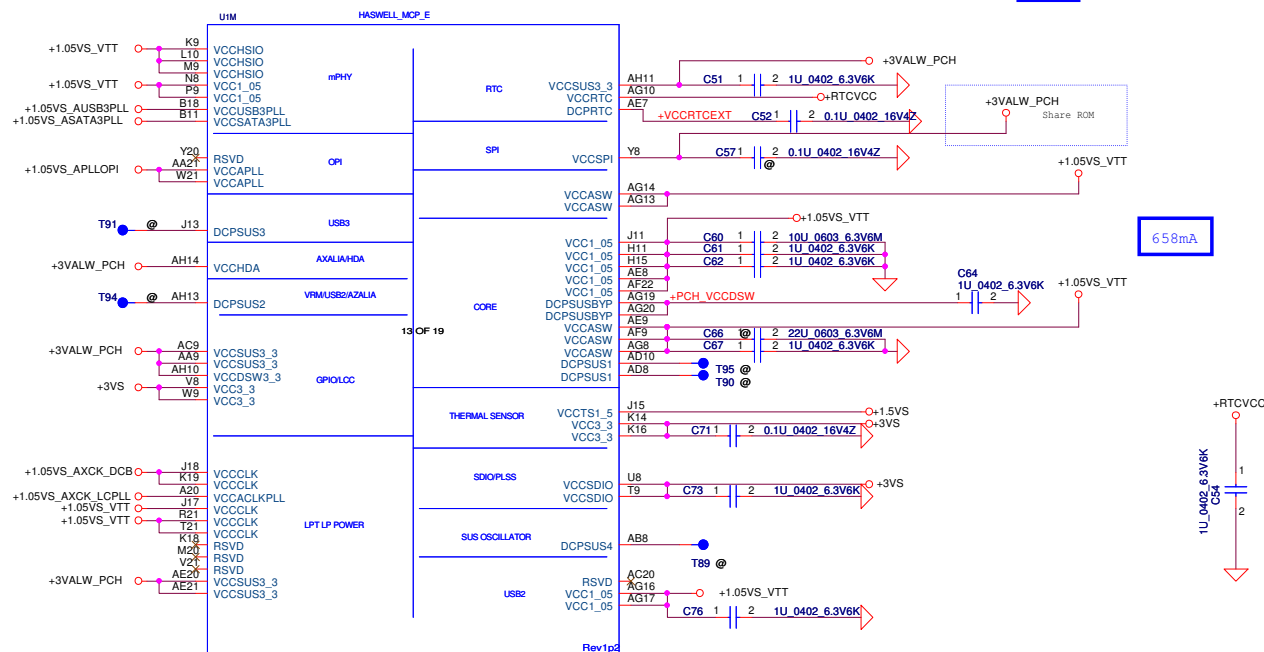
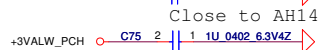
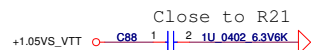
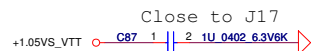
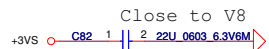
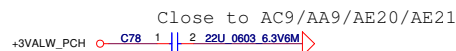
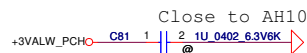
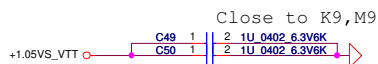
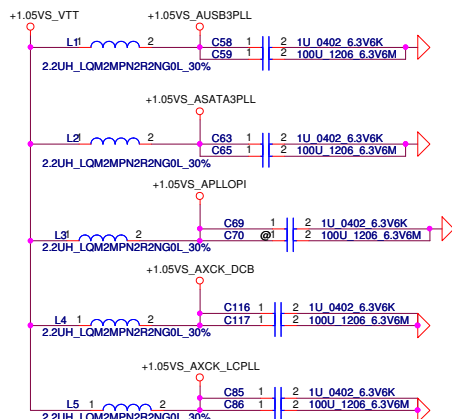
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				Date: Thursday, March 14, 2013	Sheet 8 of 46



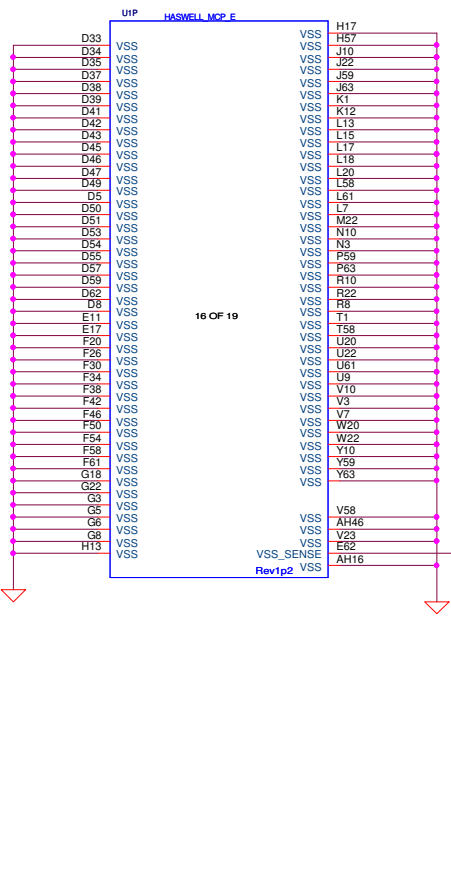
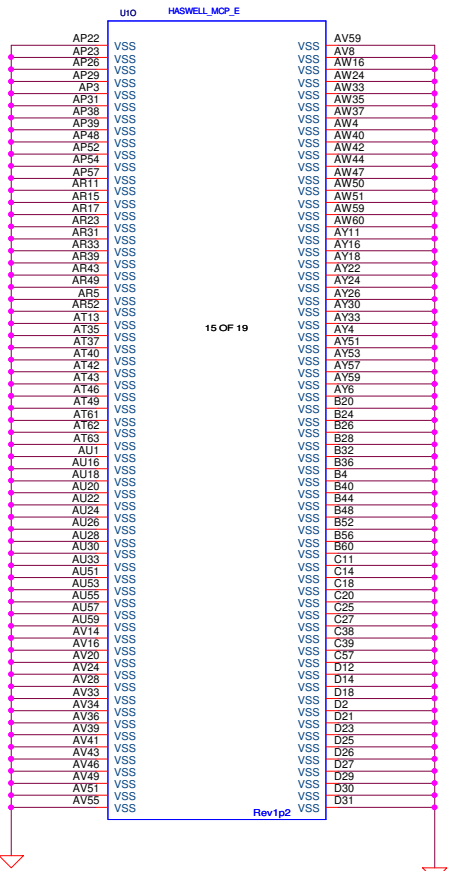
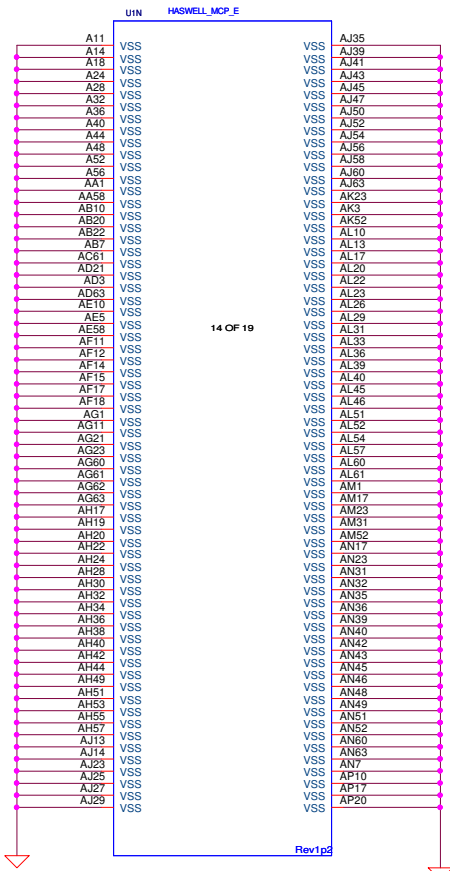


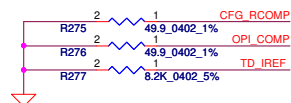
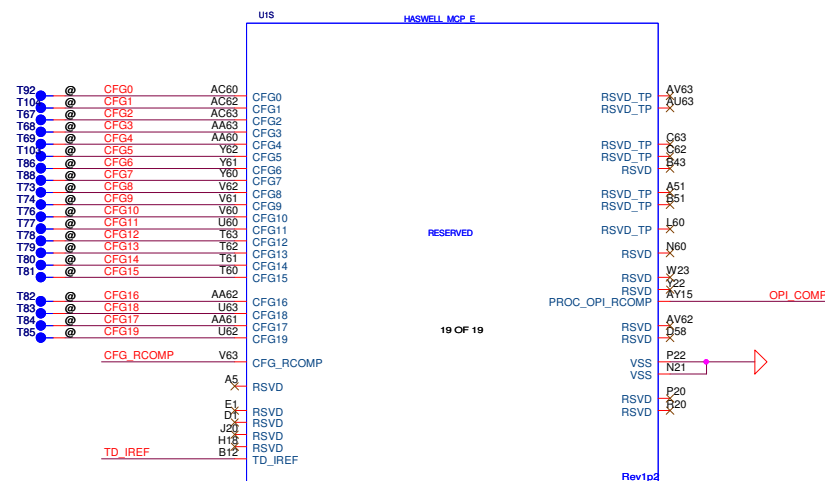
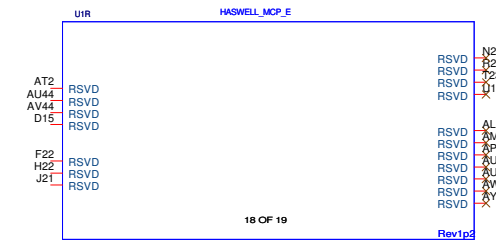
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						Doc No		Document Number		Rev	
						Custom		LA-A321PR02		0.2	
						Date		Thursday, March 14, 2013		Sheet 10 of 46	

Check Power Source Close to N8
+1.05VS_VTT C53 @ 2 1U 0402 6.3V6K

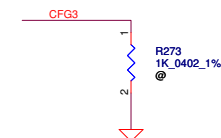


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				Customer	LA-A321PR02	0.2
				Date:	Thursday, March 14, 2013	Sheet 12 of 46

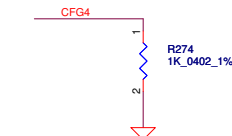




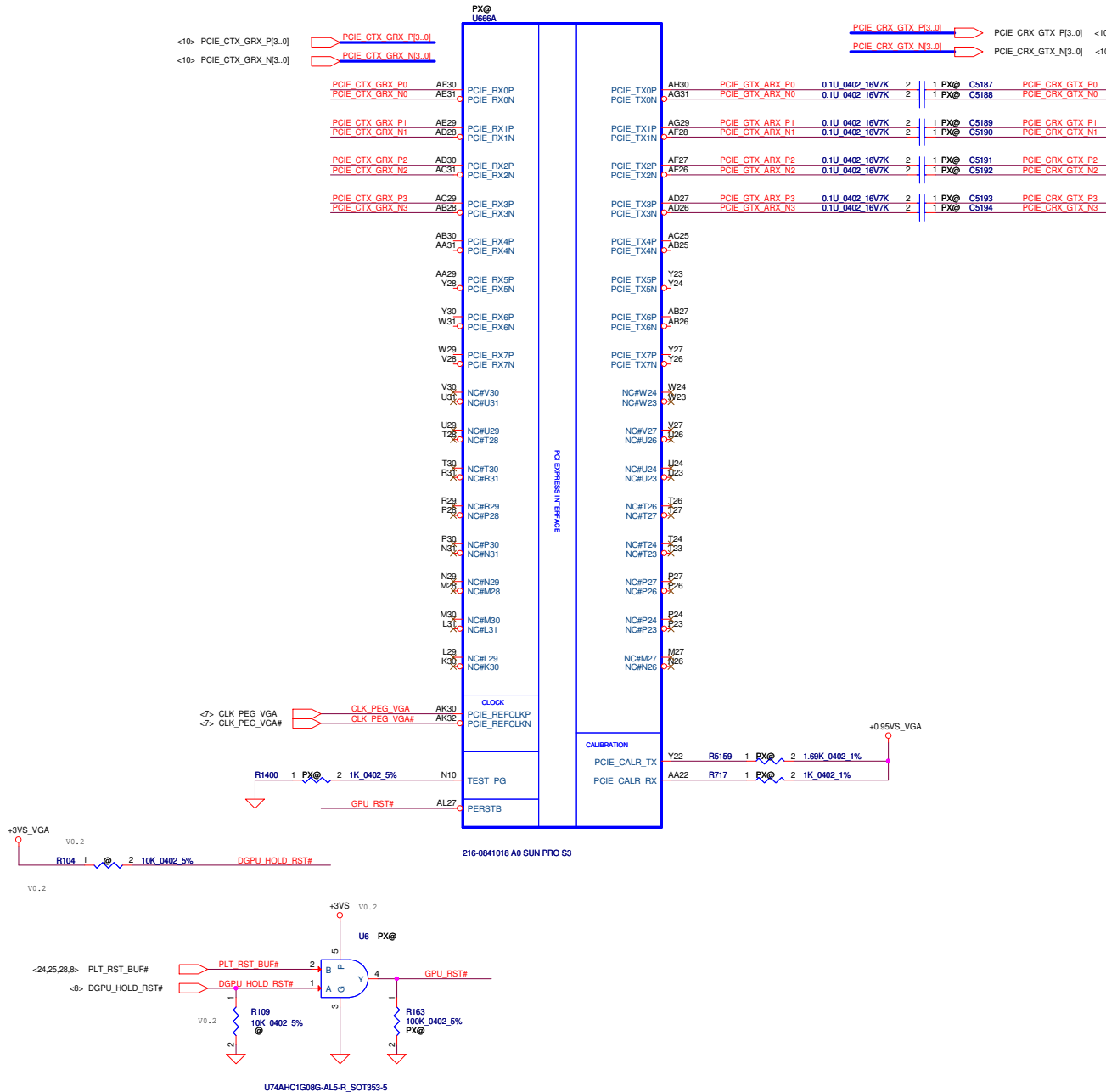
CFG Straps for Processor



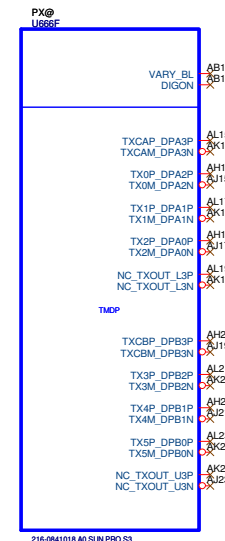
Physical Debug Enable (DFX Privacy)	
CFG3	1: DISABLED 0: ENABLED; SET DFX ENABLED BIT IN DEBUG INTERFACE MSR



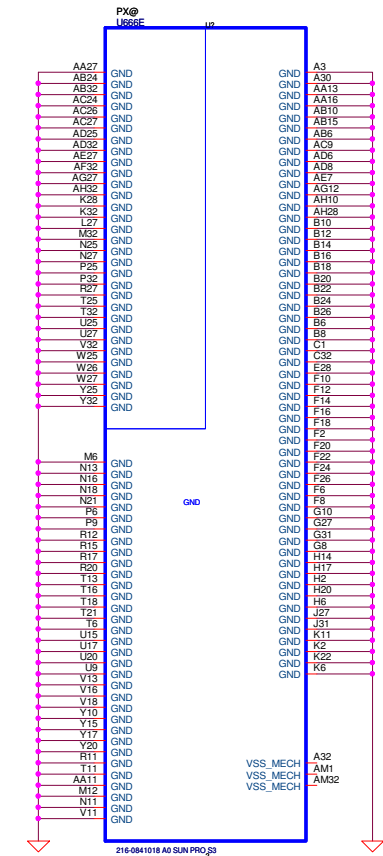
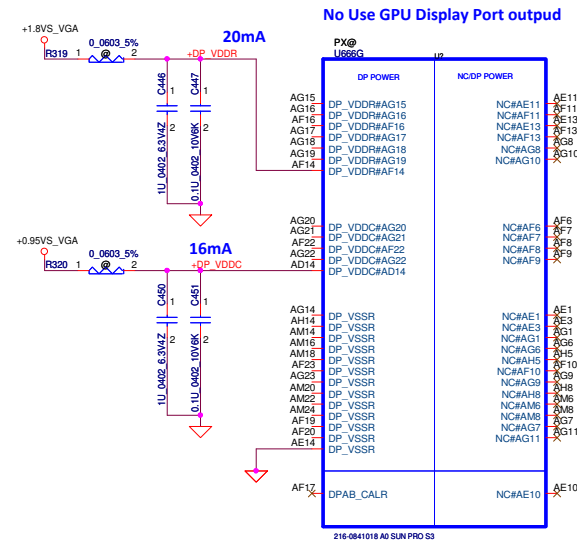
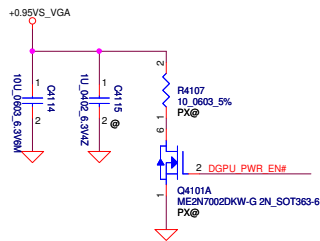
Display Port Presence Strap	
CFG4	1 : Disabled; No Physical Display Port attached to Embedded Display Port 0 : Enabled; An external Display Port device is connected to the Embedded Display Port



No Use GPU Display Port output



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Date: Thursday, March 14, 2013				Sheet 16	of 46



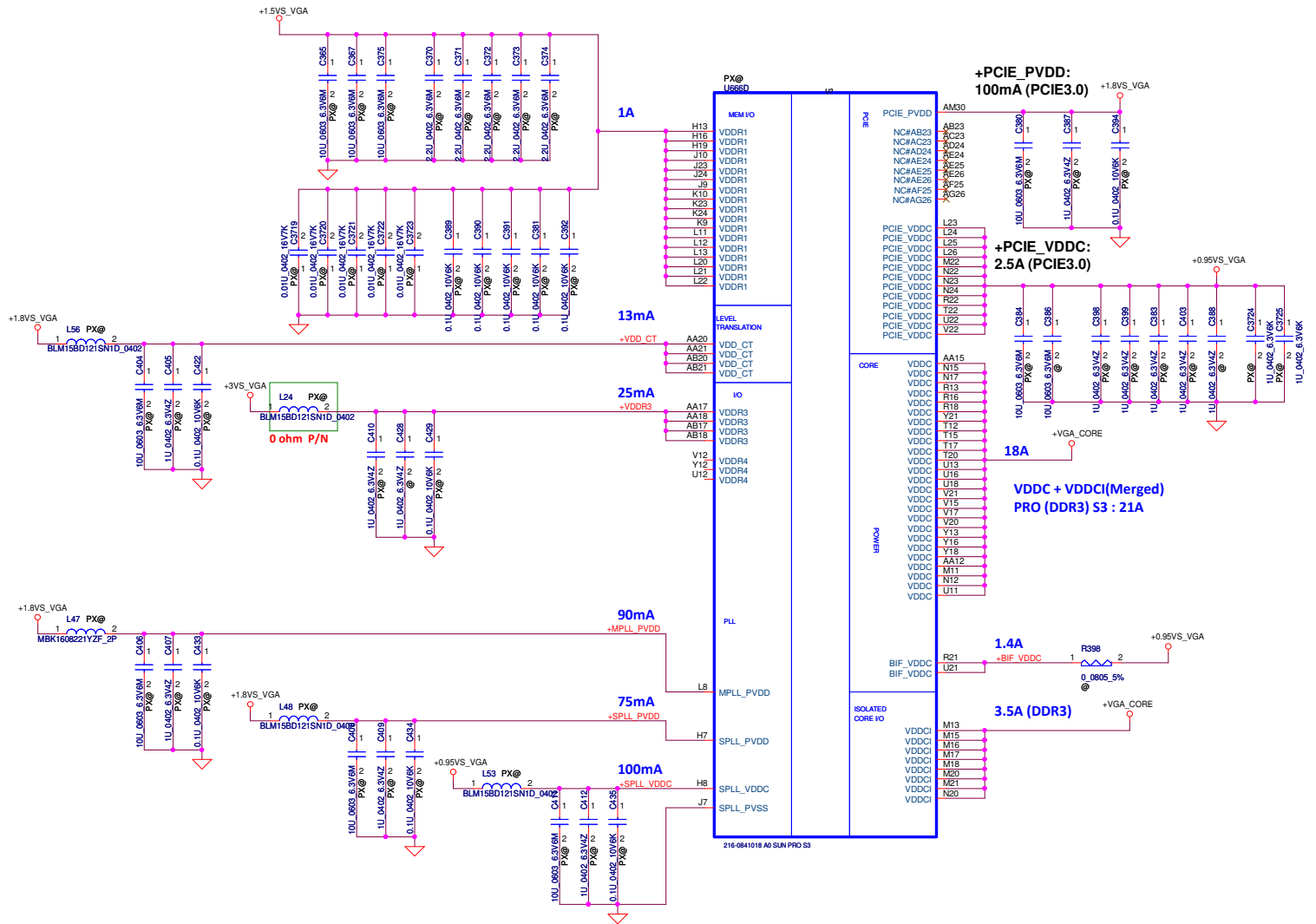
+VGA_CORE	10uF	1uF	0.1uF
VDDC	TBD	5 (1@)	10 (2@)
VDDCI	3.5A	1	3

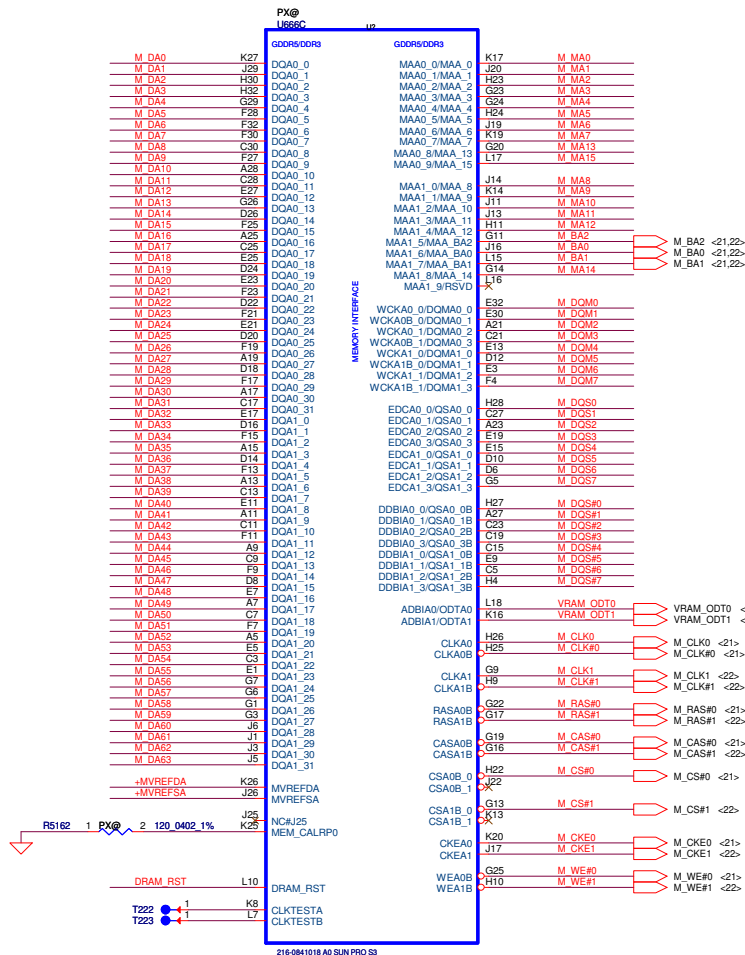
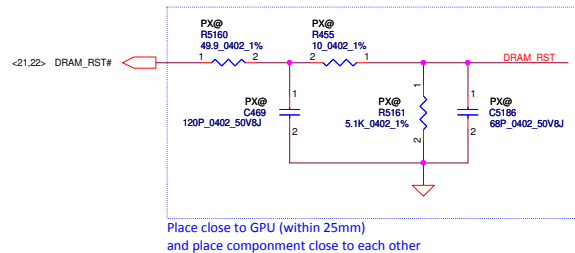
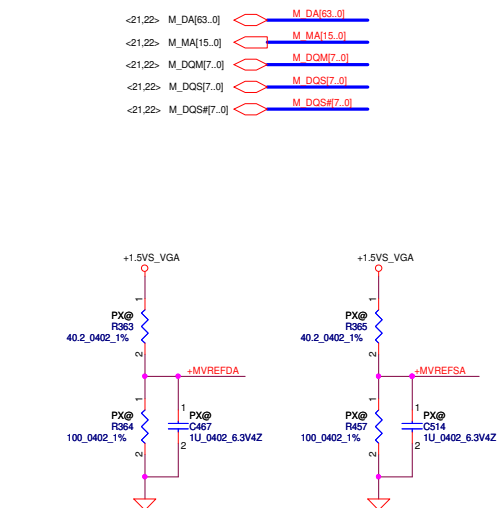
+0.95VS_VGA	10uF	1uF	0.1uF
PCIE_VDDC	2.5A	2 (1@)	5 (1@)
BIF_VDDC	1.4A	0	0
SPLL_VDDC	100mA	1	1

+1.5VS_VGA	10uF	1uF	0.1uF
VDDR1	1.5A	3	5

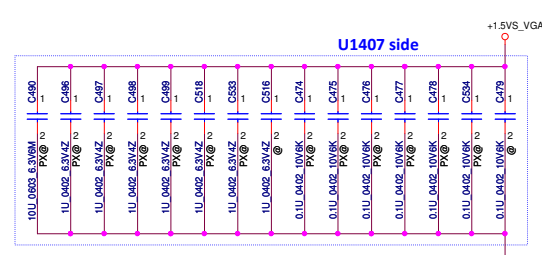
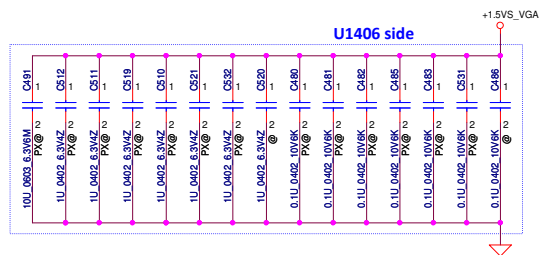
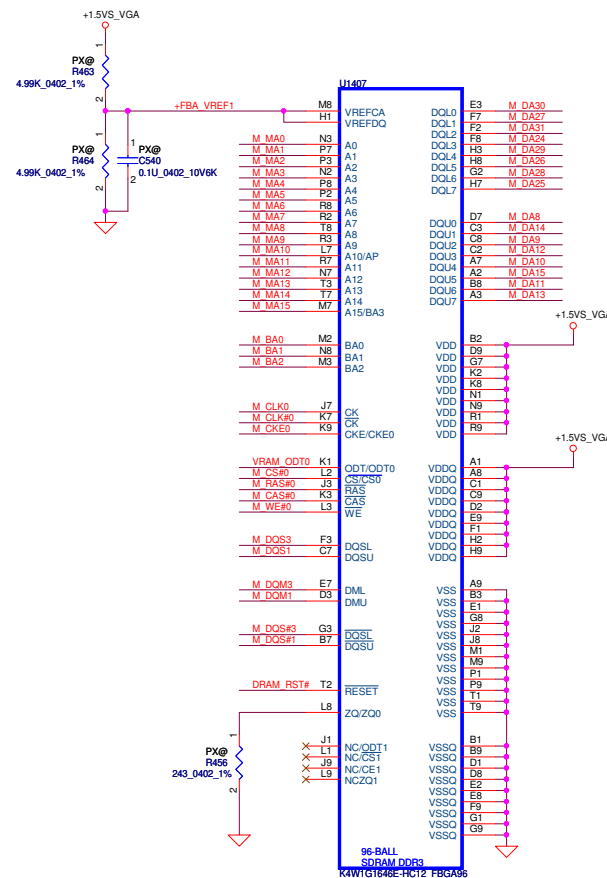
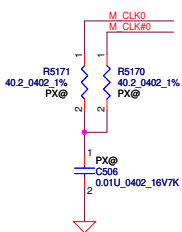
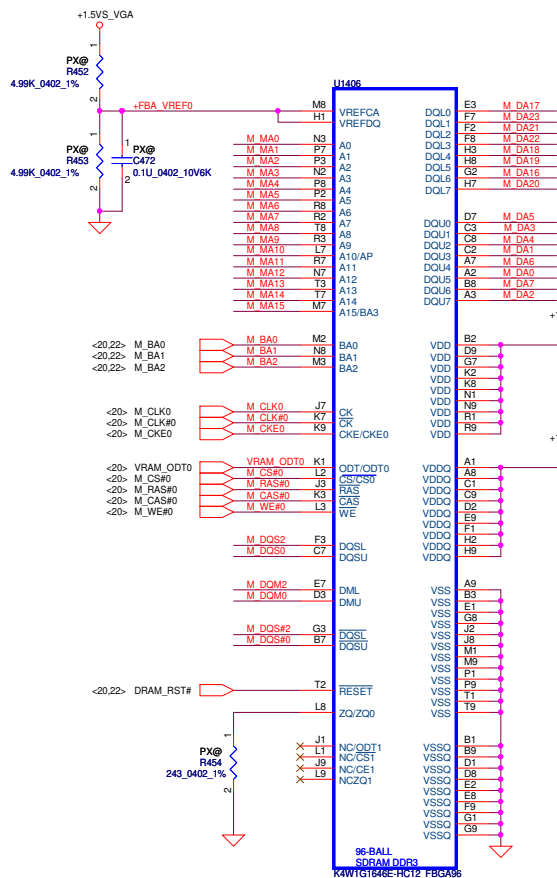
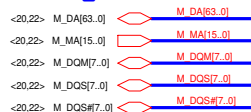
+1.8VS_VGA	10uF	1uF	0.1uF
PCIE_PVDD	100mA	1	1
MPLL_PVDD	130mA	1	1
SPLL_PVDD	75mA	1	1
VDDR4	(300mA)	0	0
VDD_CT	13mA	1	1
+TSVDD	13mA	1	1
+DP_VDDR	0	0	0
+DP_VDDC	0	0	0

+3VS_VGA	10uF	1uF	0.1uF
VDDR3	25mA	0	2 (1@)



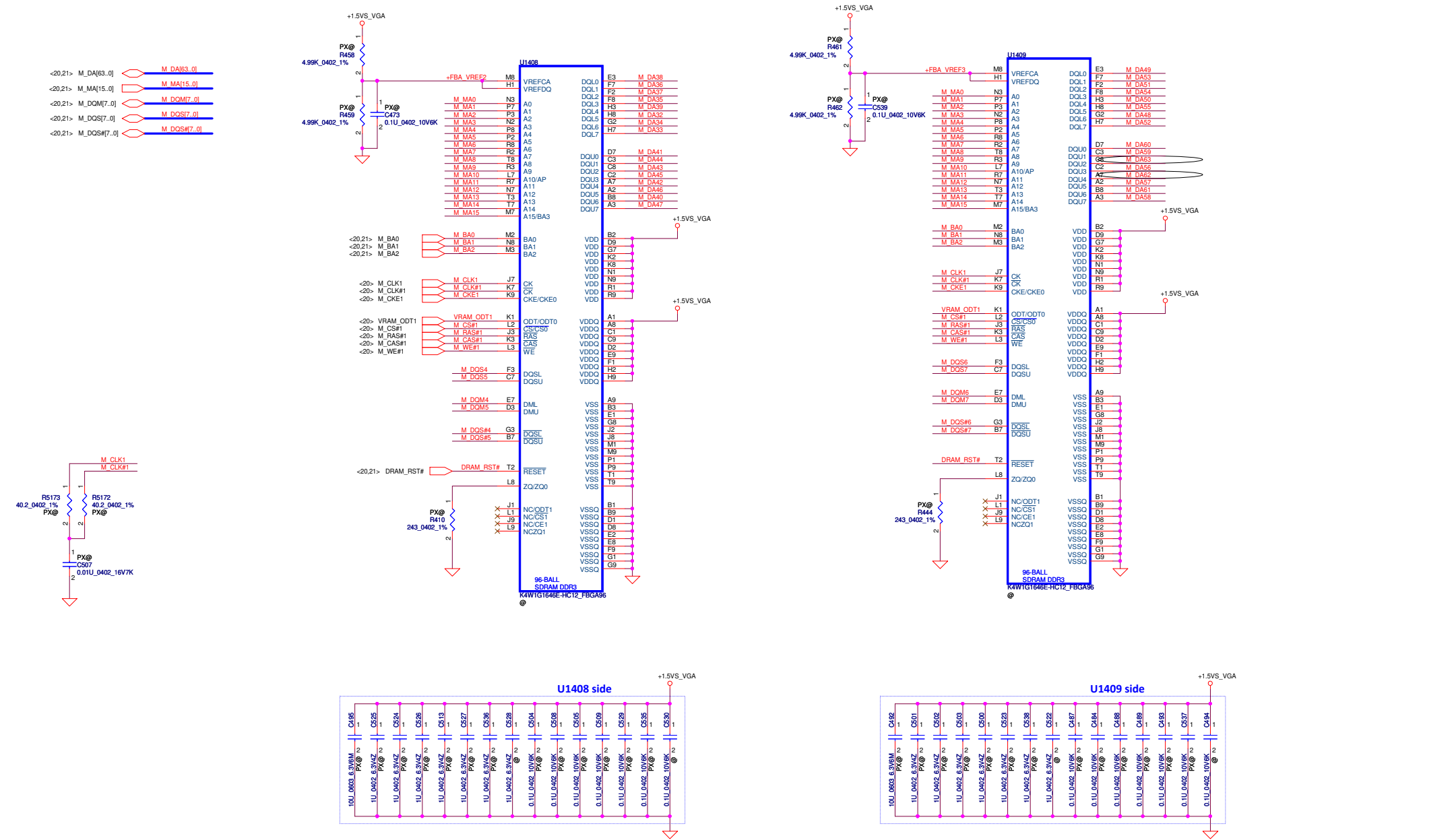


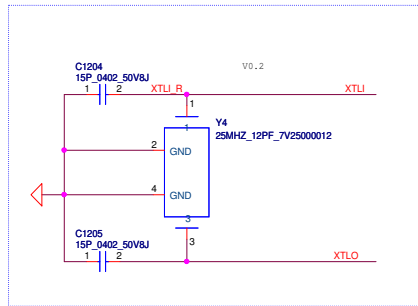
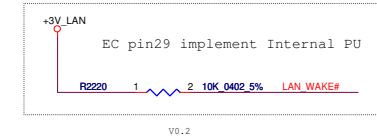
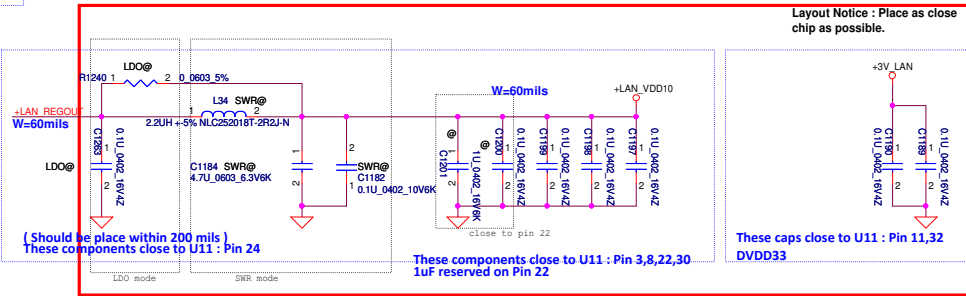
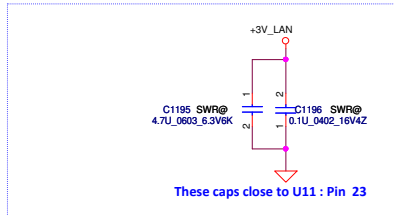
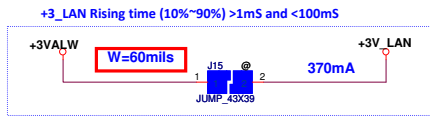
Memory Partition A - Lower 32 bits



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				SUN VRAM A Lower		
				Size	Document Number	Rev
				Custom	LA-A321PR02	02
Date:				Thursday, March 14, 2013	Sheet	21 of 46

Memory Partition A - Upper 32 bits



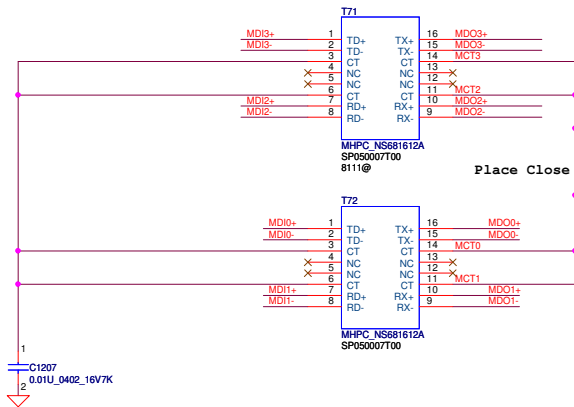
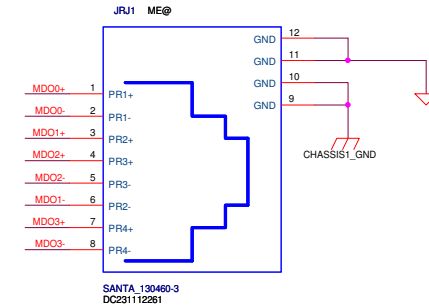
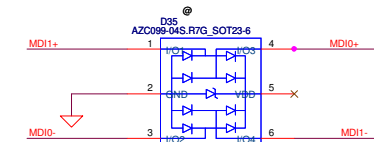
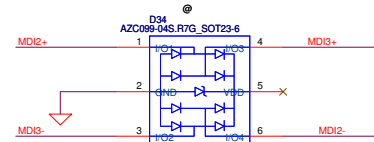
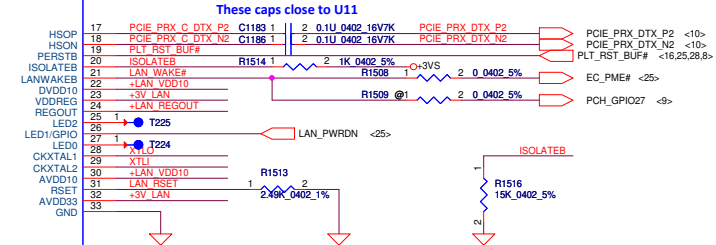


<7> LAN_CLKREQ#
<10> PCIE_PTX_C_DRX_P2
<10> PCIE_PTX_C_DRX_N2
<7> CLK_PCIE_LAN
<7> CLK_PCIE_LAN#

MDIO+ 1 MDIP0
MDIO- 2 MDIN0
+LAN_VDD10 3 AVDD10
MDI1+ 4 MDIP1
MDI1- 5 MDIN1
MDI2+ 6 MDIP2
MDI2- 7 MDIN2
+LAN_VDD10 8 AVDD10
MDI3+ 9 MDIP3
MDI3- 10 MDIN3
+3V_LAN 11 REFCLK_P
LAN_CLKREQ# 12 REFCLK_N

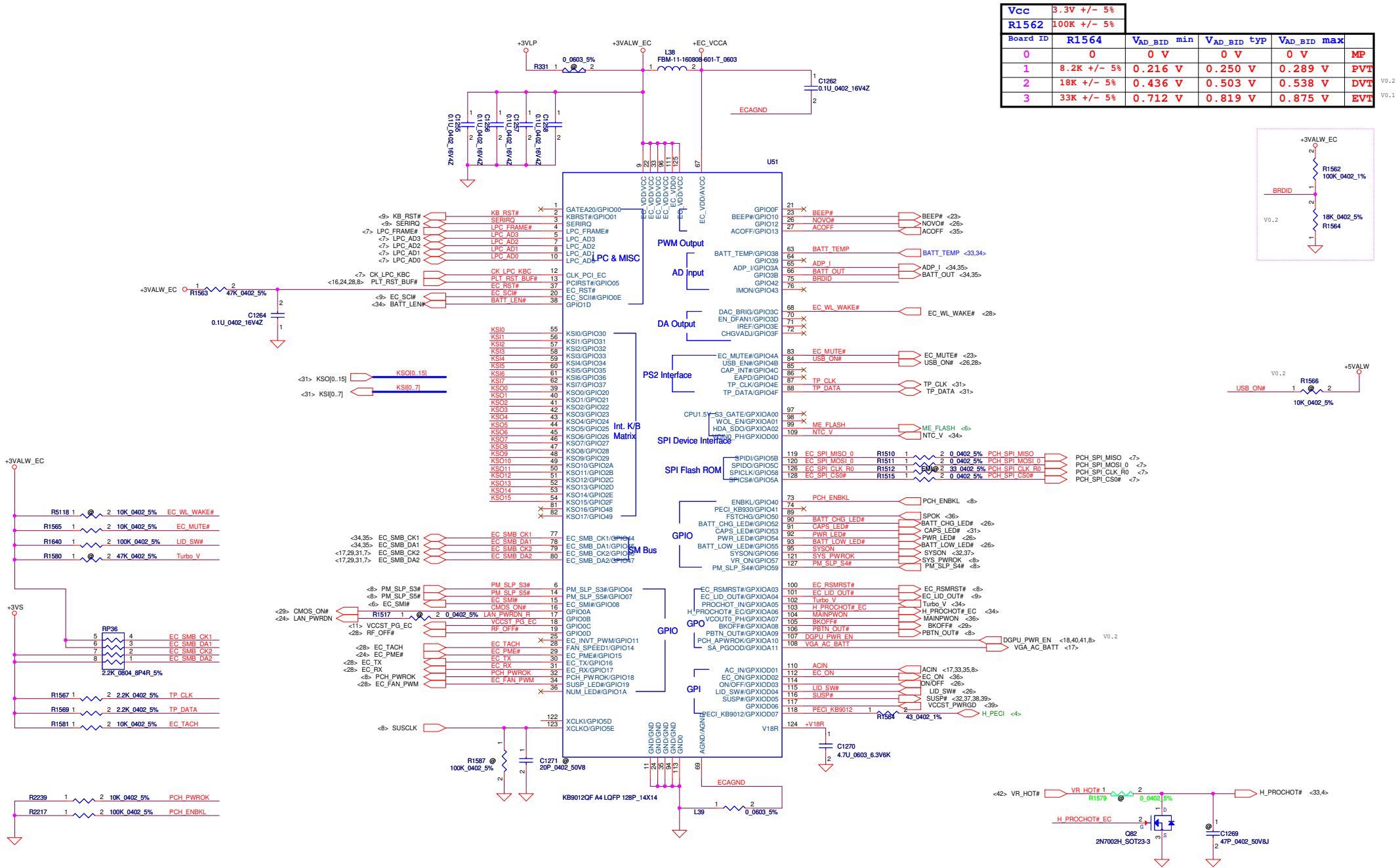
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RTL8106EUS
SA00006ML00

U11
RTL8111GUS-QG_QFN32_4X4
SA00006ML00

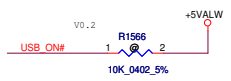
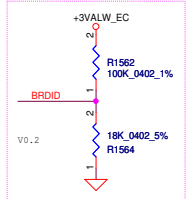


D34/D35
1'S PN:SC300001G00
2'S PN:SC300002E00

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Size		Document Number		LA-A321PR02	
Date:		Tuesday, March 19, 2013		Sheet 24 of 46	



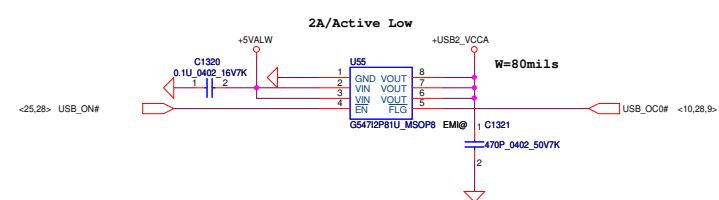
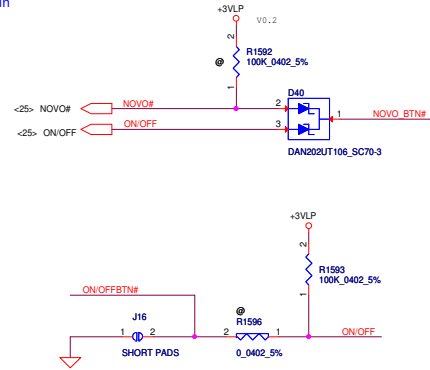
Vcc	3.3V +/- 5%
R1562	100K +/- 5%
Board ID	R1564
VAD_BID min	0 V
VAD_BID typ	0 V
VAD_BID max	0 V
0	0
1	8.2K +/- 5%
2	18K +/- 5%
3	33K +/- 5%
MP	PVT
DVT	EVT



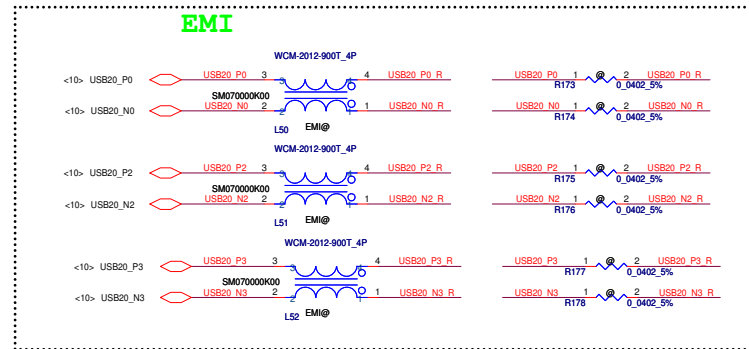
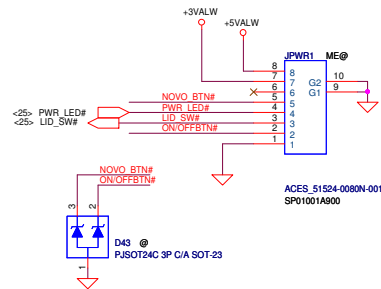
Security Classification	Compal Secret Data	Title	Compal Electronics, Inc.
Issued Date	2011/06/15	Deciphered Date	2012/07/11
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Custom	LA-A321PR02	Rev	0.2
Date	Thursday, March 14, 2013	Sheet	25 of 46

I/O Board CONN.

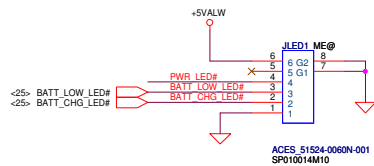
Connector: 0.3A / pin



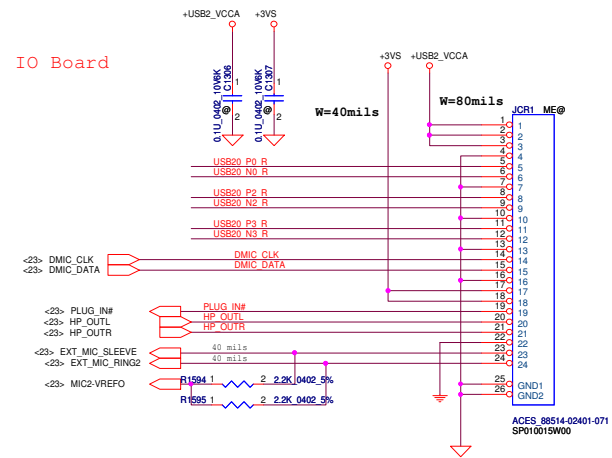
Power Board



LED Board

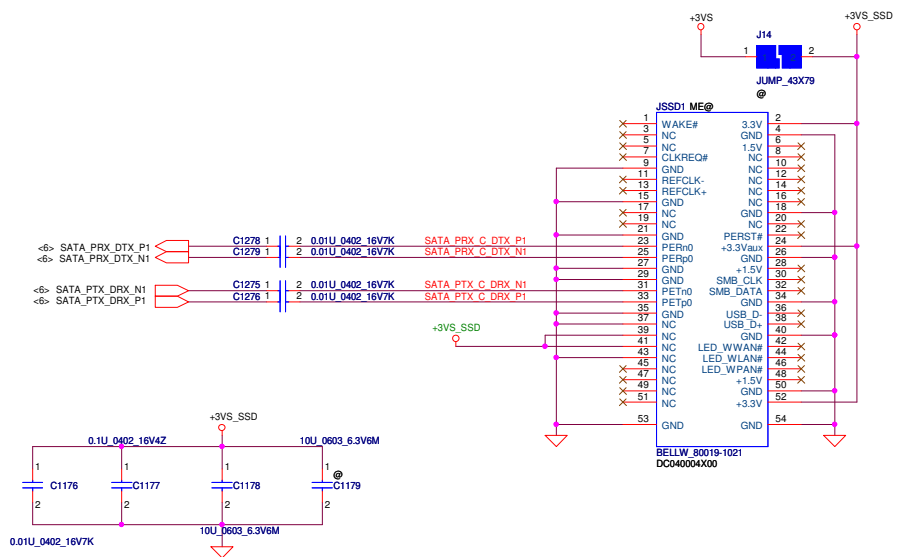


IO Board

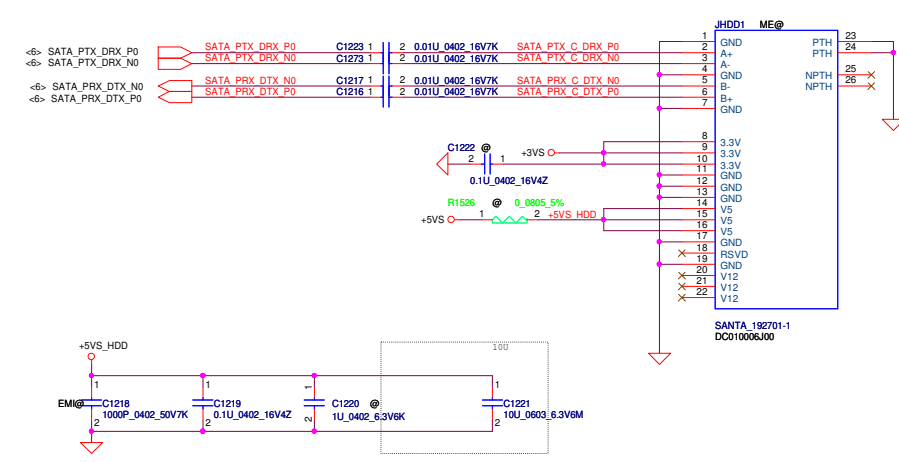


Security Classification		Compal Secret Data		Title	
Issued Date	2011/06/15	Deciphered Date	2012/07/11	Size	Document Number
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Date: Thursday, March 14, 2013				Sheet	26 of 46

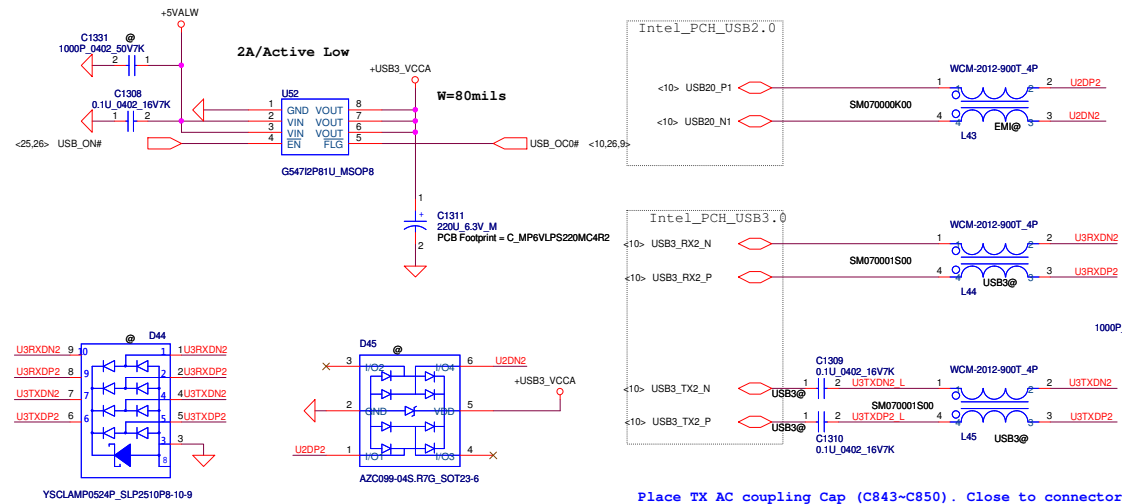
mSATA CONN.



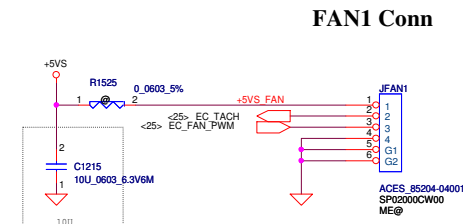
SATA HDD CONN.



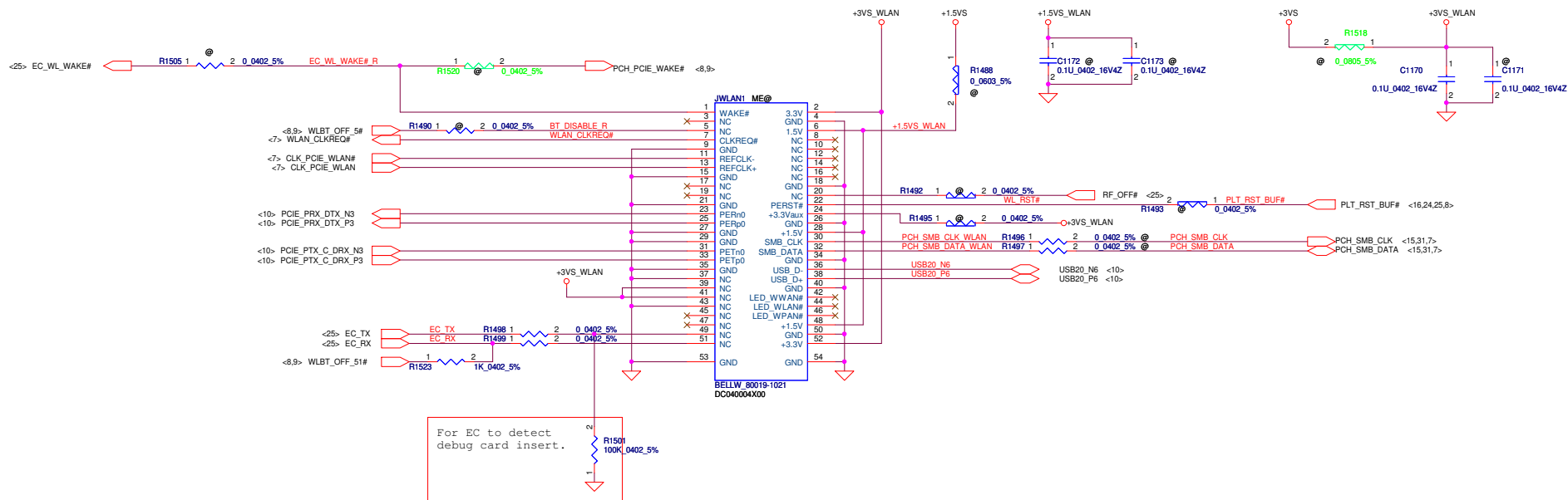
USB 3.0 Charger & Conn.



FAN CONN.



Mini-Express Card for WLAN(Half)



Note: The real behavior of BT_DISABLE are
BT_DISABLE=LOW, BT=OFF
BT_DISABLE=HIGH, BT=ON

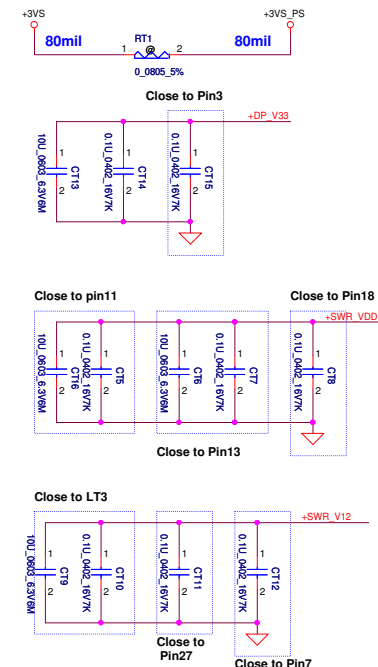
Security Classification		Compal Secret Data		Title	
Issued Date	2011/07/21	Deciphered Date	2012/12/31	Mini-Card	
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				LA-A321PR02	
				Date:	Thursday, March 14, 2013
				Sheet	28 of 46

The schematic diagram illustrates the electrical connections between the RT122R-VE-G0 and the RTD2132S. Key components and connections include:

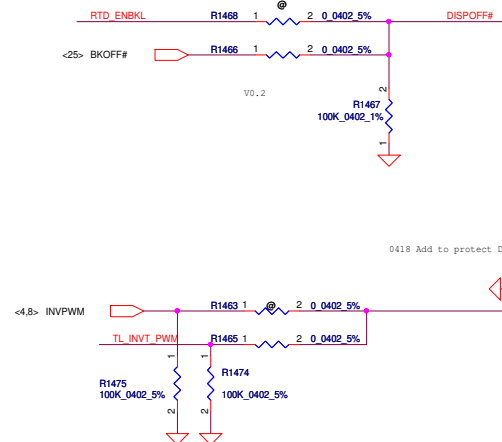
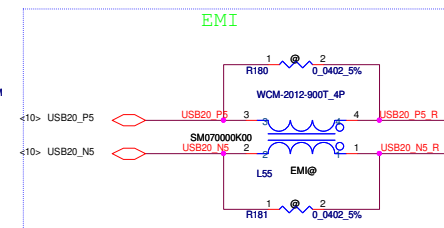
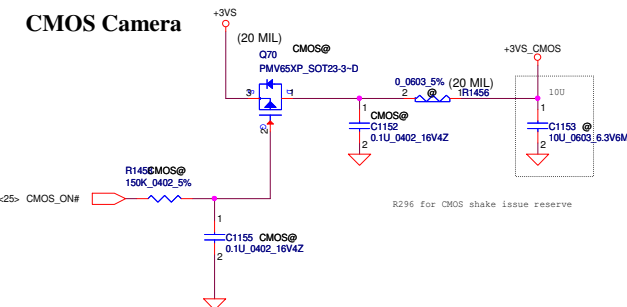
- Power Supply:** +SWR_V12, +SWR_V12, +SWR_V12.
- Signal Connections:** EDP_AUXP, EDP_AUXN, EDP_TXP0, EDP_TXN0, EDP_HPDI.
- Capacitors:** R1473 (100K_0402_5%), R1560 (1K_0402_5%).
- Resistors:** R1473 (100K_0402_5%), R1560 (1K_0402_5%).
- Integrated Circuits:** RT122R-VE-G0, RTD2132S.
- Other Components:** FBMA-L11-201209-221LMA30T, FBMA-L11-201209-221LMA30T, 4.7UH_P0301B-4R7MS_1.1A_20%, 40mil, 60mil, 20mil, 60mil.

MIIC_SDA MIIC_SCL	0	1
0	X	EC CODE
1	Internal ROM	EEPROM

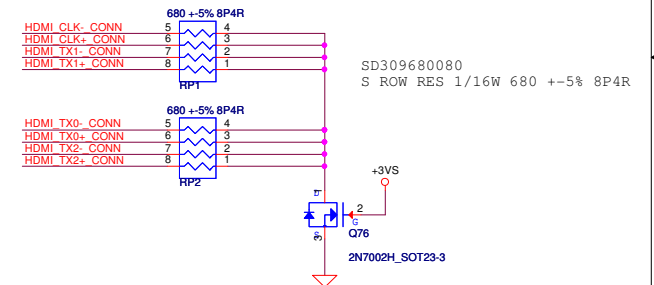
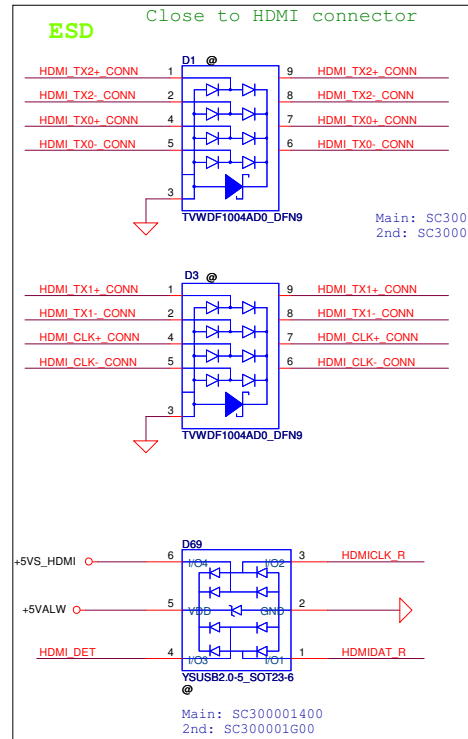
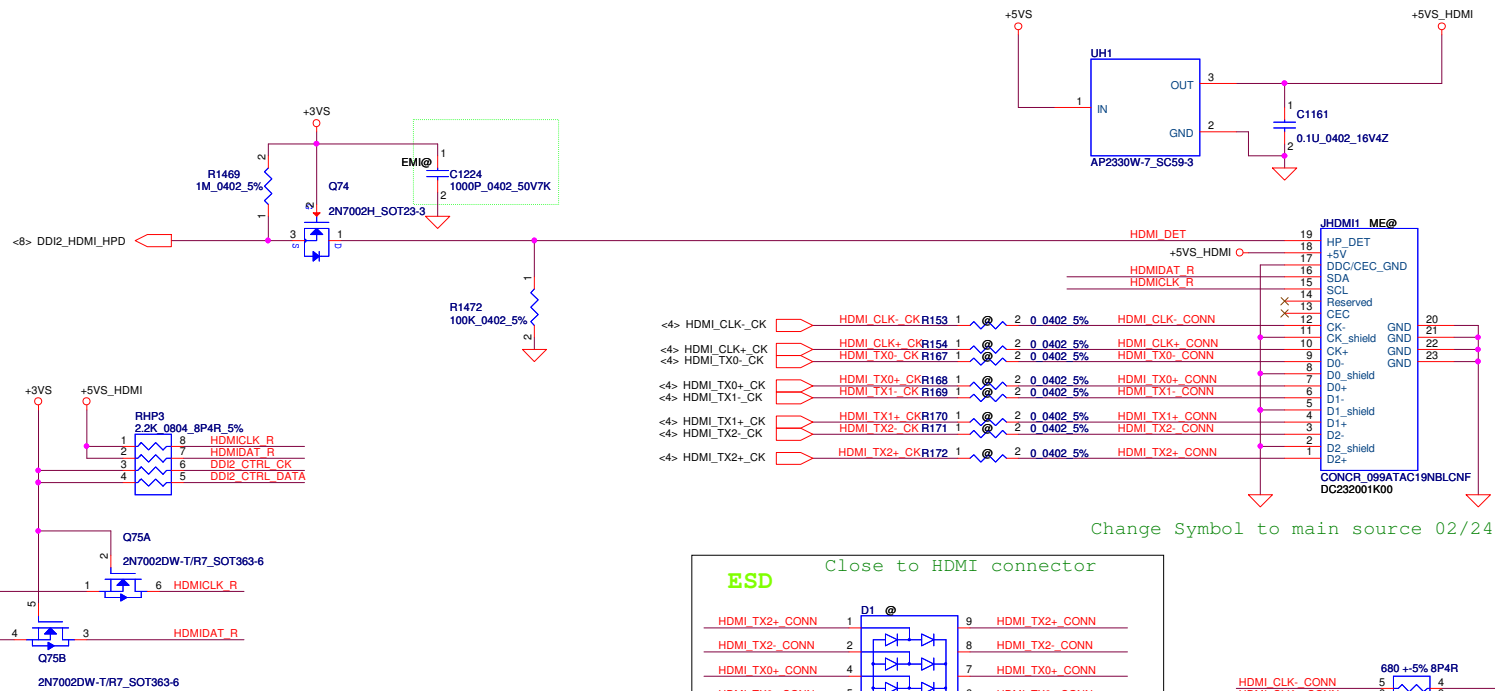
The schematic shows the MIIC_SDA signal line. It is pulled up to +3V5_PS by two resistors, RT5 and RT6, both labeled 4.7K_0402_5%. The signal line is labeled MIIC_SDA. The resistors are connected to the signal line at nodes 1 and 2, which are labeled RT5 and RT6 respectively. The signal line is also connected to the MIIC_SDA pin of the U100 component.



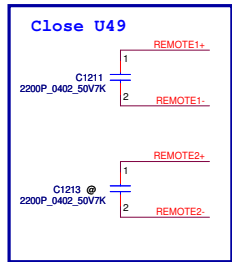
CMOS Camera



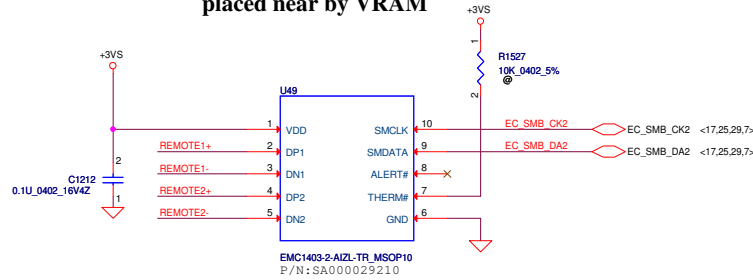
Security Classification		Compal Secret Data		Compal Electronics, Inc. LVDS/CAMERA	
Issued Date	2011/06/15	Deciphered Date	2012/07/11	Title	
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				Custom Document Number LA-A321PR02	0.2
Date:		Thursday, March 14, 2013		Sheet	29 of 46



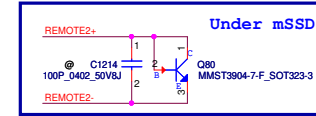
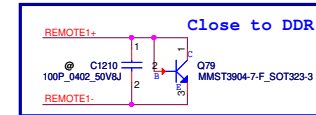
Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2011/06/15	Deciphered Date	2012/07/11	Title	HDMI CONN
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				Date: Thursday, March 14, 2013	Sheet 30 of 46



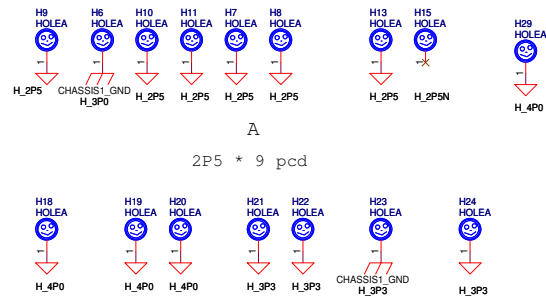
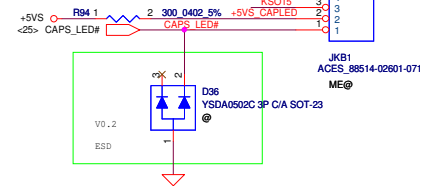
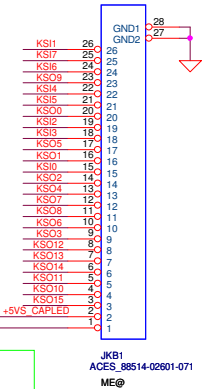
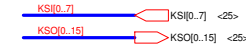
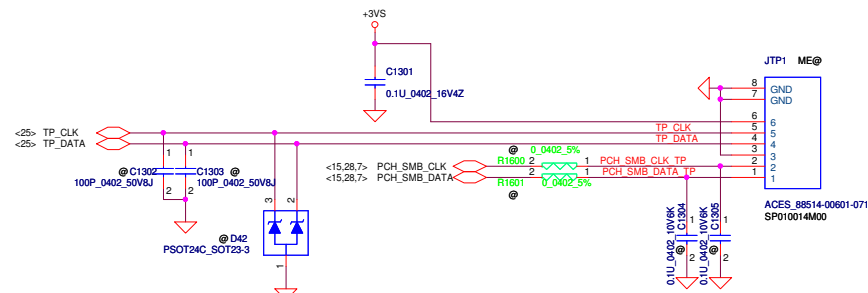
SMSC thermal sensor placed near by VRAM



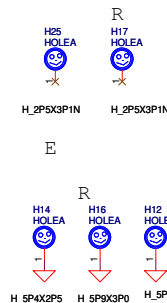
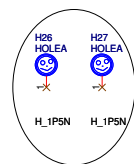
Address 1001_101xb F75303M P/N:SA000046C00



REMOTE1, 2+/-:
Trace width/space:10/10 mil
Trace length:<8"



0418 Add for LAN screw hole

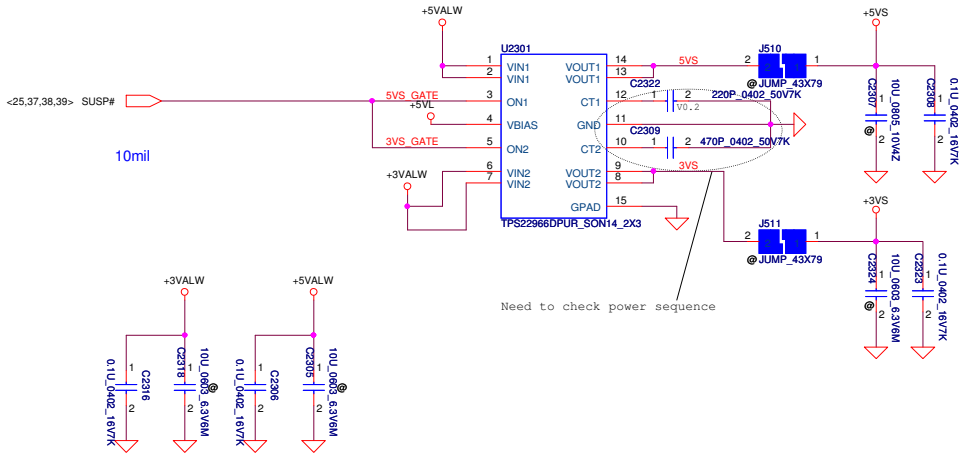


M/B 橢圓孔

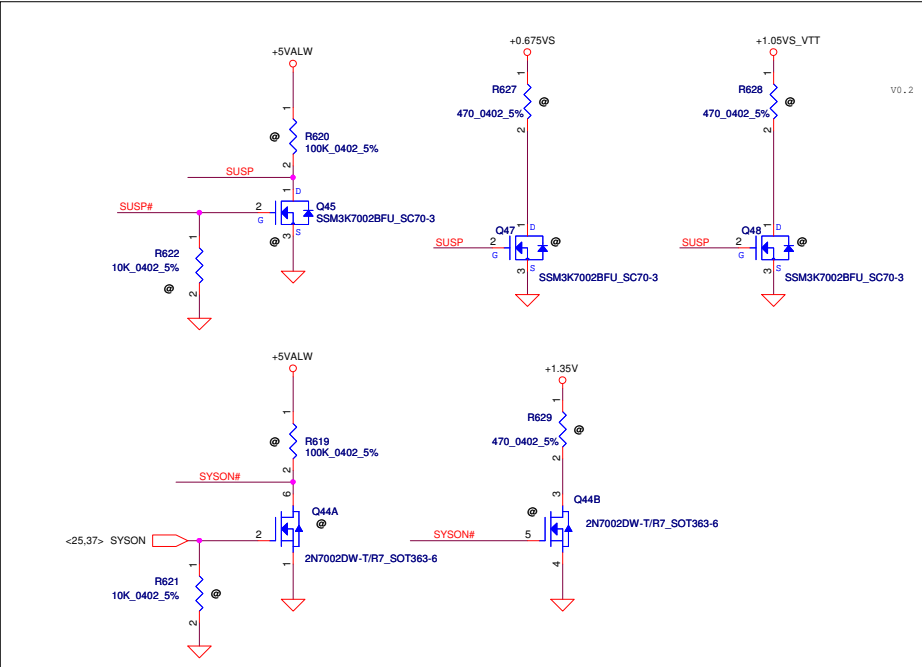
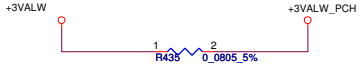
M/B KB 橢圓孔

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				Date: Tuesday, March 19, 2013	Sheet 31 of 46

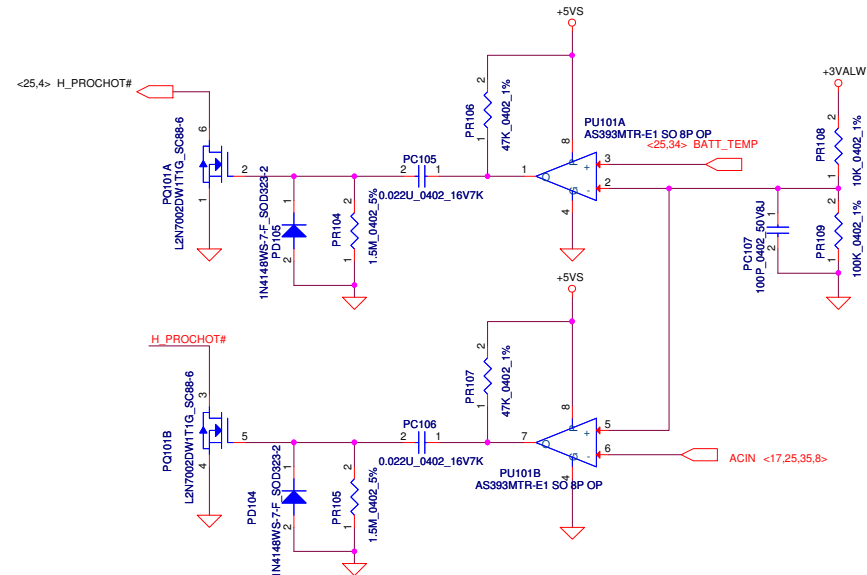
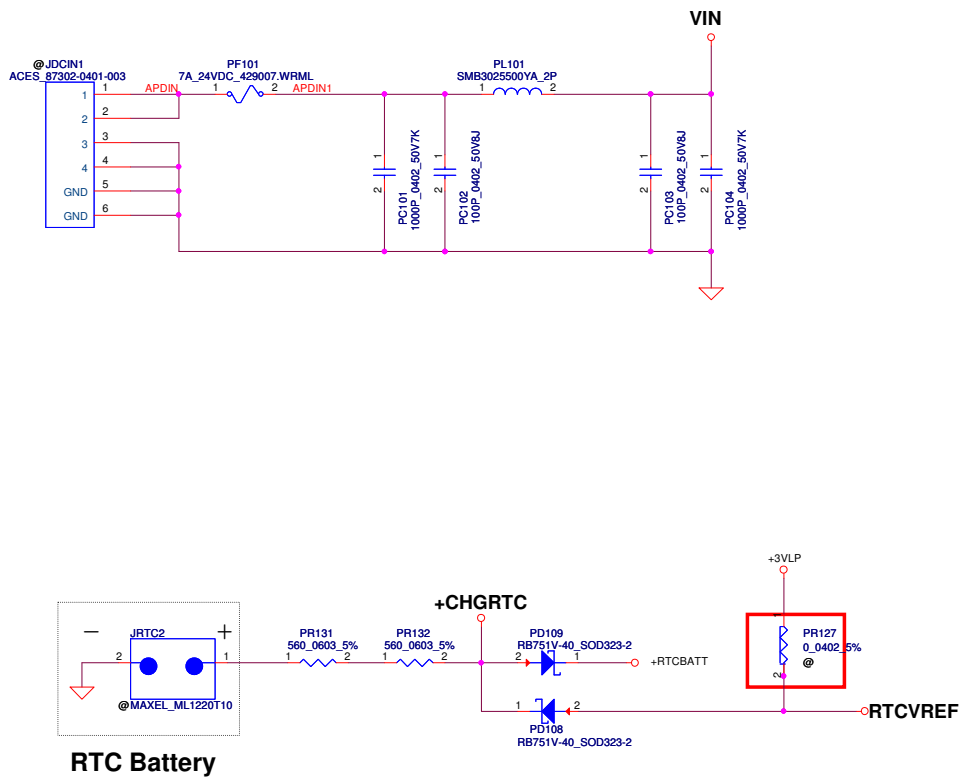
+5VALW TO +5VS
+3VALW TO +3VS



+3VALW TO +3VALW(PCH AUX Power)
+5VALW TO +5VALW(PCH AUX Power)

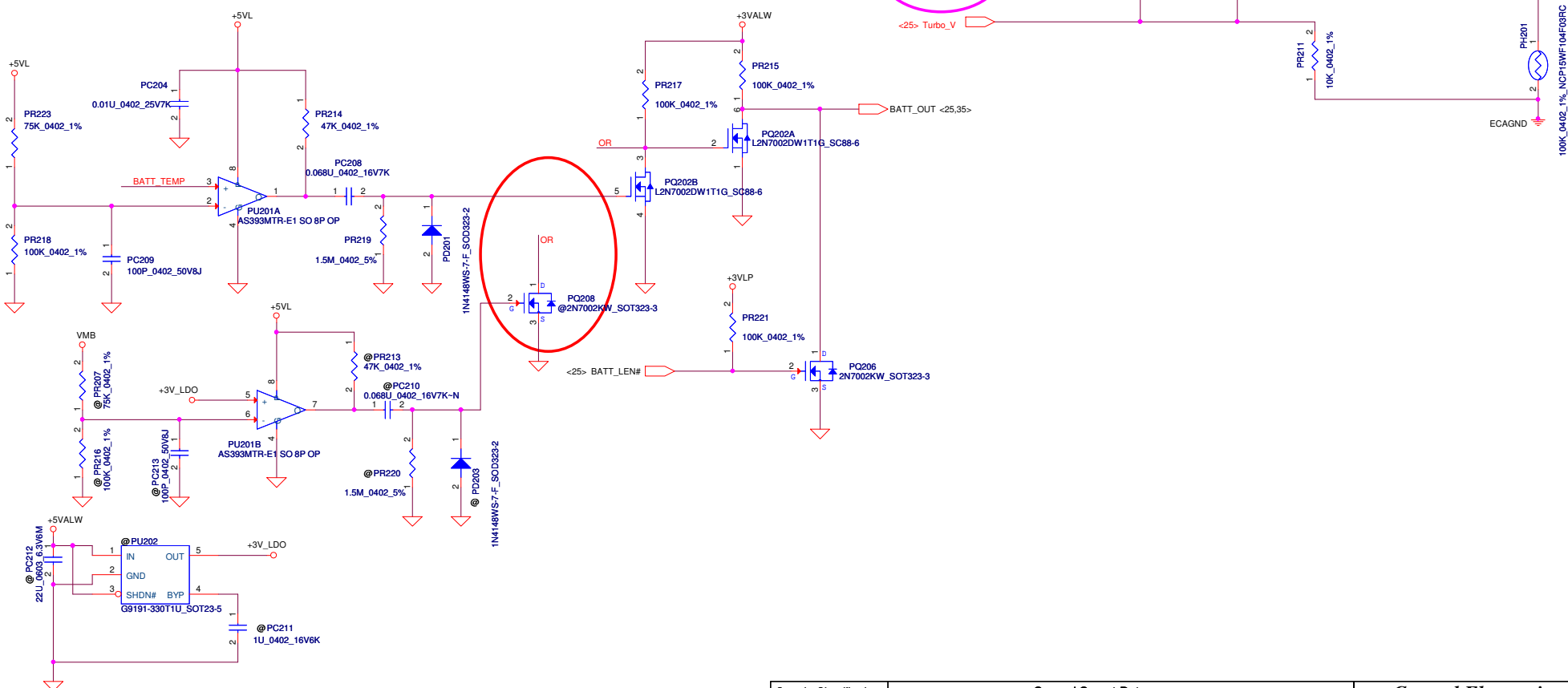


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Size		Document Number		Rev	
Custom		LA-A321PR02		0.2	
Date:		Thursday, March 14, 2013		Sheet 32 of 46	

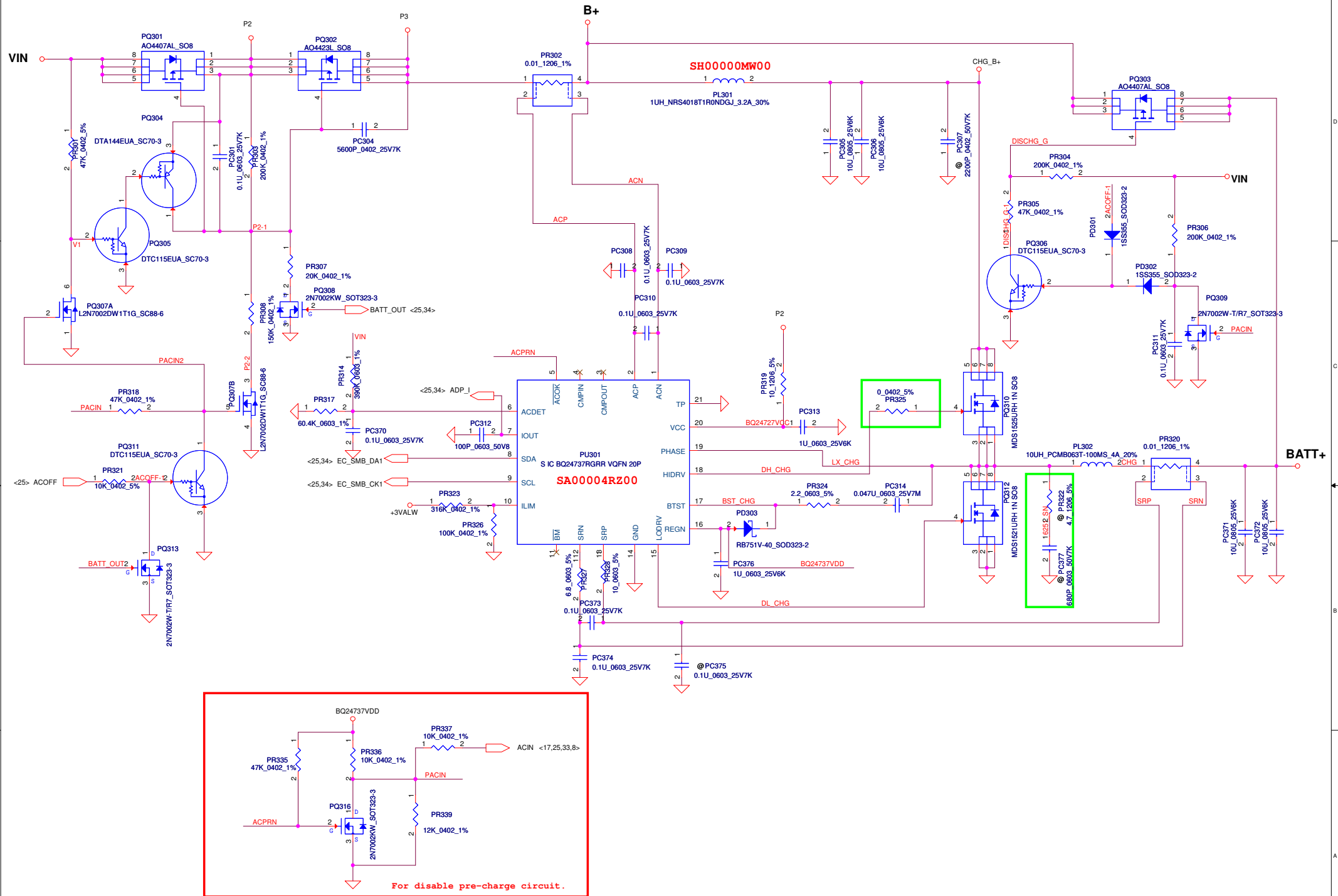


The schematic diagram illustrates the power supply section of the BQ76940 evaluation board. It features two battery inputs, JBATT1 and JBATT2, connected to the VMB2 pin. The circuit includes a 12A 65V MOSFET (PF201), a 250mA MOSFET (PI201), and various passive components like capacitors (PC201, PC202) and resistors (PR203, PR206, PR204). The output is connected to the BATT+ pin. The diagram also shows the connection of the BQ76940 pins to the board components.

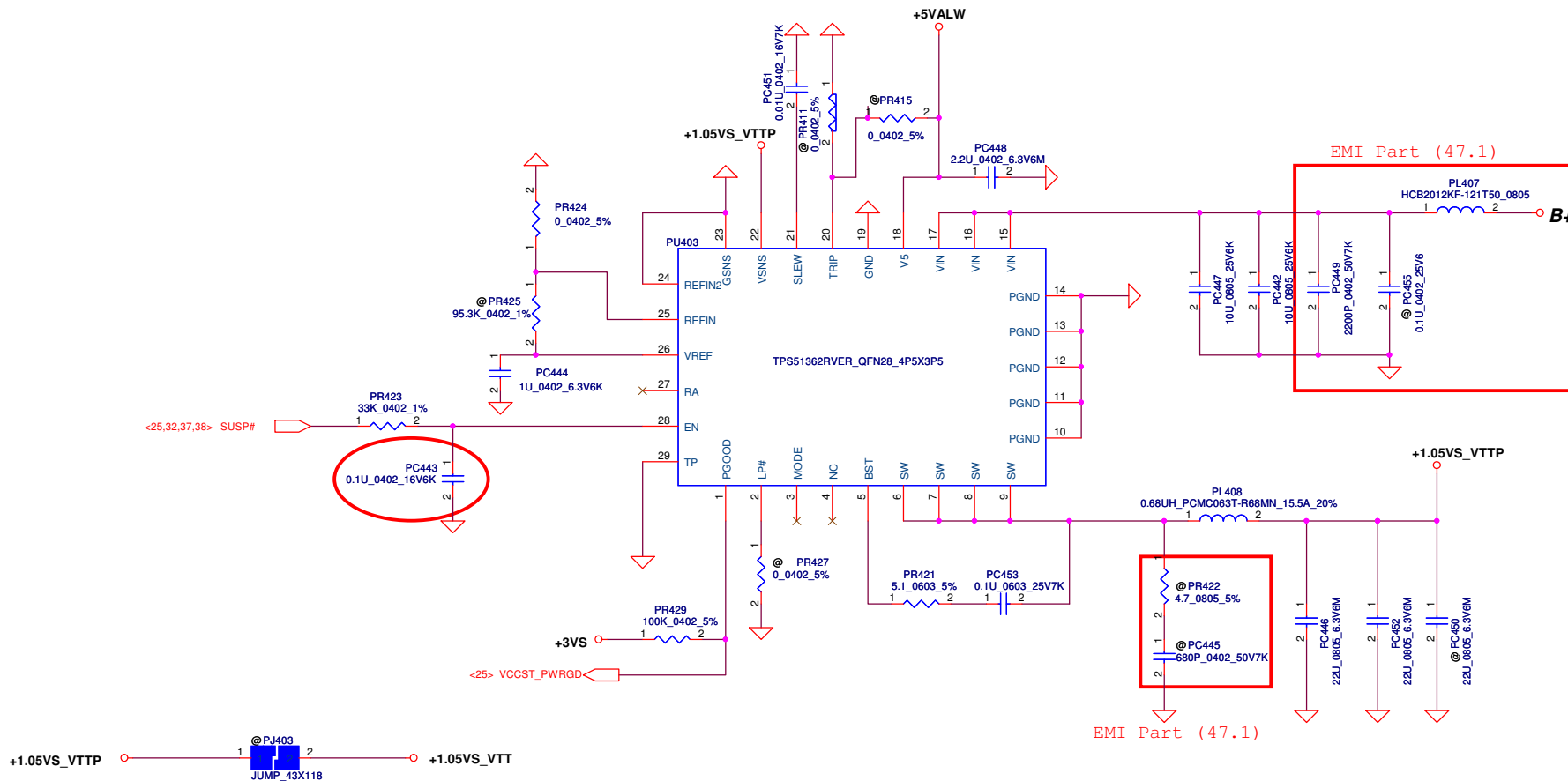
65W(UMA) : PR205=1.65K(SD034165180)



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						Customer	LA-A321PR01	0.1
Date:		Thursday, March 14, 2013	Sheet		34 of 46			



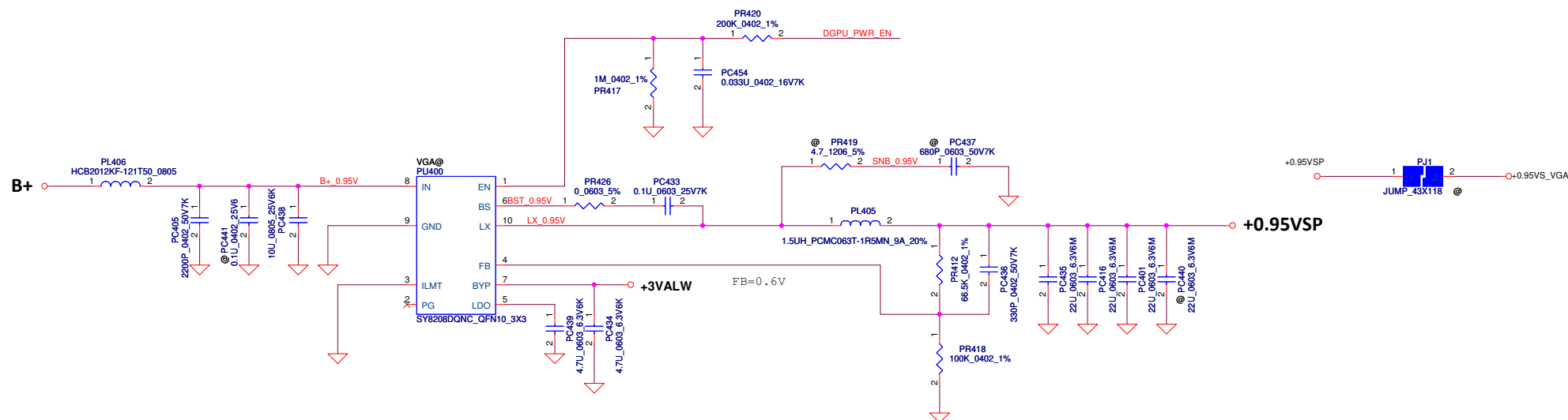
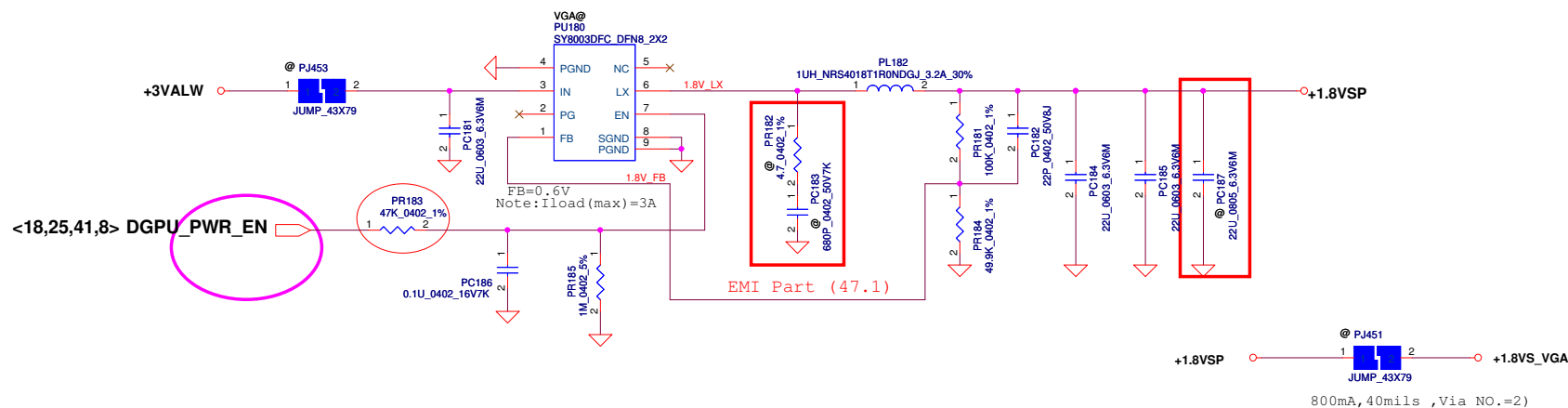
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				Date:	Thursday, March 14, 2013
				Sheet	35 of 46
				Rev	0.2



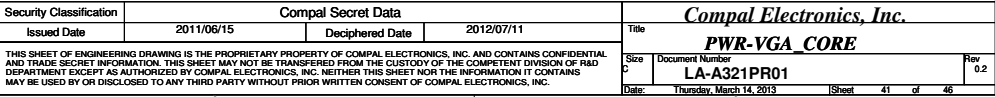
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				Date:	Friday, March 15, 2013
				Sheet	39 of 46
				Rev	0.2

EMI

1.8V controller (35.15), Support component (35.16)



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				Document Number
				Custom
				LA-A321PR01
				Rev
				0.2
				Date
				Thursday, March 14, 2013
				Sheet
				40 of 46



+CPU_CORE

30 X 22u/0805

RF request

Reserve

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				Date:	Thursday, March 14, 2013
				Sheet	43 of 46
				Rev	0.2

Version change list (P.I.R. List)

Page 1 of 1
for PWR

Item	Modify List	PG#	Reason for change	Date	Phase
1	For HW sequence	44	ADD PR821 100K ohm pull high +VS_VGA	3/11	SDV
2	For HW sequence	37	Remove PC512 0.1U_0402	3/11	SDV
3	For HW sequence	39	Change PC443 to 0.1U_0402	3/11	SDV
4	For HW sequence	40	Change PC186 to 0.1uf	2/7	SDV
5	For HW sequence	40	Change PC454 to 0.033uf	2/7	SDV
6	For HW sequence	40	Change PR183 to 47K ohm	2/7	SDV
7					
9					
10					
11					
12					
13					
14					
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16					
17					

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Document Number				Rev	
C38-AMD KABINI Schematic				0.2	
Date: Thursday, March 14, 2013				Sheet 44 of 46	

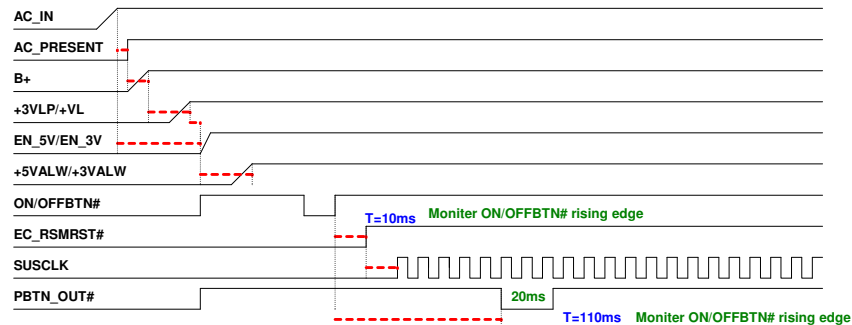
Version change list (P.I.R. List)

Page 1 of 1
for HW

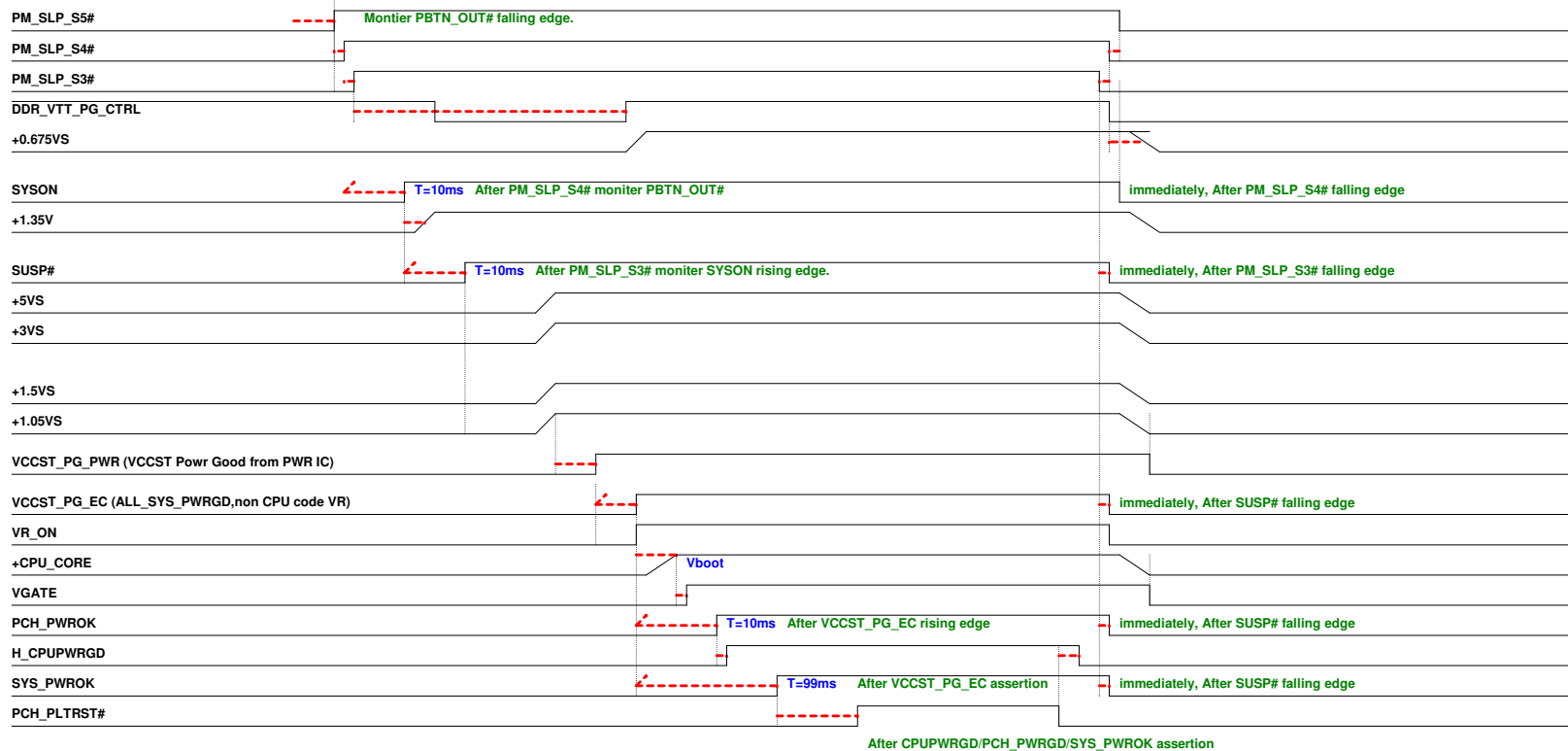
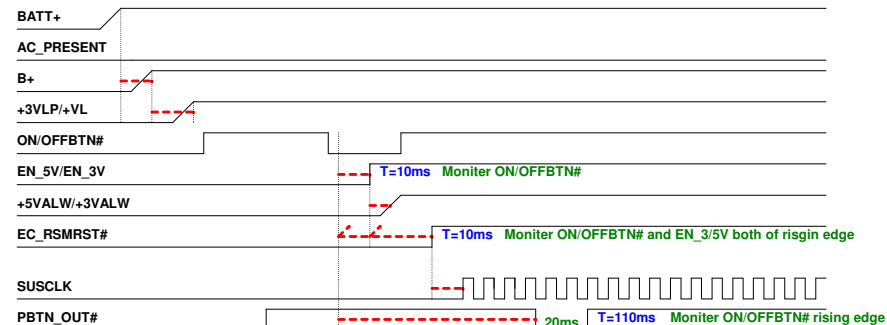
Item	Reason for change	PG#	Modify List	Date	Phase
1					
2					
3					
4					
5					
6					
7					
9					
10					
11					
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14					
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16					
17					

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				Date:	Thursday, March 14, 2013
				Sheet	45 of 46

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				Sheet	46 of 46
				Rev	0.2