

Compal confidential

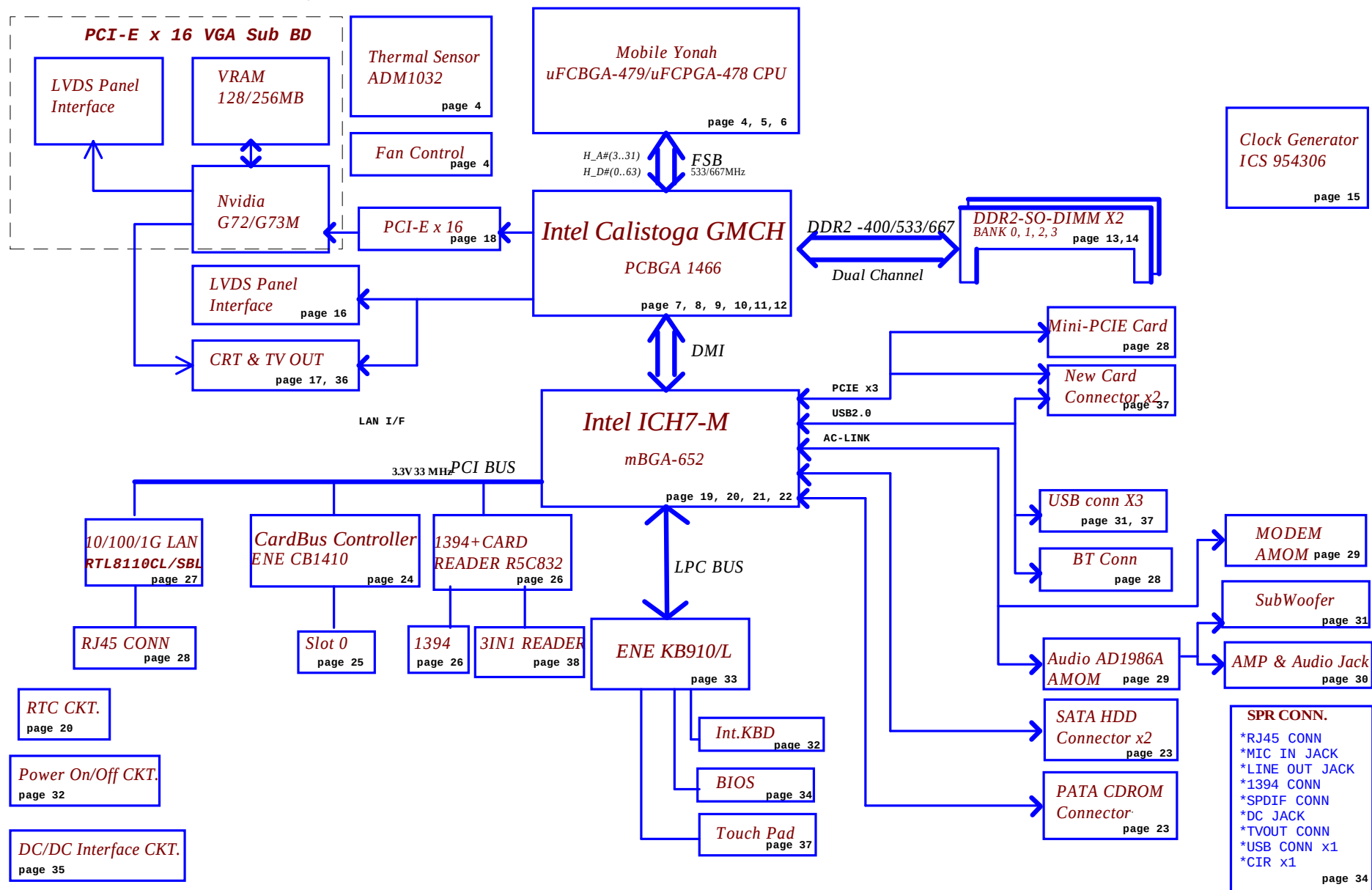
IGL50/51 Schematics Document

Mobile Yonah uFCPGA with Intel Calistoga_GM/PM+ICH7-M core logic

2006-05-15

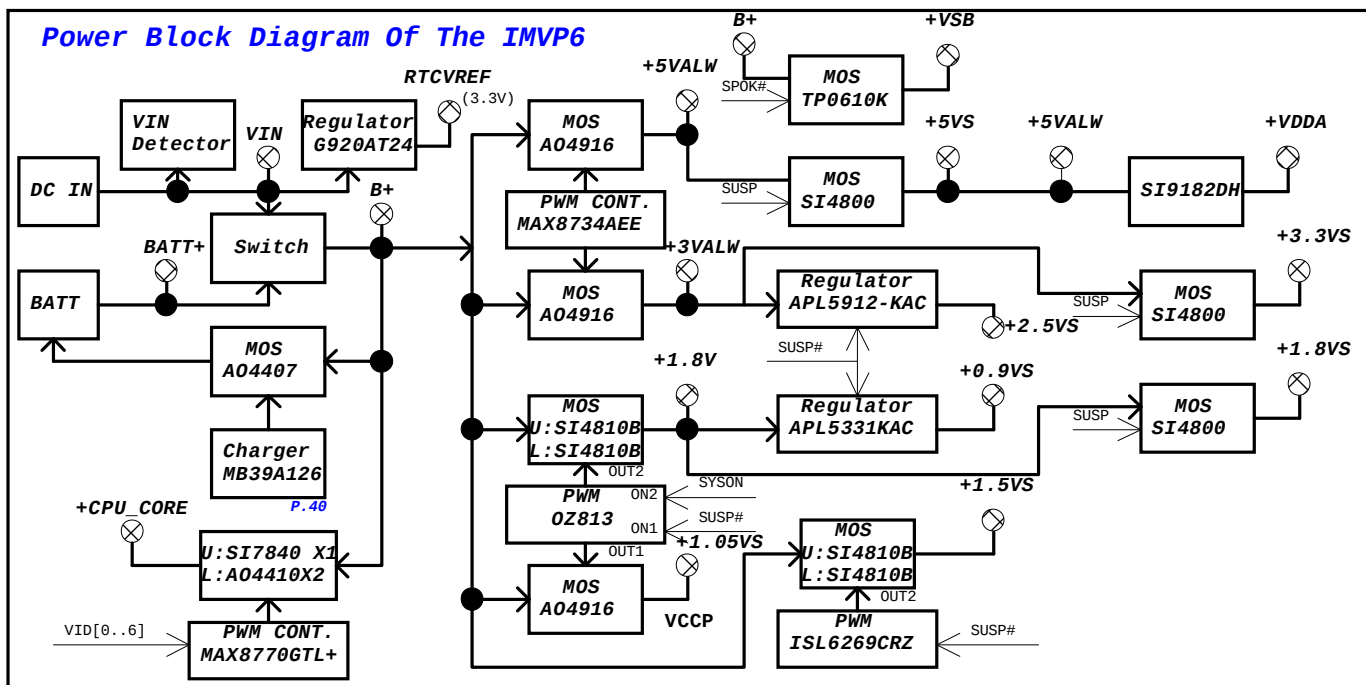
REV:0.1

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/03/10	Deciphered Date	2006/03/10	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Cover Sheet	
				Size	Document Number
				Custom	IGL50/51 LA-3771
Date: 星期五, 六月 08, 2006		Sheet 1 of 48		Rev 0.1	



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/03/10	Deciphered Date	2006/03/10	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	IGL50/51 LA-3771
				Date	Rev
				日期: 星期六, 08, 2006	0.1
				Sheet	2 of 48

Power Block Diagram Of The IMVP6



PCI DEVICES

EXTERNAL	IDSEL#	REQ/GNT#	PIRQ
CARD BUS CB1410	AD20	2	PCI_PIRQA#
CARD READER & 1394 R5C832	AD22	0	PCI_PIRQG# PCI_PIRQH#
LAN CONTROLLER RTL8110SBL/CL	AD17	3	PCI_PIRQF#

PCIE LANE

LANE	DEVICE
1	Express Card
2	Mini Card

I2C / SMB Address

DEVICE	ADDRESS R/W
KB910/L (SM1-Pulled-Up 5V)	
AT24C16AN	A3/A2 H
SMART BATTERY	17/16 H
KB910/L (SM2-Pulled-Up 3.3V)	
ADM1032AR	99/98 H
G7xM (I2CC-Pulled-Up 3.3V)	
G781-1 (RESERVED)	9B/9A
ICH7M SM Bus	
ICS9LPR325AKLFT	D3/D2 H (3.3V)
DDR II DIMM0	A1/A0 H (3.3V)
DDR II DIMM1	A3/A2 H (3.3V)
Express Card	NC (2.5V)
Mini-Express	NC (2.5V)

USB

PORT	DEVICE
0	LEFT SIDE
1	BLUE TOOTH
2	RIGHT SIDE
3	JP810
4	RIGHT SIDE
5	CMOS
6	RIGHT SIDE

BOM Structure

MARK	FUNCTION
@	NC FOR ALL
EXP@	PCIE-NEW CARD
BT@	BLUE TOOTH
UMA@	Internal 945GM
VGA@	External G7xM
SUBWOOFER@	SUBWOOFER
HGT30@	HGT30
CB@	PCMCIA/CARD BUS
GIGA@	8110SBL(SCL)Giga LAN
10/100@	8110CL 10/100Mb LAN

Voltage Rails

power plane	+B LD03 LD05	+5VALW +3VALW	+1.8V +5V	+5VS +3VS +2.5VS +1.8VS +1.5VS +VGA CORE +1.2VS +0.9VS +CPU CORE +VCCP
State S4 : STD S5 : SOFT OFF				
S0	0	0	0	0
S1	0	0	0	0
S3 : STR	0	0	0	X
S5 S4/AC	0	0	X	X
S5 S4/ Battery only	0	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X

MB ID

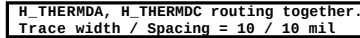
MB ID	P NAME
0	IGL-50
1	IGL-51

BRD ID

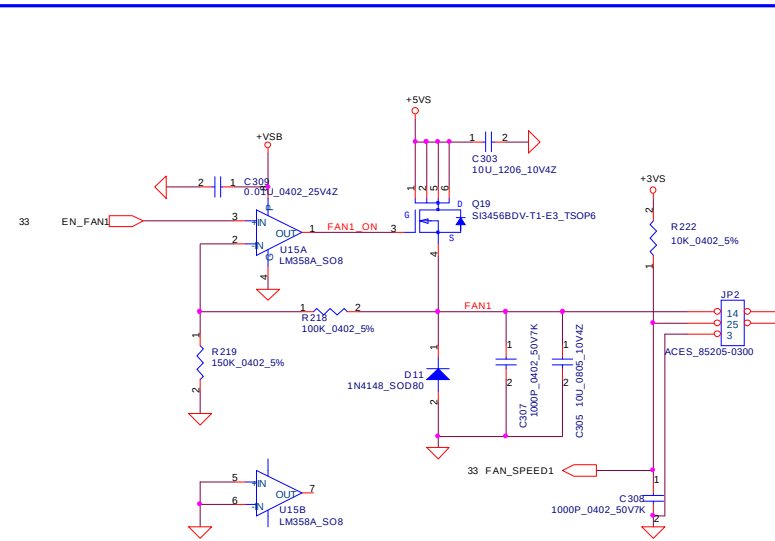
ID	MB REV#	R115(Rb)	Vab
0	R0.1 (EVT)	0	0V
1	R0.2 (DVT)	8.2K	0.25V
2	R0.3 (PVT)	18K	0.50V
3	R1.0 (MP)	33K	0.82V
4		56K	1.19V
5		100K	1.65V
6		200K	2.20V
7		NC	3.30V

Security Classification	Compal Secret Data			Title	
Issued Date	2005/03/10	Deciphered Date	2006/03/10	Compal Electronics, Inc.	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINED HEREIN MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	IGL50/51 LA-3771
				Date	Rev
				2006.06.08	0.1
				Sheet	48

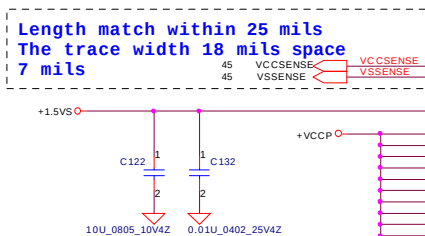
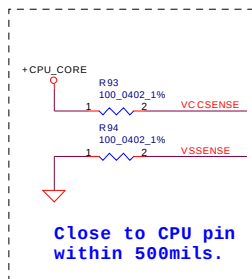
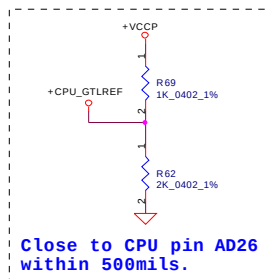
+3VALW	KB910L SB RTL8110SBL/CL	mA 160mA
+CPU_CORE	CPU	36A
+VCCP	CPU NB	2.5A 9.8A (14.7A)
+5VS	EXPRESS CARD HDD ODD MDC APA2066 TPA0211 AD1986 USB PORT * 6	1A 1.5A 1.8A 300mA 1A mA 70mA 3A
+3VS	NB EXPRESS CARD CLK_GEN LCDVCC VGA CARD (G7XM) SB R5C832 BIOS ROM KB910L CB1410	480mA 1A 200mA 1A 655mA 680mA mA 15mA 200mA mA
+2.5VS	VGA CARD (G7XM) NB	130mA (143mA)
+1.8V	DDR2_DIMM NB (667Mhz)	8A 3.1A
+1.8VS	GDDR2 VGA CARD (G7XM)	6A 4.06A
+0.9VREF	DDR2_DIMM	10mA
+0.9VS	GDDR2 DDR2_DIMM	1A 2A
+1.5V	SB	40mA
+1.5VS	NB SB MiniCard EXPRESS CARD VGA CARD (G7XM)	8.9A(13.8A) 3.8A 1A 0.65A 2A



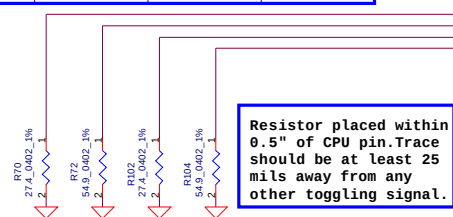
Address: 100_1100



Security Classification	Compal Secret Data			Compal Electronics, Inc. Yonah CPU in mFCPGA479		
Issued Date	2005/10/06	Deciphered Date	2006/10/06	Title		
THIS SET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	
				Cust	IGL50/51	LA-3771
				Date:	星期四, 六月 08, 2006	Sheet 4 of 48



CPU_BSEL	CPU_BSEL2	CPU_BSEL1	CPU_BSEL0
133	0	0	1
166	0	1	1



JP1B

YONAH

POWER, GROUND, RESERVED SIGNALS AND NC

JP1C

YONAH

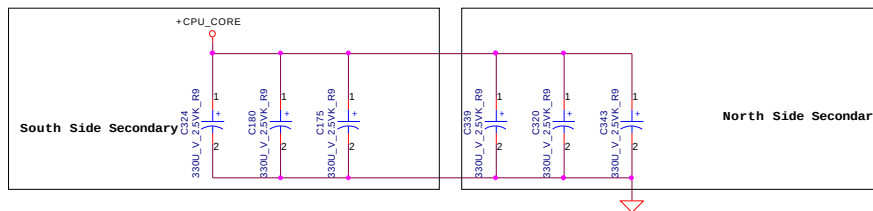
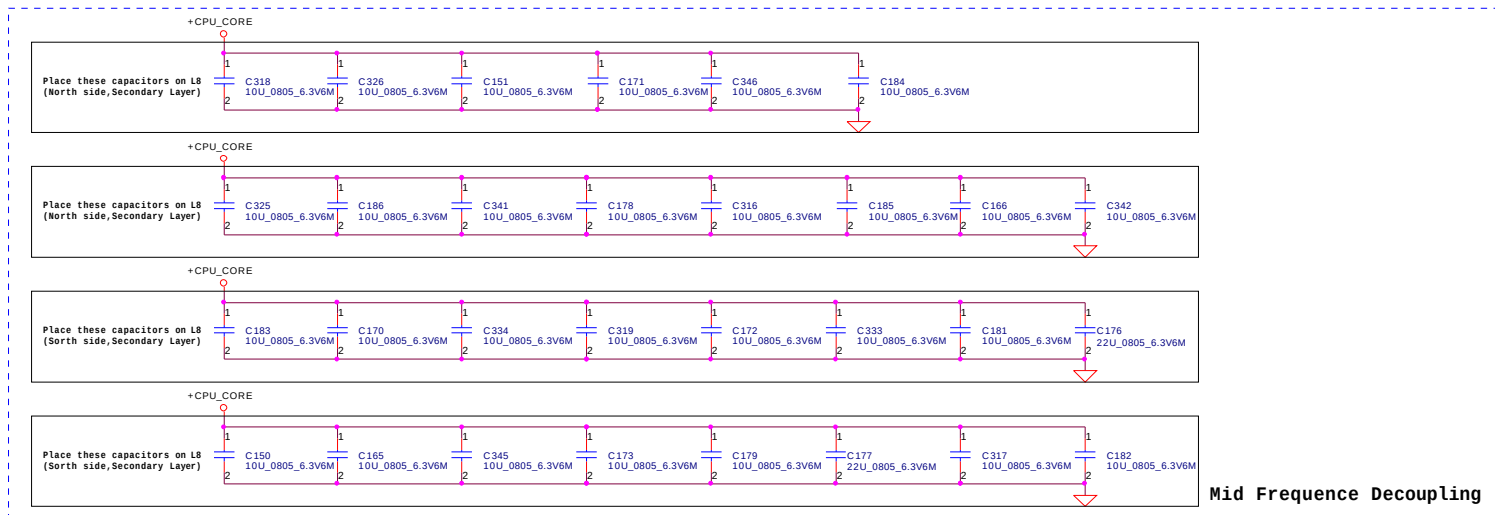
POWER, GROUND

TYCO_1-1674770-2_Yonah-D
ME@

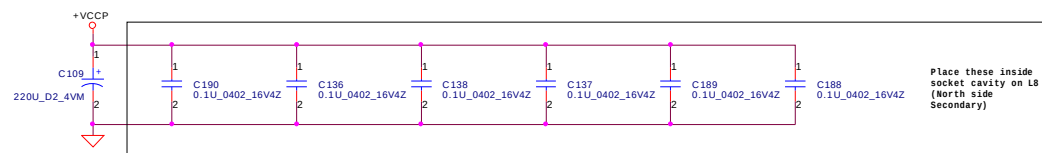
TYCO_1-1674770-2_Yonah-D
ME@

Security Classification	Compal Secret Data		
Issued Date	2005/10/06	Deciphered Date	2006/10/06
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE REPRODUCED OR TRANSMITTED IN ANY FORM OR BY ANY MEANS, ELECTRONIC OR MECHANICAL, INCLUDING PHOTOCOPYING, RECORDING, OR BY ANY INFORMATION STORAGE AND RETRIEVAL SYSTEM, WITHOUT THE WRITTEN PERMISSION OF COMPAL ELECTRONICS, INC.			

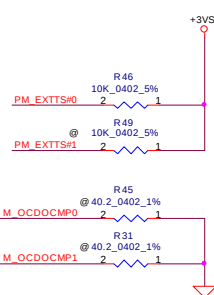
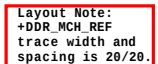
Compal Electronics, Inc.		
Yonah CPU in mFCPGA479		
Size	Document Number	Rev
Customer	IGL50/51 LA-3771	0.1
Date	星期日, 六月 08, 2006	Sheet 5 of 48



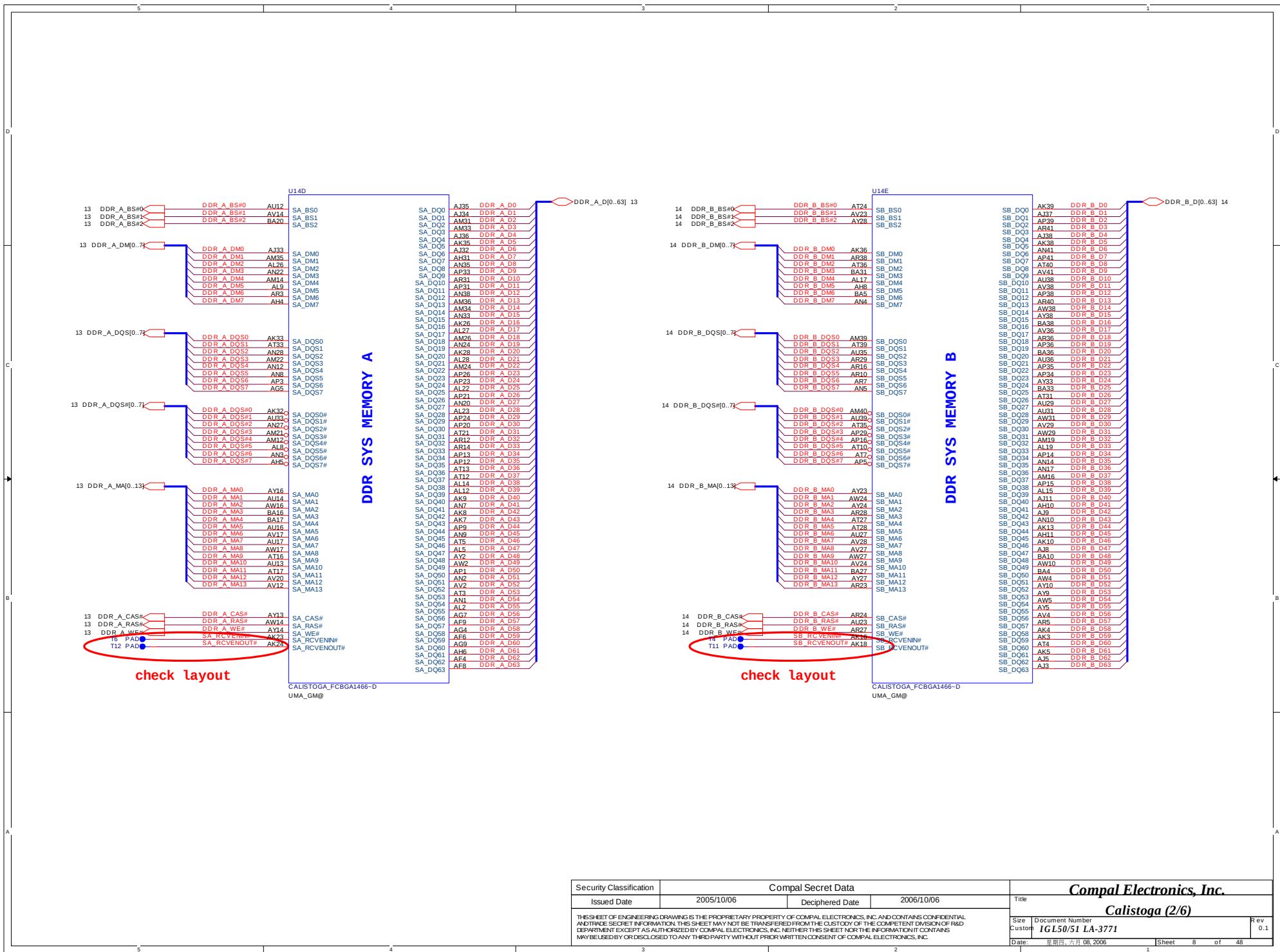
ESR <= 1.5m ohm
Capacitor > 1980uF

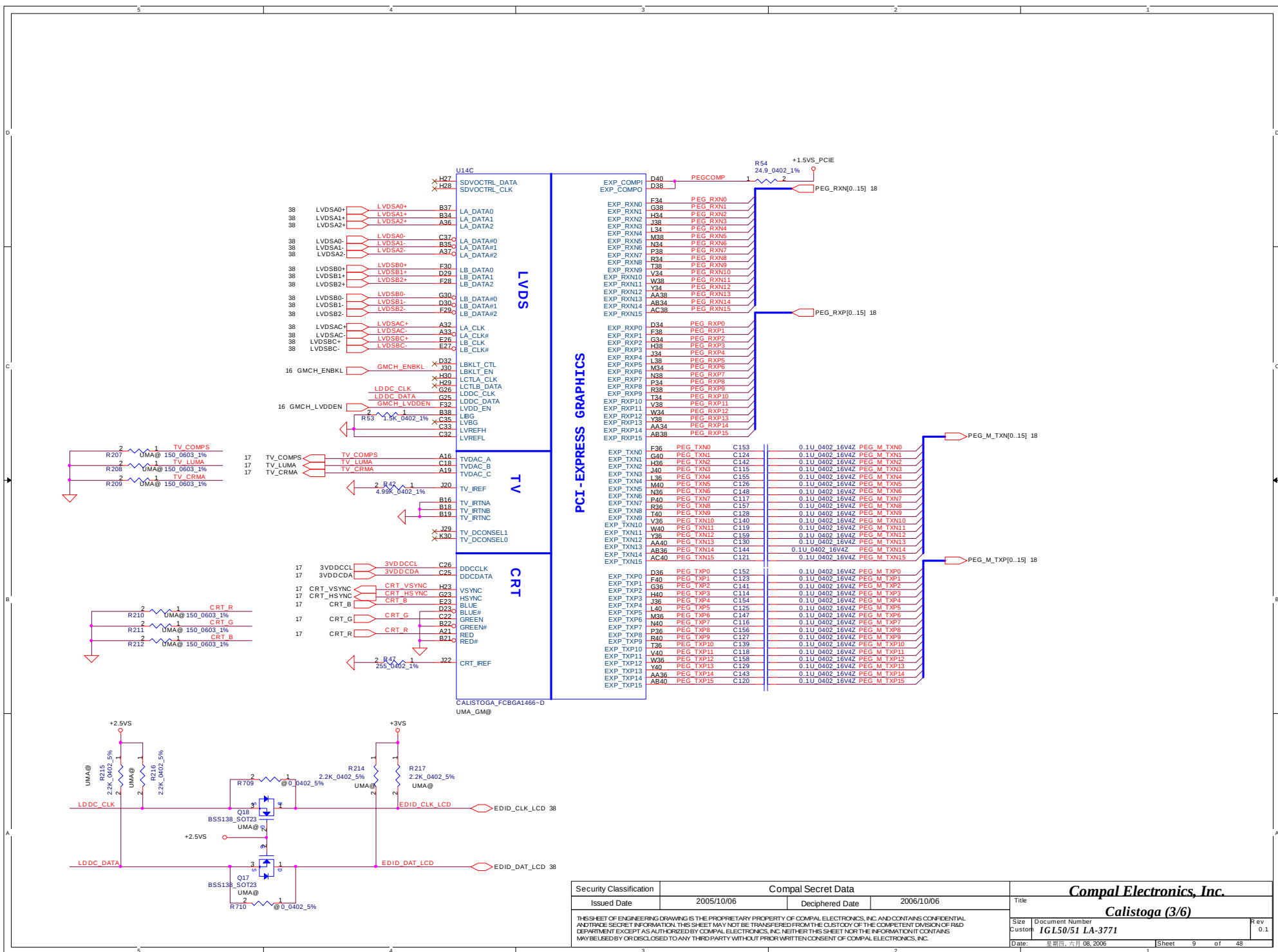


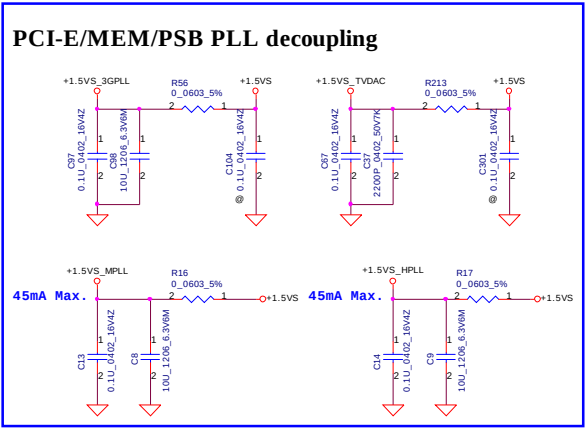
Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/10/06	Deciphered Date	2006/10/06	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				Customer
				Rev
				0.1
				Date: 星期四, 六月 08, 2006
				Sheet 6 of 48



Security Classification	Compal Secret Data			Compal Electronics, Inc. Calistoga (1/6)		
Issued Date	2005/10/06	Deciphered Date	2006/10/06	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custom	1GL50/S1 LA-3771	0.1
				Date:	星期四, 六月 08, 2006	Sheet 7 of 48







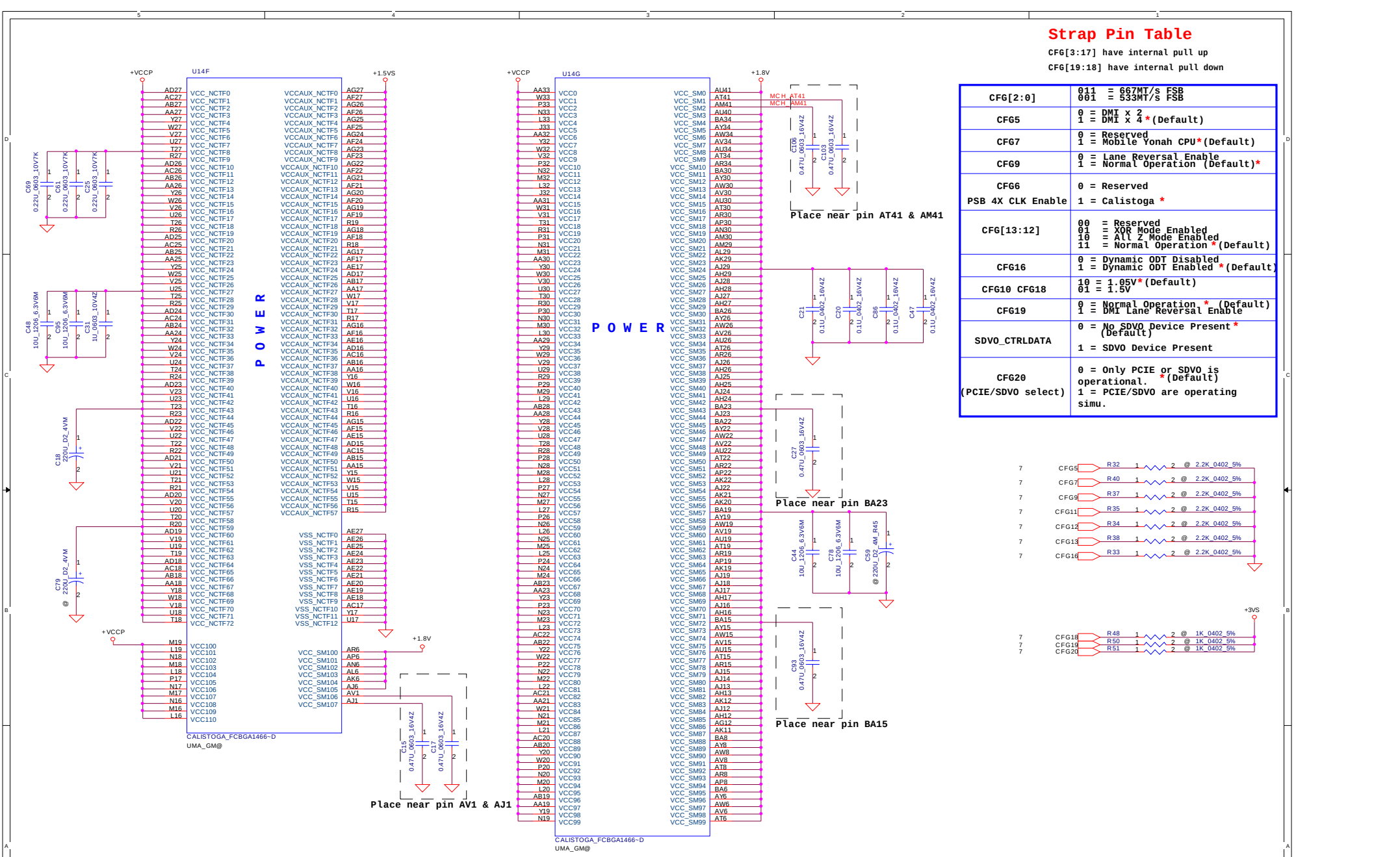
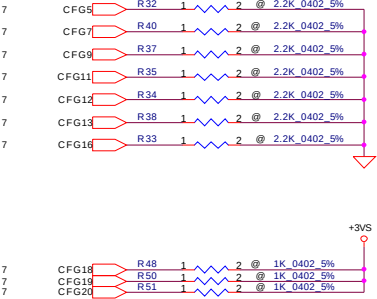
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

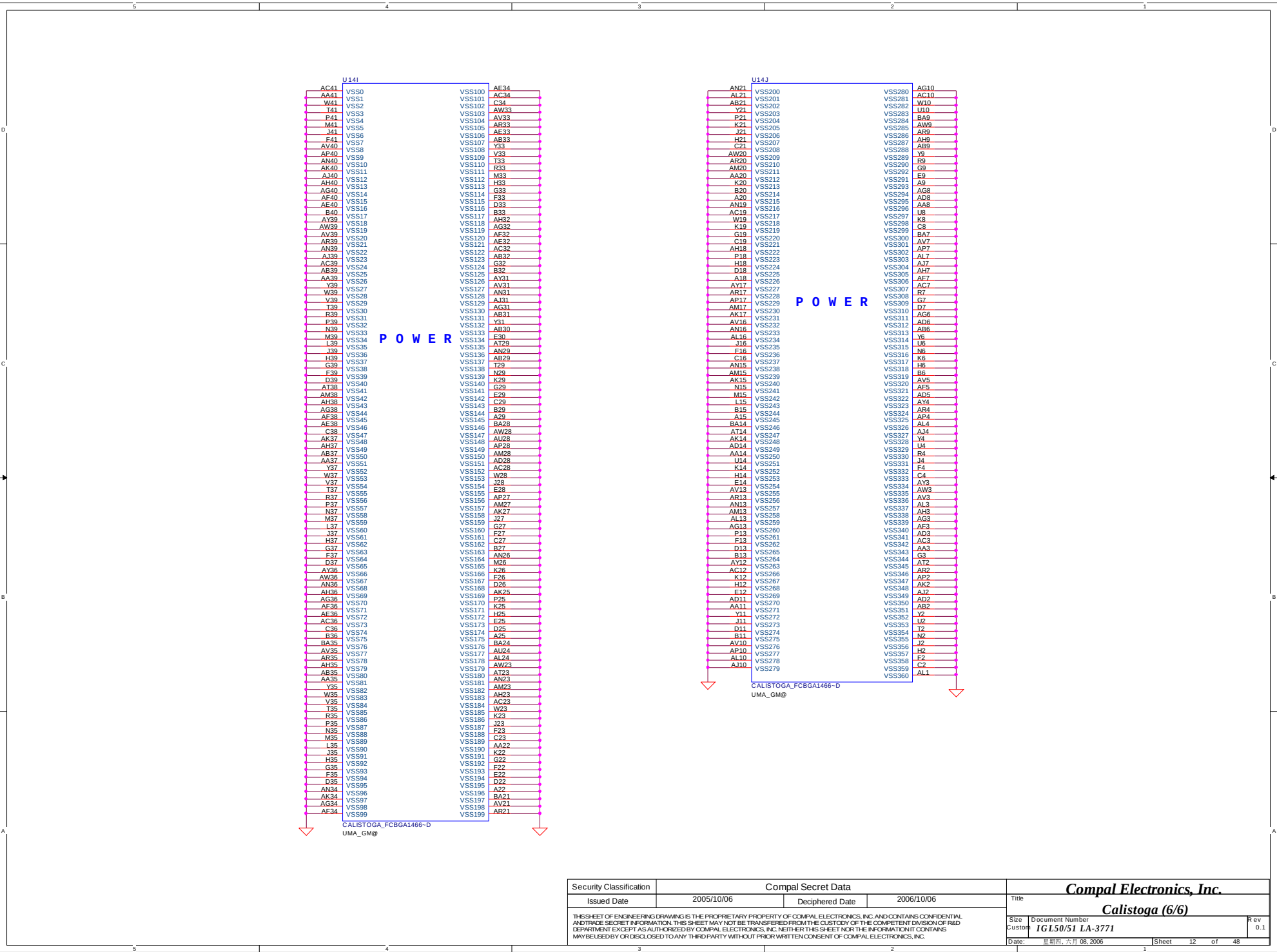
Strap Pin Table

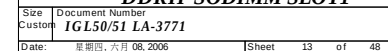
CFG[3:17] have internal pull up

CFG[19:18] have internal pull down

CFG[2:0]	011 = 667MT/s FSB 001 = 533MT/s FSB
CFG5	0 = DMI x 2 1 = DMI x 4 * (Default)
CFG7	0 = Reserved 1 = Mobile Yonah CPU* (Default)
CFG9	0 = Lane Reversal Enable 1 = Normal Operation (Default)*
CFG66	0 = Reserved PSB 4X CLK Enable 1 = Calistoga *
CFG[13:12]	00 = Reserved 01 = XOR Mode Enabled 10 = All 2 Mode Enabled 11 = Normal Operation * (Default)
CFG16	0 = Dynamic ODT Disabled 1 = Dynamic ODT Enabled * (Default)
CFG10 CFG18	10 = 1.05V* (Default) 01 = 1.5V
CFG19	0 = No SDVO Device Present * (Default) 1 = DMI Lane Reversal Enable
SDVO_CTRLDATA	0 = No SDVO Device Present 1 = SDVO Device Present
CFG20 (PCIE/SDVO select)	0 = Only PCIE or SDVO is operational. * (Default) 1 = PCIE/SDVO are operating simu.





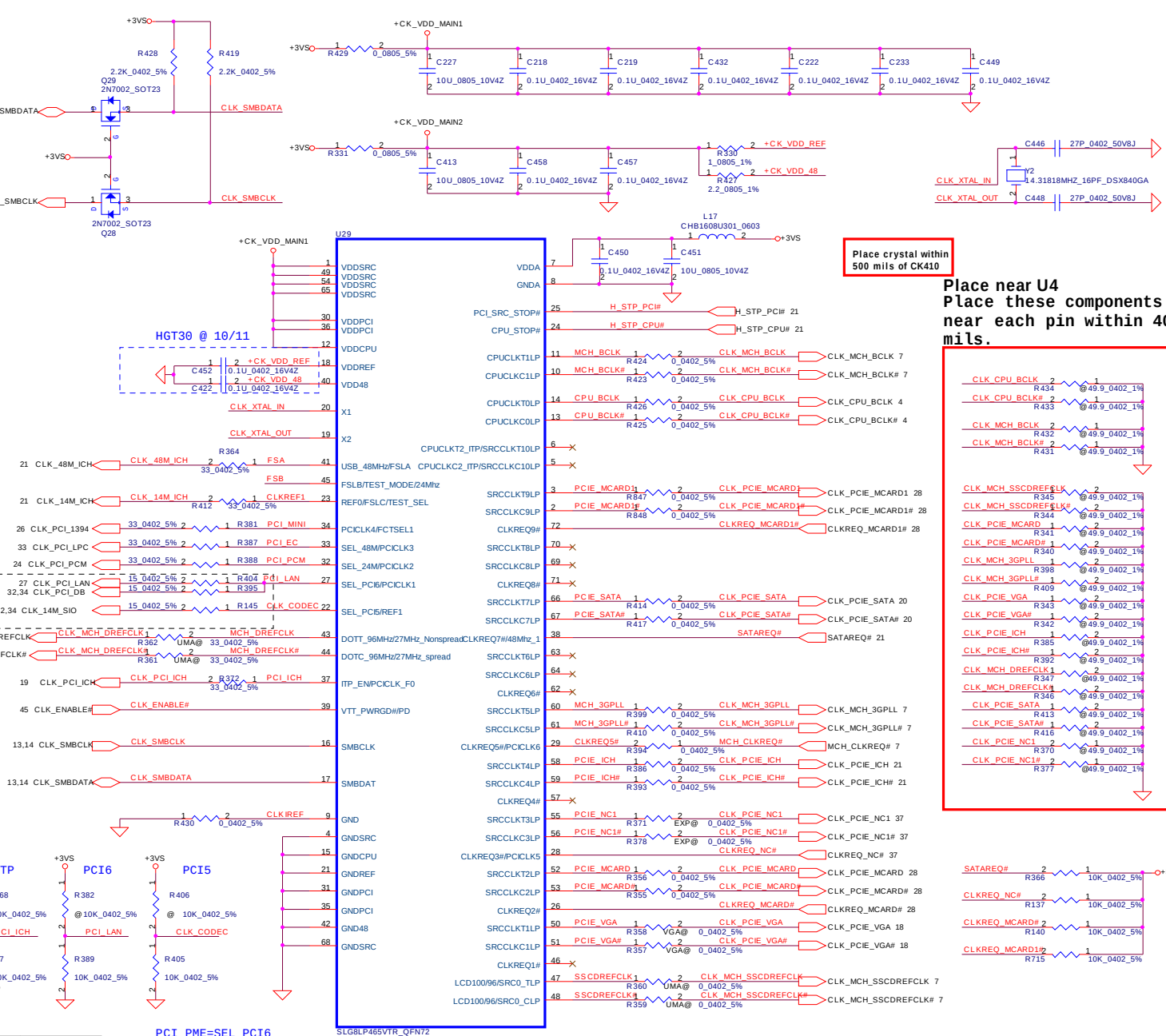
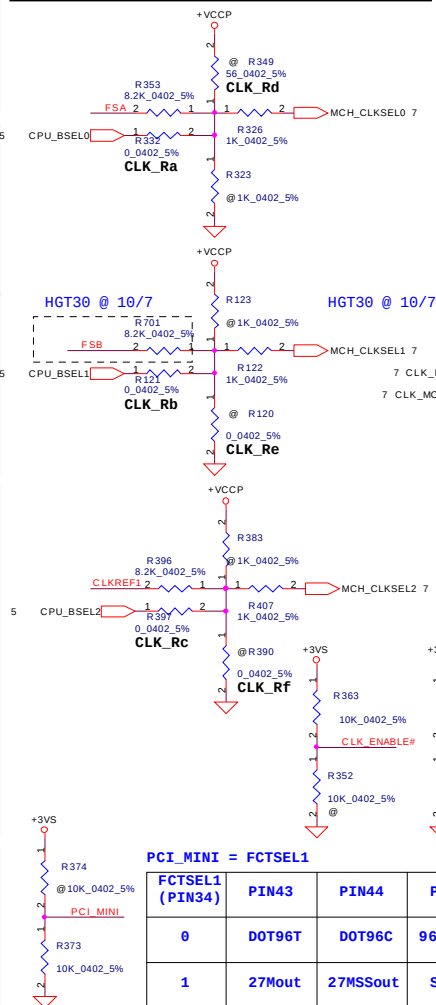


FSLC CLKSEL2	FSLB CLKSEL1	FSLA CLKSEL0	CPU MHZ	SRC MHZ	PCI MHZ
0	0	1	133	100	33.3
0	1	1	166	100	33.3

Table : ICS954306

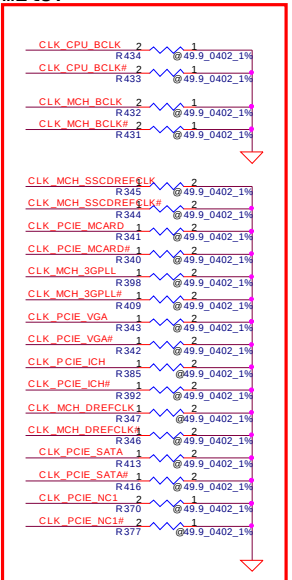
FSB Frequency Set:

CPU Driven	Stuff	CLK_Ra	CLK_Rb	CLK_Rc
* (Default)	No Stuff	CLK_Rd	CLK_Re	CLK_Rf
533MHz	Stuff	CLK_Rd	CLK_Re	CLK_Rf
	No Stuff	CLK_Ra	CLK_Rb	CLK_Rc
667MHz	Stuff	CLK_Rd	CLK_Re	CLK_Rf
	No Stuff	CLK_Ra	CLK_Rb	CLK_Rc



Place crystal within 500 mils of CK410

Place near U4
Place these components near each pin within 40 mils.

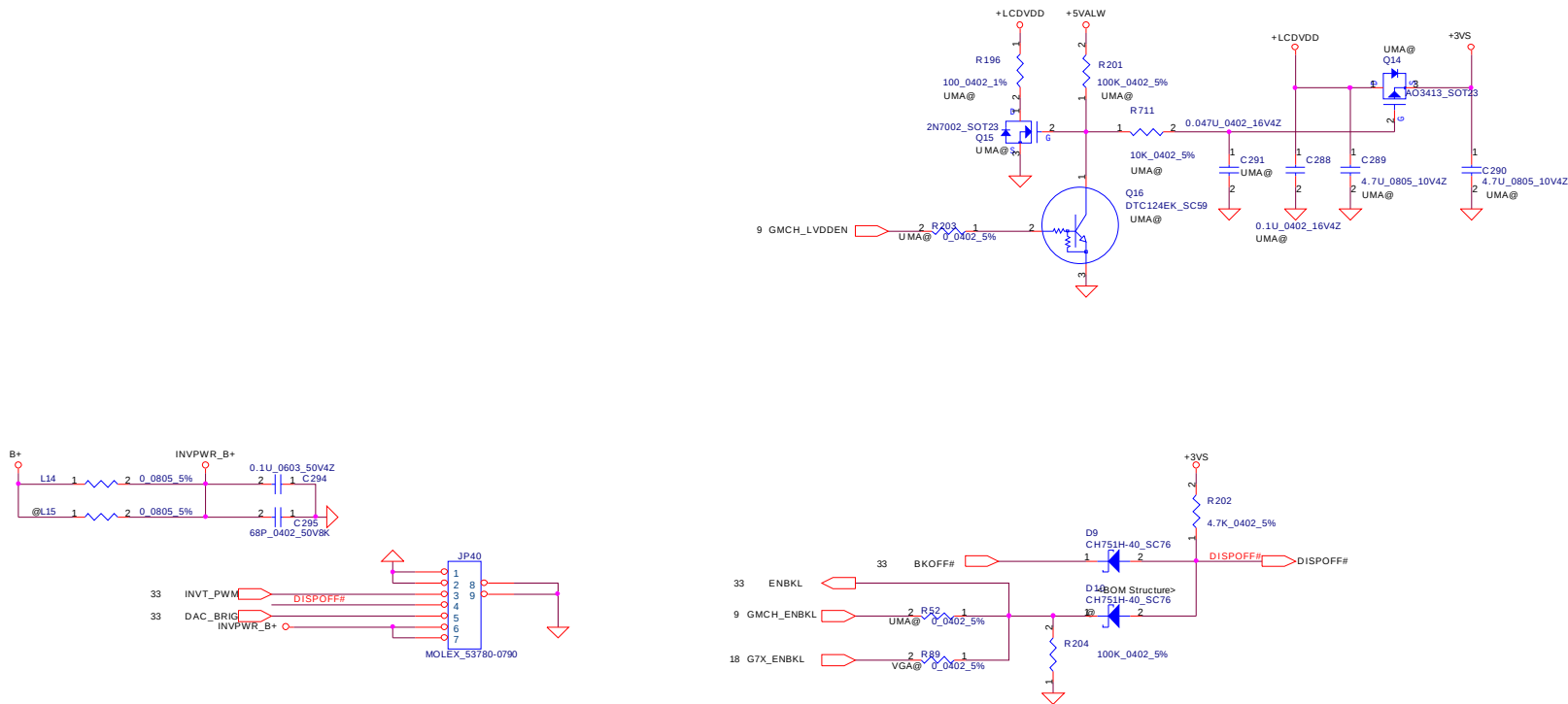


Security Classification		Compal Secret Data		Title	
Issued Date		Deciphered Date		Size	
2005/10/06		2006/10/06		Document Number	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		Date		Rev	
		星期四, 六月 08, 2006		15 of 48	

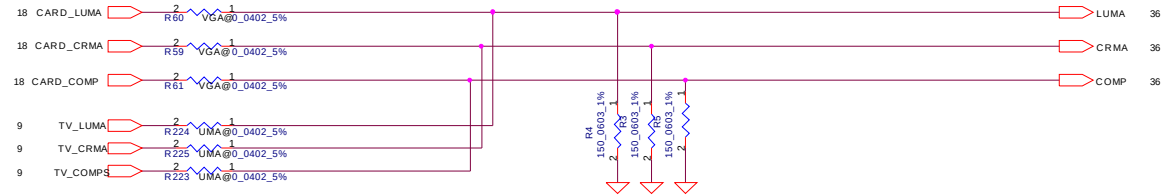
Compal Electronics, Inc.

Clock generator

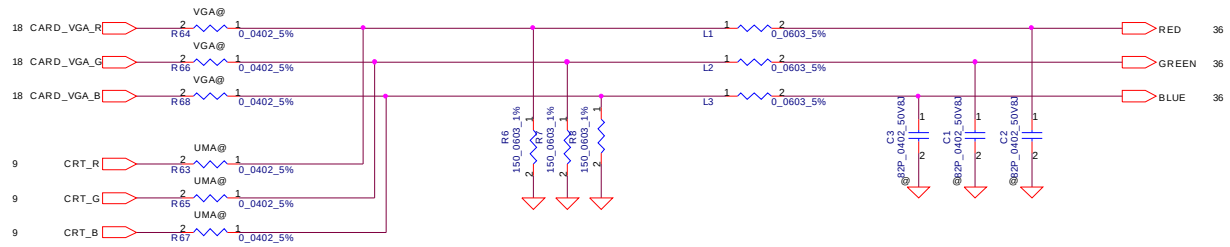
IGL50/51 LA-3771



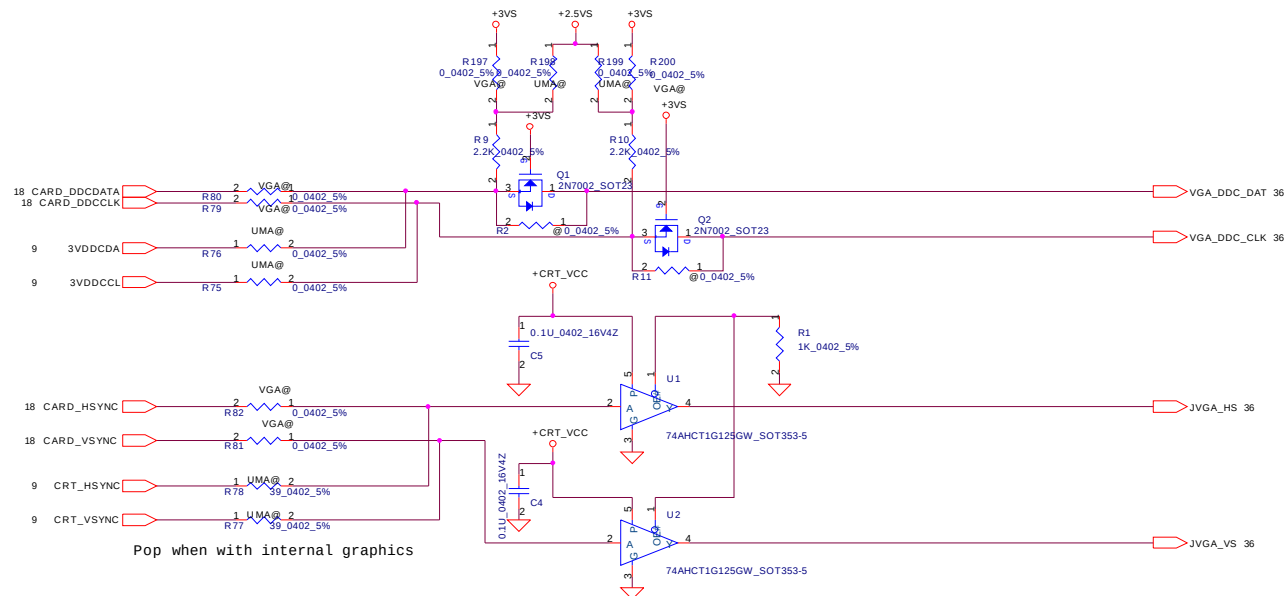
Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2005/10/06	Deciphered Date	2006/10/06	Title LVDS Connector		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custom	IGL50/51 LA-3771	0.1
				Date	星期四, 六月 08, 2006	Sheet 16 of 48

TV-OUT Conn.

Pop when with internal graphics

CRT Conn.

Pop when with internal graphics

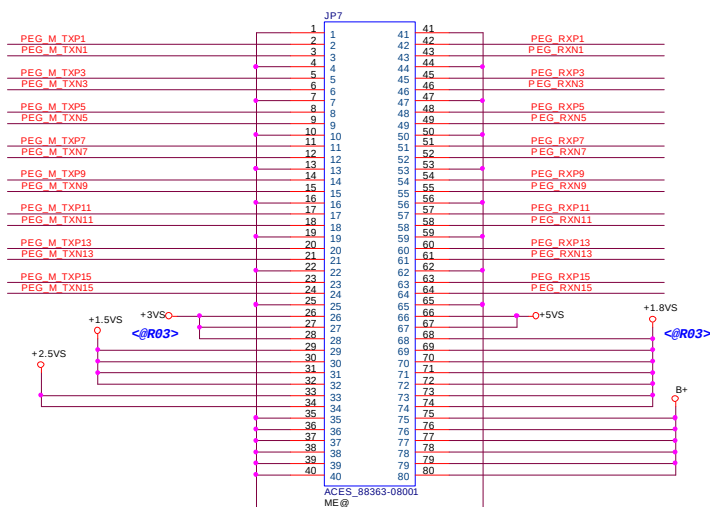


Pop when with internal graphics

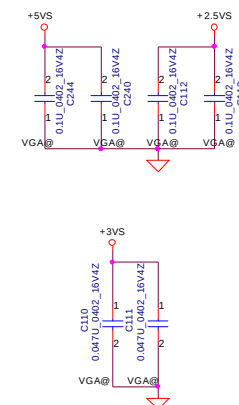
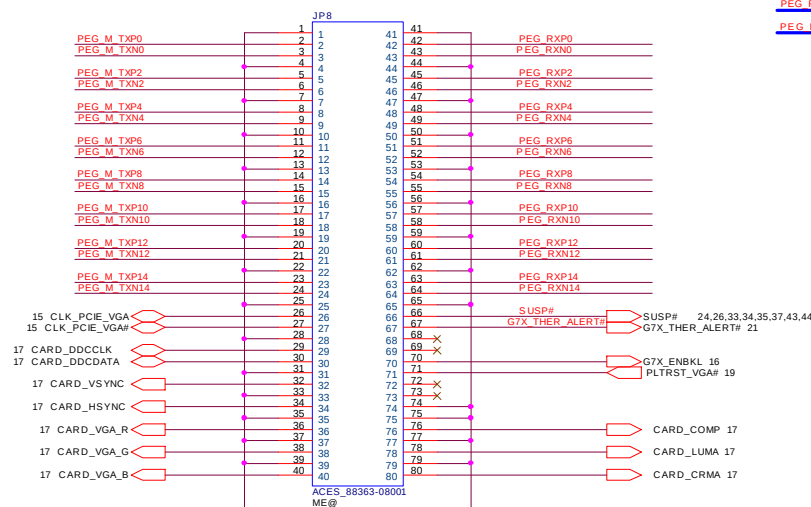
Security Classification		Compal Secret Data		Compal Electronics, Inc. CRT & TVout	
Issued Date	2005/10/06	Deciphered Date	2006/10/06	Title	
THIS SET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	1GL50/S1 LA-3771
					Rev 0.1
Date: 星期日, 六月 08, 2006				Sheet 17 of 48	

MAX. 4.06A @ 1.8V
MAX. 130mA @ 2.5V
MAX. 655mA @ 3.3V

PEG_M_TXP[0..15] PEG_M_TXP[0..15] 9
PEG_M_TXN[0..15] PEG_M_TXN[0..15] 9
PEG_RXP[0..15] PEG_RXP[0..15] 9
PEG_RXN[0..15] PEG_RXN[0..15] 9

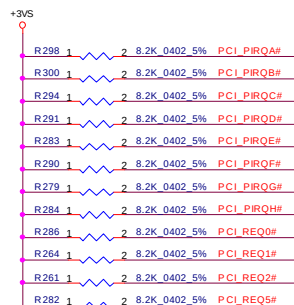
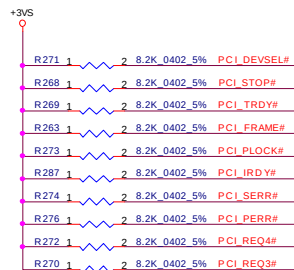


< New Add Pin.28 for +3VS, Pin.68 for +1.8VS @R03 >

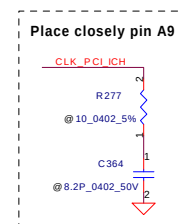
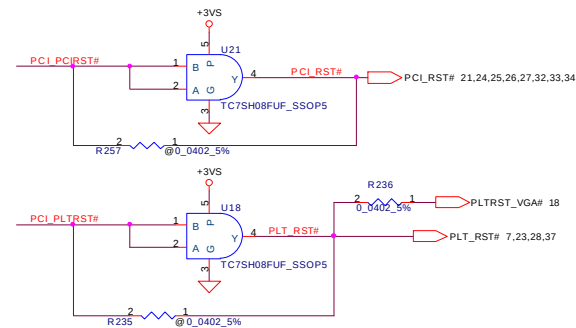
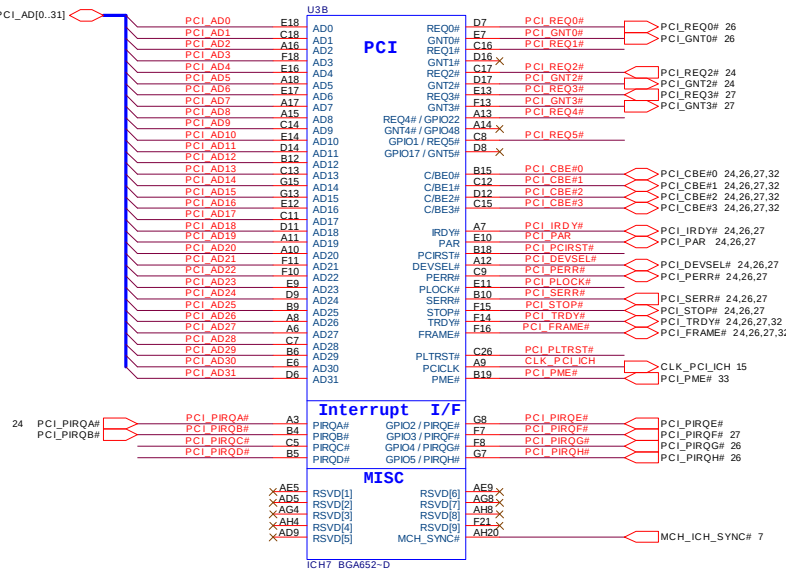


THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
VGA/B connector			
Title	Size	Document Number	Rev
	Custom	1GL50/51 LA-3771	0.1
Date	星期四, 六月 08, 2006		
	Sheet	18	of 48

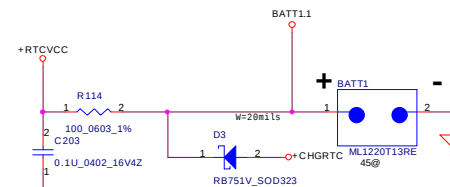
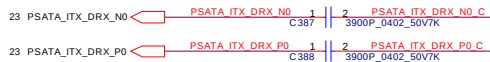


24,26,27,32 PCI_AD[0..31]

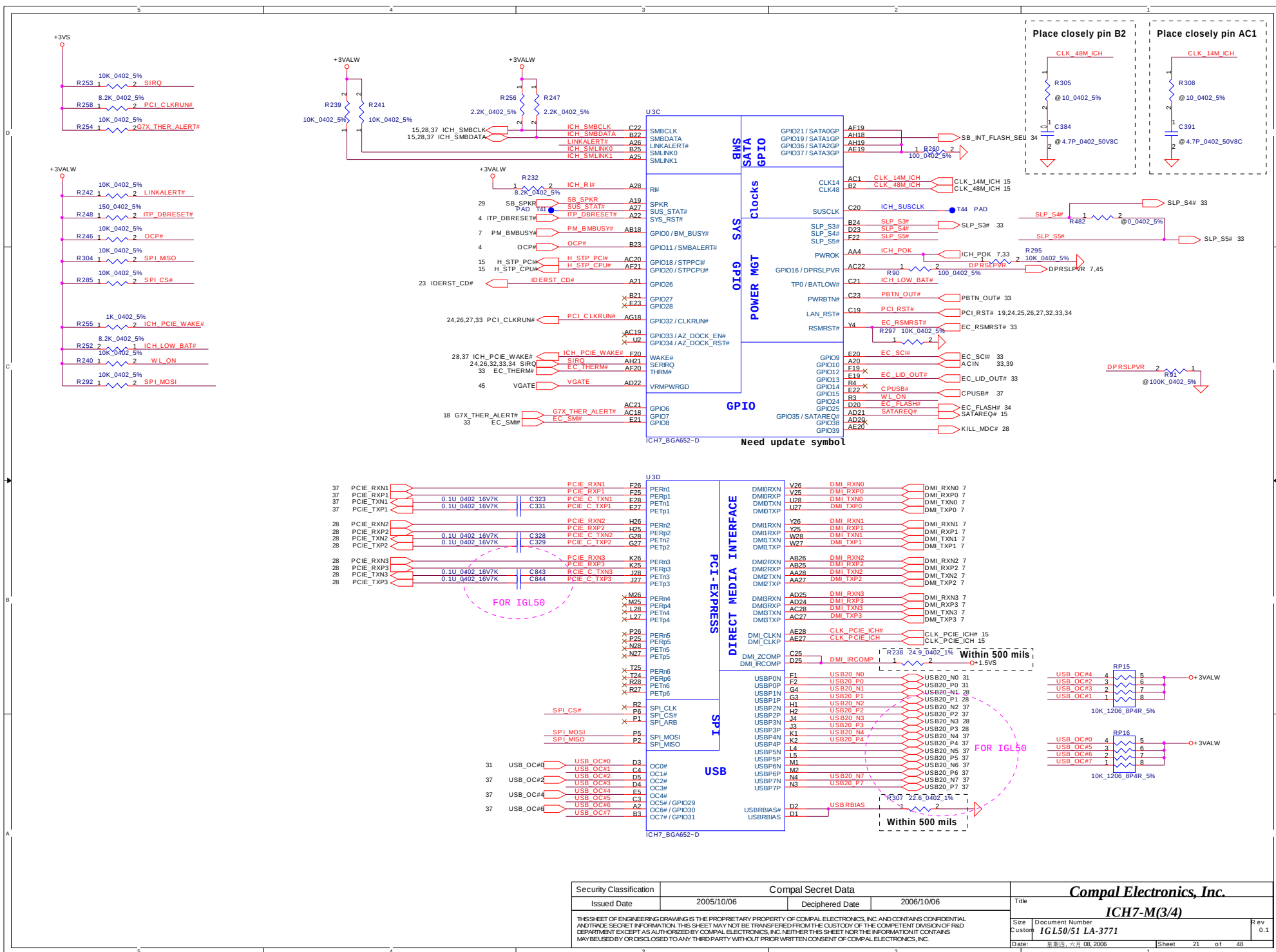


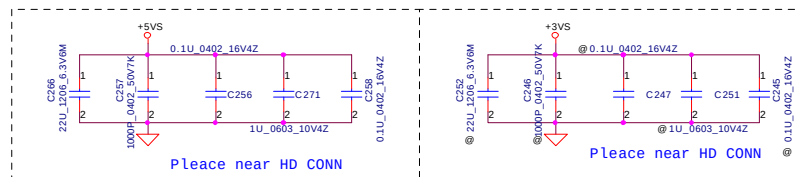
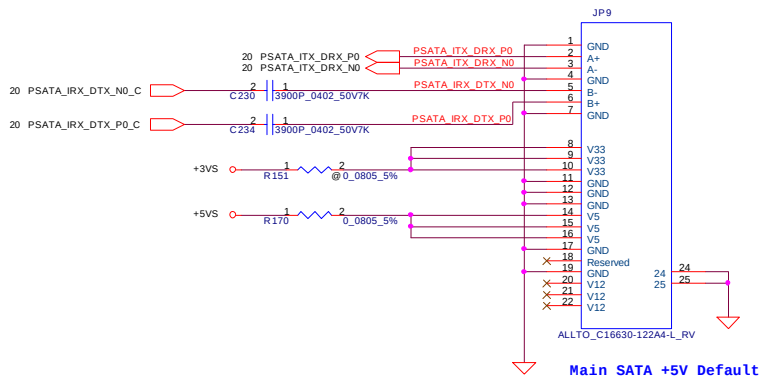
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		2005/10/06	Deciphered Date	2006/10/06	Title
					ICH7-M(1/4)
					Size Document Number
					IGL50/51 LA-3771
					R ev 0.1
					Date: 星期四, 六月 08, 2006 Sheet 19 of 48

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

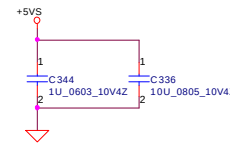
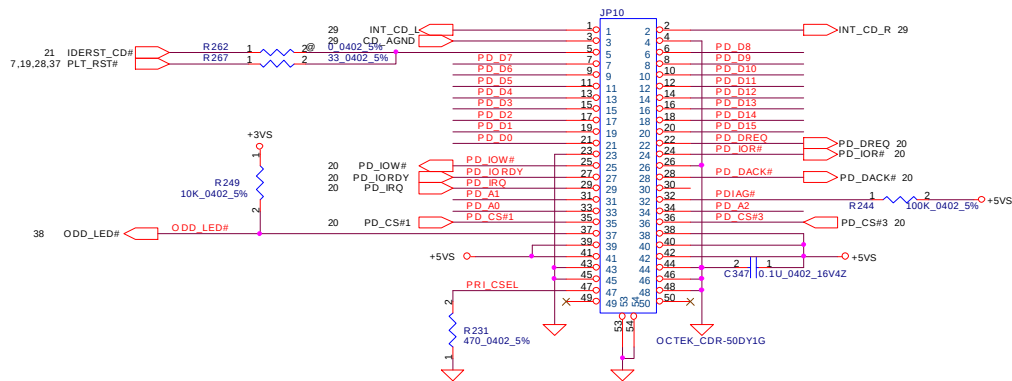


Security Classification		Compal Secret Data		Compal Electronics, Inc.				
Issued Date	2005/10/06	Deciphered Date	2006/10/06					
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE LOANED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Title				
				ICH7-M(2/4)				
				Size	Document Number		Rev 0	
				Custom	1GL50/51 LA-3771			
Date:				星期四, 六月 08, 2006				
				Sheet 20 of 48				



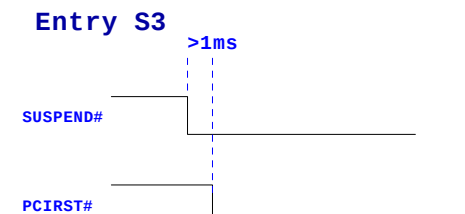
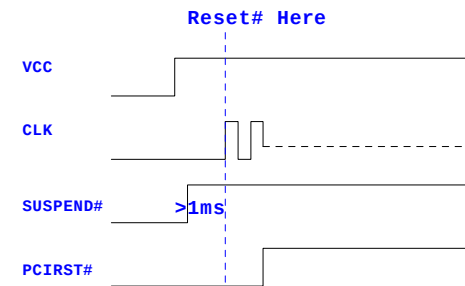


PD_D[0..15] PD_D[0..15] 20
PD_A[0..2] PD_A[0..2] 20

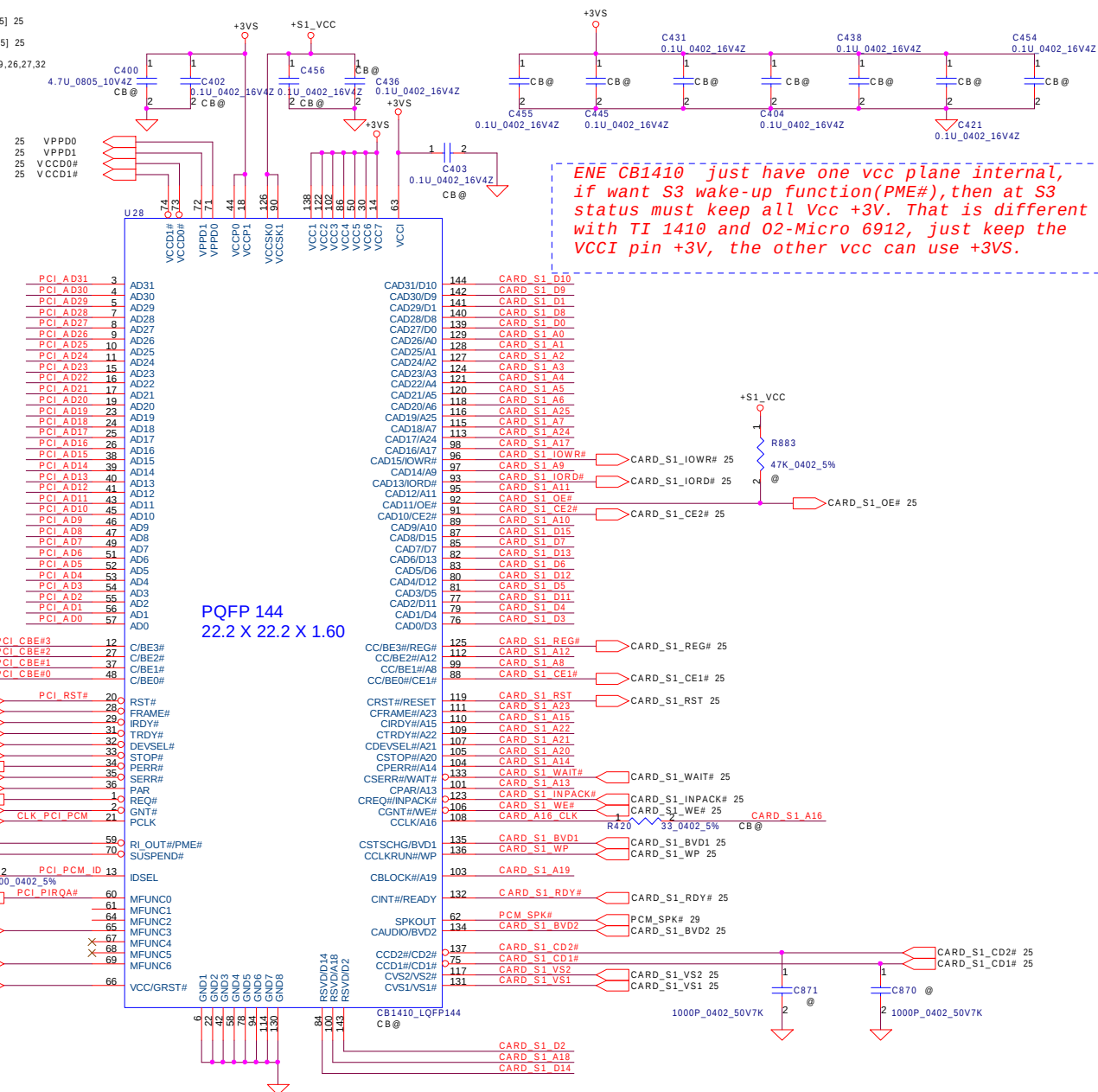


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		2005/10/06	Deciphered Date	2006/10/06	Title
					HDD & CDROM
					Size Document Number
					Custom IGL50/51 LA-3771
					Rev 0.1
					Date: 星期日, 六月 08, 2006
					Sheet 23 of 48

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

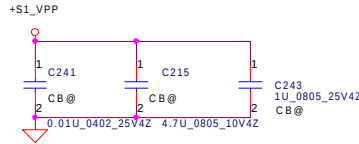


Note: MF0 -- MF6 must refer the data sheet for design.



Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2005/10/06	Deciphered Date	2006/10/06	Title	CB ENE1410(One Solt)	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custome	IGL50/51 LA-3771	0.1
Date:				日期	2006.08.08	Sheet 24 of 48

The schematic diagram illustrates the power supply section for the CP-2211-SSOP16 chip. The chip (U5) is connected to several external voltage sources and capacitors. The +5VS, +3VS, and +12V inputs are connected to the VCC pins of the chip. The +S1_VCC input is connected to the VPP pin. The chip also has pins for VCCD0, VCCD1, VPPD0, and VPPD1, which are connected to the VCCD0#, VCCD1#, VPPD0, and VPPD1 signals. The OC pin is connected to the PCI_RST# signal. The chip is also connected to ground (GND) and a shutdown pin (SHDN). The diagram includes capacitors C193, C194, C197, C198, C210, and C209, which are used for decoupling and filtering. The chip is labeled CP-2211-SSOP16.



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/10/06	Deciphered Date	2006/10/06	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				CardBus Socket	
				Document Number	
				IGL50151 LA-3771	
Date: 星期四, 2006/08/08				Sheet	25 of 48

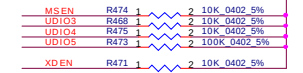
R5C832

SD, MMC, MS, XD multi-function pin define

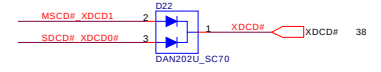
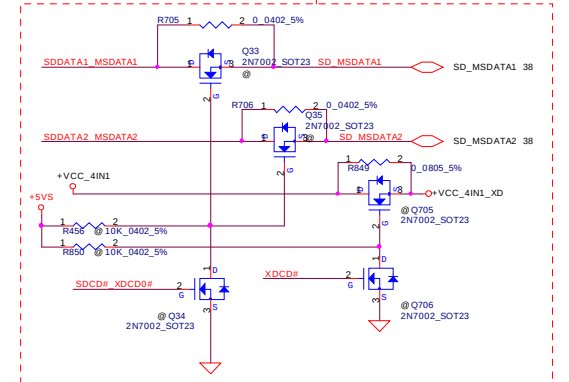
MDIO PIN Name	SD Card PIN Name	MMC Card PIN Name	MS Card PIN Name	XD Card PIN Name
MDIO00	SDCD#	MMCCD#		XDCD0#
MDIO01			XDCD1#	
MDIO02			XDCD2#	
MDIO03	SDWP#		XDR/B#	
MDIO04	SDPWR0	MMCPWR	MSWR	XDPWR
MDIO05	SDPWR1			XDWP#
MDIO06	SDLED#	MMCLD#	MSLED#	XDLED#
MDIO07			MSEXTCK	
MDIO08	SDCCMD	MMCCMD	MSBS	XDWE#
MDIO09	SDCLK	MMCLK	MSCCLK	XDRE#
MDIO10	SDCDAT0	MMCDAT	MSCDAT0	XDCDAT0
MDIO11	SDCDAT1		MSCDAT1	XDCDAT1
MDIO12	SDCDAT2		MSCDAT2	XDCDAT2
MDIO13	SDCDAT3		MSCDAT3	XDCDAT3
MDIO14				XDCDAT4
MDIO15				XDCDAT5
MDIO16				XDCDAT6
MDIO17				XDCDAT7
MDIO18				XCDCLE
MDIO19				XDALE

Function set pin define

UDIO3	UDIO4	MSEN	XDEN	Function
Pull-up	Pull-up	Pull-up	Pull-up	Enable SD, XD, MS, MMC Card



Solve MS Duo Adaptor short problem





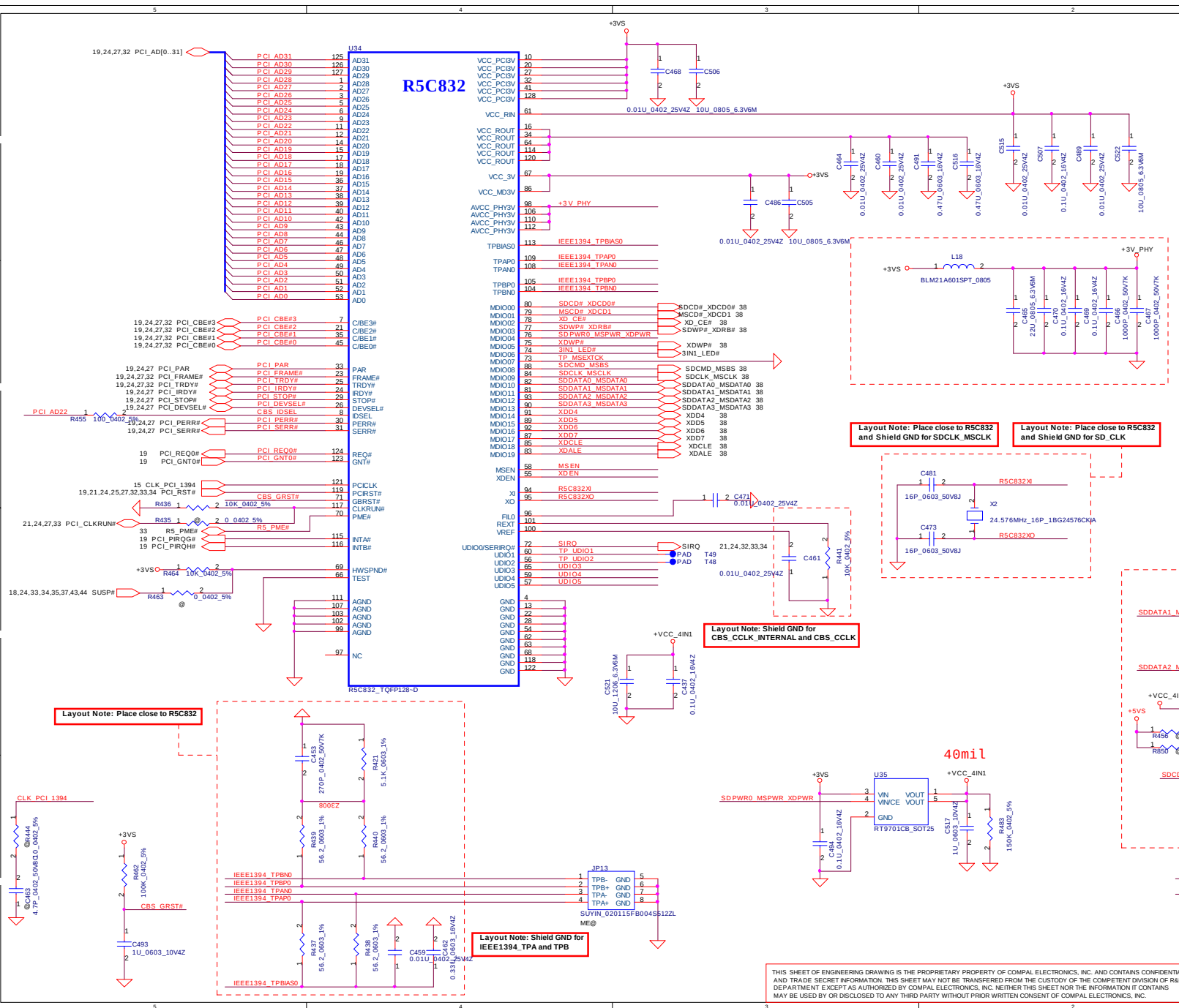
Compal Electronics, Inc.

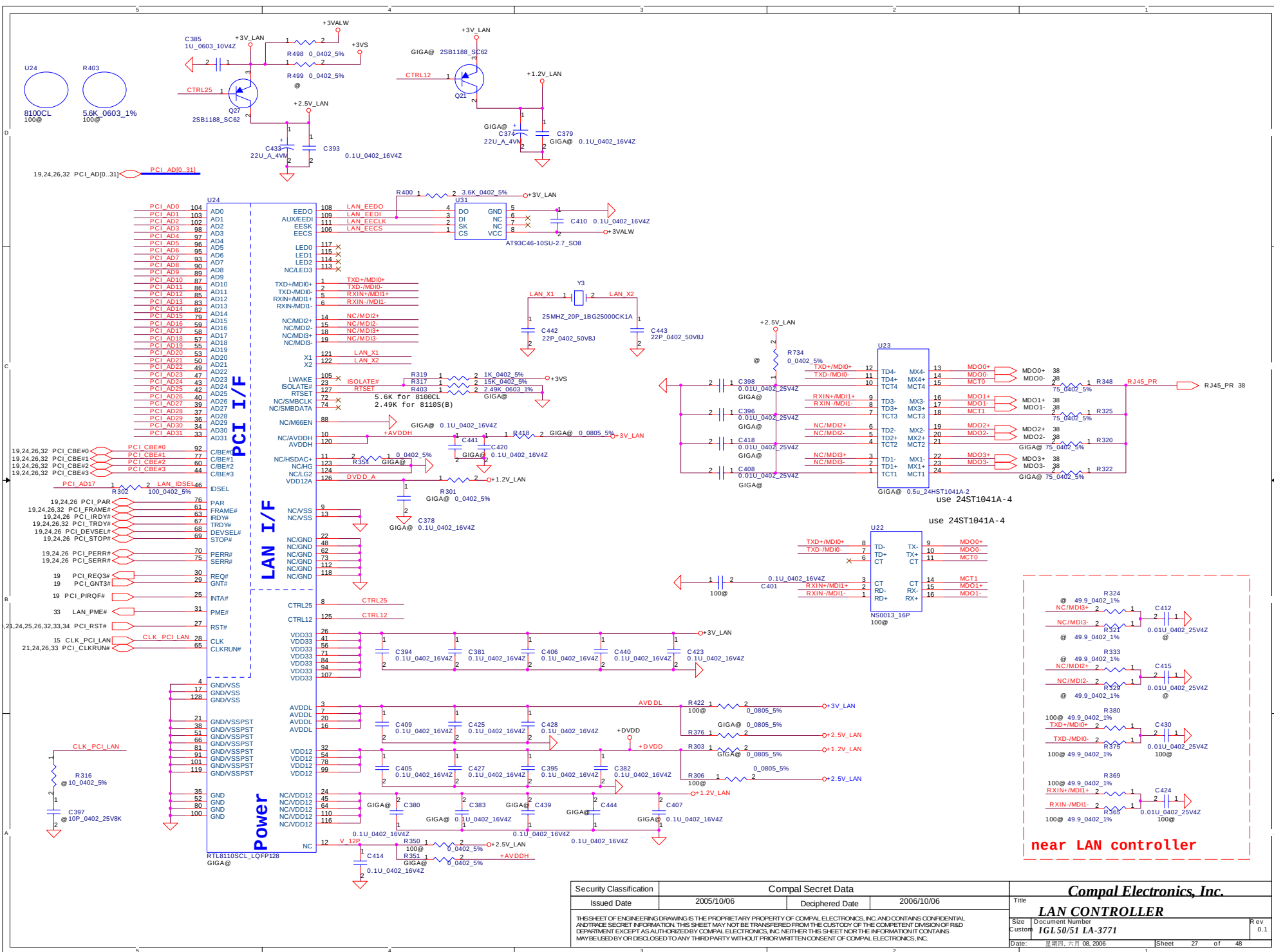
1394+3 in 1 Card

HGT30/31 LA3061

Date: 2005.08.05 Sheet: 26 of 48

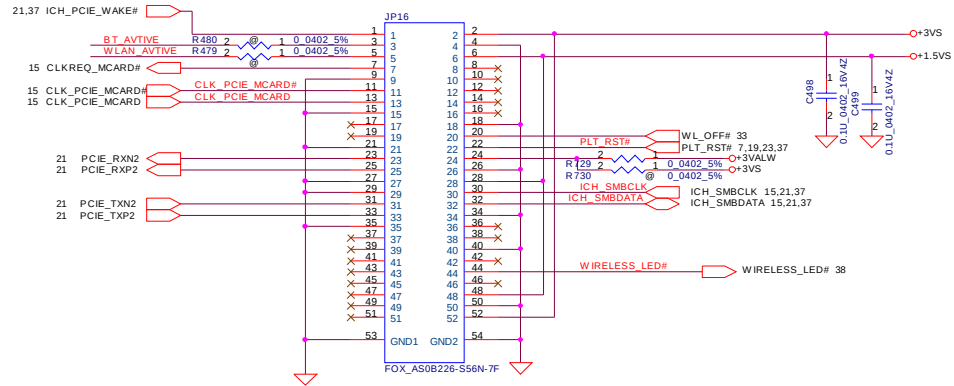
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OR R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.



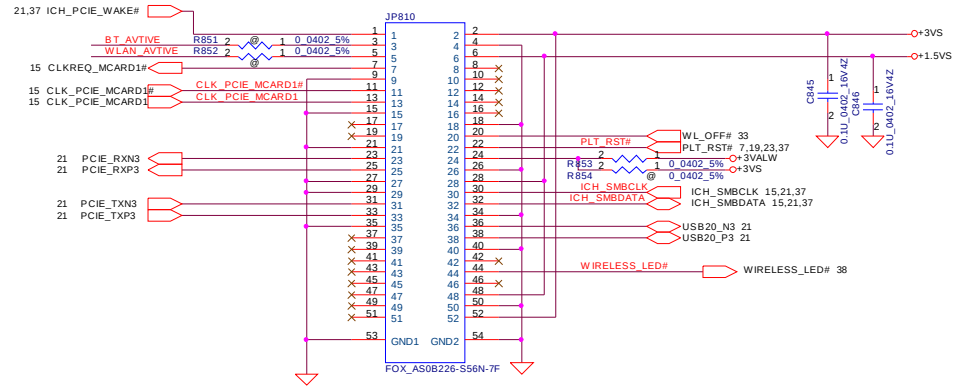


Security Classification		Compal Secret Data		Compal Electronics, Inc. LAN CONTROLLER	
Issued Date	2005/10/06	Deciphered Date	2006/10/06	Title	
THIS SET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	1GL50/51 LA-3771
					Rev 0.1
Date: 星期日, 六月 08, 2006				Sheet 27 of 48	

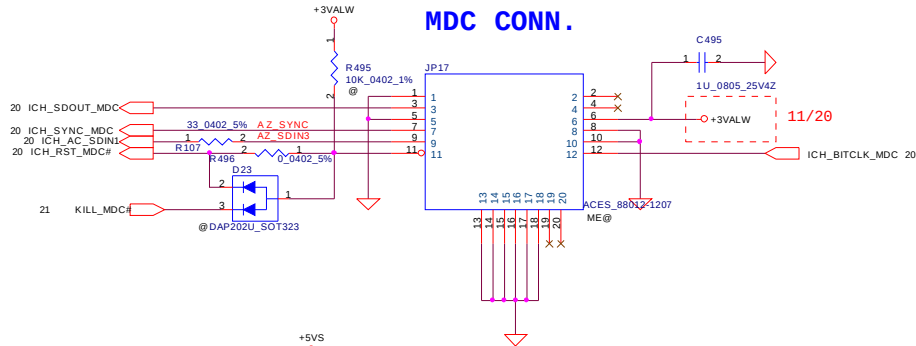
Mini-Express Card(Slot 1-WLAN)



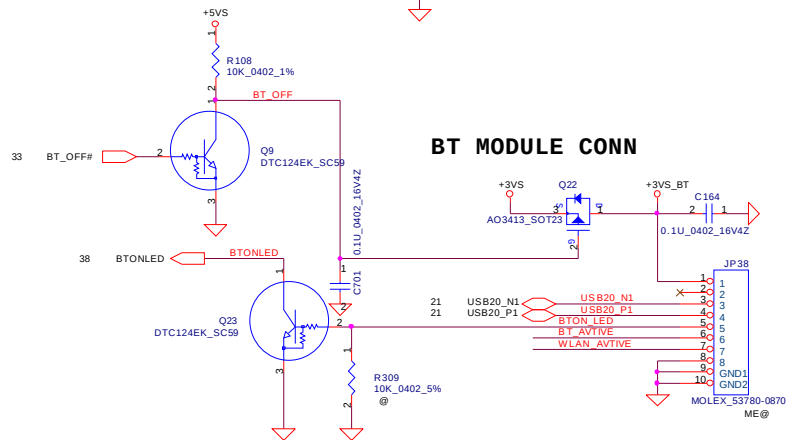
Mini-Express Card(Slot 2-WLAN)



MDC CONN.



BT MODULE CONN



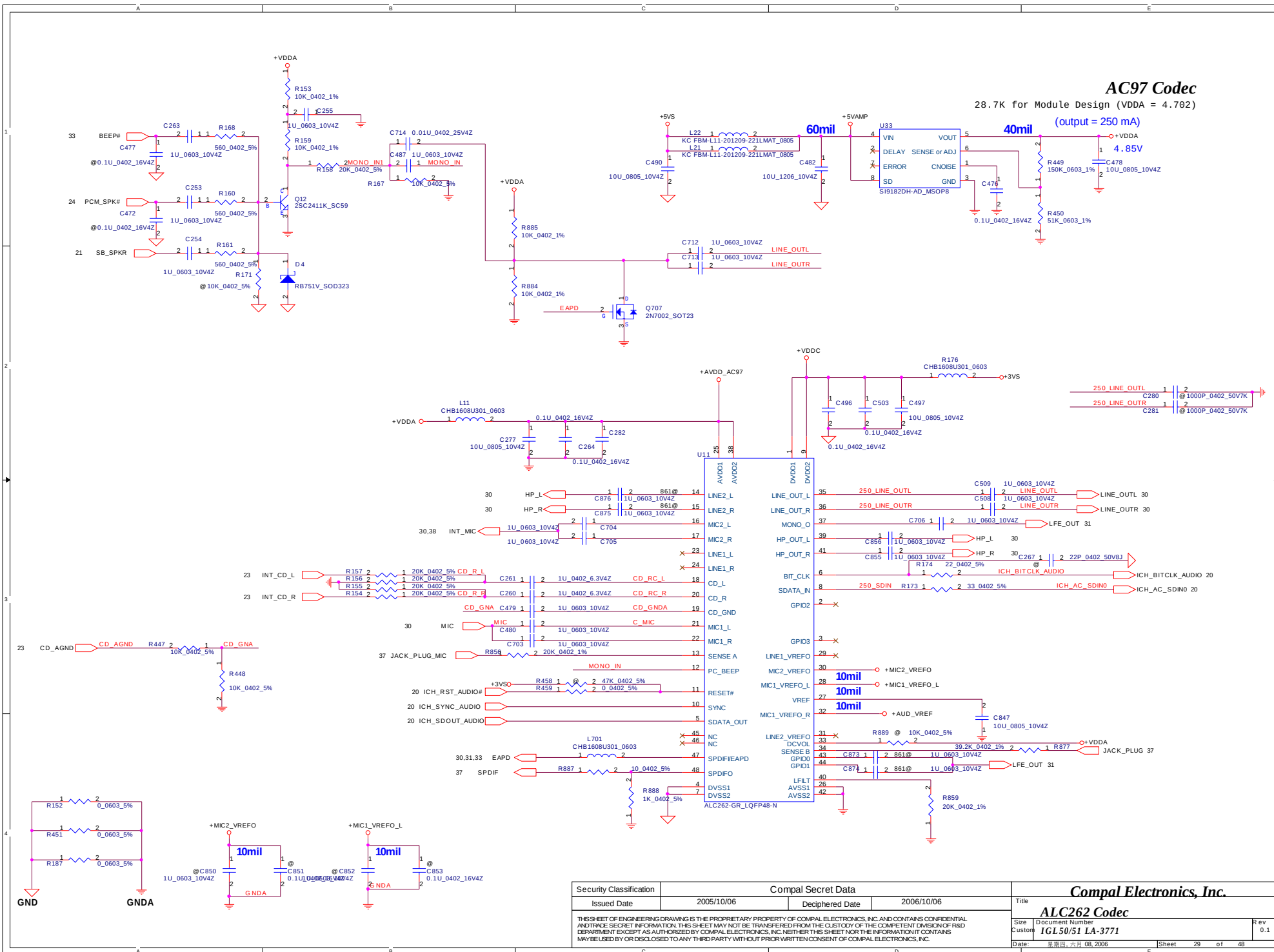
Security Classification	Compal Secret Data			<i>Compal Electronics, Inc.</i>		
Issued Date	2005/10/06	Deciphered Date	2006/10/06	Title	<i>Mini Card / MDC CONN</i>	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custn	1GL50/51 LA-3771	0.1

AC97 Codec

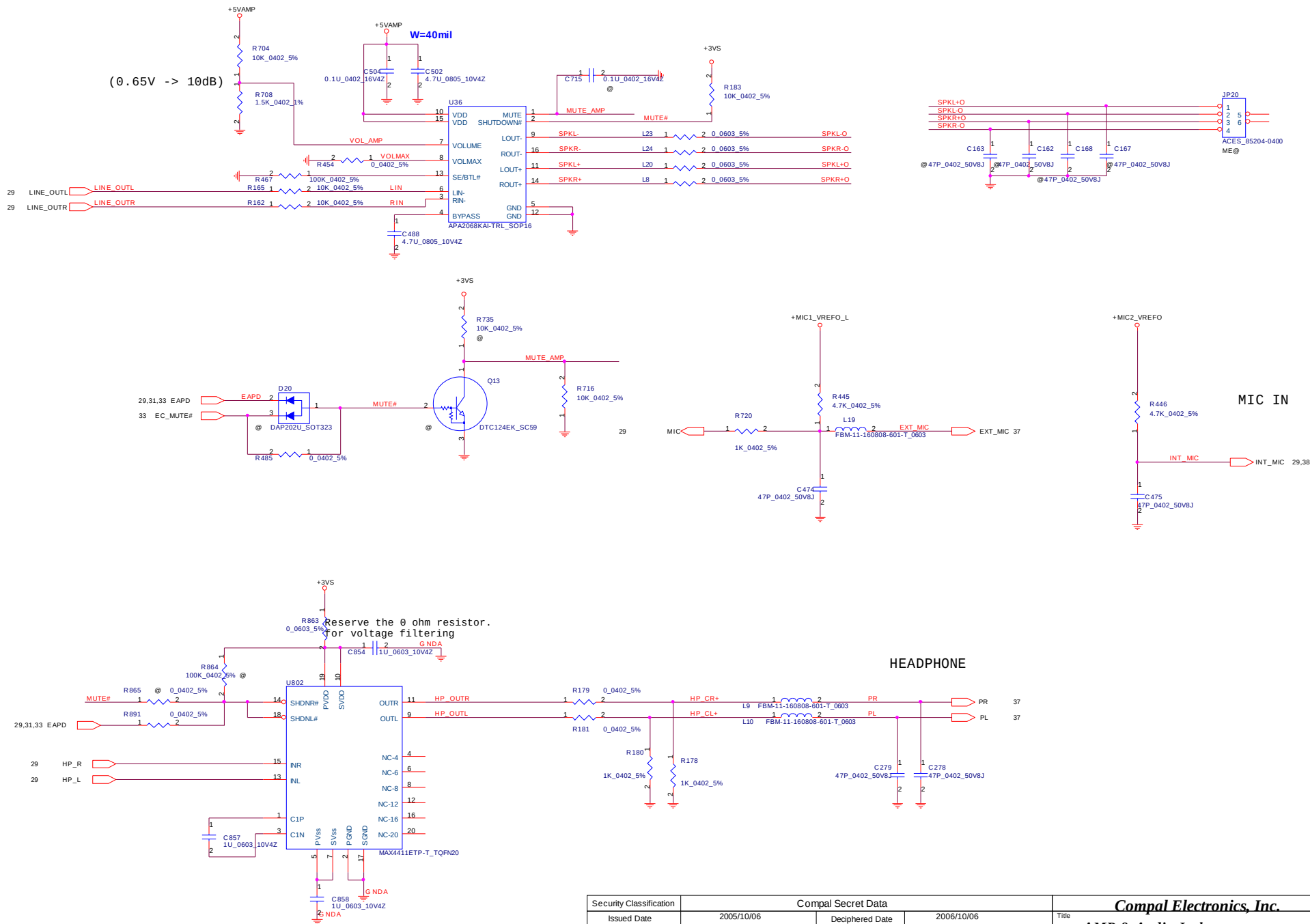
28.7K for Module Design (VDDA = 4.702)

(output = 250 mA)

4.85V

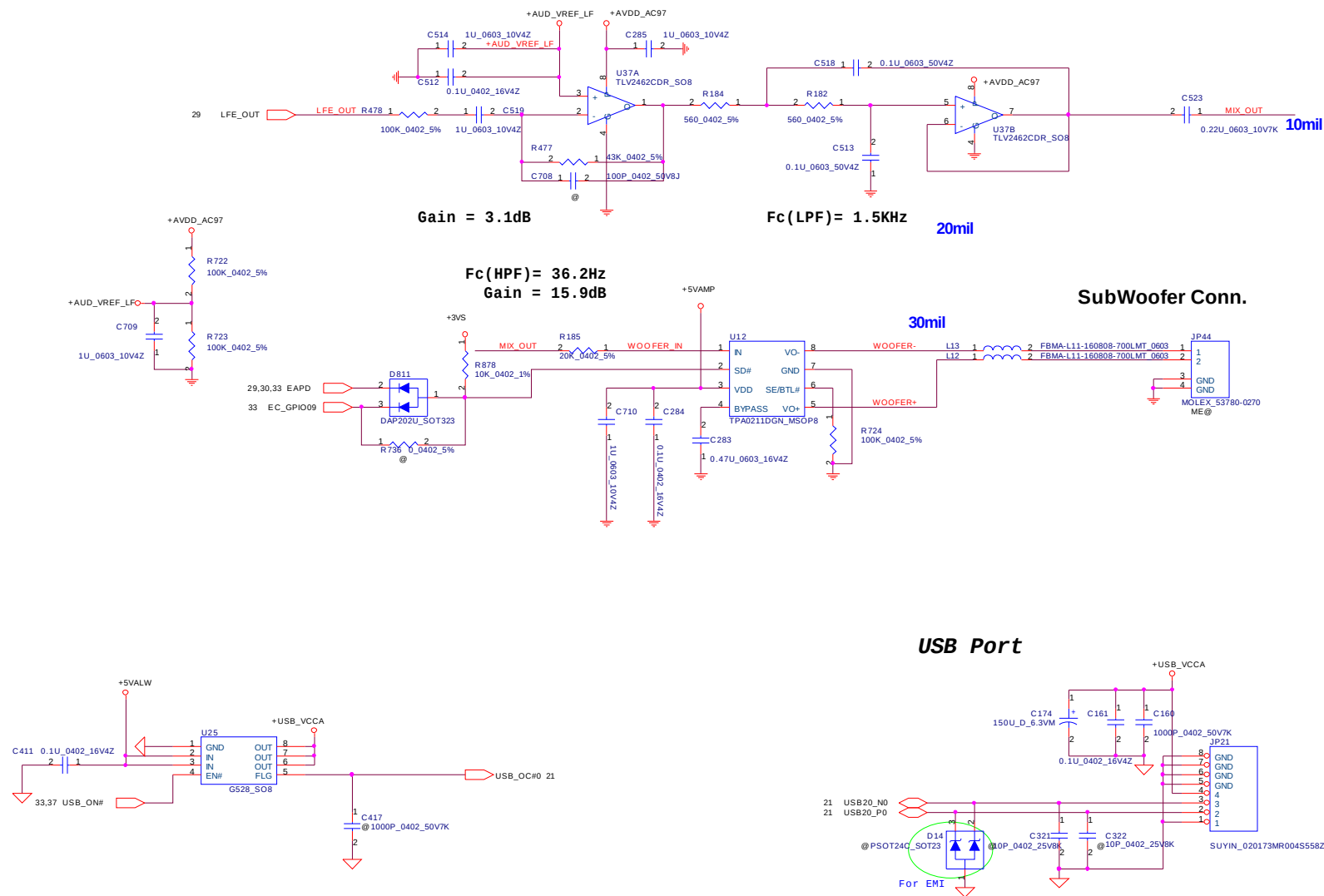


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		2005/10/06		Title	
		Deciphered Date		ALC262 Codec	
		2006/10/06		Size	
				Document Number	
				IGL50/51 LA-3771	
				Rev	
				0.1	
				Date: 星期日, 六月 08, 2006	
				Sheet 29 of 48	



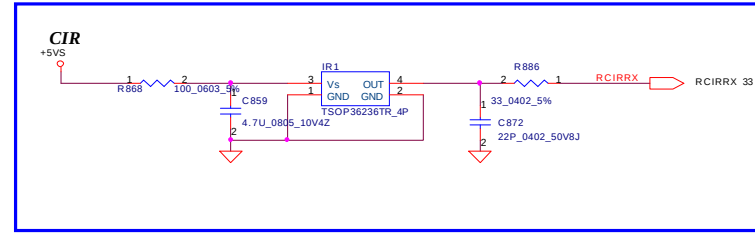
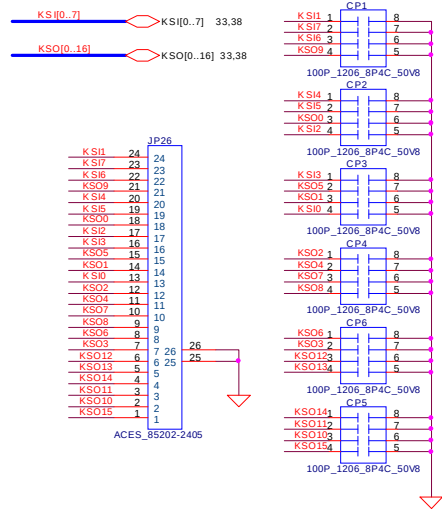
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/10/06	Deciphered Date	2006/10/06	Title AMP & Audio Jack	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Document Number IGL50/51 LA-3771
				Date: 星期四, 六月 08, 2006	Sheet 30 of 48
				Rev 0.1	

WOOFER@

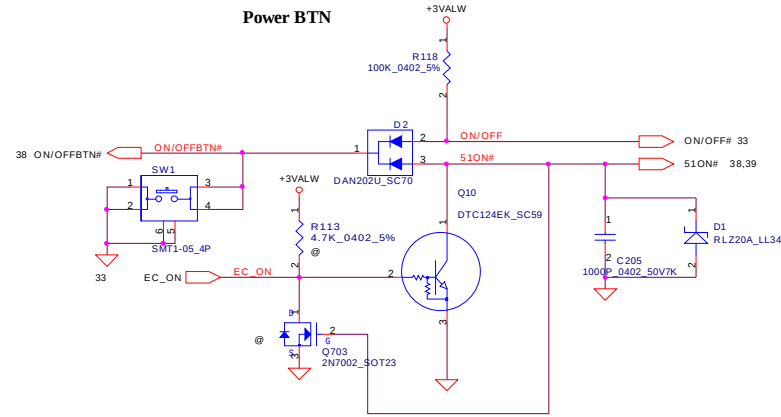


Security Classification		Compal Secret Data		Compal Electronics, Inc. Bluetooth & USB CONN.		
Issued Date	2005/10/06	Deciphered Date	2006/10/06			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Document Number 1GL50/51 LA-3771	Rev 0.
Date:				日期: 六月 06, 2006	Sheet 31 of 48	

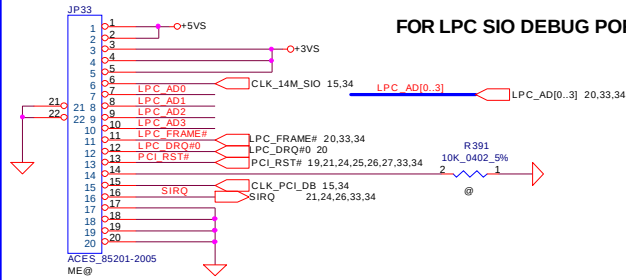
INT_KBD CONN.(TYPE "D" KB)



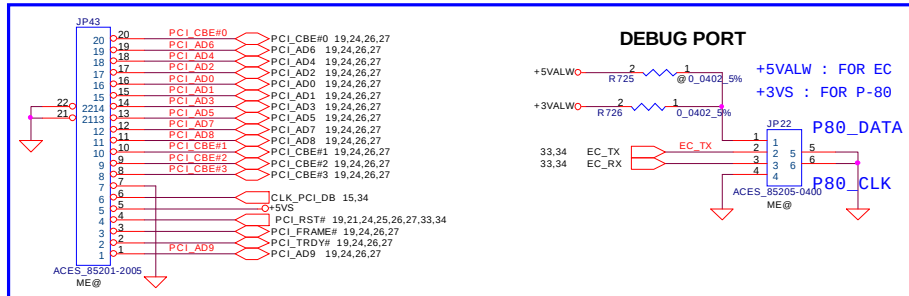
Power BTN



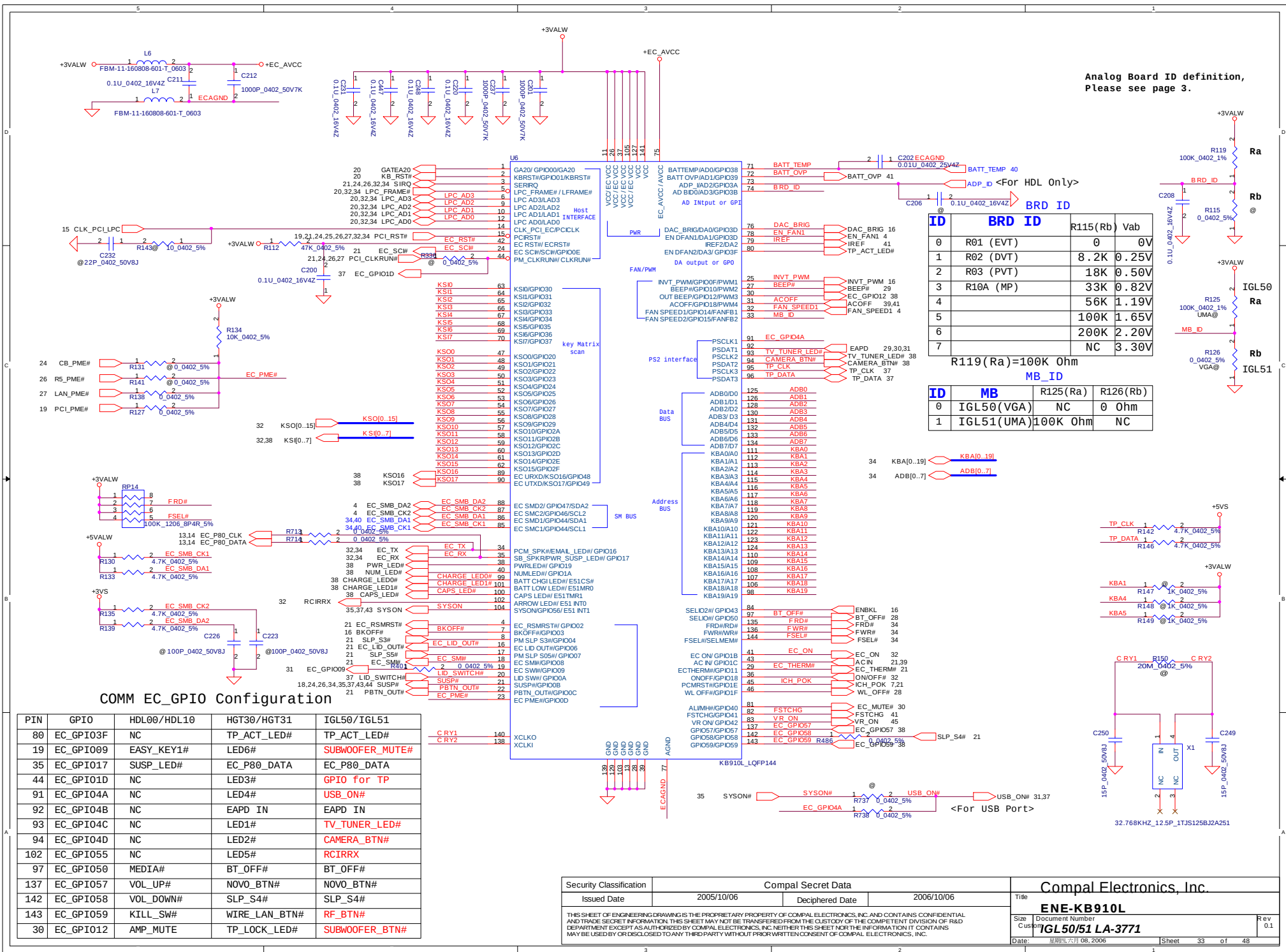
FOR LPC SIO DEBUG PORT

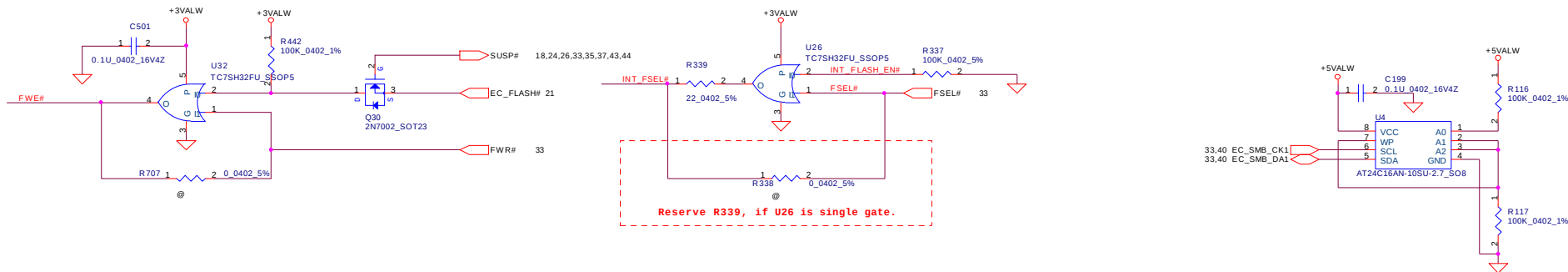


DEBUG PORT



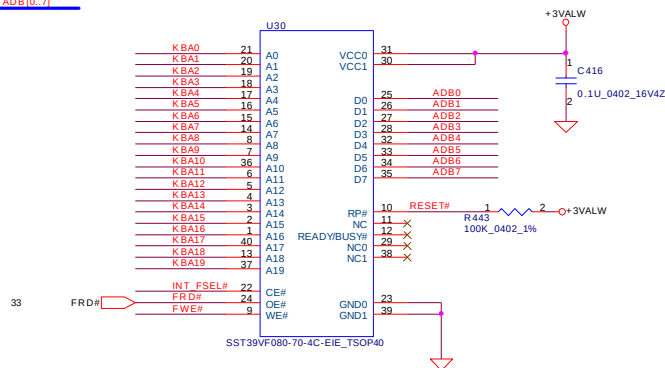
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		Deciphered Date		Title	
2005/10/06		2006/10/06		KBD,ON/OFF,T/P,LED/B,DEBUG	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE REPRODUCED OR TRANSMITTED IN ANY FORM OR BY ANY MEANS, ELECTRONIC OR MECHANICAL, WITHOUT PRIOR WRITTEN PERMISSION OF COMPAL ELECTRONICS, INC.		Size		Document Number	
				IGL50/51 LA-3771	
				Rev	
				0.1	
				Date: 星期日, 六月 08, 2006	
				Sheet 32 of 48	



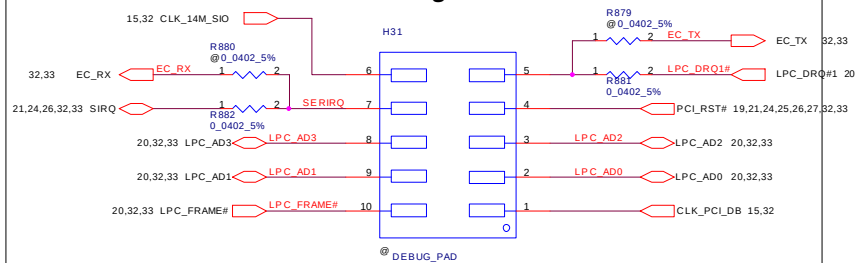


33 KBA[0..19] KBA[0..19]
33 ADB[0..7] ADB[0..7]

1MB Flash ROM



New LPC Debug Pad ---- MB side



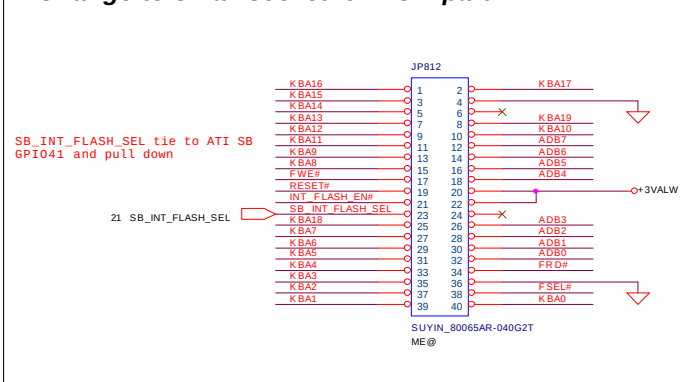
Under DDR ME Assignment Area

Keep Resistor near Debug Pad and in the same side

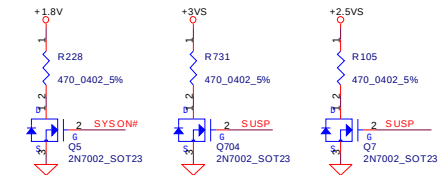
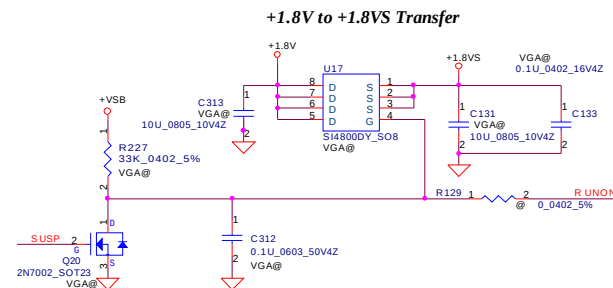
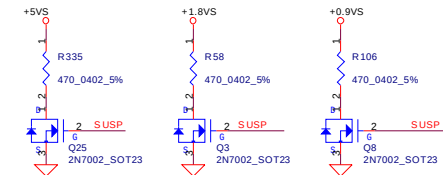
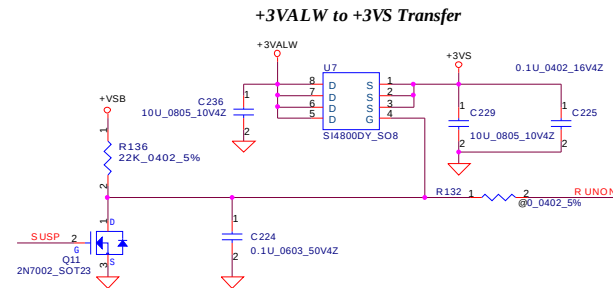
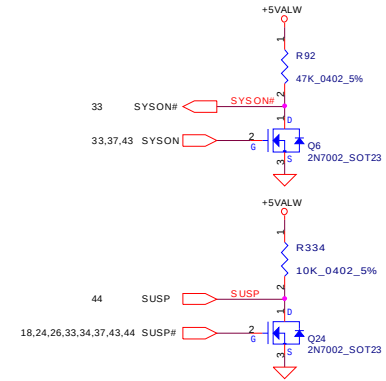
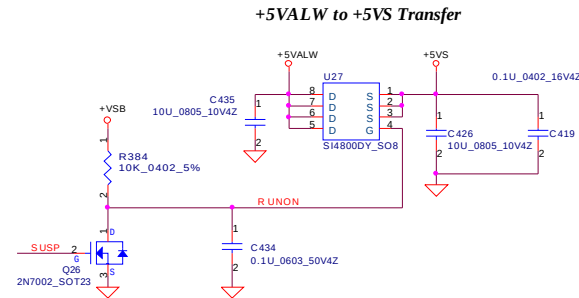
Standard side DIMM ---- Pin 1 near DIMM

Reverse side DIMM ---- Pin 1 keep away DIMM

Change to small socket for ROM part

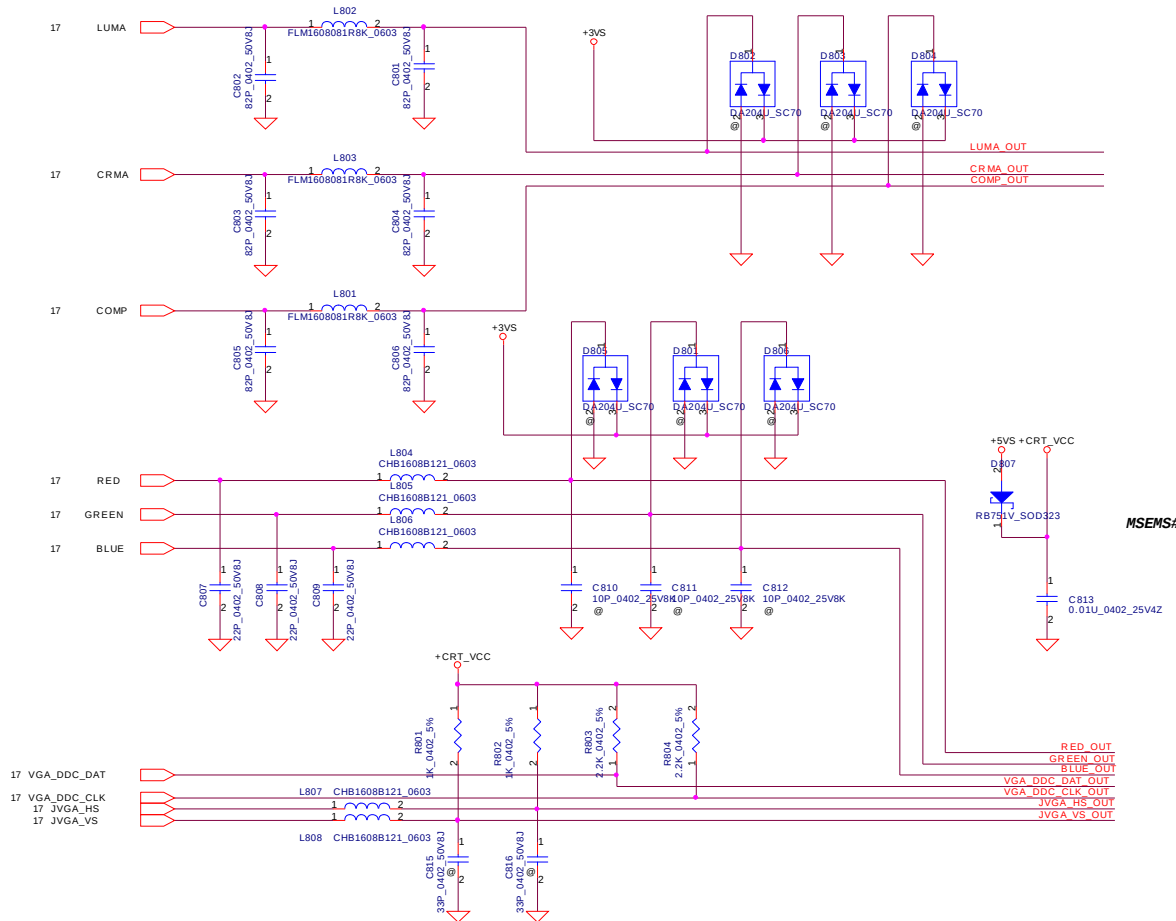


Security Classification	Compal Secret Data		Title	
Issued Date	2005/10/06	Deciphered Date	2006/10/06	Compal Electronics, Inc.
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				BIOS & EC I/O Port
Size	Custom	Document Number	1GL50/51 LA-3771	Rev 0.1
Date:	星期四, 六月 08, 2006	Sheet	34	of 48



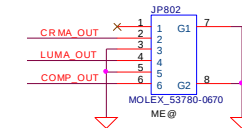
Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/10/06	Deciphered Date	2006/10/06	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				DC/DC Circuit
Size	Document Number	Rev		0.1
Custom	1GL50/51 LA-3771	Date		星期四, 六月 08, 2006
		Sheet	35	of 48

CLOSE TO JTVOUT1

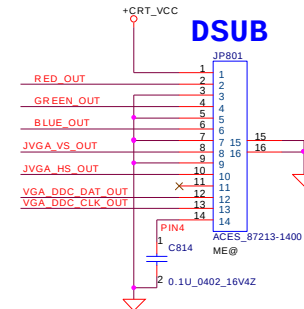


VGA I/O PORT Connector

S-VIDEO



DSUB



PIN ASSIGNMENT

PIN	D-SUB	FUNCTION	PIN	SVIDEO	FUNCTION
1	9	+CRT_VCC	1	1	NC
2	1	RED	2	4	CRMA
3	6	GND	3	2	GND
4	2	GREEN	4	3	LUMA
5	7	GND	5	5	GND
6	3	BLUE	6	6	CVBS
7	8	GND			
8	14	VSYNC			
9	10	GND			
10	13	HSYNC			
11	11	SENSE			
12	12	SM_DAT			
13	15	SM_CLK			
14	4	PIN4			

Security Classification		Compal Secret Data		Title	
Issued Date		Deciphered Date		Document Number	
2005/03/10		2006/03/10		IGL 50/51 LA-3771	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		Date		Rev	
2005/03/10		2006/03/10		0.1	
2005/03/10		2006/03/10		0.1	

Compal Electronics, Inc.

CRT & TVout Connector

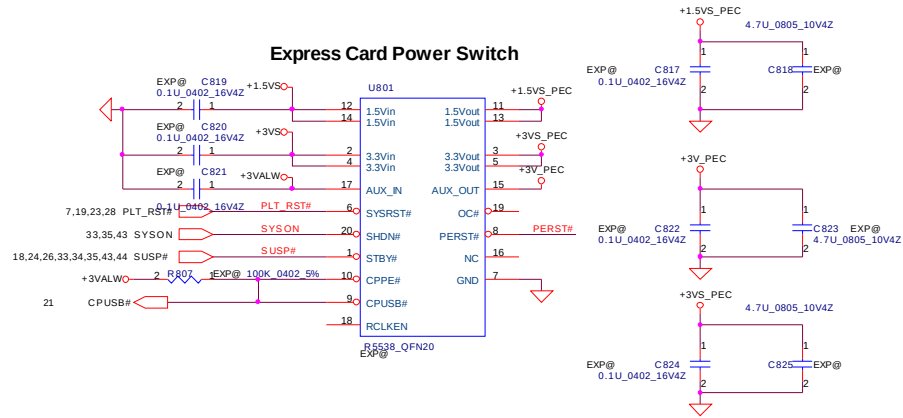
IGL 50/51 LA-3771

Date: 2005/03/10

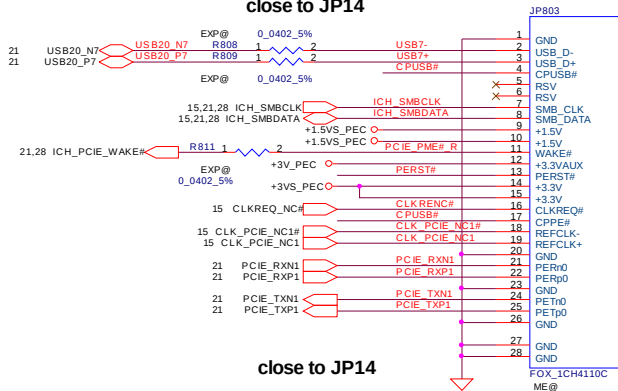
Sheet 36 of 48

NEW CARD FOR C38

Express Card Power Switch



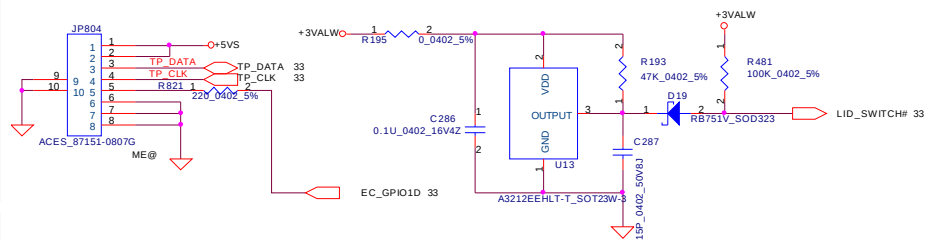
close to JP14



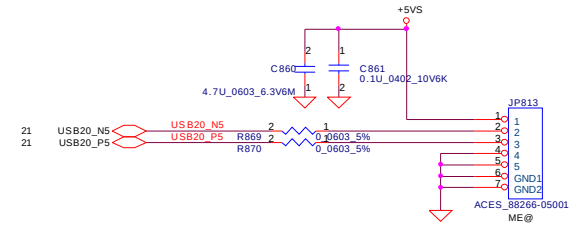
close to JP14

T/P Board

LID Switch

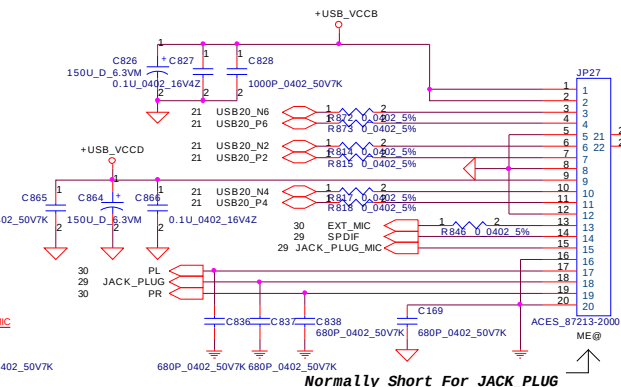
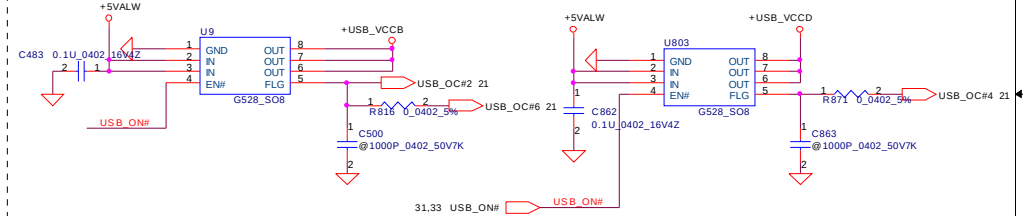


CMOS Camera Conn



USB board

Pulled up on ICH7M side



C835 ~ C838 For EMI Solution

Normally Short For JACK_PLUG

Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/03/10	Deciphered Date	2006/03/10	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				INDICATE LED
				Size
				Document Number
				Customer
				IGL50/51 LA-3771
				Rev
				0.1
				Date
				星期日, 六月 08, 2006
				Sheet
				37 of 48

4 in 1 Card Reader

Pinout diagram for the 4 in 1 Card Reader (TAITW_R012-210-LR) showing connections for SD cards (SD0, SD1, SD2, SD3) and SDIO (SD0, SD1, SD2, SD3) to the TAITW_R012-210-LR.

Pin 1: +VCC_4IN1_XD
Pin 2: XD-VCC
Pin 3: SD-VCC
Pin 4: MS-VCC
Pin 5: SD-CLK
Pin 6: SD-DAT0
Pin 7: SD-DAT1
Pin 8: SD-DAT2
Pin 9: SD-DAT3
Pin 10: SD-CMD
Pin 11: SD-CD-SW
Pin 12: SD-CD-COM
Pin 13: SD-WP-SW
Pin 14: SD-WP-COM
Pin 15: MS-SCLK
Pin 16: MS-DATA0
Pin 17: MS-DATA1
Pin 18: MS-DATA2
Pin 19: MS-DATA3
Pin 20: MS-MS
Pin 21: MS-BS
Pin 22: SD-GND
Pin 23: N.C.
Pin 24: N.C.
Pin 25: SHIELD GND
Pin 26: SHIELD GND

UMA LCD/PANEL Conn.

Pinout diagram for the UMA LCD/PANEL Conn. (ACES_87216-30006) showing connections for LVDS signals (LVDS0+, LVDS0-, LVDS1+, LVDS1-, LVDS2+, LVDS2-, LVDS3+, LVDS3-, LVDS4+, LVDS4-) and EDID signals (EDID_CLK_LCD, EDID_DAT_LCD) to the ACES_87216-30006.

Pin 1: EDID_CLK_LCD
Pin 2: EDID_DAT_LCD
Pin 3: LVDS0+
Pin 4: LVDS0-
Pin 5: LVDS1+
Pin 6: LVDS1-
Pin 7: LVDS2+
Pin 8: LVDS2-
Pin 9: LVDS3+
Pin 10: LVDS3-
Pin 11: LVDS4+
Pin 12: LVDS4-
Pin 13: LVDS0+
Pin 14: LVDS0-
Pin 15: LVDS1+
Pin 16: LVDS1-
Pin 17: LVDS2+
Pin 18: LVDS2-
Pin 19: LVDS3+
Pin 20: LVDS3-
Pin 21: LVDS4+
Pin 22: LVDS4-
Pin 23: EDID_CLK_LCD
Pin 24: EDID_DAT_LCD
Pin 25: LVDS0+
Pin 26: LVDS0-
Pin 27: LVDS1+
Pin 28: LVDS1-
Pin 29: LVDS2+
Pin 30: LVDS2-
Pin 31: LVDS3+
Pin 32: LVDS3-
Pin 33: LVDS4+
Pin 34: LVDS4-
Pin 35: EDID_CLK_LCD
Pin 36: EDID_DAT_LCD
Pin 37: LVDS0+
Pin 38: LVDS0-
Pin 39: LVDS1+
Pin 40: LVDS1-
Pin 41: LVDS2+
Pin 42: LVDS2-
Pin 43: LVDS3+
Pin 44: LVDS3-
Pin 45: LVDS4+
Pin 46: LVDS4-
Pin 47: EDID_CLK_LCD
Pin 48: EDID_DAT_LCD
Pin 49: LVDS0+
Pin 50: LVDS0-
Pin 51: LVDS1+
Pin 52: LVDS1-
Pin 53: LVDS2+
Pin 54: LVDS2-
Pin 55: LVDS3+
Pin 56: LVDS3-
Pin 57: LVDS4+
Pin 58: LVDS4-
Pin 59: EDID_CLK_LCD
Pin 60: EDID_DAT_LCD
Pin 61: LVDS0+
Pin 62: LVDS0-
Pin 63: LVDS1+
Pin 64: LVDS1-
Pin 65: LVDS2+
Pin 66: LVDS2-
Pin 67: LVDS3+
Pin 68: LVDS3-
Pin 69: LVDS4+
Pin 70: LVDS4-
Pin 71: EDID_CLK_LCD
Pin 72: EDID_DAT_LCD
Pin 73: LVDS0+
Pin 74: LVDS0-
Pin 75: LVDS1+
Pin 76: LVDS1-
Pin 77: LVDS2+
Pin 78: LVDS2-
Pin 79: LVDS3+
Pin 80: LVDS3-
Pin 81: LVDS4+
Pin 82: LVDS4-
Pin 83: EDID_CLK_LCD
Pin 84: EDID_DAT_LCD
Pin 85: LVDS0+
Pin 86: LVDS0-
Pin 87: LVDS1+
Pin 88: LVDS1-
Pin 89: LVDS2+
Pin 90: LVDS2-
Pin 91: LVDS3+
Pin 92: LVDS3-
Pin 93: LVDS4+
Pin 94: LVDS4-
Pin 95: EDID_CLK_LCD
Pin 96: EDID_DAT_LCD
Pin 97: LVDS0+
Pin 98: LVDS0-
Pin 99: LVDS1+
Pin 100: LVDS1-
Pin 101: LVDS2+
Pin 102: LVDS2-
Pin 103: LVDS3+
Pin 104: LVDS3-
Pin 105: LVDS4+
Pin 106: LVDS4-
Pin 107: EDID_CLK_LCD
Pin 108: EDID_DAT_LCD
Pin 109: LVDS0+
Pin 110: LVDS0-
Pin 111: LVDS1+
Pin 112: LVDS1-
Pin 113: LVDS2+
Pin 114: LVDS2-
Pin 115: LVDS3+
Pin 116: LVDS3-
Pin 117: LVDS4+
Pin 118: LVDS4-
Pin 119: EDID_CLK_LCD
Pin 120: EDID_DAT_LCD
Pin 121: LVDS0+
Pin 122: LVDS0-
Pin 123: LVDS1+
Pin 124: LVDS1-
Pin 125: LVDS2+
Pin 126: LVDS2-
Pin 127: LVDS3+
Pin 128: LVDS3-
Pin 129: LVDS4+
Pin 130: LVDS4-
Pin 131: EDID_CLK_LCD
Pin 132: EDID_DAT_LCD
Pin 133: LVDS0+
Pin 134: LVDS0-
Pin 135: LVDS1+
Pin 136: LVDS1-
Pin 137: LVDS2+
Pin 138: LVDS2-
Pin 139: LVDS3+
Pin 140: LVDS3-
Pin 141: LVDS4+
Pin 142: LVDS4-
Pin 143: EDID_CLK_LCD
Pin 144: EDID_DAT_LCD
Pin 145: LVDS0+
Pin 146: LVDS0-
Pin 147: LVDS1+
Pin 148: LVDS1-
Pin 149: LVDS2+
Pin 150: LVDS2-
Pin 151: LVDS3+
Pin 152: LVDS3-
Pin 153: LVDS4+
Pin 154: LVDS4-
Pin 155: EDID_CLK_LCD
Pin 156: EDID_DAT_LCD
Pin 157: LVDS0+
Pin 158: LVDS0-
Pin 159: LVDS1+
Pin 160: LVDS1-
Pin 161: LVDS2+
Pin 162: LVDS2-
Pin 163: LVDS3+
Pin 164: LVDS3-
Pin 165: LVDS4+
Pin 166: LVDS4-
Pin 167: EDID_CLK_LCD
Pin 168: EDID_DAT_LCD
Pin 169: LVDS0+
Pin 170: LVDS0-
Pin 171: LVDS1+
Pin 172: LVDS1-
Pin 173: LVDS2+
Pin 174: LVDS2-
Pin 175: LVDS3+
Pin 176: LVDS3-
Pin 177: LVDS4+
Pin 178: LVDS4-
Pin 179: EDID_CLK_LCD
Pin 180: EDID_DAT_LCD
Pin 181: LVDS0+
Pin 182: LVDS0-
Pin 183: LVDS1+
Pin 184: LVDS1-
Pin 185: LVDS2+
Pin 186: LVDS2-
Pin 187: LVDS3+
Pin 188: LVDS3-
Pin 189: LVDS4+
Pin 190: LVDS4-
Pin 191: EDID_CLK_LCD
Pin 192: EDID_DAT_LCD
Pin 193: LVDS0+
Pin 194: LVDS0-
Pin 195: LVDS1+
Pin 196: LVDS1-
Pin 197: LVDS2+
Pin 198: LVDS2-
Pin 199: LVDS3+
Pin 200: LVDS3-
Pin 201: LVDS4+
Pin 202: LVDS4-
Pin 203: EDID_CLK_LCD
Pin 204: EDID_DAT_LCD
Pin 205: LVDS0+
Pin 206: LVDS0-
Pin 207: LVDS1+
Pin 208: LVDS1-
Pin 209: LVDS2+
Pin 210: LVDS2-
Pin 211: LVDS3+
Pin 212: LVDS3-
Pin 213: LVDS4+
Pin 214: LVDS4-
Pin 215: EDID_CLK_LCD
Pin 216: EDID_DAT_LCD
Pin 217: LVDS0+
Pin 218: LVDS0-
Pin 219: LVDS1+
Pin 220: LVDS1-
Pin 221: LVDS2+
Pin 222: LVDS2-
Pin 223: LVDS3+
Pin 224: LVDS3-
Pin 225: LVDS4+
Pin 226: LVDS4-
Pin 227: EDID_CLK_LCD
Pin 228: EDID_DAT_LCD
Pin 229: LVDS0+
Pin 230: LVDS0-
Pin 231: LVDS1+
Pin 232: LVDS1-
Pin 233: LVDS2+
Pin 234: LVDS2-
Pin 235: LVDS3+
Pin 236: LVDS3-
Pin 237: LVDS4+
Pin 238: LVDS4-
Pin 239: EDID_CLK_LCD
Pin 240: EDID_DAT_LCD
Pin 241: LVDS0+
Pin 242: LVDS0-
Pin 243: LVDS1+
Pin 244: LVDS1-
Pin 245: LVDS2+
Pin 246: LVDS2-
Pin 247: LVDS3+
Pin 248: LVDS3-
Pin 249: LVDS4+
Pin 250: LVDS4-
Pin 251: EDID_CLK_LCD
Pin 252: EDID_DAT_LCD
Pin 253: LVDS0+
Pin 254: LVDS0-
Pin 255: LVDS1+
Pin 256: LVDS1-
Pin 257: LVDS2+
Pin 258: LVDS2-
Pin 259: LVDS3+
Pin 260: LVDS3-
Pin 261: LVDS4+
Pin 262: LVDS4-
Pin 263: EDID_CLK_LCD
Pin 264: EDID_DAT_LCD
Pin 265: LVDS0+
Pin 266: LVDS0-
Pin 267: LVDS1+
Pin 268: LVDS1-
Pin 269: LVDS2+
Pin 270: LVDS2-
Pin 271: LVDS3+
Pin 272: LVDS3-
Pin 273: LVDS4+
Pin 274: LVDS4-
Pin 275: EDID_CLK_LCD

4 in 1 Card Reader

Pinout diagram for the 4 in 1 Card Reader (TAITW_R012-210-LR) showing connections for SD cards (SD0, SD1, SD2, SD3) and SDIO (SD0, SD1, SD2, SD3) to the TAITW_R012-210-LR.

Pin 1: +VCC_4IN1_XD
Pin 2: XD-VCC
Pin 3: SD-VCC
Pin 4: MS-VCC
Pin 5: SD-CLK
Pin 6: SD-DAT0
Pin 7: SD-DAT1
Pin 8: SD-DAT2
Pin 9: SD-DAT3
Pin 10: SD-CMD
Pin 11: SD-CD-SW
Pin 12: SD-CD-COM
Pin 13: SD-WP-SW
Pin 14: SD-WP-COM
Pin 15: MS-SCLK
Pin 16: MS-DATA0
Pin 17: MS-DATA1
Pin 18: MS-DATA2
Pin 19: MS-DATA3
Pin 20: MS-MS
Pin 21: MS-BS
Pin 22: SD-GND
Pin 23: N.C.
Pin 24: N.C.
Pin 25: SHIELD GND
Pin 26: SHIELD GND

UMA LCD/PANEL Conn.

Pinout diagram for the UMA LCD/PANEL Conn. (ACES_87216-30006) showing connections for LVDS signals (LVDS0+, LVDS0-, LVDS1+, LVDS1-, LVDS2+, LVDS2-, LVDS3+, LVDS3-) and EDID signals (EDID_CLK_LCD, EDID_DAT_LCD) to the ACES_87216-30006.

Pin 1: +VCC_4IN1_XD
Pin 2: XD-VCC
Pin 3: SD-VCC
Pin 4: MS-VCC
Pin 5: SD-CLK
Pin 6: SD-DAT0
Pin 7: SD-DAT1
Pin 8: SD-DAT2
Pin 9: SD-DAT3
Pin 10: SD-CMD
Pin 11: SD-CD-SW
Pin 12: SD-CD-COM
Pin 13: SD-WP-SW
Pin 14: SD-WP-COM
Pin 15: MS-SCLK
Pin 16: MS-DATA0
Pin 17: MS-DATA1
Pin 18: MS-DATA2
Pin 19: MS-DATA3
Pin 20: MS-MS
Pin 21: MS-BS
Pin 22: SD-GND
Pin 23: N.C.
Pin 24: N.C.
Pin 25: SHIELD GND
Pin 26: SHIELD GND

RJ11+RJ45 CONN

27 MDO0+ RJ45 PR 1 TX1+ JP807

27 MDO0- RJ45 PR 2 TX1-

27 MDO1+ RJ45 PR 3 RX1+

27 MDO2+ RJ45 PR 4 TX2+

27 MDO2- RJ45 PR 5 TX2-

27 MDO1- RJ45 PR 6 RX1-

27 MDO3+ RJ45 PR 7 RX2+

27 MDO3- RJ45 PR 8 RX2-

RJ45

RJ11

ALLTO_C100B6-110A4-L

ME @ 1 2

1000P_1206_2KV7K

C840

C834

0.1U_0402_16V4Z

4.7U_0805_10V4Z

R827

R828

0.0603_5%

0.0603_5%

RJ TIP

RJ RING

C833

MDC CONN

RJ45

RJ11

ALLTO_C100B6-110A4-L

ME @ 1 2

1000P_1206_2KV7K

C840

C834

0.1U_0402_16V4Z

4.7U_0805_10V4Z

R827

R828

0.0603_5%

0.0603_5%

RJ TIP

RJ RING

C833

EDL71_MDC

[illegible]

LED Indicator

3

B

2

B A

1

B A

SUBWOOFER_ON/OFF_BTN#
RF_ON/OFF_BTN#

Wireless / Bluetooth LED

Blue
Amber
BATT_CHG_LED#

Blue
Amber
BATT_LOW_LED#

STATUS	
AC	BLUE
Chargin	Blinking Blue
Low BATT	Amber

Function

KEY Matrix	K016	K017
KS10	DW-UP	DW-DOWN
KS11	DW-ENTER	MUTE

Dial Wheel

NOVO BTN

NOVA_BTN#

29,30 INT_

33 TV_TUNER_L

33 NUM_LE

33 CAPS_LE

20 SATA_LED

33 CD-ROM

33 ODD_LED

32 ON/OFFBTN

C839, C841

LED Indicator

3

B

2

B A

1

B A

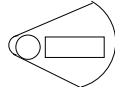
SUBWOOFER_ON/OFF_BTN#

RF_ON/OFF_BTN#

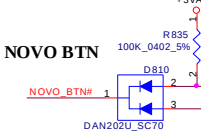
Function

KEY Matrix	K016	K017
KS10	DW-UP	DW-DOWN
KS11	DW-ENTER	MUTE

Dial Wheel



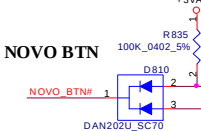
NOVO BTN



NOVA_BTN#



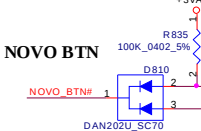
NOVA_BTN#



NOVA_BTN#



NOVO_BTN



NOVA_BTN#



LED Indicator

3

B

2

B A

1

B A

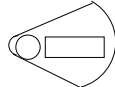
SUBWOOFER_ON/OFF_BTN#

RF_ON/OFF_BTN#

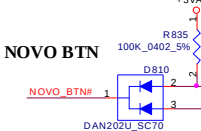
Function

KEY Matrix	K016	K017
KS10	DW-UP	DW-DOWN
KS11	DW-ENTER	MUTE

Dial Wheel



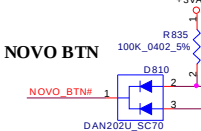
NOVO BTN



NOVA_BTN#



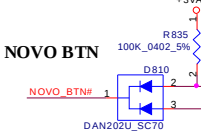
NOVA_BTN#



NOVA_BTN#



NOVO_BTN



NOVA_BTN#



LED Indicator

3

B

2

B A

1

B A

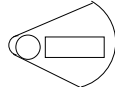
SUBWOOFER_ON/OFF_BTN#

RF_ON/OFF_BTN#

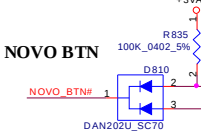
Function

KEY Matrix	K016	K017
KS10	DW-UP	DW-DOWN
KS11	DW-ENTER	MUTE

Dial Wheel



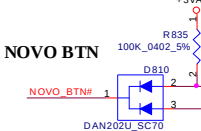
NOVO BTN



NOVA_BTN#



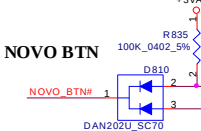
NOVA_BTN#



NOVA_BTN#



NOVO_BTN



NOVA_BTN#



LED Indicator

3

B

2

B A

1

B A

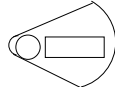
SUBWOOFER_ON/OFF_BTN#

RF_ON/OFF_BTN#

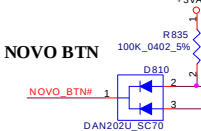
Function

KEY Matrix	K016	K017
KS10	DW-UP	DW-DOWN
KS11	DW-ENTER	MUTE

Dial Wheel



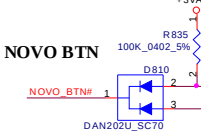
NOVO BTN



NOVA_BTN#



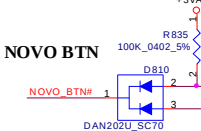
NOVA_BTN#



NOVA_BTN#



NOVO_BTN



NOVA_BTN#



LED Indicator

3

B

2

B A

1

B A

SUBWOOFER_ON/OFF_BTN#

RF_ON/OFF_BTN#

Function

KEY Matrix	K016	K017
KS10	DW-UP	DW-DOWN
KS11	DW-ENTER	MUTE

Dial Wheel

NOVO BTN

NOVA_BTN#

NOVA_BTN#

NOVA_BTN#

NOVO_BTN

NOVA_BTN#

LED Indicator

3

B

2

B A

1

B A

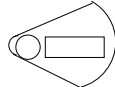
SUBWOOFER_ON/OFF_BTN#

RF_ON/OFF_BTN#

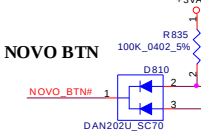
Function

KEY Matrix	K016	K017
KS10	DW-UP	DW-DOWN
KS11	DW-ENTER	MUTE

Dial Wheel



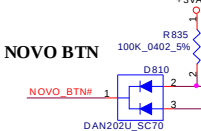
NOVO BTN



NOVA_BTN#



NOVA_BTN#



NOVA_BTN#



LED Indicator

3

B

2

B A

1

B A

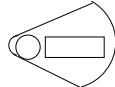
SUBWOOFER_ON/OFF_BTN#

RF_ON/OFF_BTN#

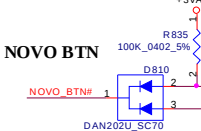
Function

KEY Matrix	K016	K017
KS10	DW-UP	DW-DOWN
KS11	DW-ENTER	MUTE

Dial Wheel



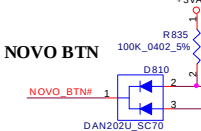
NOVO BTN



NOVA_BTN#



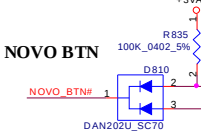
NOVA_BTN#



NOVA_BTN#



NOVO_BTN



NOVA_BTN#



LED Indicator

3

B

2

B A

1

B A

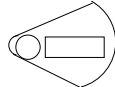
SUBWOOFER_ON/OFF_BTN#

RF_ON/OFF_BTN#

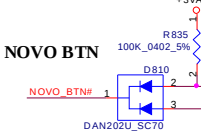
Function

KEY Matrix	K016	K017
KS10	DW-UP	DW-DOWN
KS11	DW-ENTER	MUTE

Dial Wheel



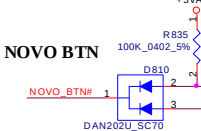
NOVO BTN



NOVA_BTN#



NOVA_BTN#



NOVA_BTN#



[illegible][illegible]

UMA LCD/PANEL Conn.

(60 MIL) JP815

+LCDVDDO

LVDSA+ LVDSA- LVDSA2+ LVDSA2- LVDSA1+ LVDSA1- LVDSA0+ LVDSA0-

EDID_CLK_LCD EDID_DAT_LCD

LVDSB+ LVDSB- LVDSB2+ LVDSB2- LVDSB1+ LVDSB1- LVDSB0+ LVDSB0-

GNDGND ME@ACES_87216-30006

Left Switch BD

C_GPIO12 C_GPIO59 PS_LED# ONLED# E_LED0# E_LED1# VR_LED#

BTONLED# CHARGE_LED0# CHARGE_LED1# PWR_LED#

R805 R806 R810 R812 R813

150 0402_5% 220 0402_5% 150 0402_5%

R822 R830 100K_0402_5% 100K_0402_5%

+3VS +5VALW +5VS

C869 C831 0.1U_0402_16V4Z 0.1U_0402_16V4Z

C867 C868 0.1U_0402_16V4Z 0.1U_0402_16V4Z

JP816 MOLEX_S3780-1470-D

Right Switch BD

+5VALW +5VS +VCC5_LED

R818 R820 100P_0402_5% 100P_0402_5%

R829 R831 R890 100K_0402_5% 100K_0402_5% 10K_0402_5%

KSO16 KSI1 KSIO KSO17 NOVO_BTN#

D808 D809 CH751H-40_SC76 CH751H-40_SC76

R834 R833 R832 220 0402_5% 220 0402_5% 220 0402_5%

0.1U_0402_16V4Z 0.1U_0402_16V4Z 0.1U_0402_16V4Z 1000P_0402_50VK

C841 C832 C842 C839 470P_0402_50VBJ 1000P_0402_50VK

JP808 ACES_87151-16071 G18 G17

C842 For EMI Solution

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2005/03/10	Deciphered Date	2006/03/10	Title	INDICATE LED	
THIS SET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custody	1GL50/51 LA-3771	0.1
Date: 05 MAR 2006 09:00:00				Sheet: 50 of 48		

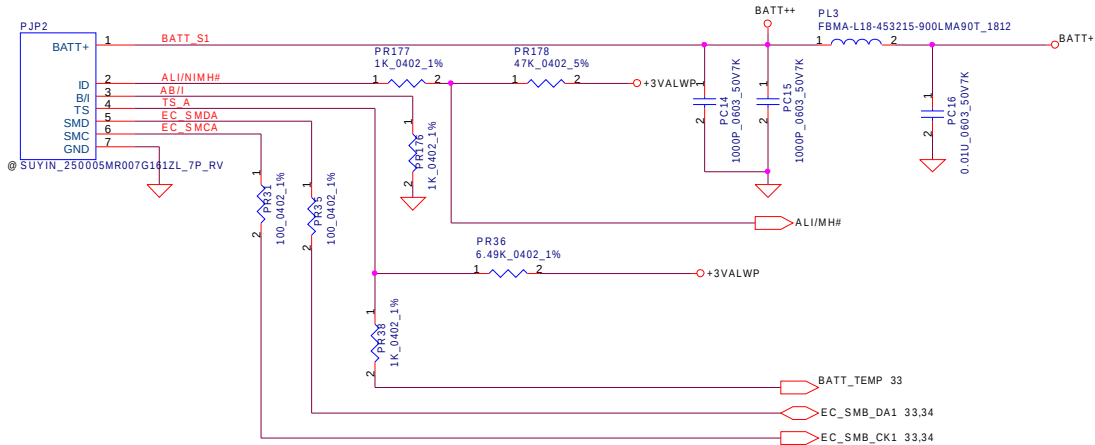
Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2005/03/10	Deciphered Date	2006/03/10	Title	INDICATE LED	
THIS SET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custody	1GL50/51 LA-3771	0.1
Date: 05 MAR 2006 09:00:00				Sheet: 50 of 48		

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2005/03/10	Deciphered Date	2006/03/10	Title	INDICATE LED	
THIS SET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custody	1GL50/51 LA-3771	0.1
Date: 05 MAR 2006 09:00:00				Sheet: 50 of 48		

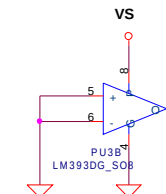
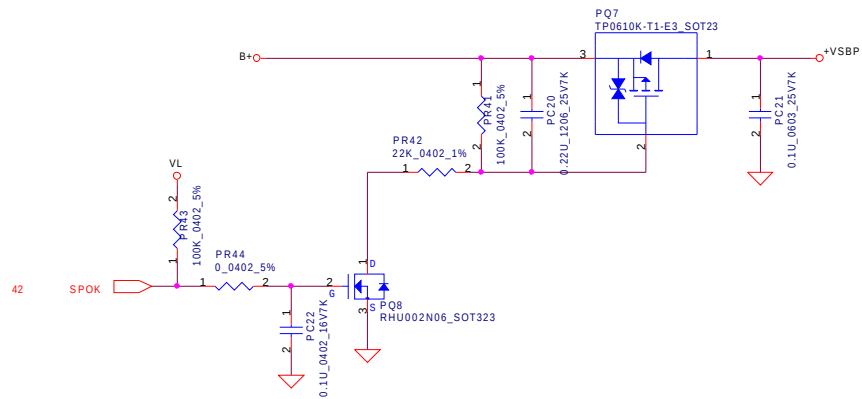
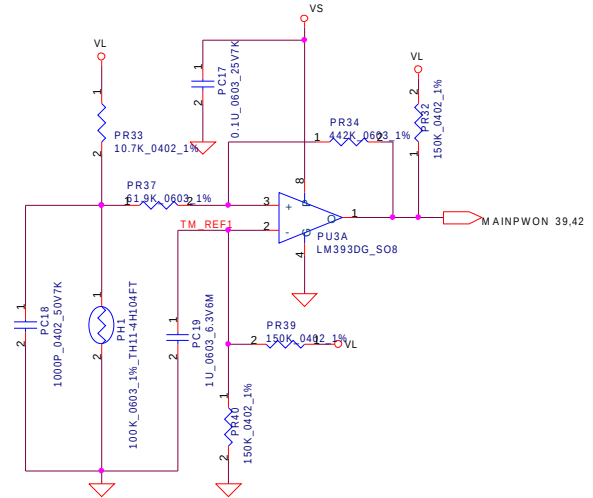
Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2005/03/10	Deciphered Date	2006/03/10	Title	INDICATE LED	
THIS SET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custody	1GL50/51 LA-3771	0.1
Date: 05 MAR 2006 09:00:00				Sheet: 50 of 48		

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2005/03/10	Deciphered Date	2006/03/10	Title	INDICATE LED	
THIS SET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custody	1GL50/51 LA-3771	0.1
Date: 05 MAR 2006 09:00:00				Sheet: 50 of 48		

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2005/03/10	Deciphered Date	2006/03/10	Title	INDICATE LED	
THIS SET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custody	1GL50/51 LA-3771	0.1
Date: 05 MAR 2006 09:00:00				Sheet: 50 of 48		



PH1 under CPU botten side :
CPU thermal protection at 85 degree C
Recovery at 70 degree C

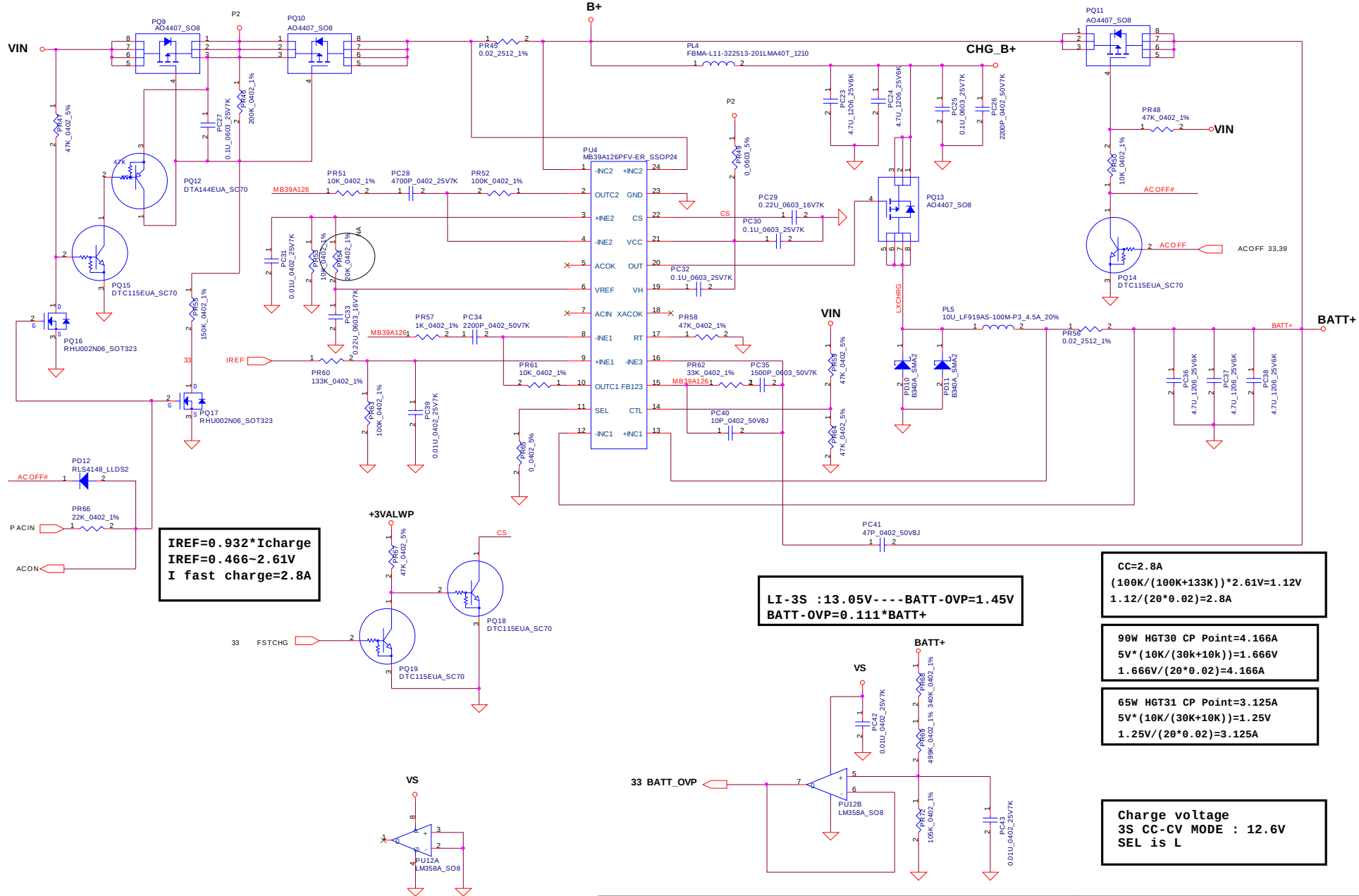


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/08/01	Deciphered Date	2006/08/01	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				BATTERY CONN. / OTP	
Size	B	Document Number		Date:	Rev
				星期四, 六月 08, 2006	0.1
Sheet		40 of 48			

65W PR45=0.02_2512_1% PR54=30K_0402_1% Iadp=0~3.125A
90W PR45=0.02_2512_1% PR54=20K_0402_1% Iadp=0~4.166A

Fosc=14100/Rt=14100/47=300KHz

Charger



IREF=0.932*Icharge
IREF=0.466~2.61V
I fast charge=2.8A

LI-3S :13.05V---BATT-OVP=1.45V
BATT-OVP=0.111*BATT+

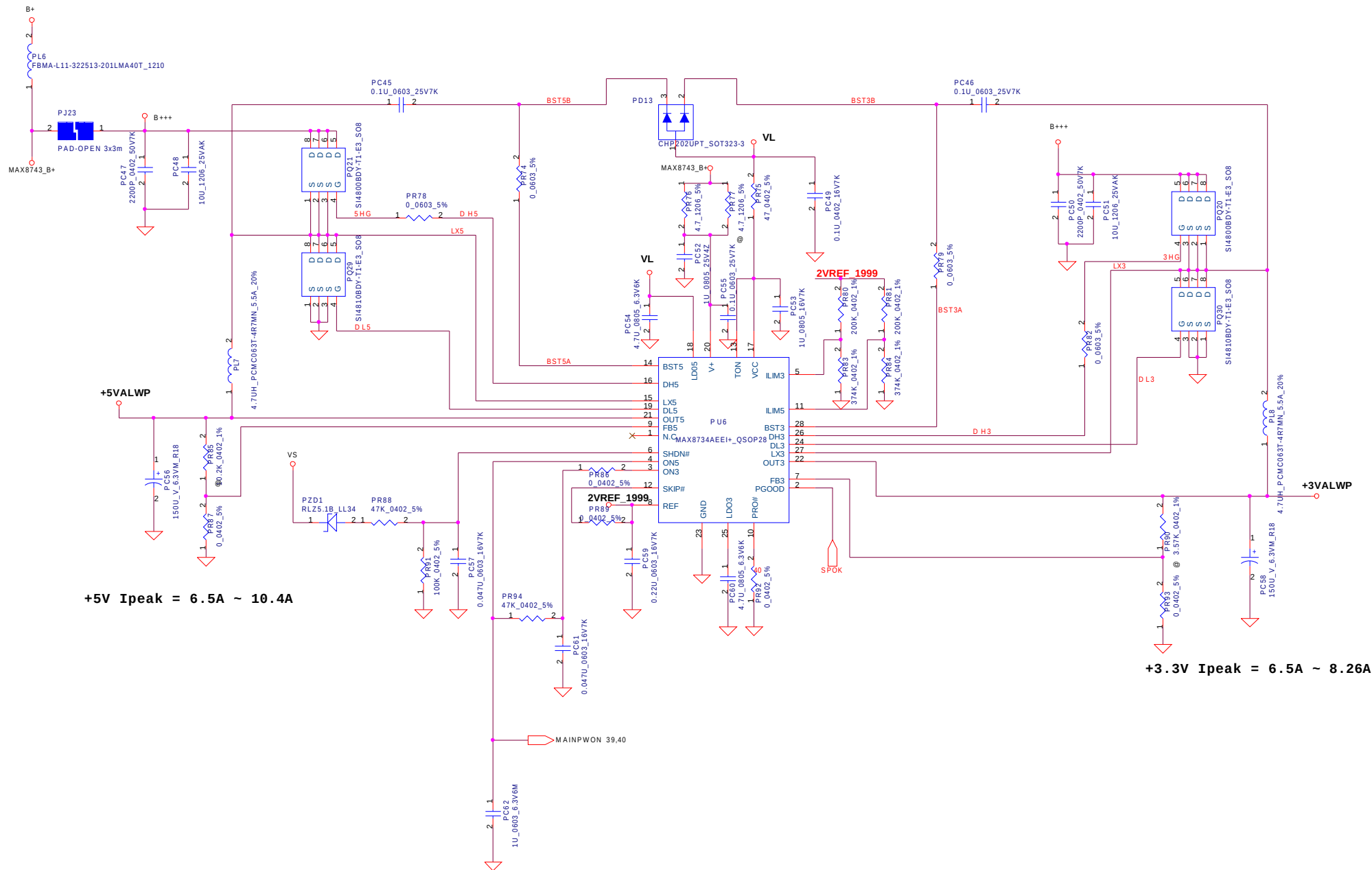
CC=2.8A
(100K/(100K+133K))*2.61V=1.12V
1.12/(20*0.02)=2.8A

90W HGT30 CP Point=4.166A
5V*(10K/(30K+10K))=1.666V
1.666V/(20*0.02)=4.166A

65W HGT31 CP Point=3.125A
5V*(10K/(30K+10K))=1.25V
1.25V/(20*0.02)=3.125A

Charge voltage
3S CC-CV MODE : 12.6V
SEL is L

Security Classification		Compal Secret Data		Compal Electronics, Inc.					
Issued Date		2005/08/01	Deciphered Date	2006/08/01		Title			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				PWR-Charger					
				Size	Document Number		Rev		
				B			0.1		
				Date:	星期日, 六月 08, 2006		Sheet	41	of 48

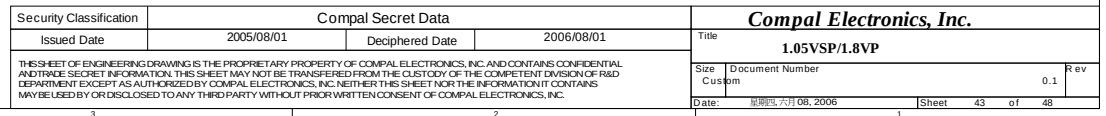


+5V Ipeak = 6.5A ~ 10.4A

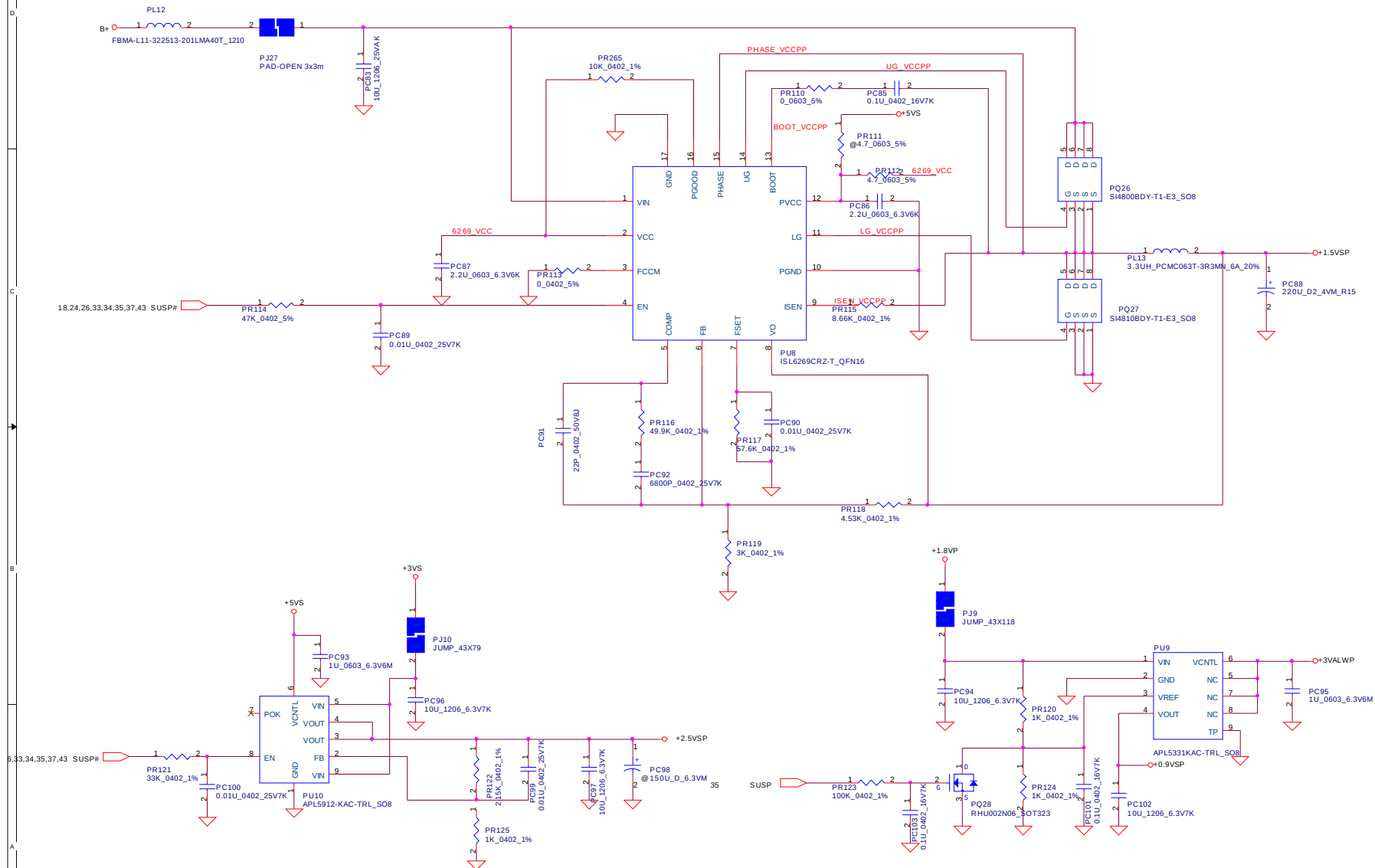
+3.3V Ipeak = 6.5A ~ 8.26A

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/08/01	Deciphered Date	2006/08/01	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				+5VALWP/+3VALWP	
Size	Document Number	Rev		Date: 星期日, 六月 08, 2006	
Custom		0.1		Sheet 42 of 48	

+1.05VSP Ilimit=6.33A~10.03A



+1.5VSP I_{limit} = 9.38A~12.5A



Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date		2005/08/01	Deciphered Date	2006/08/01		Title
				+VCCPP/+2.5VSP/0.9VSP		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND/OR SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINED MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custom		0.1
3		2		Date: 星期五, 7月08, 2006		
				Sheet 44 of 48		

Version change list (P.I.R. List)

Item	Fixed Issue	Reason for change	Rev.	PG #	Modify List	VER	Phase
1							
2							
3							
4							
5							
6							
7							
8							
9							
10							
11							
8							
9							

Version change list (P.I.R. List)

Item	Fixed Issue	Rev.	PG #	Modify List	B.Ver #	Phase
1					0.2	DVT
2					0.2	DVT
3					0.2	DVT
4					0.2	DVT
5					0.2	DVT
6					0.2	DVT
7					0.2	DVT
8					0.2	DVT
9					0.2	DVT
10					0.2	DVT
11					0.2	DVT

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
Title			
PIR			
Size	Document Number		Rev
	HGT30/31 LA-3061		0.0
Date:	日期 1, 六月 08, 2006		Sheet 47 of 48

Item	Fixed Issue	Rev.	PG #	Modify List	B.Ver #	Phase
12					0.3	PVT
13					0.3	PVT
14					0.3	PVT
15					0.3	PVT
16					0.3	PVT

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.		
Title		
PIR		
Size	Document Number	Rev
	HGT30/31 LA-3061	0.0
Date:	日期 1, 六月 08, 2006	Sheet 48 of 48