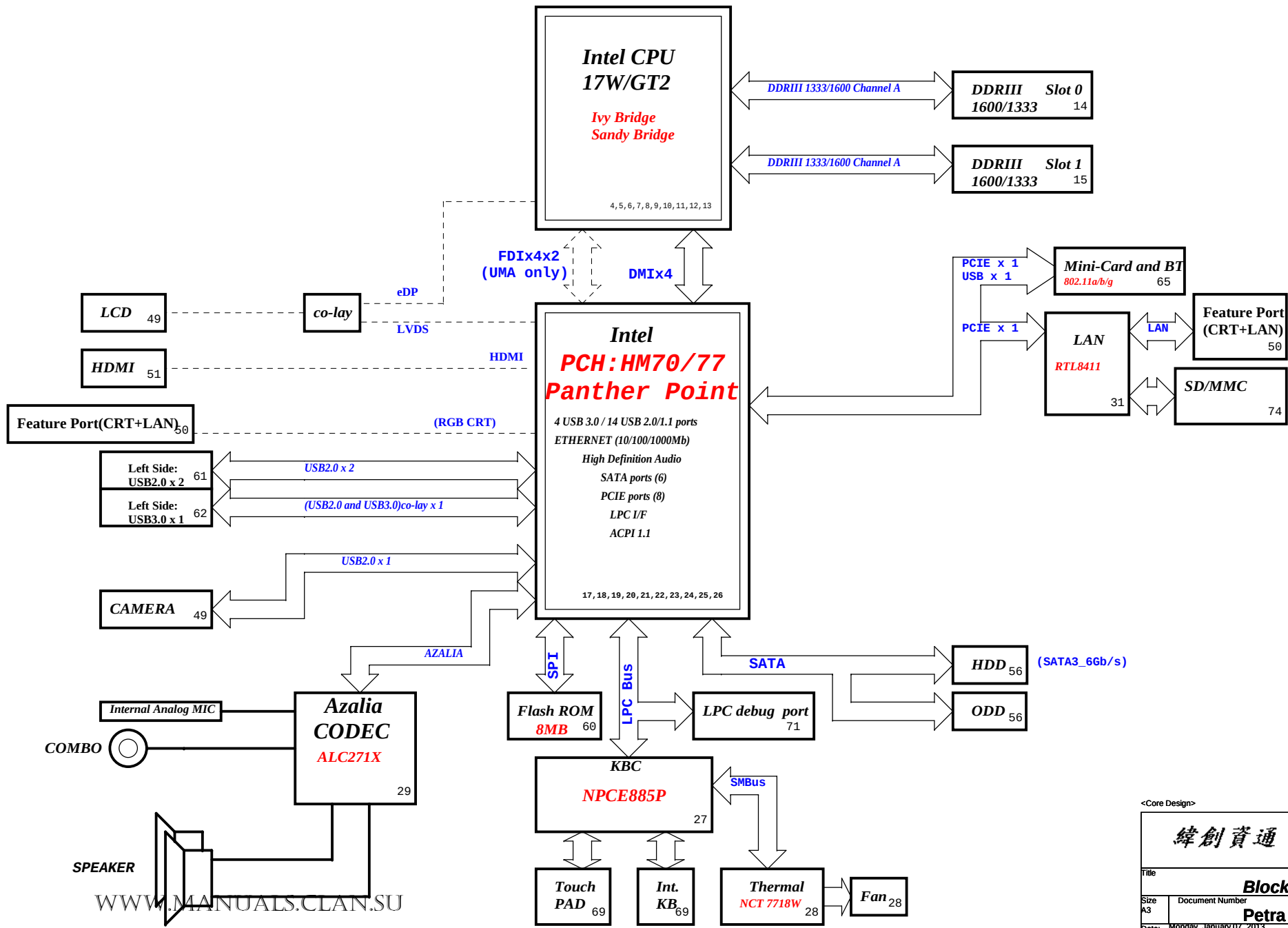


Petra UMA Schematics Document Ivy Bridge Intel PCH

DY :None Installed
DIS:DIS installed
DIS_Muxless :BOTH DIS or Muxless installed
DIS_PX:BOTH DIS or PX installed
DIS_PX_Muxless:DIS or PX or Muxless installed.
Muxless: Muxless installed.(PX4.0)
PX:MUX installed.(PX3.0)
PX_Muxless:BOTH PX or Muxless installed.
UMA:UMA installed
UMA_Muxless:BOTH UMA or Muxless installed
UMA_PX_Muxless:UMA or PX or Muxless installed

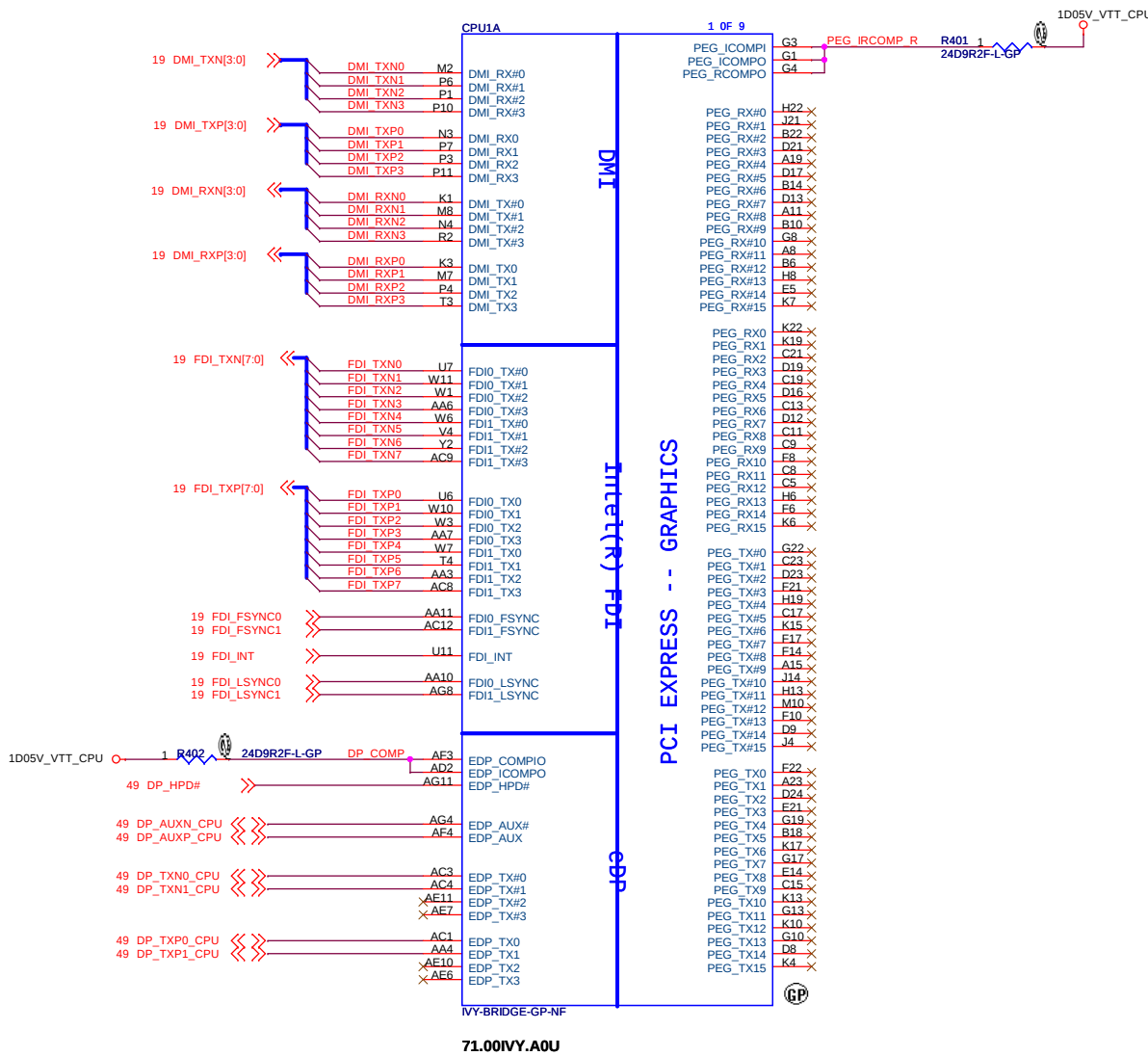
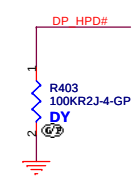
ANNIE: ONLY FOR ANNIE solution.
PSL: KBC795 PSL circuit for 10mW solution installed.
10mW: External circuit for 10mW solution installed.
65W: for 65W adaptor installed.
90W: for 90W adaptor installed.

Project code : 91.4VM01.001
PCB P/N : 48.4VM02.001
PCB No. : 11324
Revision : -1

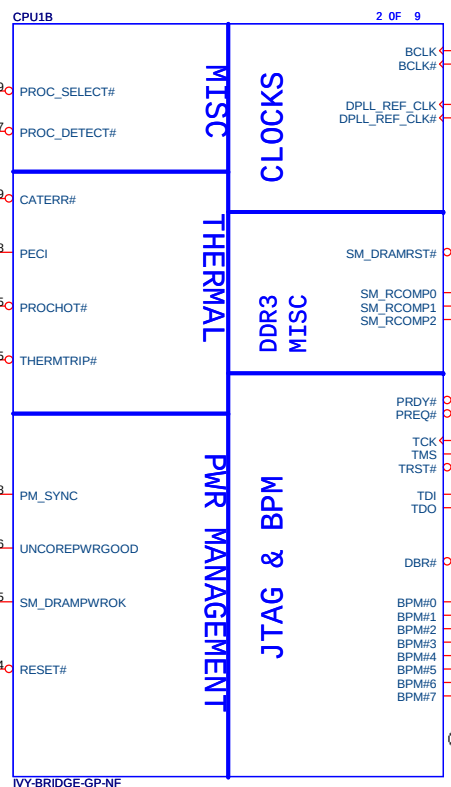
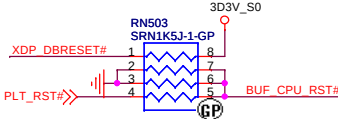
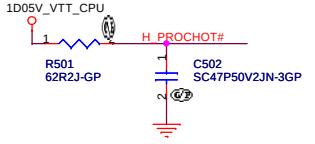


CHARGER	
BQ24727	40
INPUTS	OUTPUTS
DCBATOUT	BT+
SYSTEM DC/DC	
RT8223MGQW	41
INPUTS	OUTPUTS
DCBATOUT	5V_AUX_S5 3D3V_AUX_S5 5V_S5 3D3V_S5
CPU DC/DC	
ISL95836HRTZ	42~43
INPUTS	OUTPUTS
DCBATOUT	VCC_CORE
SYSTEM DC/DC	
ISL95836HRTZ	44
INPUTS	OUTPUTS
DCBATOUT	VCC_GFXCORE
SYSTEM DC/DC	
TPS51218DSCR	45
INPUTS	OUTPUTS
DCBATOUT	1D05V_VTT
SYSTEM DC/DC	
RT8207LGQW	46
INPUTS	OUTPUTS
DCBATOUT	1D5V_S3 0D75V_S0 DDR_VREF_S3
LDO	
RT9025-25ZSP	47
INPUTS	OUTPUTS
3D3V_S0	1D8V_S0
LDO	
G978	48
INPUTS	OUTPUTS
1D05V_VTT	0D85V_S0
PCB LAYER	
L1:Top	L4:Signal
L2:VCC	L5:GND
L3:Signal	L6:Bottom

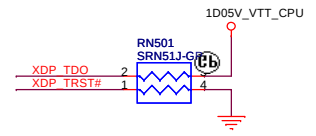
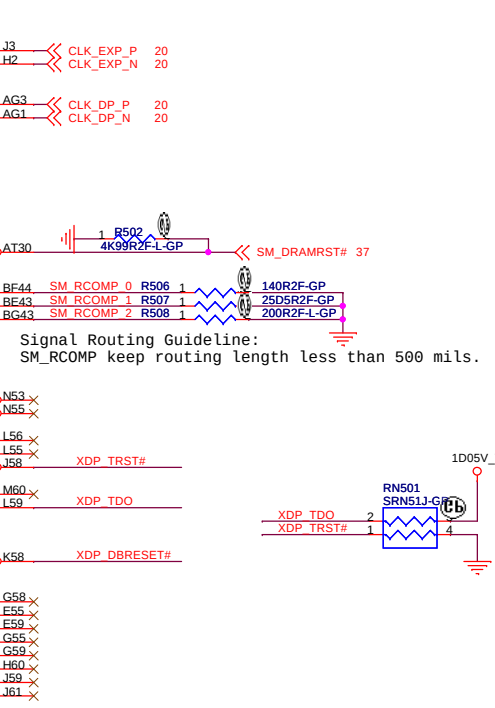
SSID = CPU



SSID = CPU



71.00IVY.A0U

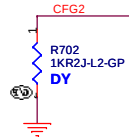


<Core Design>		
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title CPU (THERMAL/CLOCK/PM)		
Size Custom	Document Number Petra Uma	Rev -1
Date: Tuesday, July 10, 2012	Sheet 5 of	103

SSID = CPU

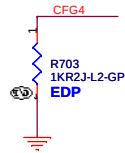
PEG Static Lane Reversal

CFG2	1: Normal Operation; Lane # definition matches socket pin map definition 0: Lane Reversed
------	--



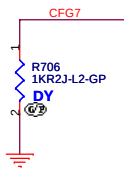
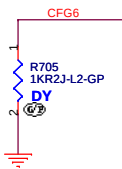
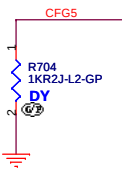
Enabl EDP function

CFG4	1: Disable 0: Enable
------	-------------------------



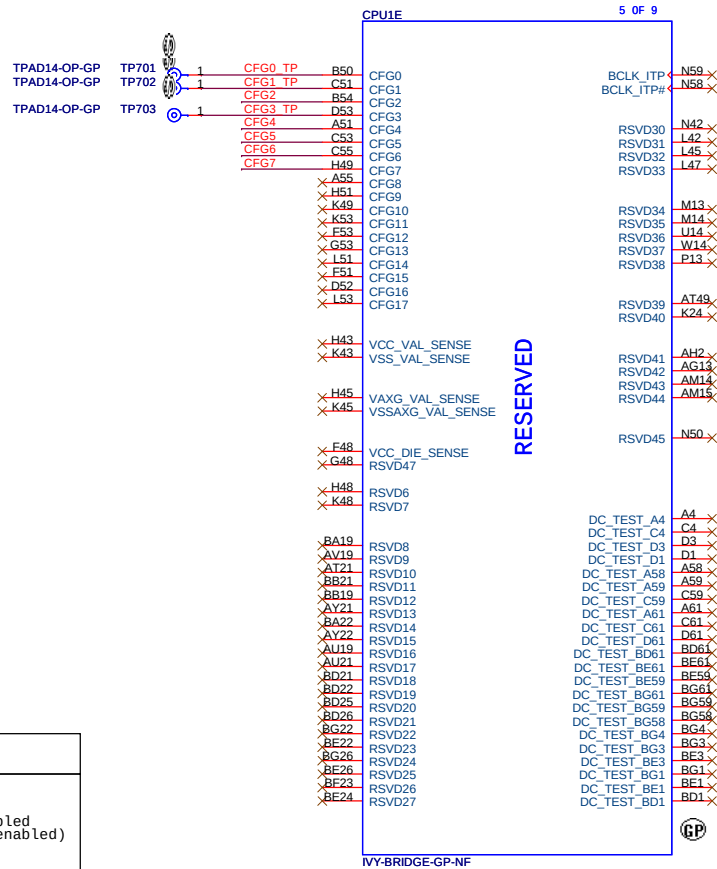
PCIe Port Bifurcation Straps

CFG[6:5]	11: x16 - Device 1 functions 1 and 2 disabled 10: x8, x8 - Device 1 function 1 enabled ; function 2 disabled 01: Reserved - (Device 1 function 1 disabled ; function 2 enabled) 00: x8,x4,x4 - Device 1 functions 1 and 2 enabled
----------	--



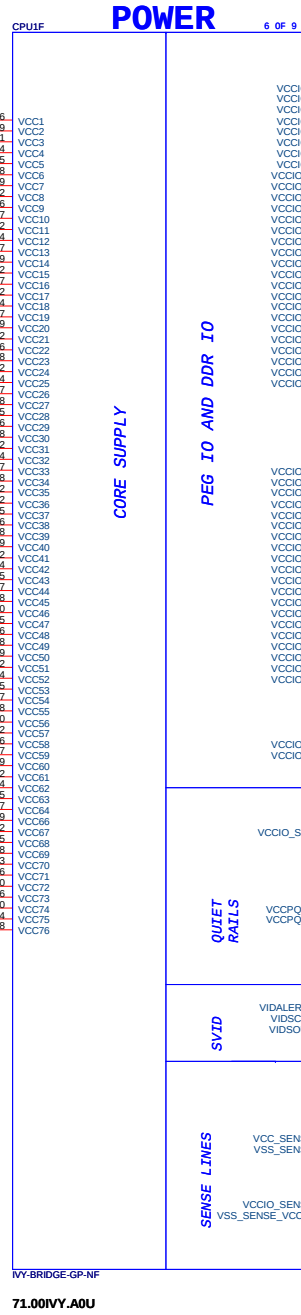
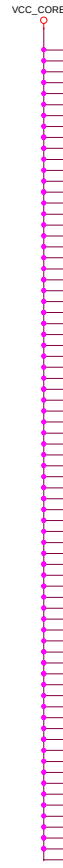
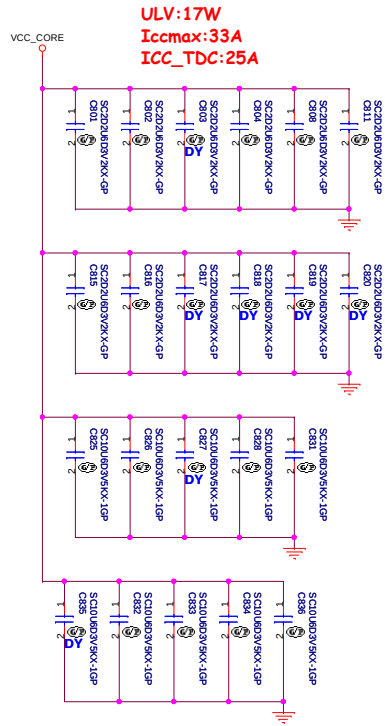
PEG DEFER TRAINING

CFG7	1: PEG Train immediately following xxRESETB de assertion 0: PEG Wait for BIOS for training
------	---

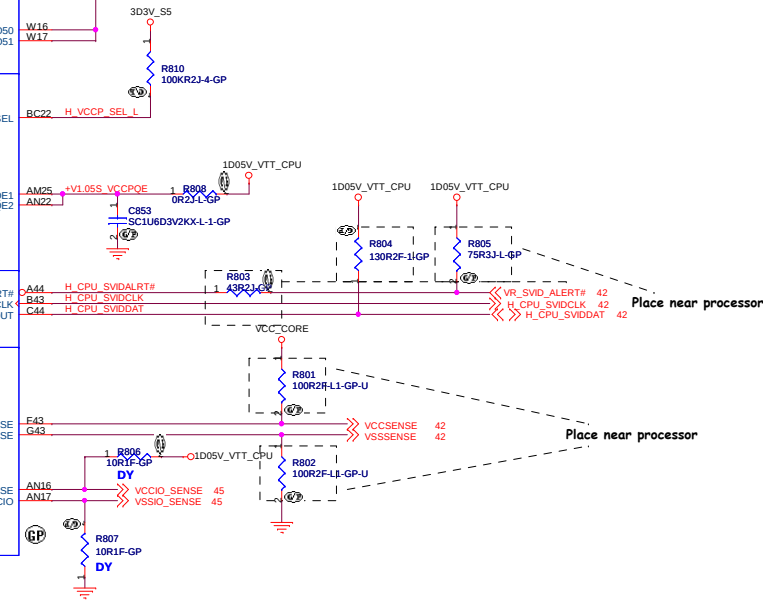
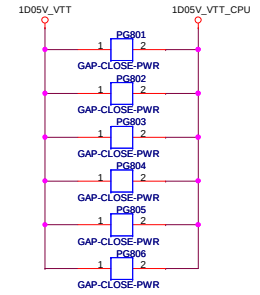


<Core Design>

SSID = CPU



Iccmax:8.5A
ICC_TDC:8.5A

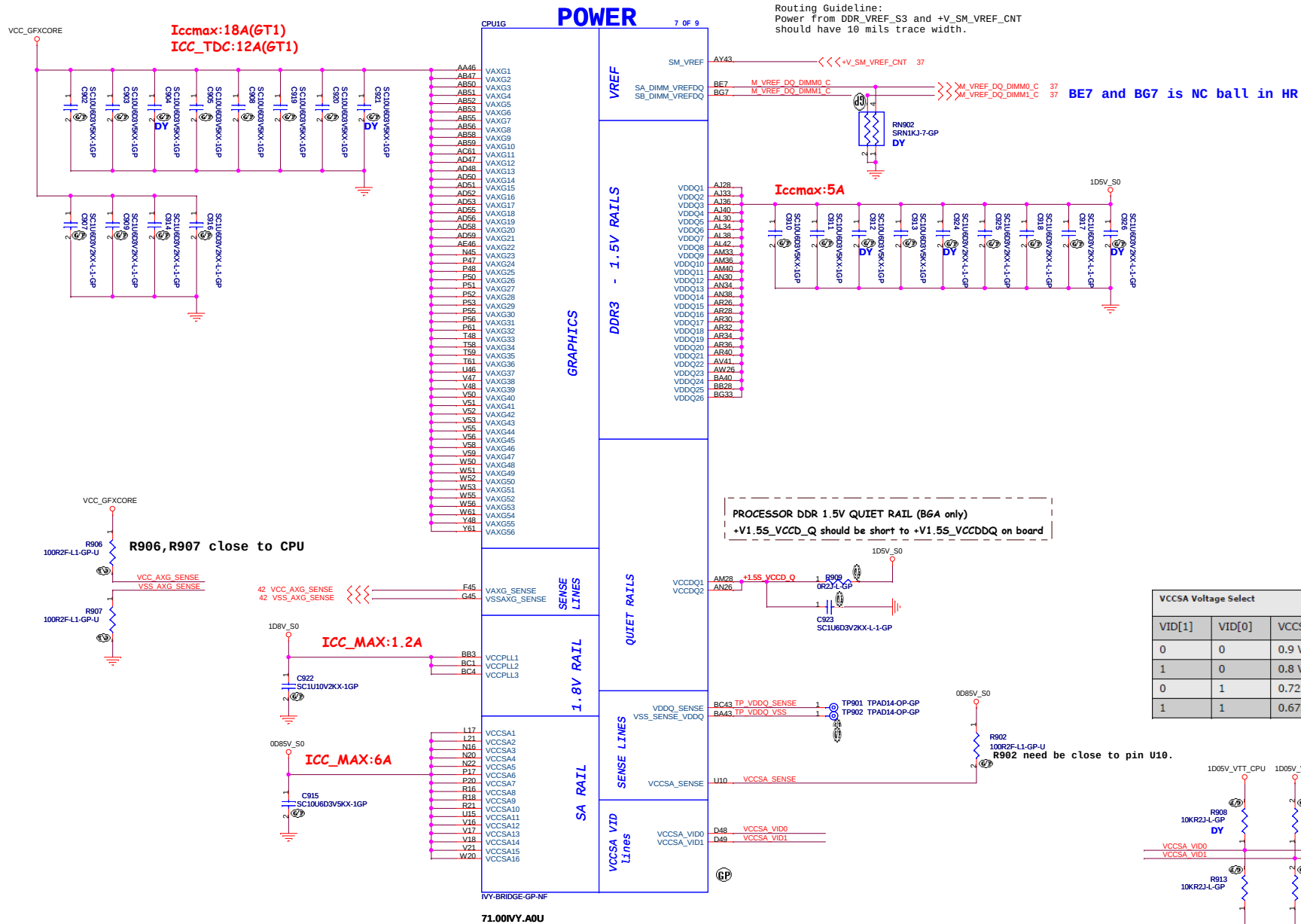


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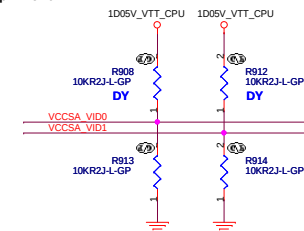
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

CPU (VCC CORE)		
Size	Document Number	Rev
Custom	Petra Uma	-1
Date:	108508y, July 10, 2012	Sheet 8 of 103

SSID = CPU



VCCSA Voltage Select		
VID[1]	VID[0]	VCCSA
0	0	0.9 V
1	0	0.8 V
0	1	0.725 V
1	1	0.675 V

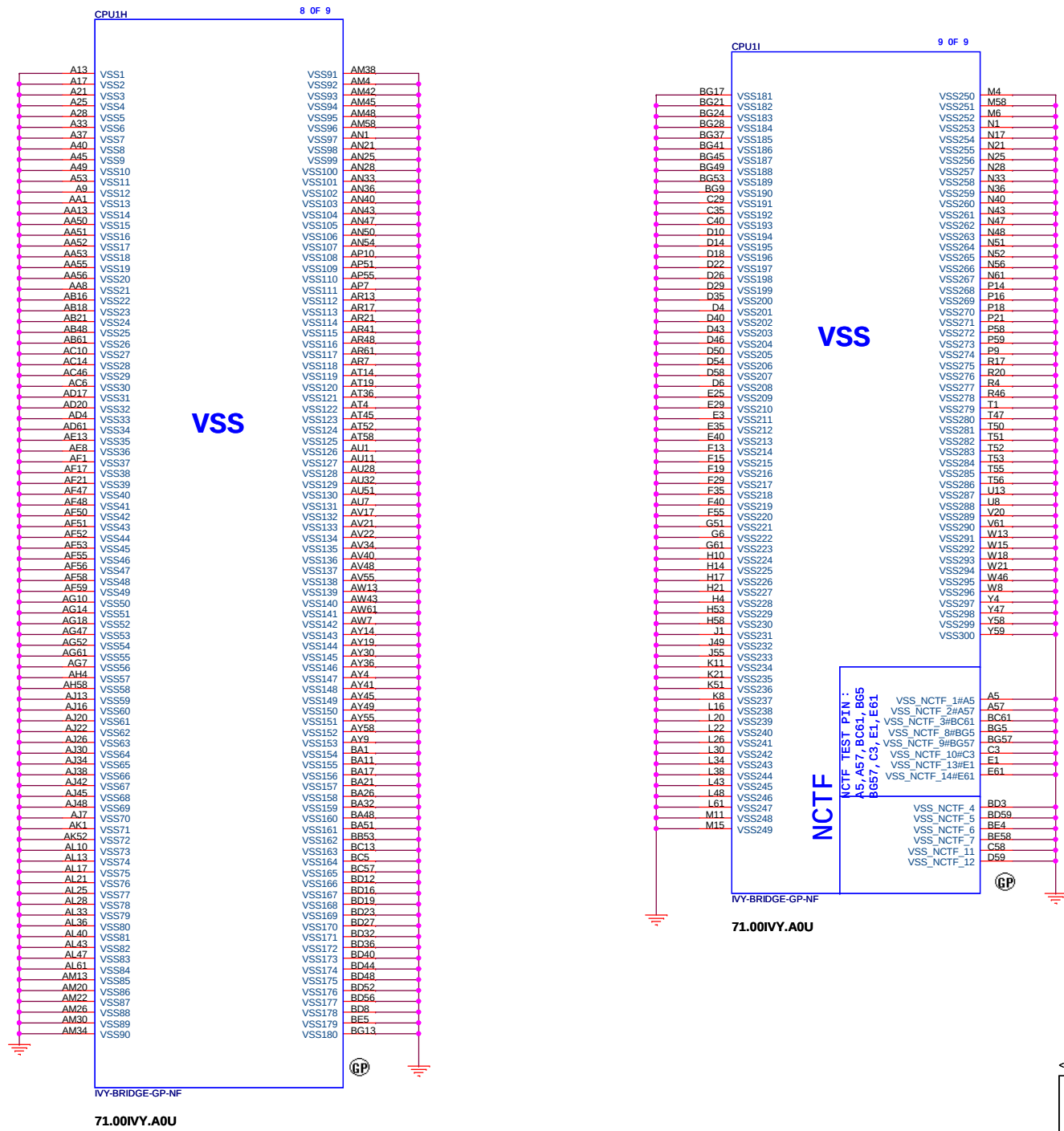


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緯創資通 **Wistron Corporation**
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Taipei Hsien 221, Taiwan, R.O.C.

Title			
CPU (VCC GFXCORE)			
Size Custom	Document Number		Rev
	Petra Uma		-1
Date:	Tuesday, July 10, 2012	Sheet 9 of	103

SSID = CPU



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<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title XDP		
Size A4	Document Number Petra Uma	Rev -1
Date Wednesday, February 22, 2012		Sheet 11 of 103

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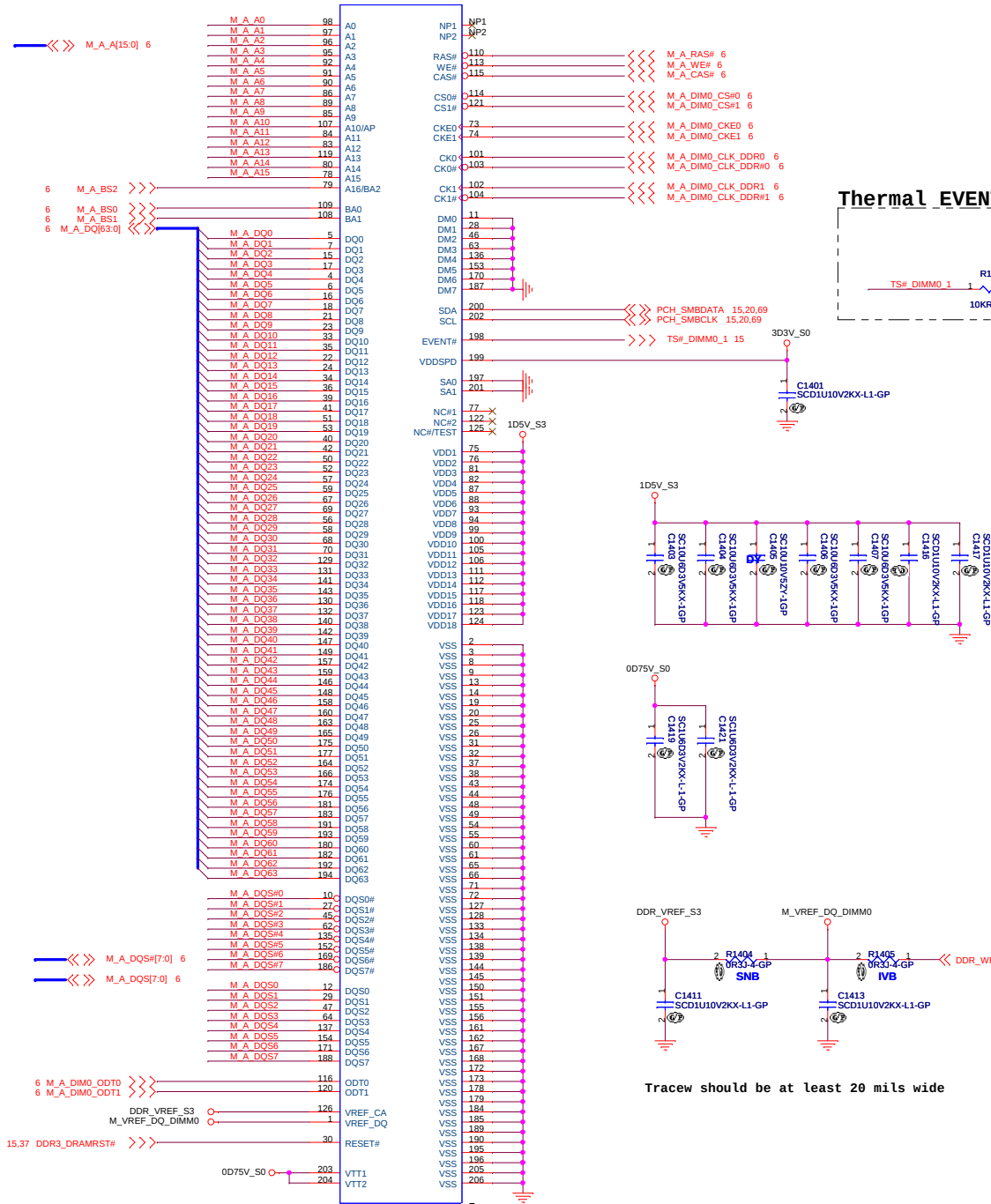
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title Reserved	
Size A4	Document Number Petra Uma
Date Wednesday, February 22, 2012	Rev -1
Date: Wednesday, February 22, 2012	
Sheet 12 of 103	

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<Core Design>

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
Reserved			
Size A4	Document Number Petra Uma		Rev -1
Date:	Wednesday, February 22, 2012	Sheet 13 of	103

SSID = MEMORY

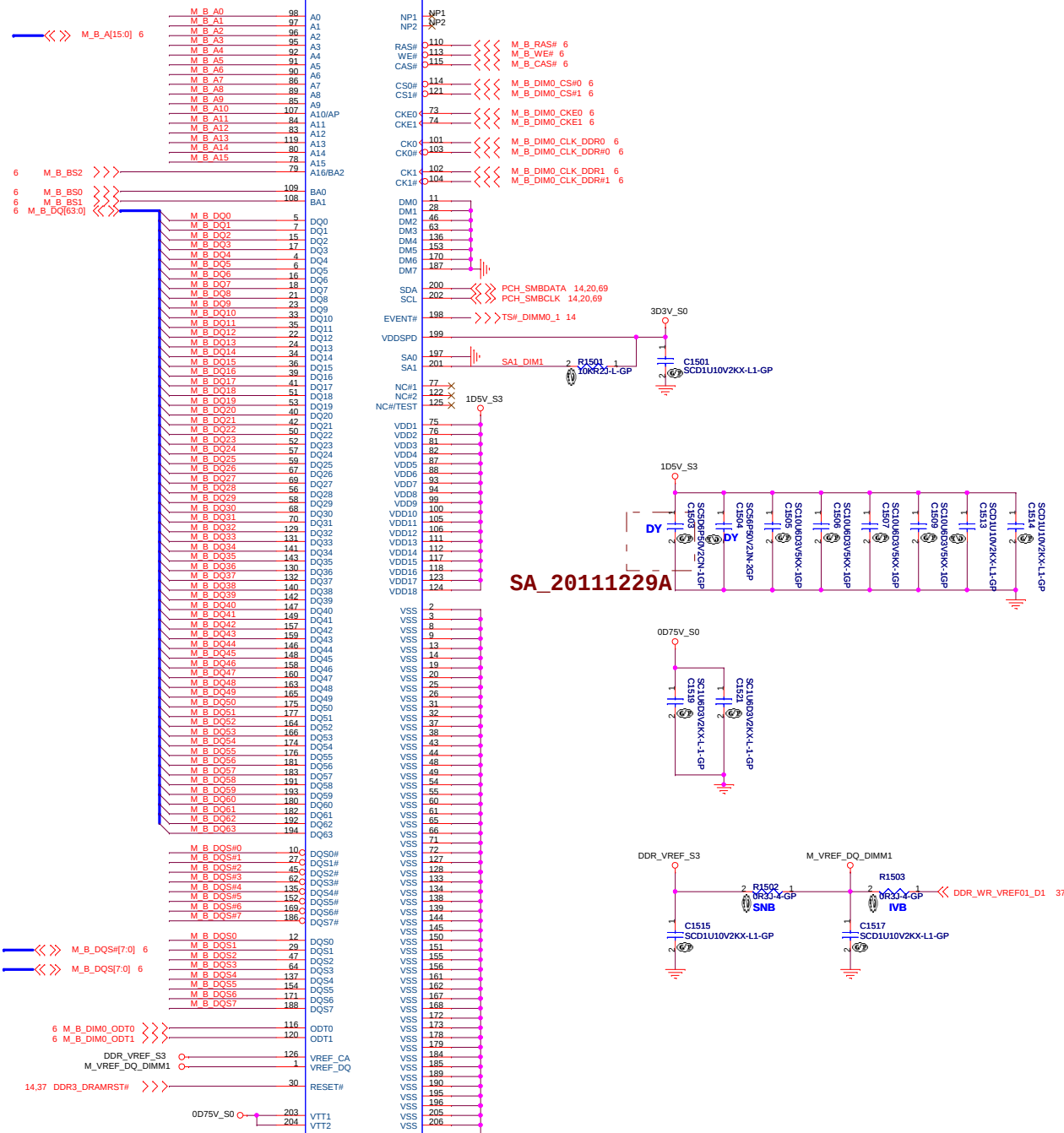


DM1
DDR3-204P-122-GP
62.10017.Z51
2nd = 62.10017.M51
3rd = 62.10024.G21

<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipex Hsien 221, Taiwan, R.O.C.		
Title		
DDR3-SODIMM1		
Size	Document Number	Rev
Custom	Petra Uma	-1
Date:	Tuesday, July 10, 2012	Sheet 14 of 103

SSID = MEMORY



<Core Design>

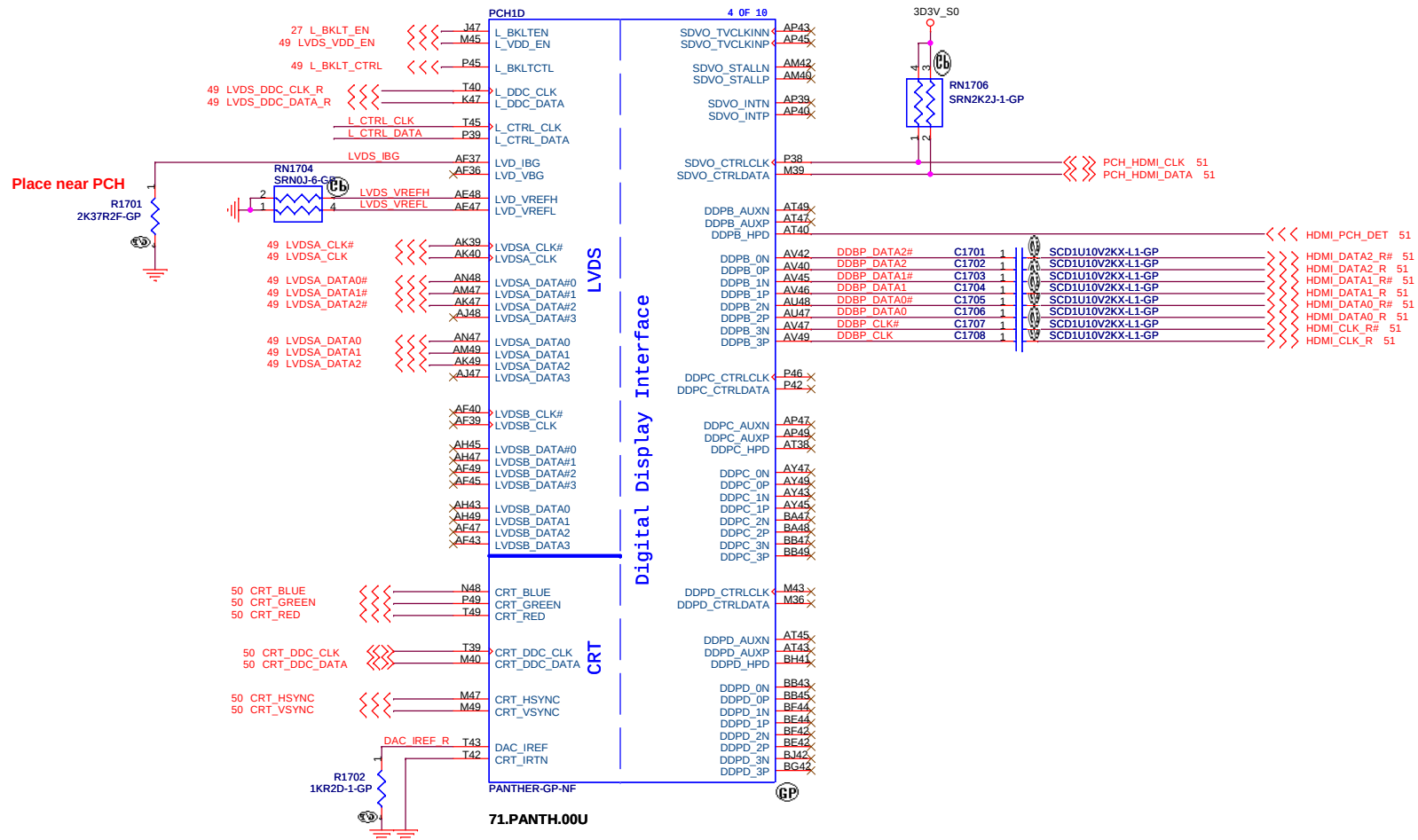
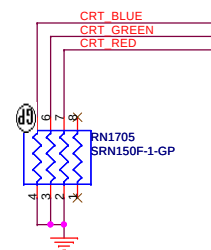
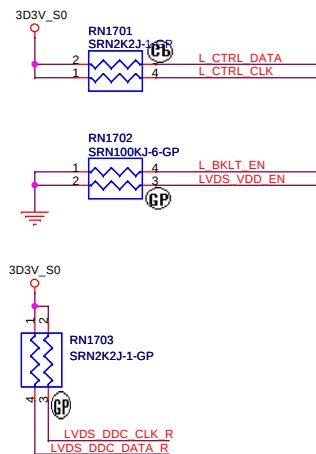
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

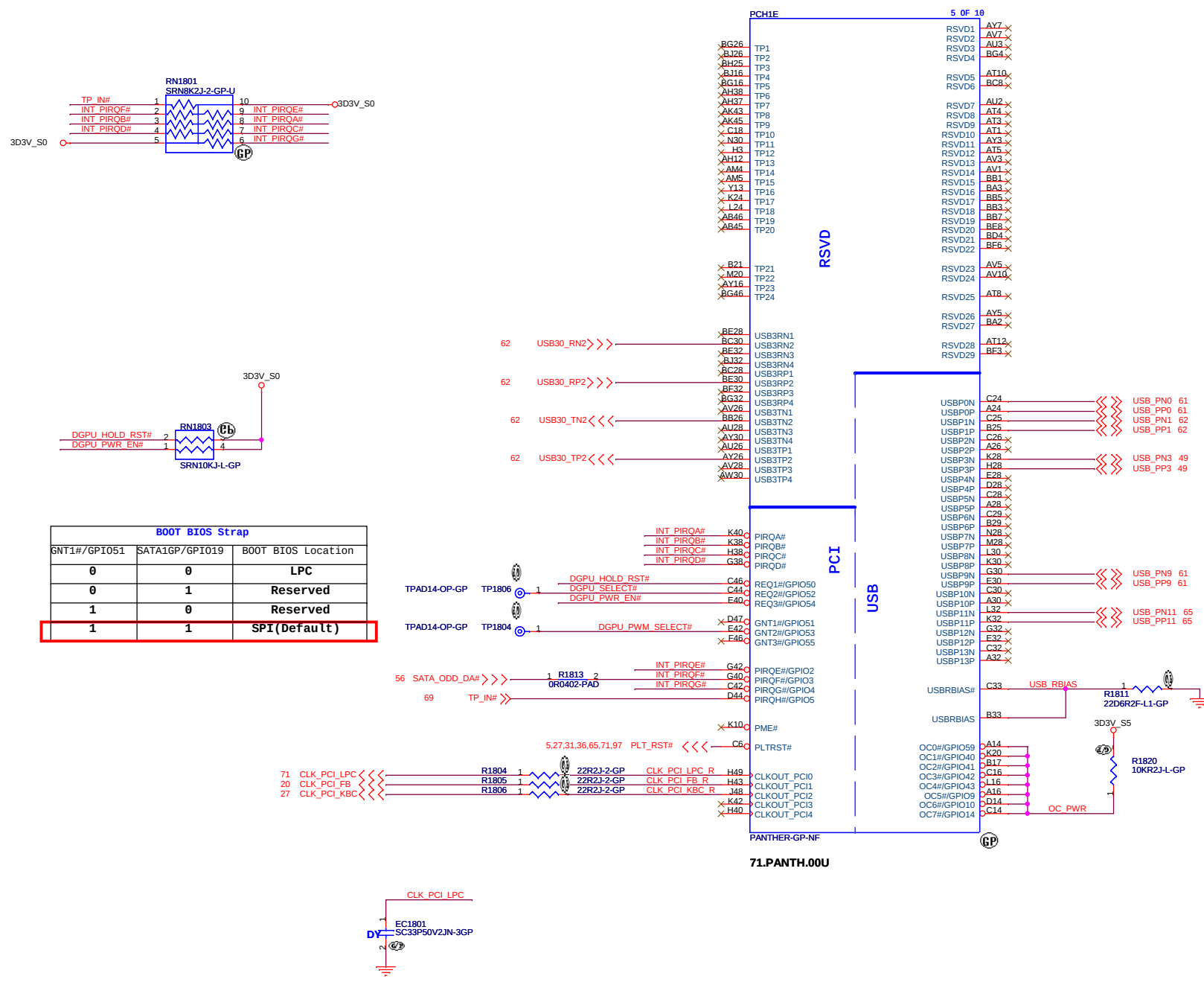
Title			DDR3-SODIMM2
Size	Document Number	Rev	
Custom		Petra Uma	
Date:	tuesday, July 10, 2012	Sheet	15 of 102

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<Core Design>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
DDR3-SODIMM2			
Size A4	Document Number Petra Uma		Rev -1
Date:	Wednesday, February 22, 2012	Sheet 16 of	103

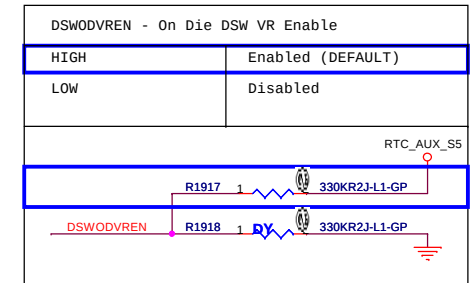




USB Table

Pair	Device
0	USB2.0 Ext. port 1
1	USB3.0/USB2.0 Ext. port 2
2	
3	CCD
4	
5	
6	may not be available
7	may not be available
8	
9	USB2.0 Ext. port 3
10	
11	Mini Card1 (WLAN+BT)
12	
13	

Signal Routing Guideline:
DMI_ZCOMP keep W=4 mils and
routing length less than 500
mils.
DMI_IRCOMP keep W=4 mils and
routing length less than 500
mils.



3D3V_S5

RN1901
SRN10K1-6-CP

8 1 BATLOW#

7 2 PM R#

6 3 AC PRESENT

5 4 SUS_PWR_ACK_R

100nF

10uF

100uF

10k

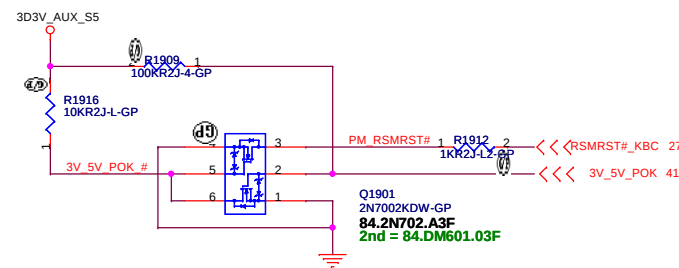
100k

RN1921 1
10K821-L-GP

PCIE_WAKE#

RN1922 1
10K821-L-GP

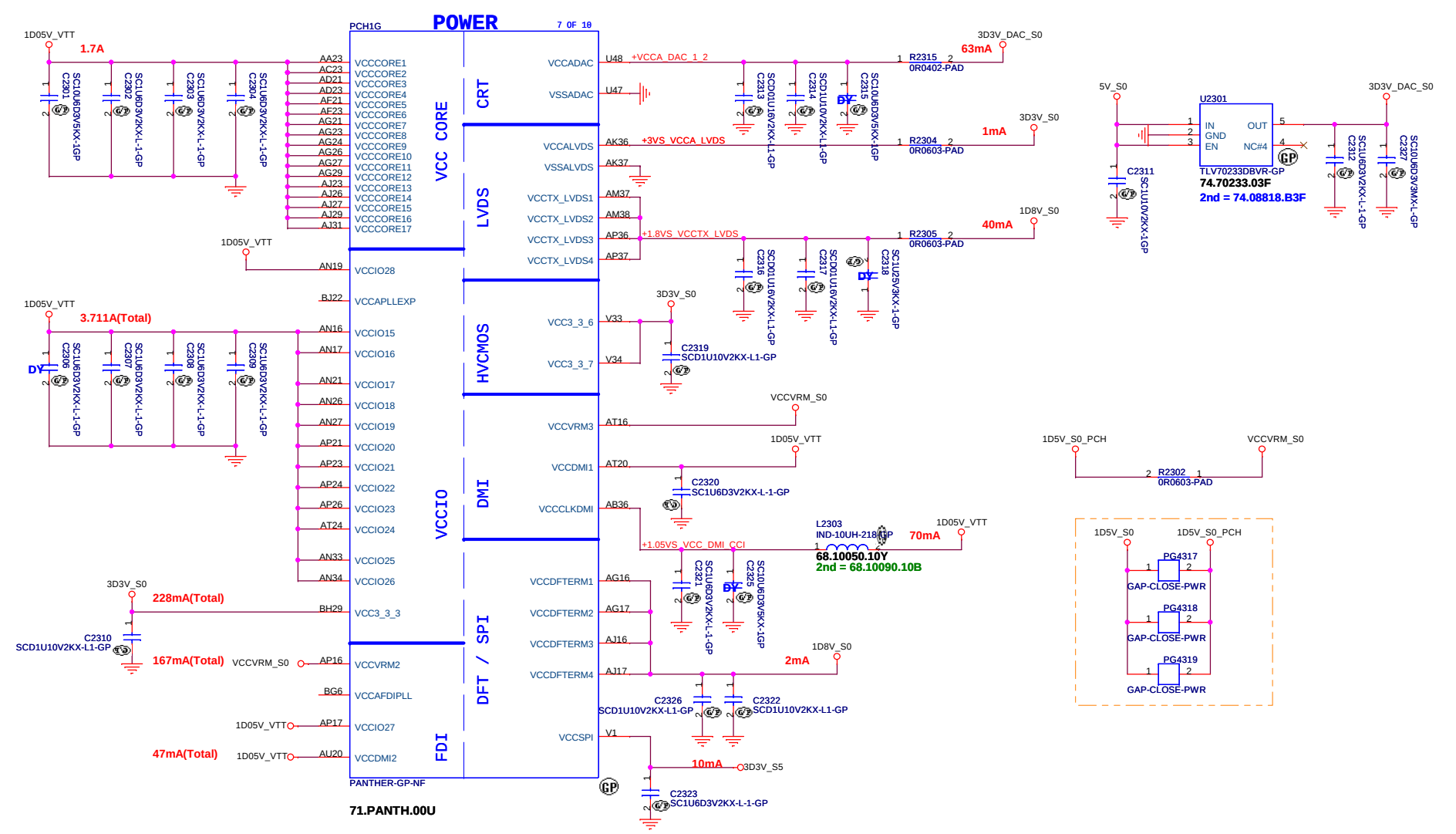
SUS_PWR_ACK#



緯創資通 **Wistron Corporation**
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Taipei Hsien 221, Taiwan, R.O.C.

Title			
PCH (DM I/FDI/PM)			
Size A3	Document Number		Rev
	Petra Uma		-1
Date:	Tuesday, July 10, 2012	Sheet 19 of	103

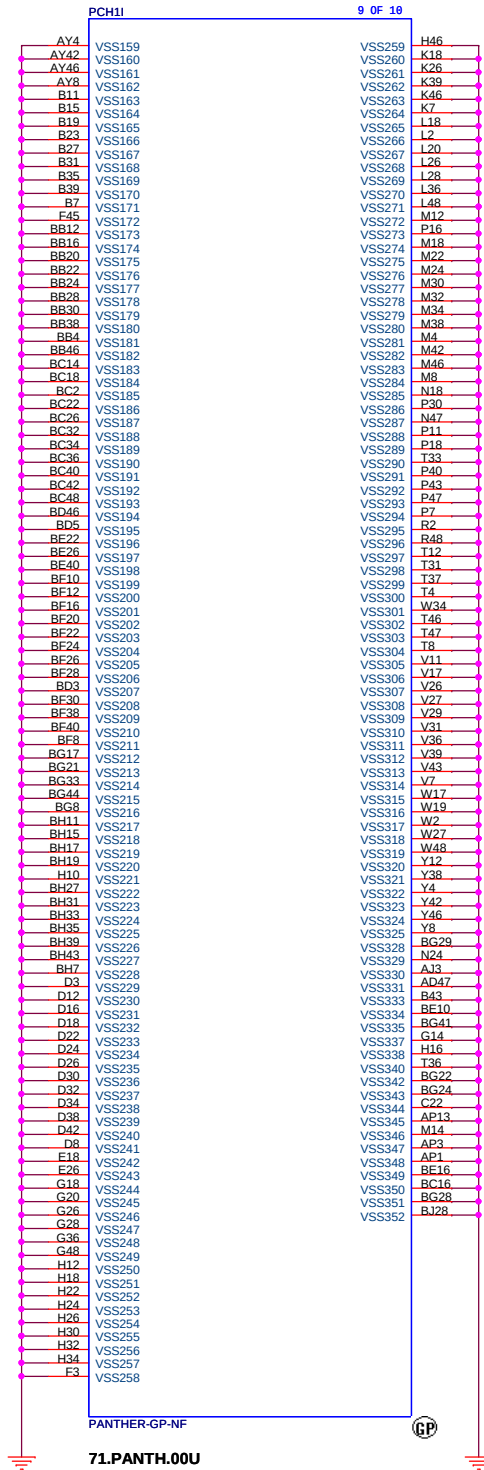
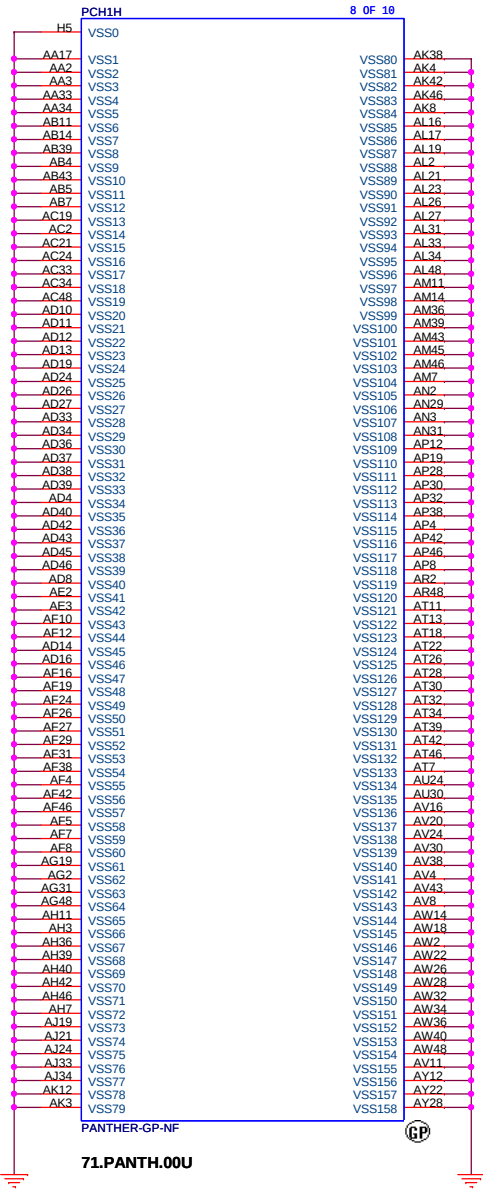




WWW.MANUALS.CLAN.SU



SSID = PCH



<Core Design>

緯創資通 Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

Title **PCH (VSS)**

Size A3 Document Number **Petra Uma** Rev **-1**

Date: Wednesday, February 22, 2012 Sheet 25 of 103

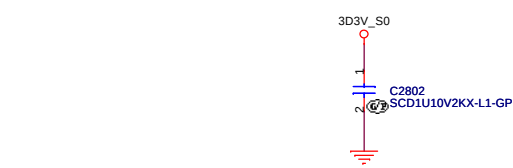
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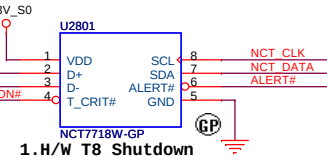
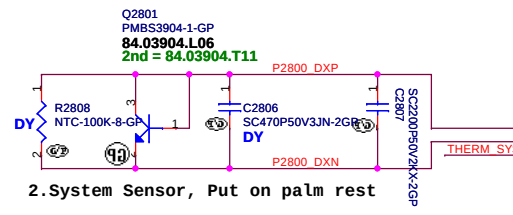
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title Clock(colay)	
Size A4	Document Number Petra Uma
Date Wednesday, February 22, 2012	Rev -1
Date Wednesday, February 22, 2012 Sheet 26 of 103	

SSID = Thermal

Thermal sensor NCT 7718W



Layout notice :
Both DXN and DXP routing 10 mil
trace width and 10 mil spacing.

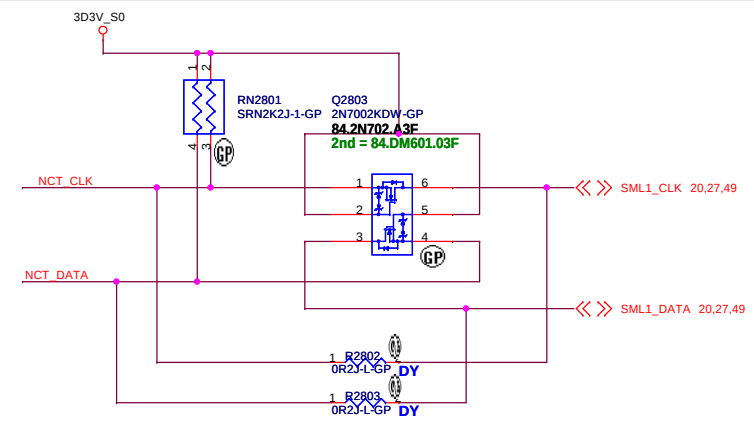


ALERT# /T CRIT#
Pull-up Resistor

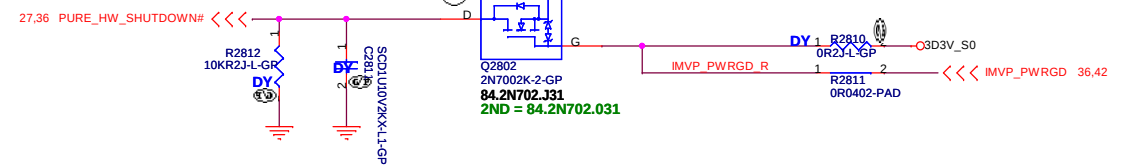
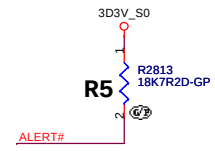
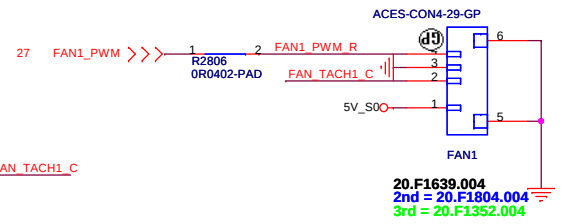
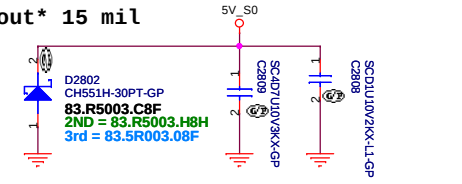
	2Kohm	7.5Kohm	10.5Kohm	14Kohm	18.7Kohm
R5	77°C	87°C	97°C	107°C	117°C
7.5Kohm	79°C	89°C	99°C	109°C	119°C
10.5Kohm	81°C	91°C	101°C	111°C	121°C
14Kohm	83°C	93°C	103°C	113°C	123°C
18.7Kohm	85°C	95°C	105°C	115°C	125°C

T_CRIT temperature strapping point

SB T8=85 degree

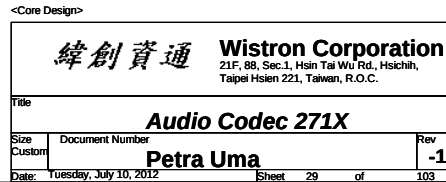


Layout 15 mil



<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Thermal 7718/Fan Controller P2793	
Size	Document Number
Custom	Rev
Date: Tuesday, July 10, 2012	Sheet 28 of 103



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<Core Design>

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
Audio AMP			
Size A4	Document Number		Rev
	Petra Uma		-1
Date:	Wednesday, February 22, 2012	Sheet 30 of	103



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<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title RTS5159 (CARD READER)		
Size A4	Document Number Petra Uma	Rev -1
Date Wednesday, February 22, 2012	Sheet 32	of 103

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<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title Reserved		
Size A4	Document Number Petra Uma	Rev -1
Date Wednesday, February 22, 2012		Sheet 33 of 103

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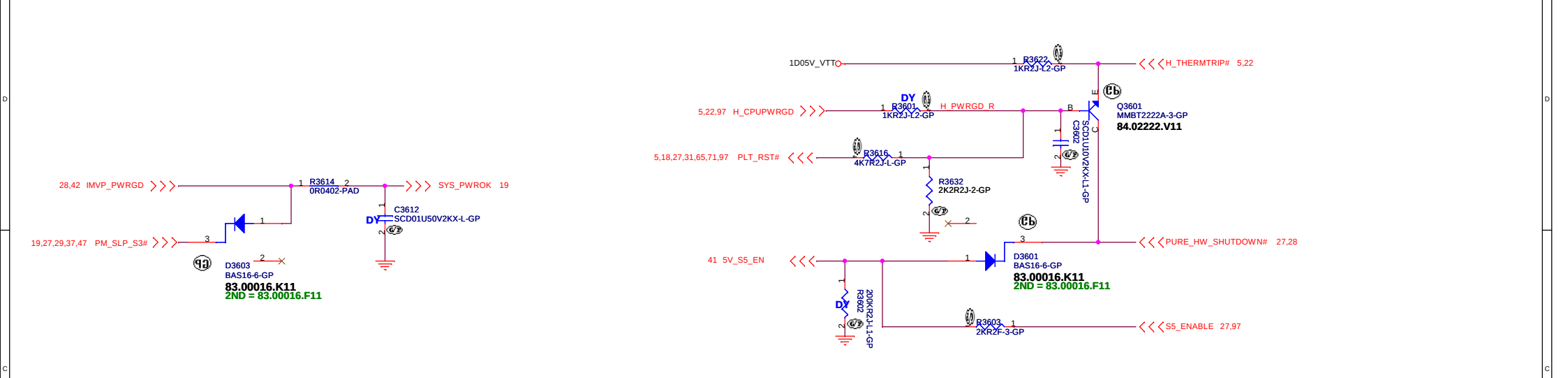
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緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title Reserved	
Size A4	Document Number Petra Uma
Date Wednesday, February 22, 2012	Rev -1
Sheet 34 of 103	

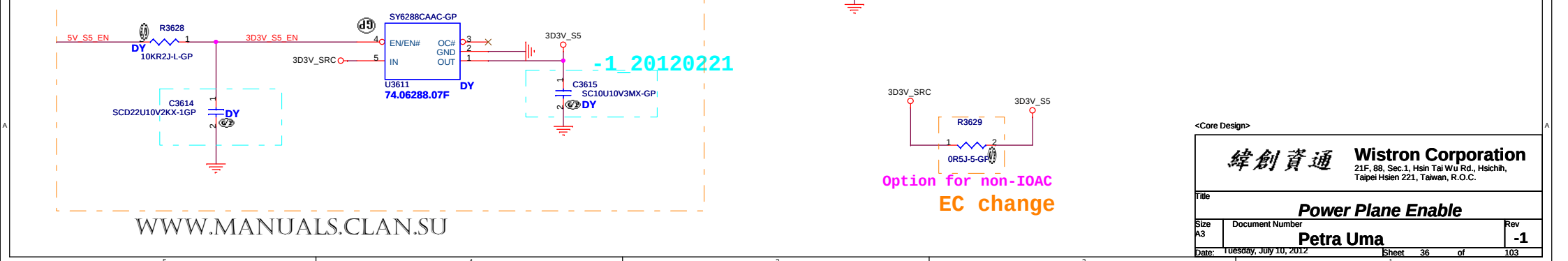
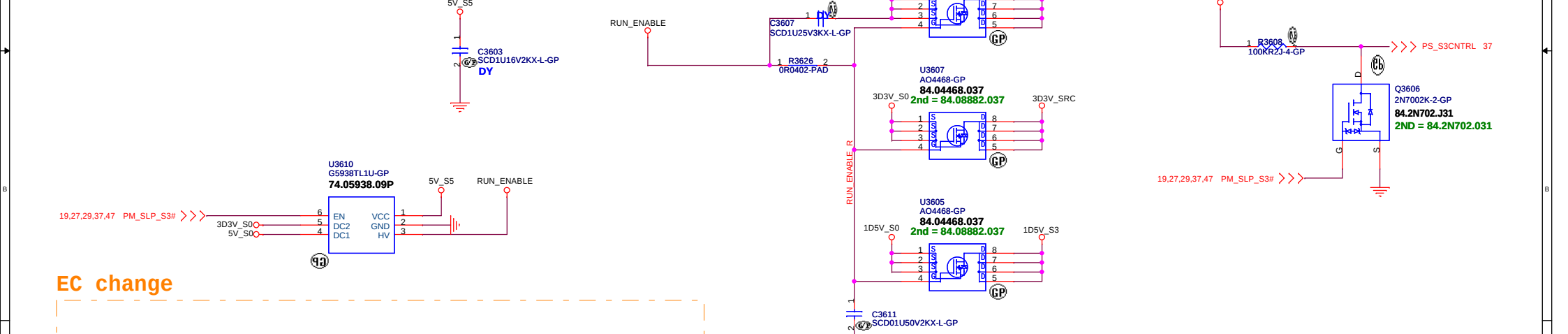
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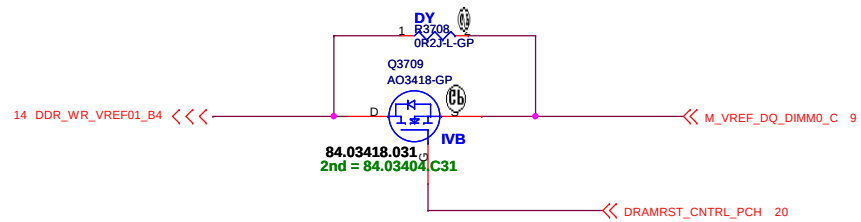
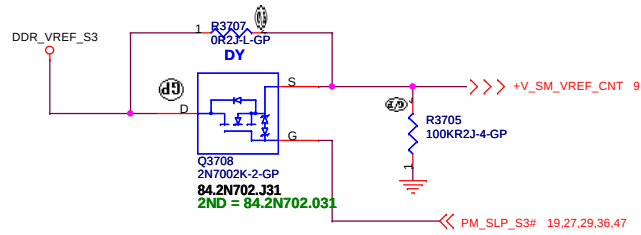
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title USB 3.0 Controller	
Size A4	Document Number Petra Uma
Date Wednesday, February 22, 2012	Rev -1
Date Wednesday, February 22, 2012 Sheet 35 of 103	



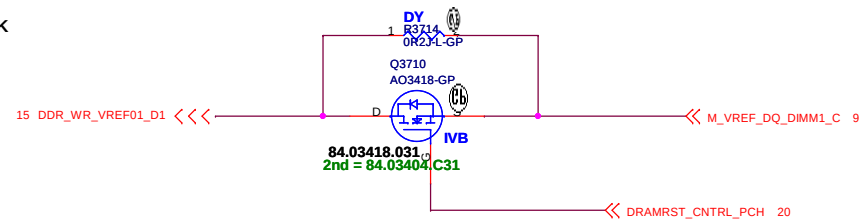
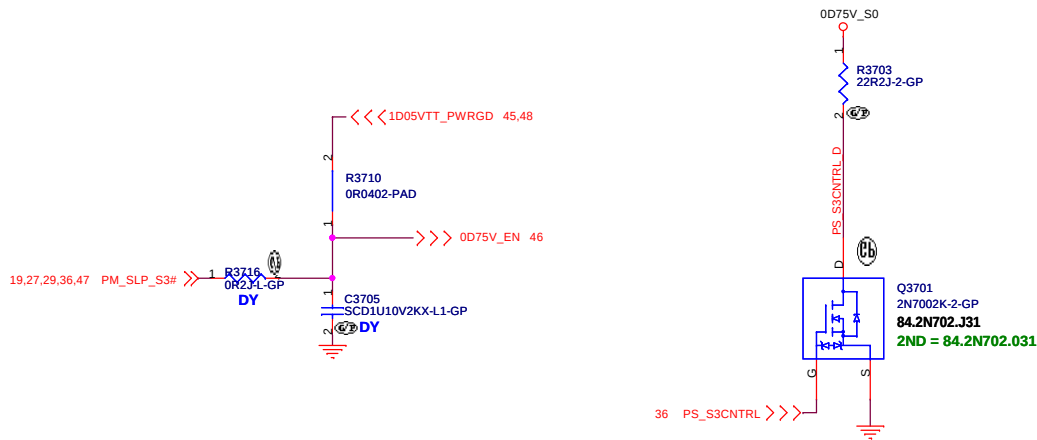
ANNIE Run Power



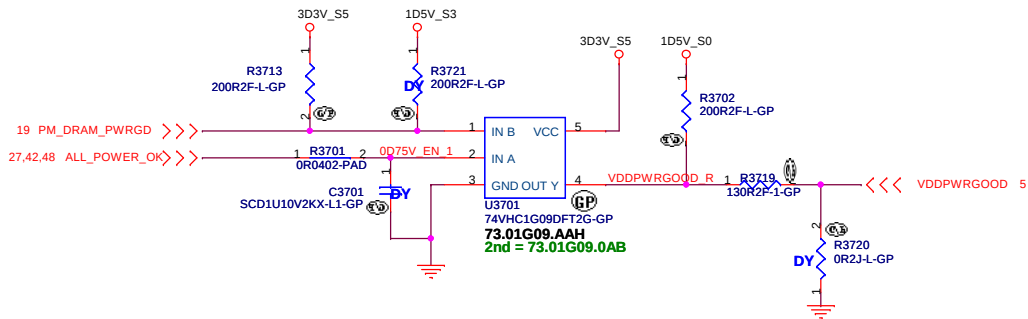
Close to CPU
S3 Power Reduction Circuit Processor VREF_DQ Implementation



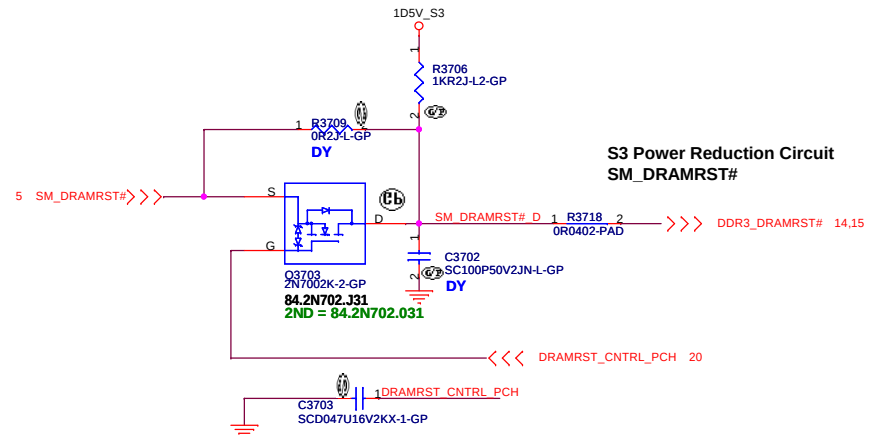
Close to DIMM
S3 Power Reduction Circuit SM_DRAMPWROK



Close to CPU
S3 Power Reduction Circuit SM_DRAMPWROK

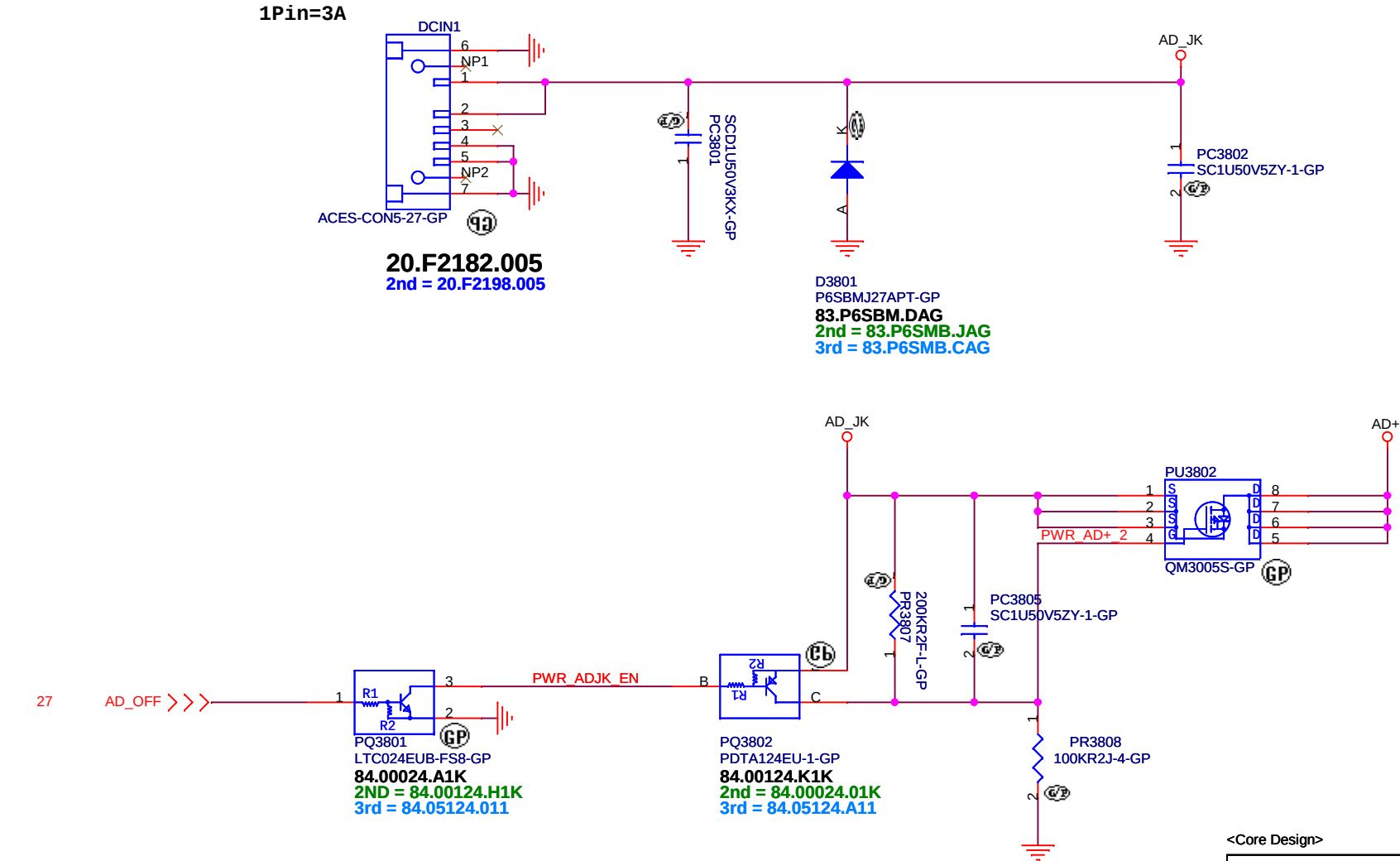


Close to CPU
S3 Power Reduction Circuit SM_DRAMPWROK



ANNIE solution

Adaptor in to generate DCBATOUT



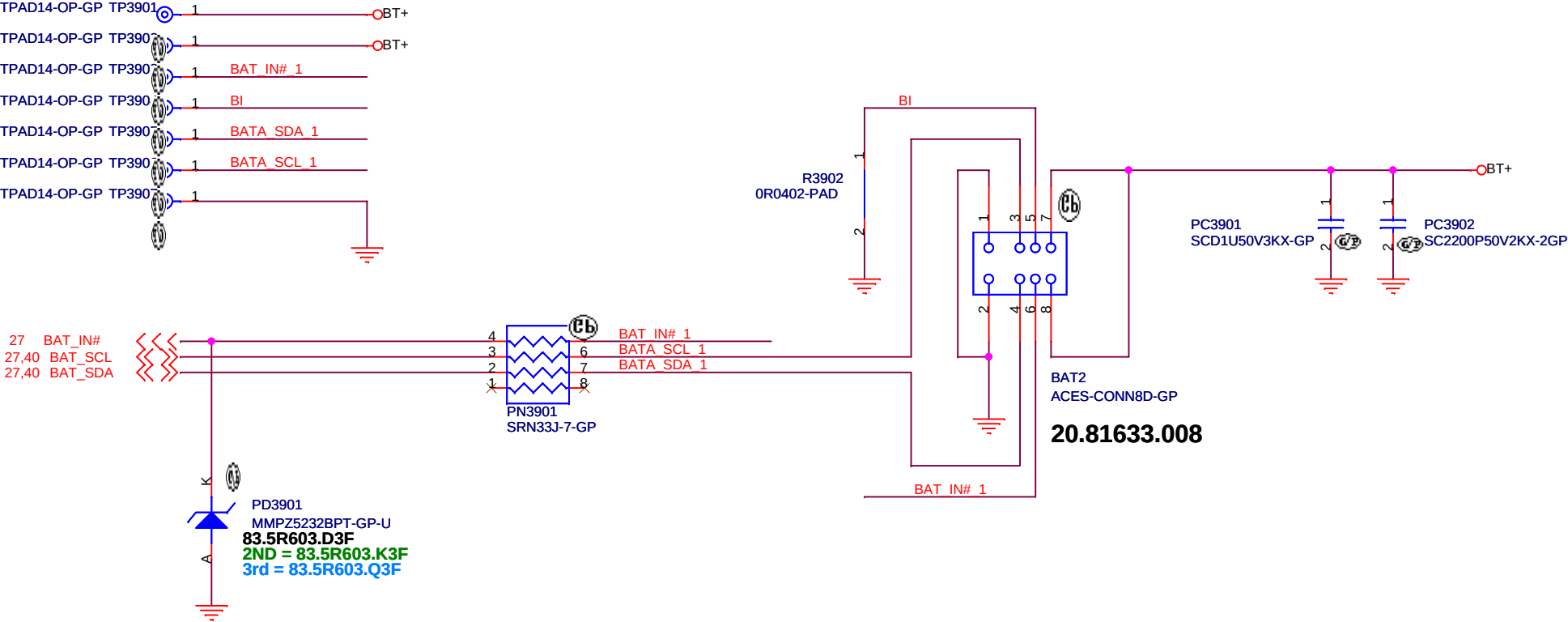
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緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			DCIN JACK	
Size	Document Number		Rev	
A4	Petra Uma		-1	
Date:	Tuesday, July 10, 2012		Sheet	38 of 103

BATTERY CONNECTOR



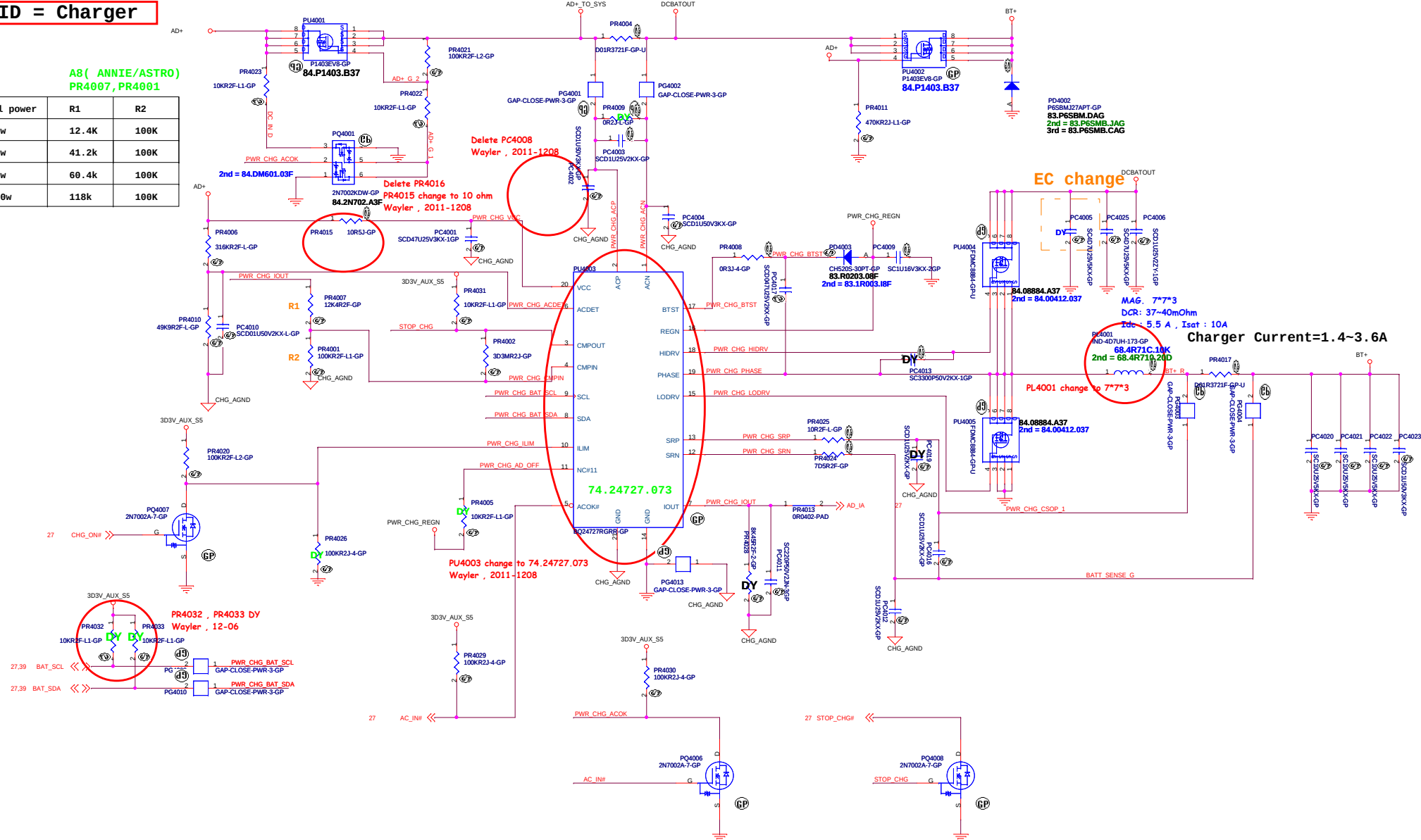
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緯創資通		Wistron Corporation	
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Title			
BATT CONN			
Size A4	Document Number Petra Uma		Rev -1
Date:	Tuesday, July 10, 2012	Sheet 39 of	103

SSID = Charger

A8(ANNIE/ASTRO)
PR4007, PR4001

AD+ total power	R1	R2
65w	12.4K	100K
80w	41.2k	100K
90w	60.4k	100K
120w	118k	100K



<Core Design>

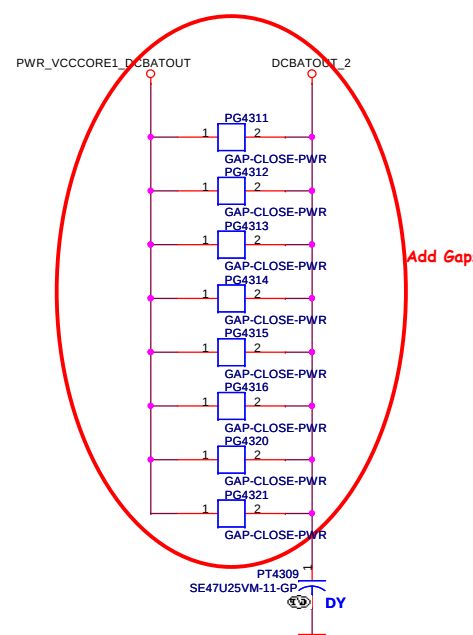
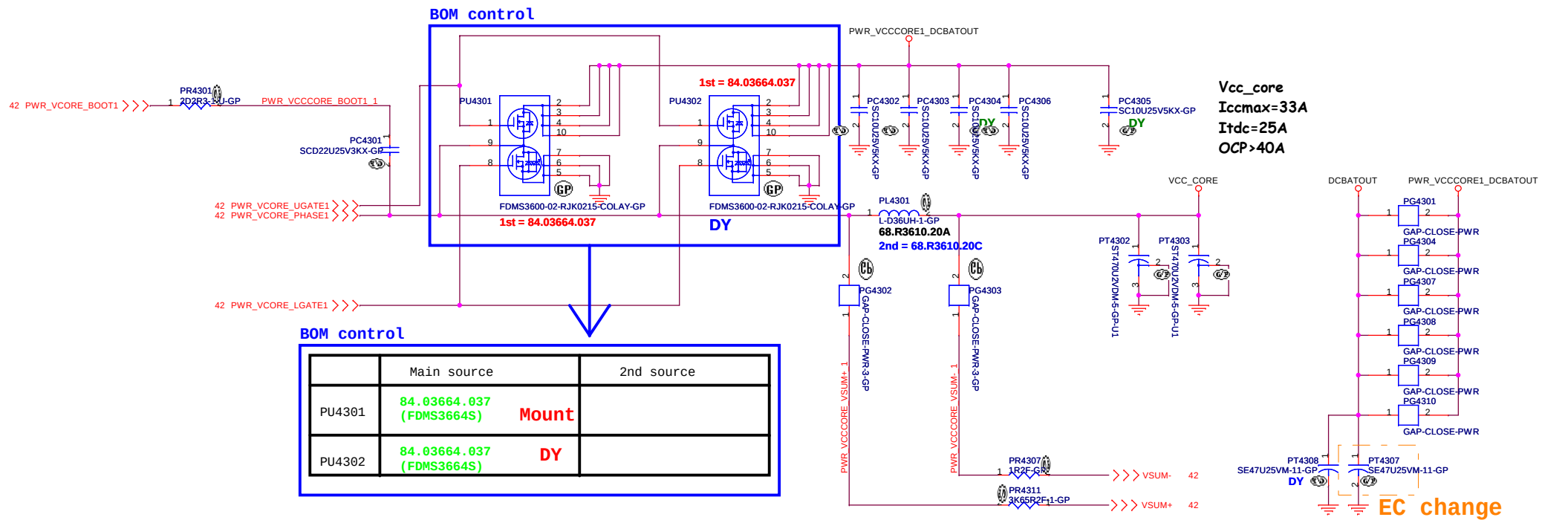
緯創資通 **Wistron Corporation**
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Taipei Hsien 221, Taiwan, R.O.C.

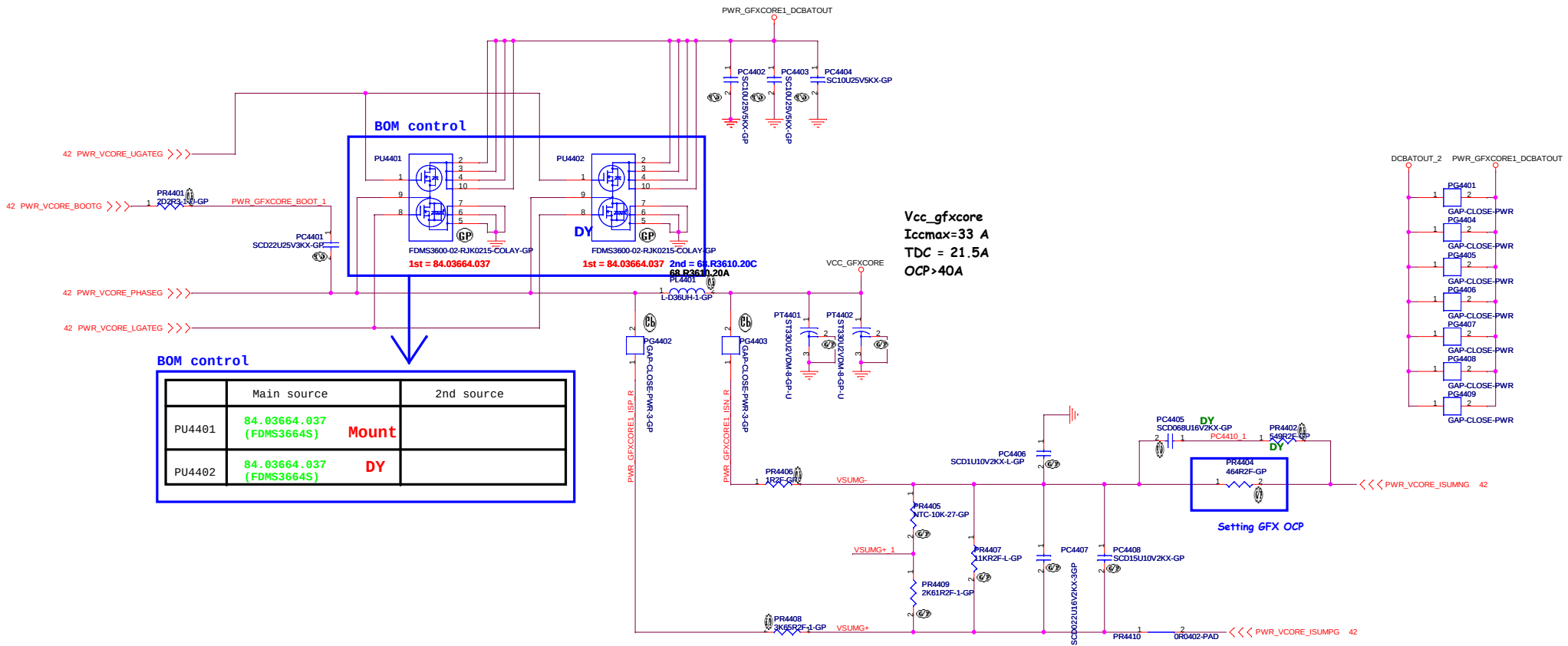
Title			
CHARGER BQ24707A			
Size Custom	Document Number		Rev
	Petra Uma		-1
Date:	Tuesday, July 10, 2012	Sheet	40 of 108

WWW.MANUALS.CLAN.SU

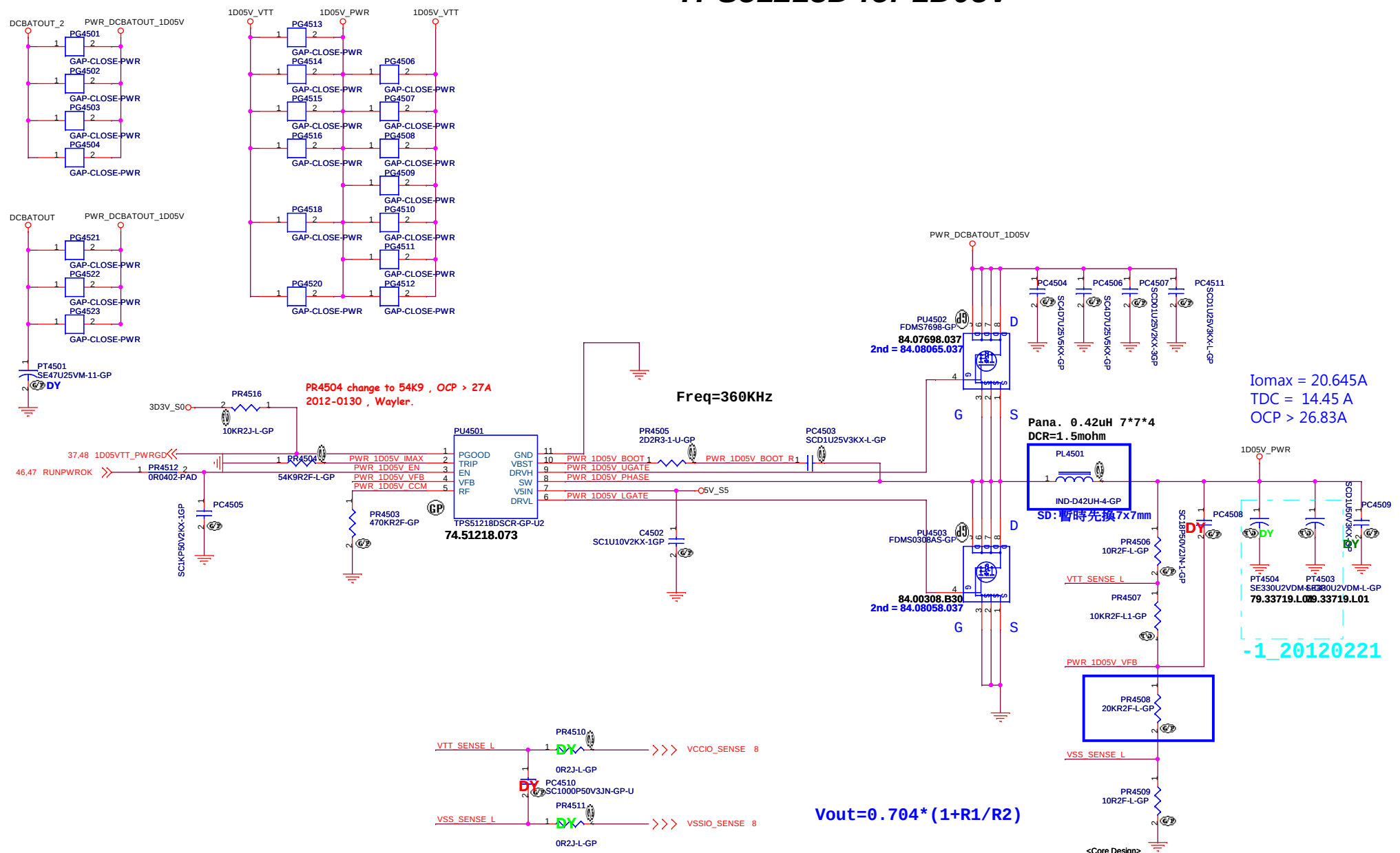


Title	ISL95836_CPU_CORE(1/3)		
Size	Document Number		Rev
Custom	Petra Uma		-
Date:	Tuesday, July 10, 2012	Sheet 42 of	102



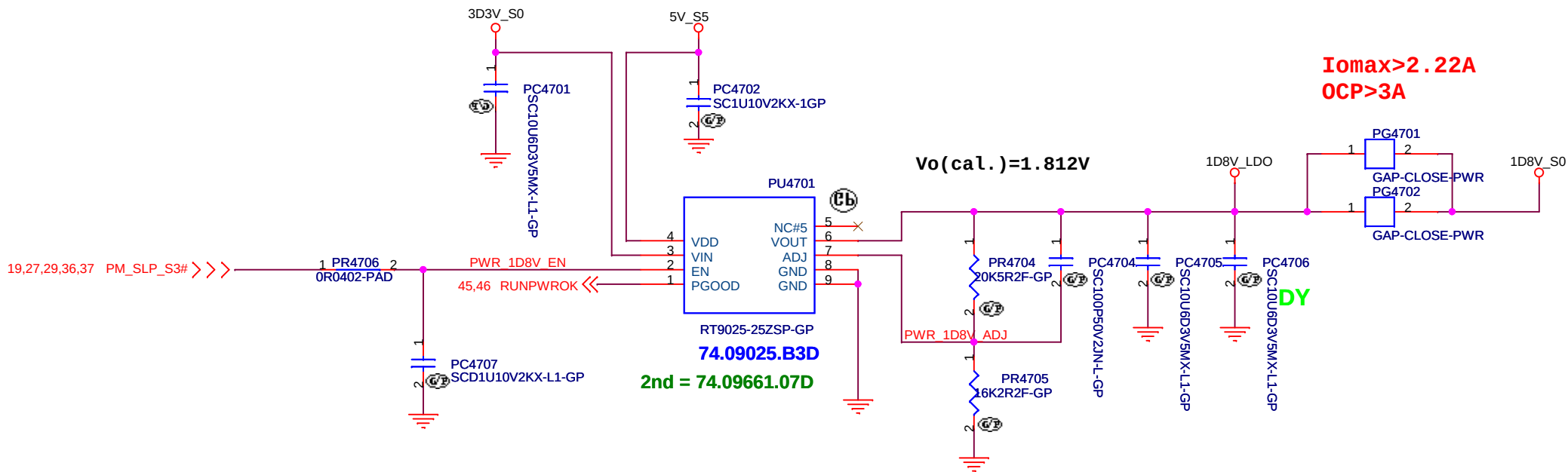


TPS51218D for 1D05V




```
SSID = PWR.Plane.Regulator_1p8v
```

RT9025 for 1D8V_S0



<Core Design>

緯創資通

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Title	Author	Year	Journal	Volume	Issue	Page
1. The Effect of Temperature on the Rate of Reaction	John Doe	2018	Journal of Chemical Education	95	3	456-462
2. Kinetics of the Reaction Between Hydrogen Peroxide and Potassium Iodide	Jane Smith	2017	Journal of Chemical Education	94	2	321-328
3. The Effect of Concentration on the Rate of Reaction	Michael Brown	2016	Journal of Chemical Education	93	1	123-130
4. The Effect of Surface Area on the Rate of Reaction	Sarah White	2015	Journal of Chemical Education	92	4	567-574
5. The Effect of Catalyst on the Rate of Reaction	David Black	2014	Journal of Chemical Education	91	5	678-685

LDO 1D8V(RT9025

Size
A4

Document Number

Petra Uma

Rev	1
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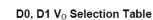
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WWW.MANUALS.CLAN.SU

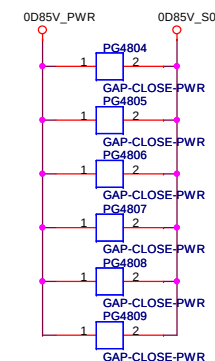
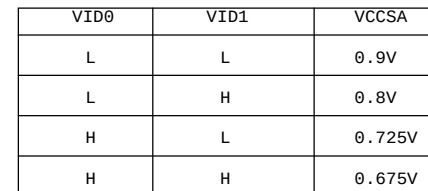
PWM SY8037 for VCCSA



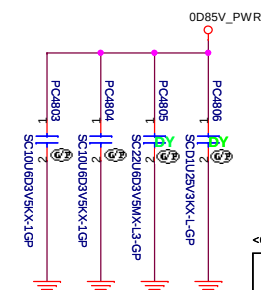
"x" means "don't care".

VEN/MODE Logic

VEN/MODE (VPP=5V)	EN logic	VEN/MODE (VPP=5V)	MODE logic
<0.6V	0	<2.0V	0
>1.0V	1	>2.6V	1



Design Current =4 A



<Core Design>

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Title

VCCSA SY8037

Size
A3

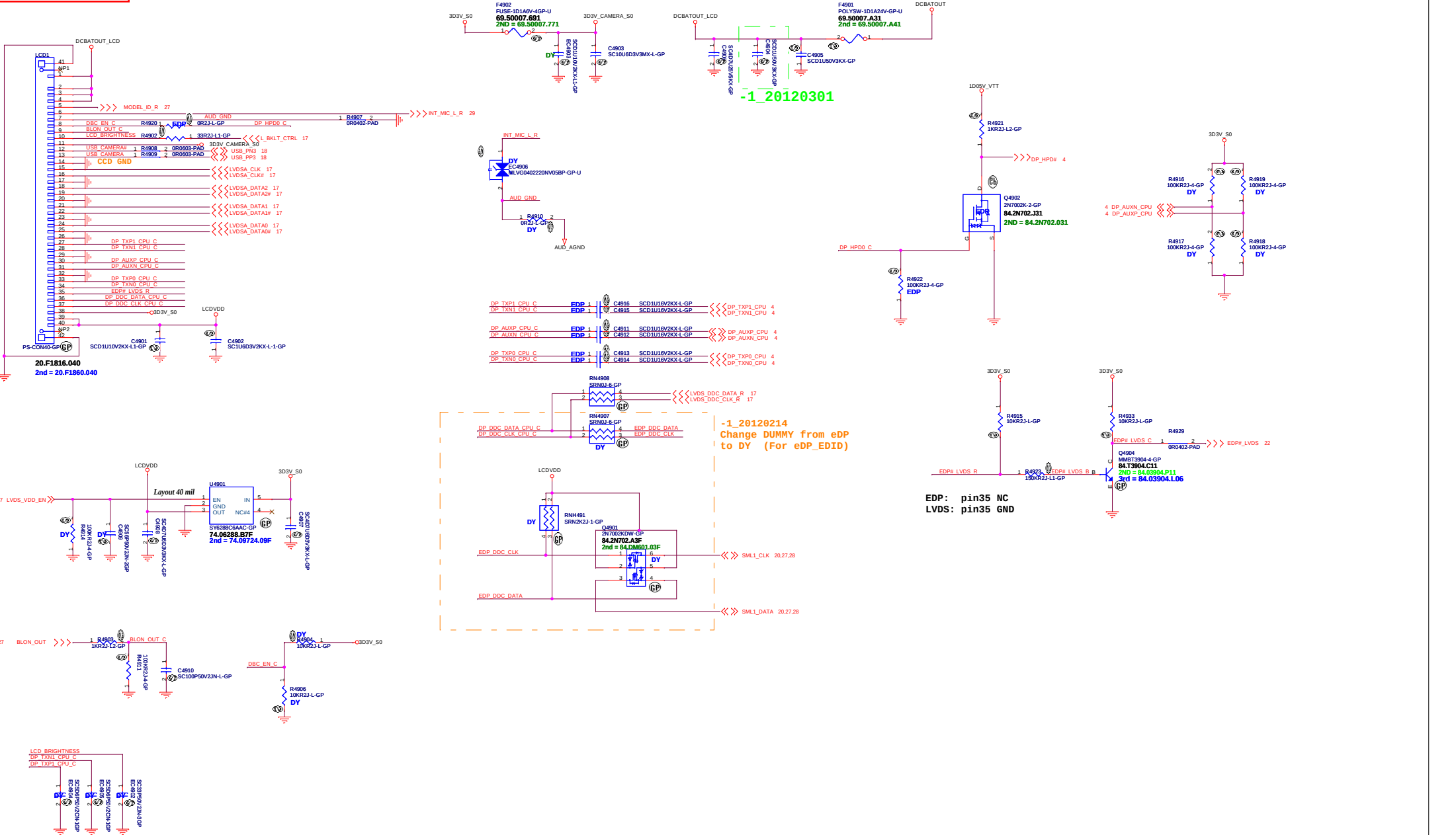
Document Number	
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Petra Uma

Rev	-1
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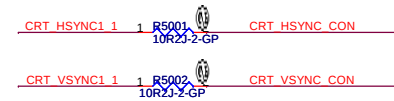
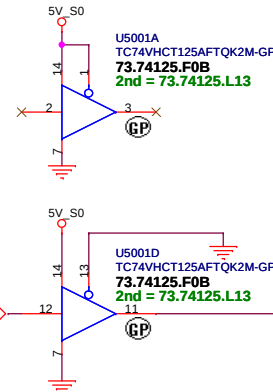
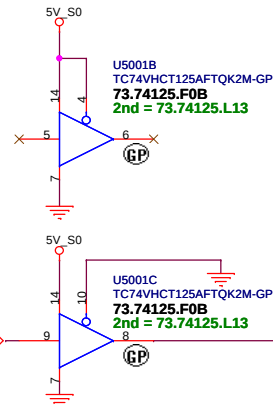
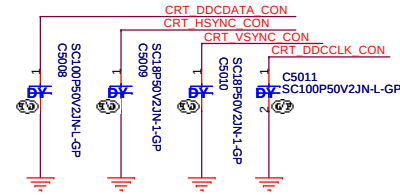
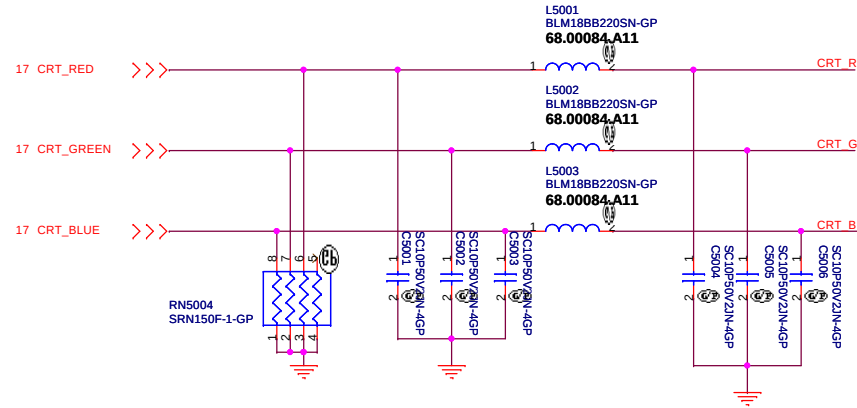
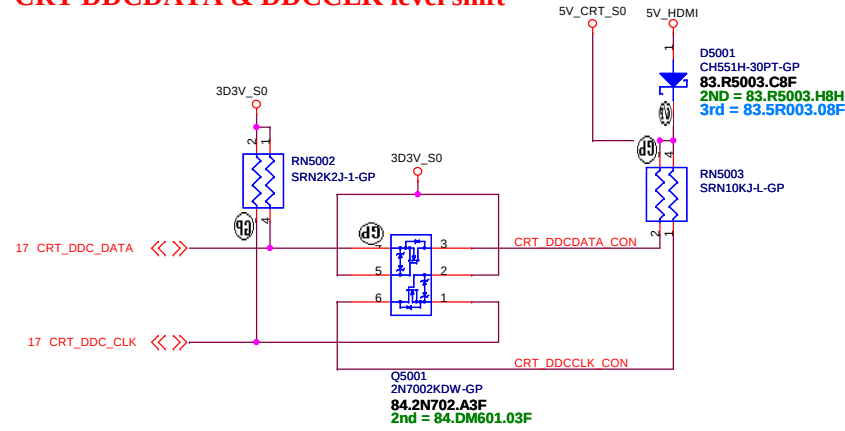
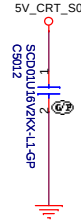
Date: Tuesday, July 10, 2012

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CRT DDCDATA & DDCCLK level shift

CRT_DDCDATA_CON >>> CRT_DDCDATA_CON 59
 CRT_DDCCLK_CON >>> CRT_DDCCLK_CON 59
 CRT_R >>> CRT_R 59
 CRT_G >>> CRT_G 59
 CRT_B >>> CRT_B 59
 CRT_HSYNC_CON >>> CRT_HSYNC_CON 59
 CRT_VSYNC_CON >>> CRT_VSYNC_CON 59

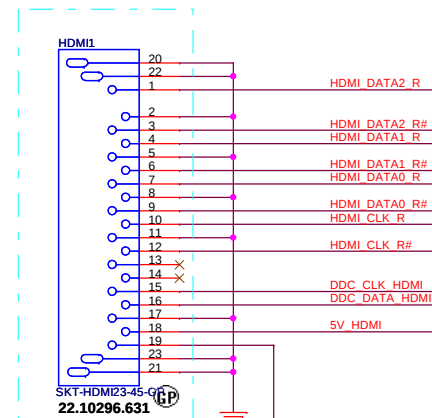
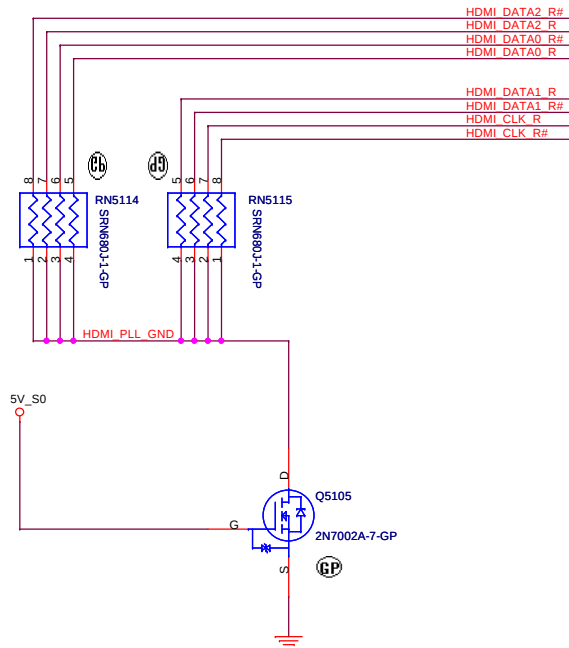


SSID = VIDEO HDMI Level Shifter & CONNECTOR

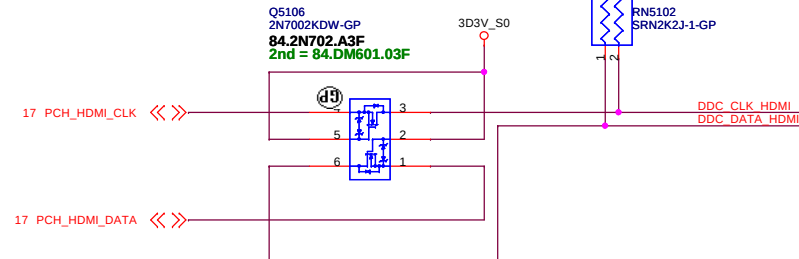
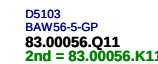
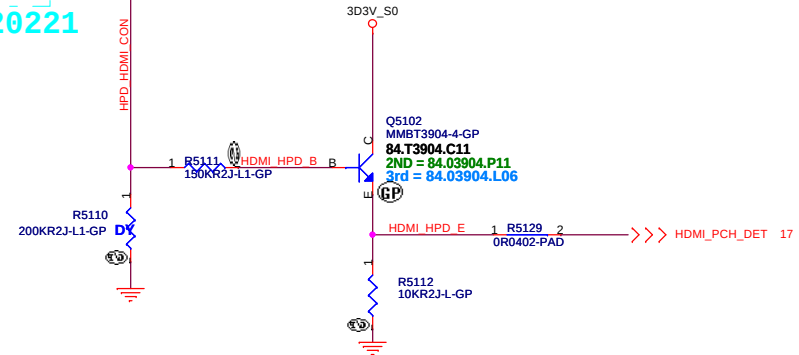
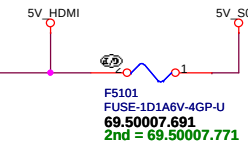
Close to HDMI Connector

17 HDMI_CLK_R# >>>
17 HDMI_CLK_R >>>
17 HDMI_DATA0_R# >>>
17 HDMI_DATA0_R >>>

17 HDMI_DATA1_R# >>>
17 HDMI_DATA1_R >>>
17 HDMI_DATA2_R# >>>
17 HDMI_DATA2_R >>>



-1_20120221



LED BACKLIGHT CONVERTER POWER

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<Core Design>

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Title

eDP

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<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title S-VIDEO	
Size A4	Document Number Petra Uma
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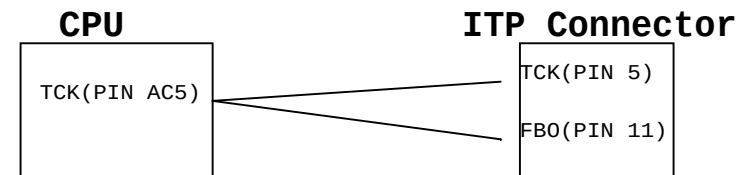
緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
Reserved			
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SSID = User.Interface

ITP Connector

H_CPURST# use pull-up Resistor close
ITP connector 500 mil (max),
others place near CPU side.

(Blanking)



<Core Design>

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Title

ITP

Size
A4

Document Number

Petra Uma

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-1

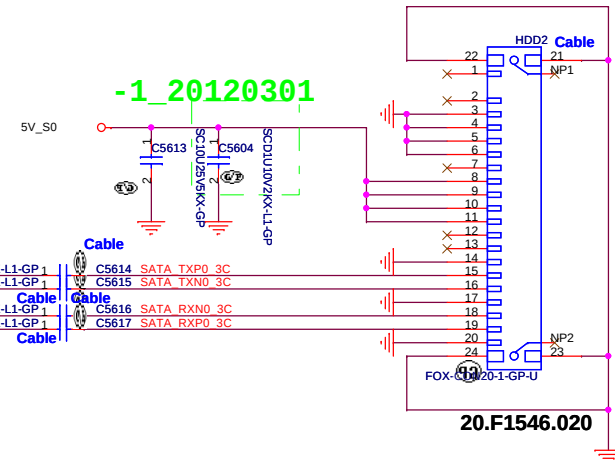
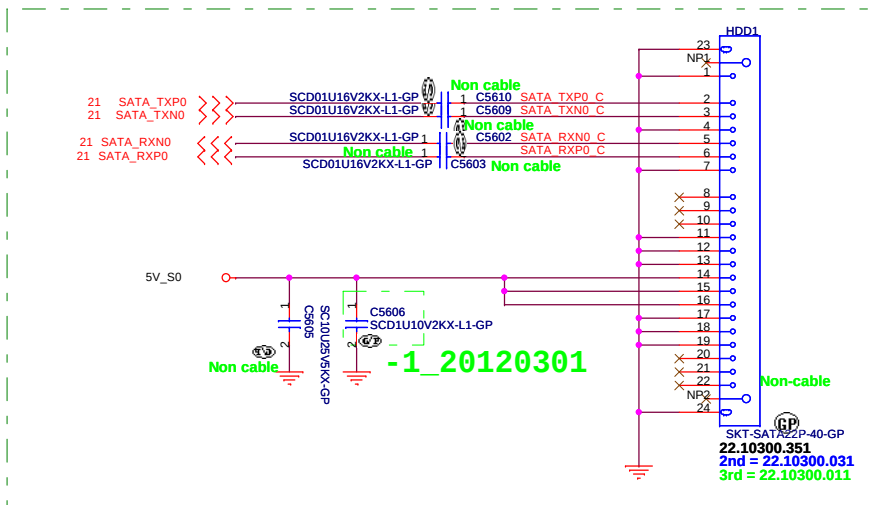
Date: Wednesday, February 22, 2012

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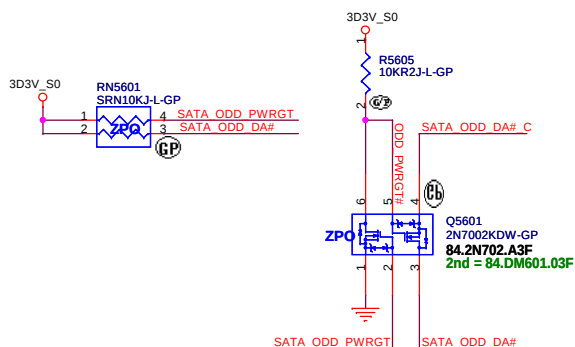
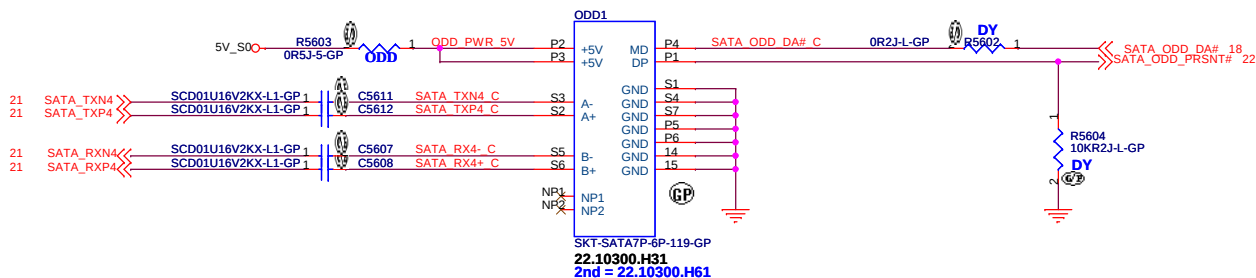
SSID = SATA

-1_20120223

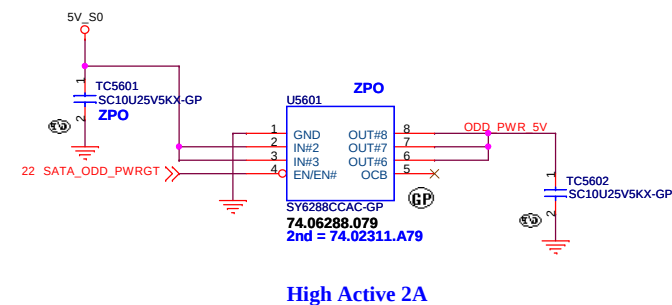
SATA HDD Connector



ODD Connector



SATA Zero Power ODD



High Active 2A

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<Core Design>

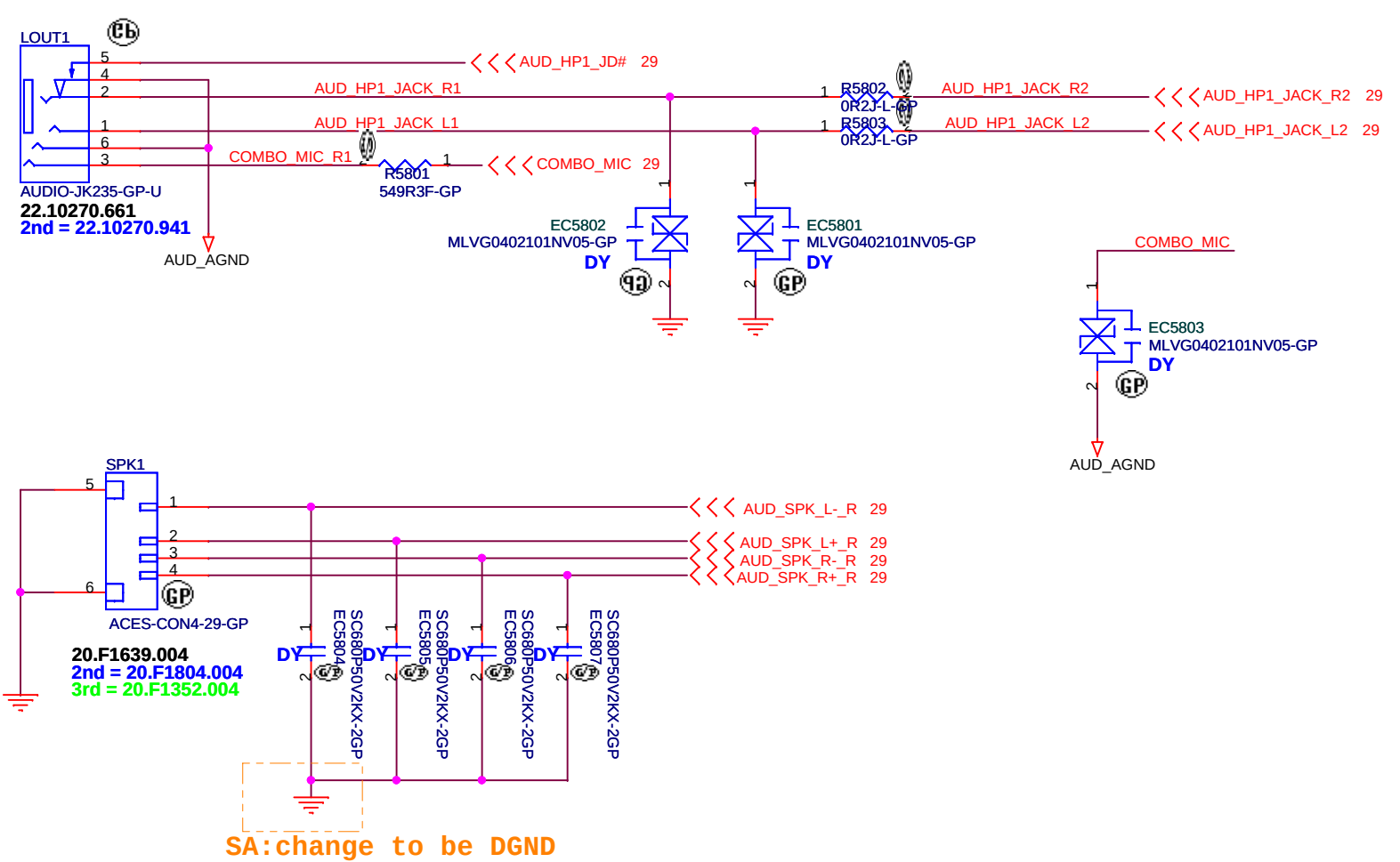
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		21F, 8B, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
HDD/ODD			
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<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title E-SATA/USB CHARGER	
Size A4	Document Number Petra Uma
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SSID = AUDIO

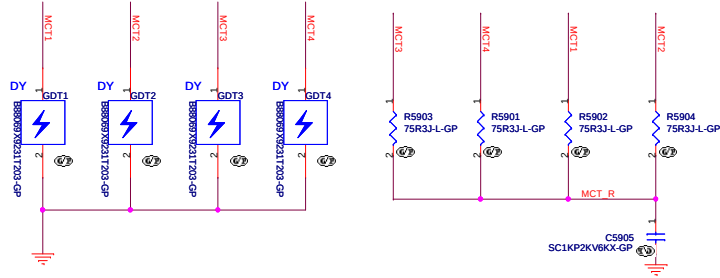
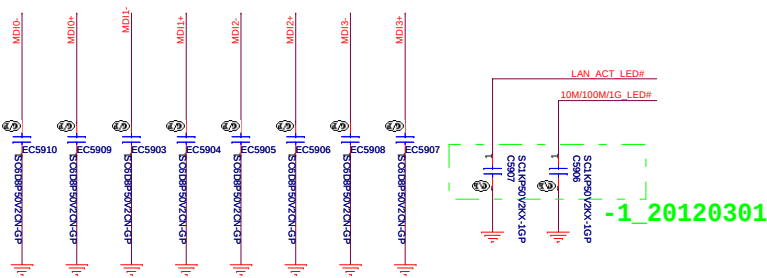
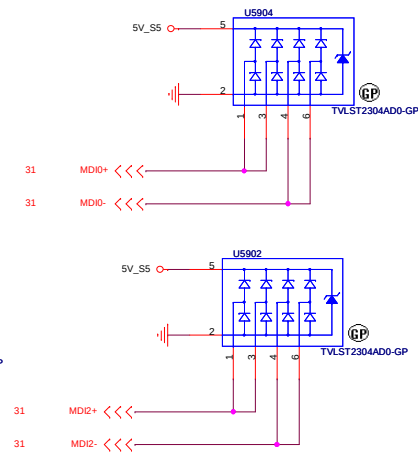
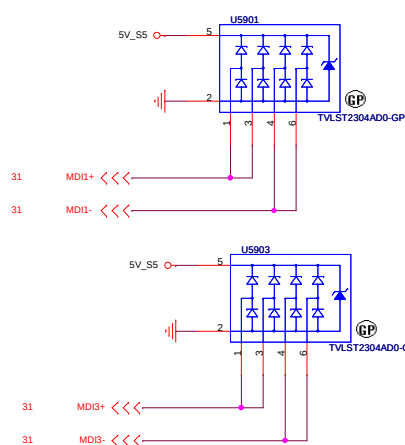
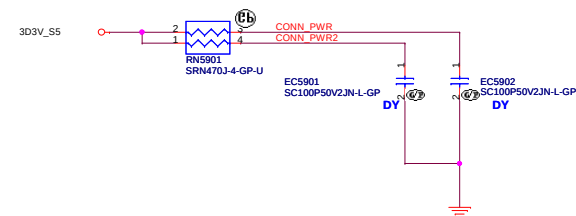
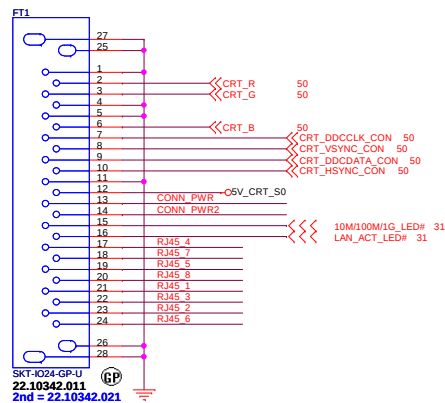
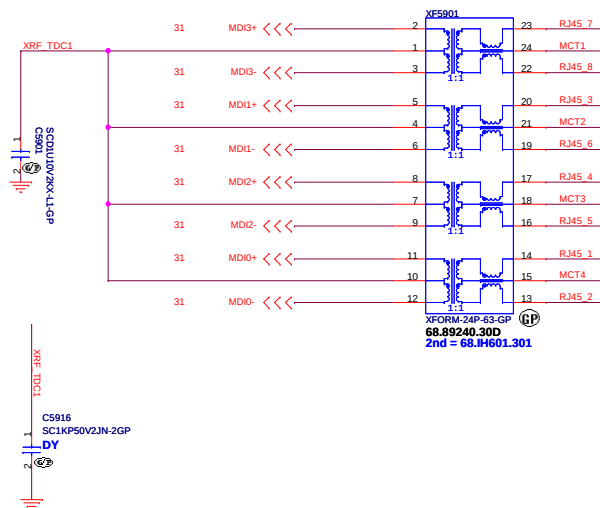


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Taipei Hsien 221, Taiwan, R.O.C.

Title			Audio Jack	
Size	Document Number		Rev	
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SSID = LAN



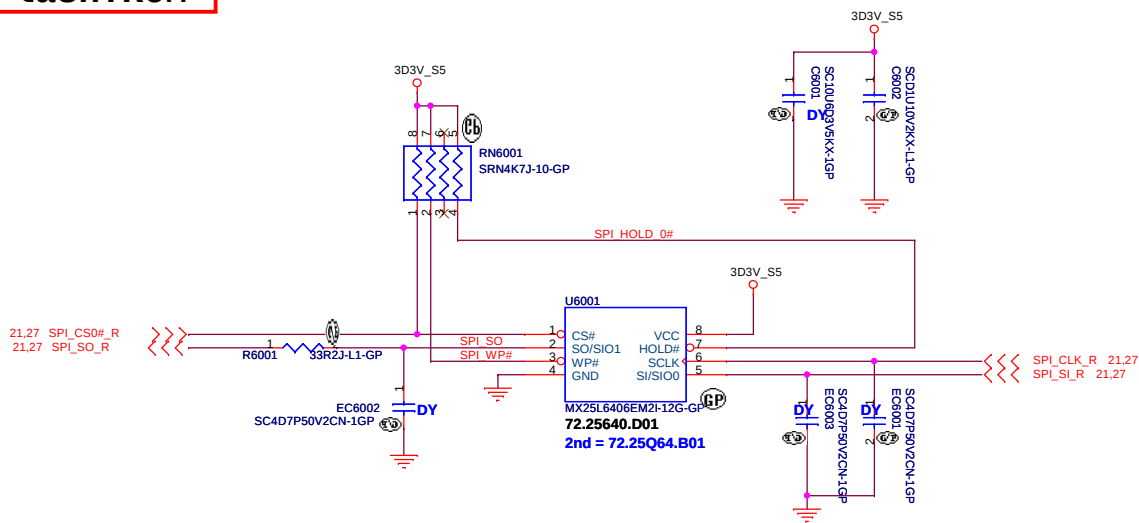
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緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title LAN CONNECTOR
Size Custom Document Number
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```
21,27 SPI_CS0#_R
21,27 SPI_SO_R
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<Core Design>

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Wistron Corporation
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Flash/RTC

Size

Document Number

Date: Tuesday, July 10, 2012

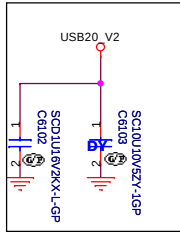
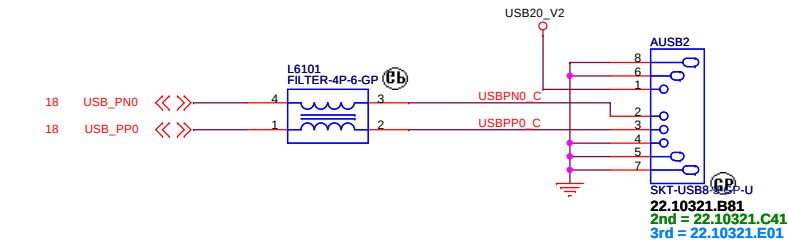
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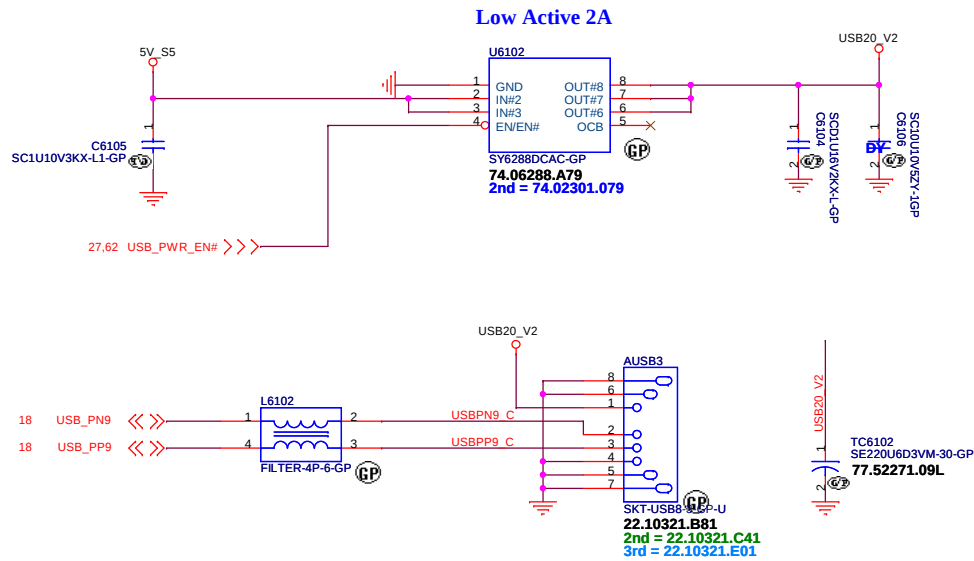
Petra Uma

Rev

SSID = USB



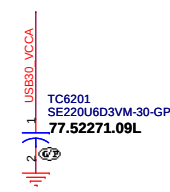
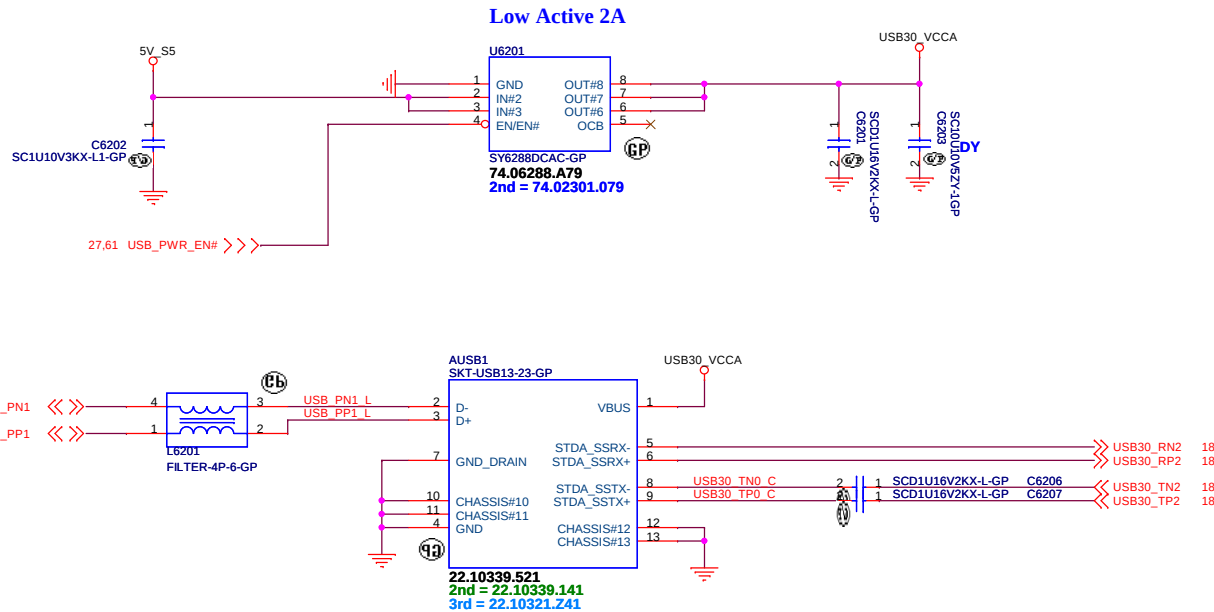
Close to AUSB2



<Core Design>

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Title			USB Power SW	
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USB 3.0 Connector Pin definition	
1	POWER
2	USB 2.0 D-
3	USB 2.0 D+
4	GND
5	StdA_SSRX- SuperSpeed RX
6	StdA_SSRX+
7	GND
8	StdA_SSTX- SuperSpeed TX
9	StdA_SSTX+

SSID = User.Interface
Bluetooth Module conn.

ANNIE Bluetooth Module

(Blanking)

<Core Design>

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Title

Bluetooth

Size
A4

Document Number
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-1

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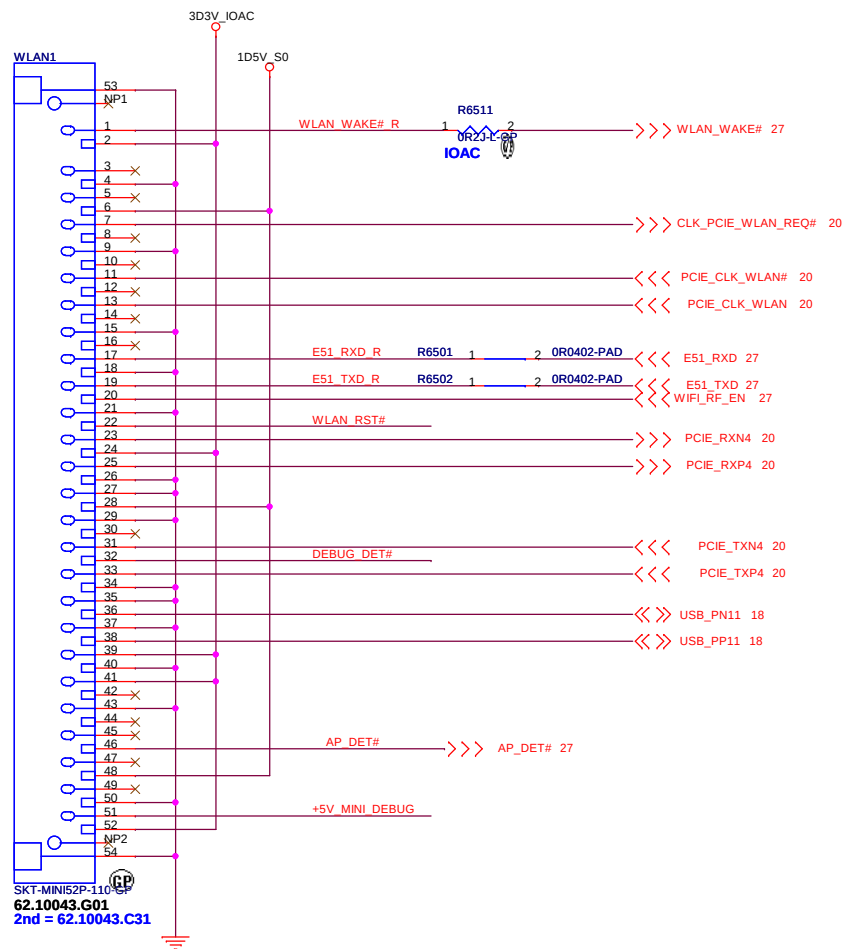
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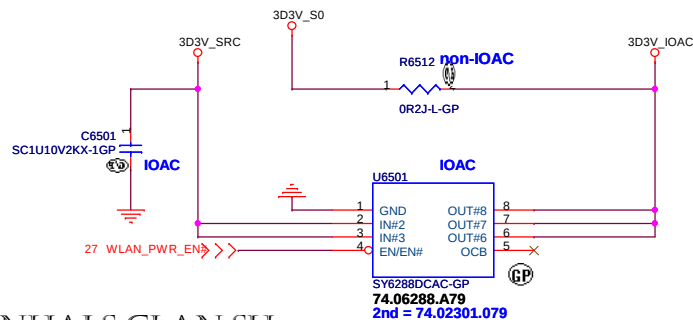
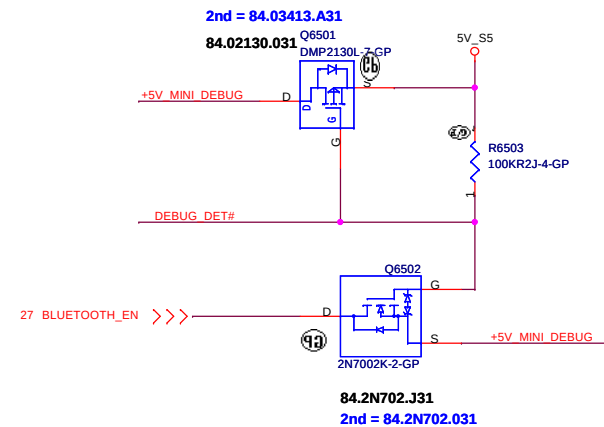
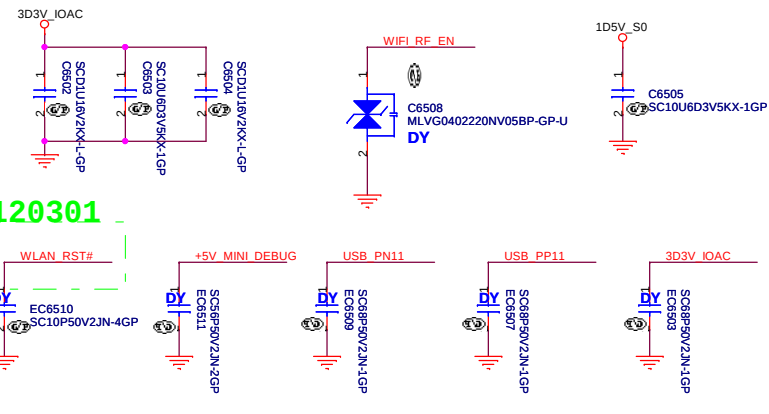
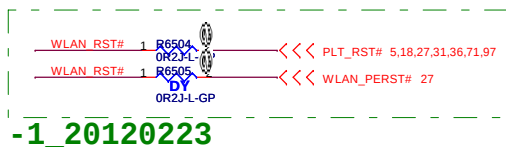
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
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SSID = Wireless

Mini Card Connector(802.11a/b/g/n)



-1_20120301



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Title			MINICARD(WLAN)/ITP CONN
Size	Document Number	Rev	
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SSID = Wireless

Mini Card Connector(WWAN)

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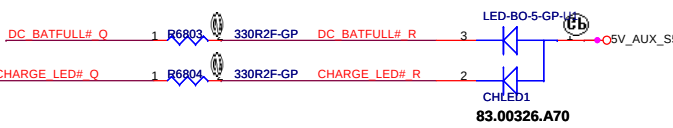
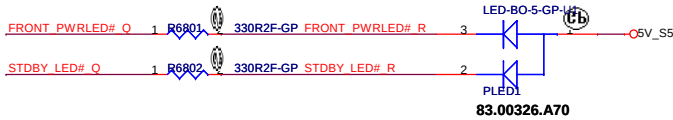
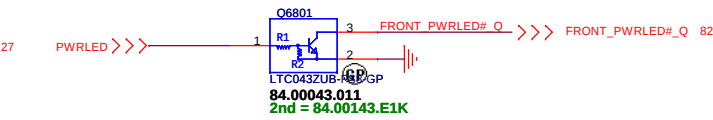
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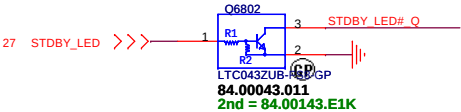
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		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title					
Reserved					
Size A4	Document Number Petra Uma		Rev -1		
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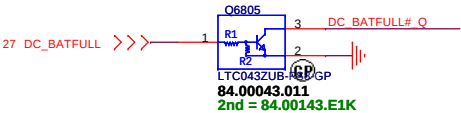
Power button LED



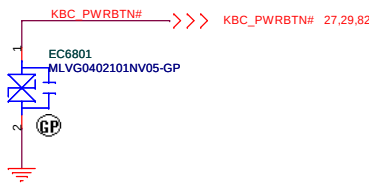
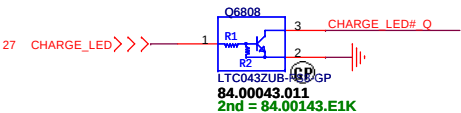
Power STDBY_LED



Battery LED2(DC_BATFULL)



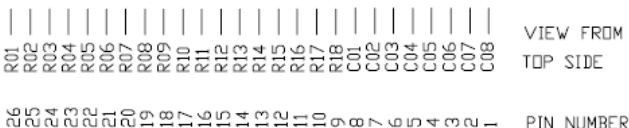
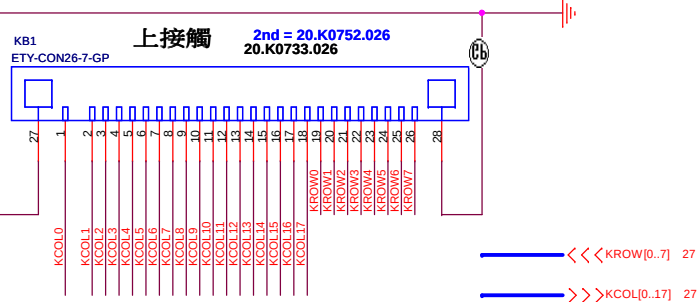
Battery LED1(CHARGE)



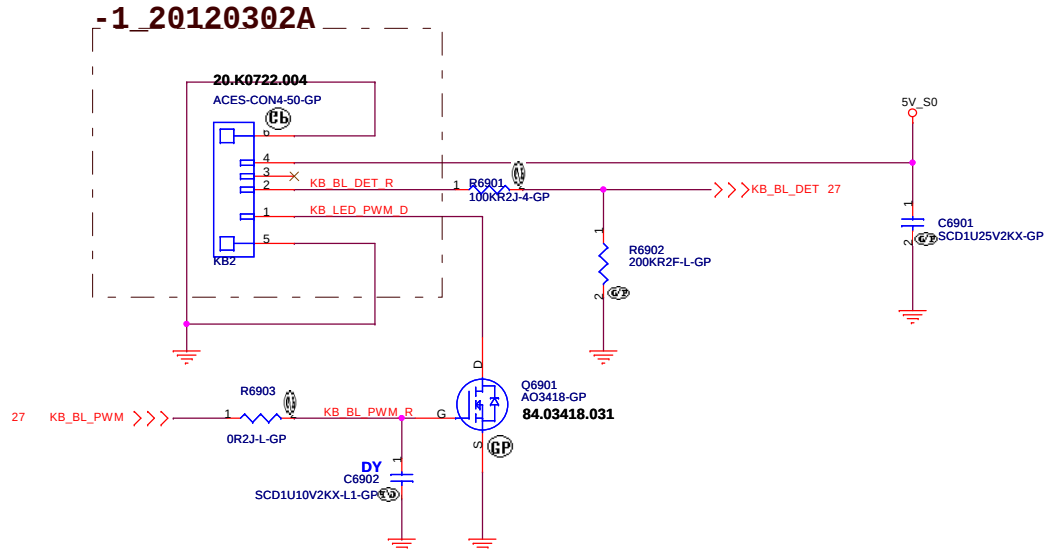
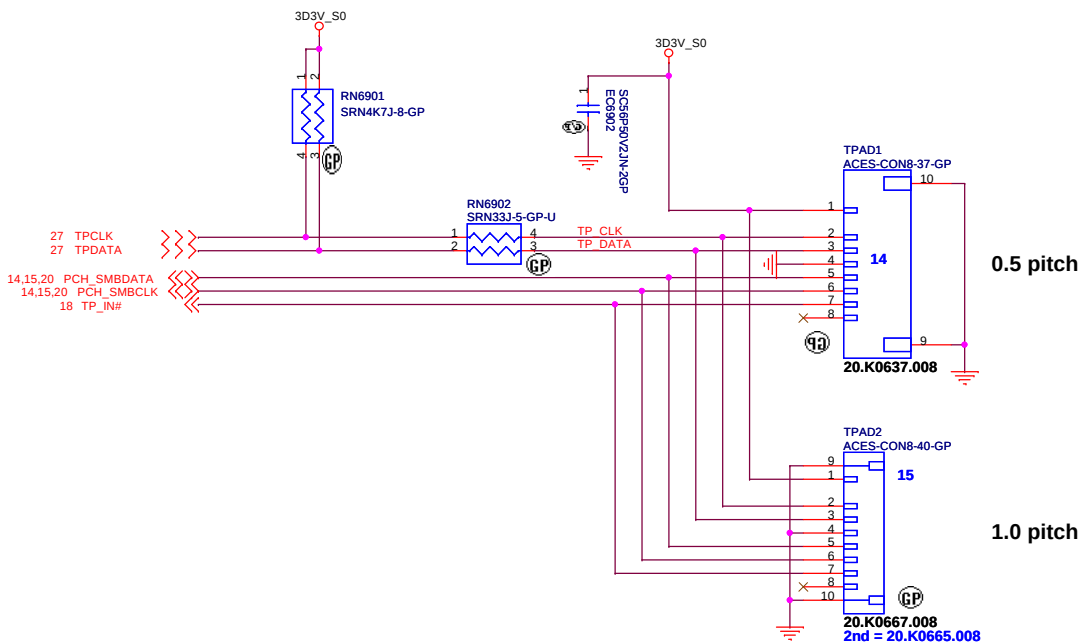
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緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<i>LED Bard/Power Button</i>			
Size	Document Number	Rev	
Custom	Petra Uma	-1	
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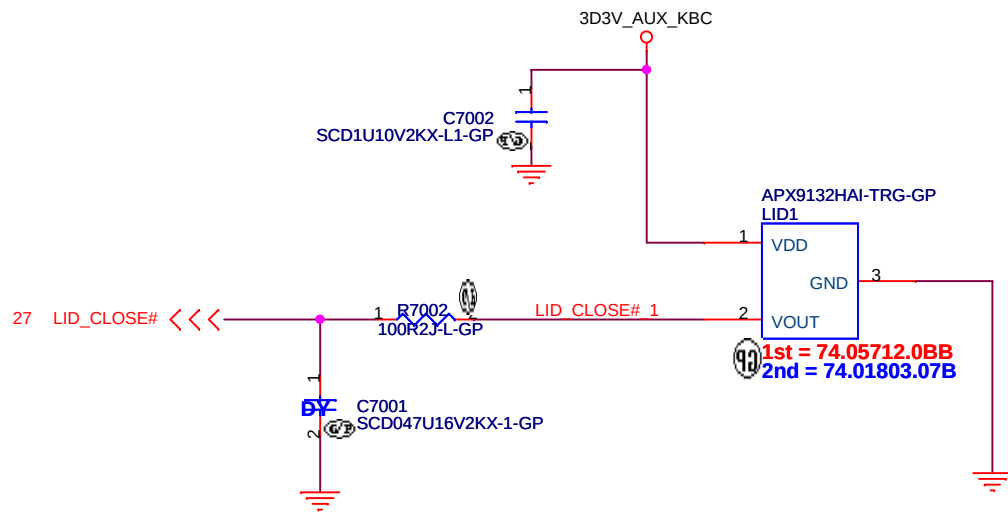
SSID = KBC

Internal KeyBoard Connector



TOUCH PAD





<Core Design>

緯創資通

Wistron Corporation
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Title

Hall Sensor

Size
A4

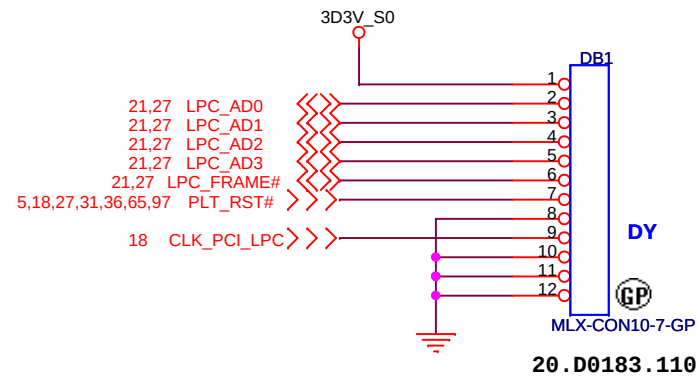
Document Number

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-1

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<Core Design>

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Title

Dubug connector

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A4

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-1

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<Core Design>

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Title

Reserved

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<Core Design>

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Taipei Hsien 221, Taiwan, R.O.C.

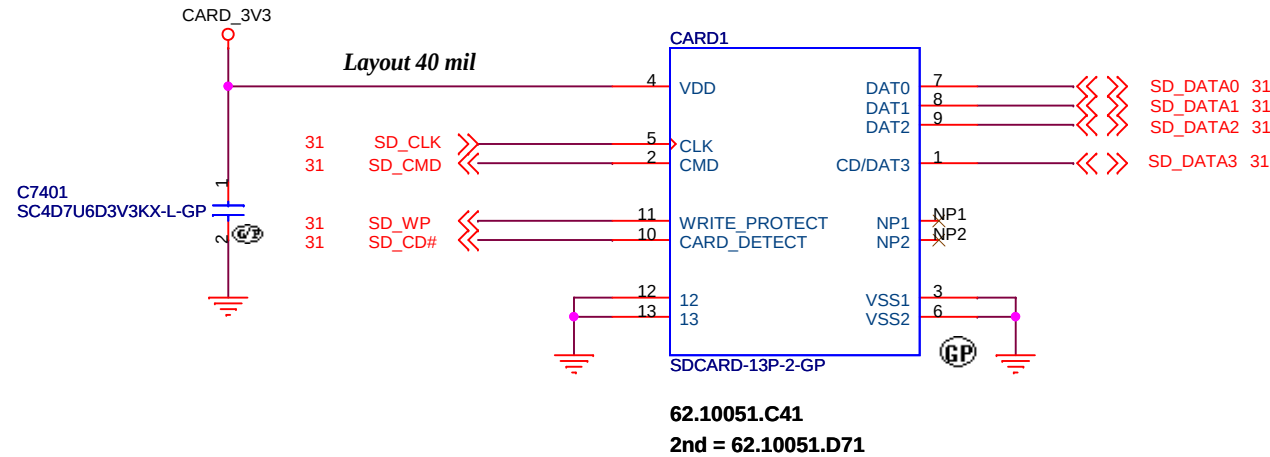
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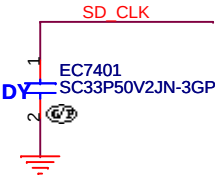
Size A4	Document Number Petra Uma	Rev -1
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SSID = SDIO

SD/MMC Card Reader



BCM57765/BCM57785 Pin Number	Signal Name	SD/MMC Interface	MS-Pro Interface	xD Interface
21	CR_CLK/RX_BY#	CR_CLK	MS_CLK	RX_BY#
26	CR_CMD/CLE	CR_CMD	MS_BS	CLE
25	CR_DATA0	CR_DATA0	MS_DATA0	XD_DATA0
24	CR_DATA1	CR_DATA1	MS_DATA1	XD_DATA1
23	CR_DATA2	CR_DATA2	MS_DATA2	XD_DATA2
22	CR_DATA3	CR_DATA3	MS_DATA3	XD_DATA3
52	CR_DATA4	CR_DATA4	MS_DATA4	XD_DATA4
53	CR_DATA5	CR_DATA5	MS_DATA5	XD_DATA5
54	CR_DATA6	CR_DATA6	MS_DATA6	XD_DATA6
55	CR_DATA7	CR_DATA7	MS_DATA7	XD_DATA7
60	ALE	-	-	ALE
1	SD_DETECT/WE#	CR_DETECT	-	WE#
9	RE#	-	-	RE#
59	CE#/MS_INS#	-	MS_INS#	CE#
57	CR_WP#/XD_WP# SD_WP#	-	-	WP#
68	XD_DETECT	-	-	XD_DETECT



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緯創資通 Wistron Corporation
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Title			CARD Reader CONN		
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SSID = ExpressCard

+1.5V_CARD Max. 650mA, Average 500mA.
+3.3V_CARD Max. 1300mA, Average 1000mA
+3.3V_CARDAUX Max. 275mA

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緯創資通

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Title

New Card

Size
A4

Document Number

Petra Uma

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-1

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<Core Design>

緯創資通		Wistron Corporation			
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title					
Reserved					
Size	Document Number		Rev		
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<Core Design>

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
Reserved			
Size A4	Document Number Petra Uma		Rev -1
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<Core Design>

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Title Reserved	
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SSID = User.Interface

Free Fall Sensor

Note

- no via, trace, under the sensor (keep out area around 2mm)
- stay away from the screw hole or metal shield soldering joints
- design PCB pad based on our sensor LGA pad size (add 0.1mm)
- solder stencil opening to 90% of the PCB pad size
- mount the sensor near the center of mass of the NB as possible as you can

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<Core Design>

緯創資通

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Title

G- Sensor

Size
A4

Document Number

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-1

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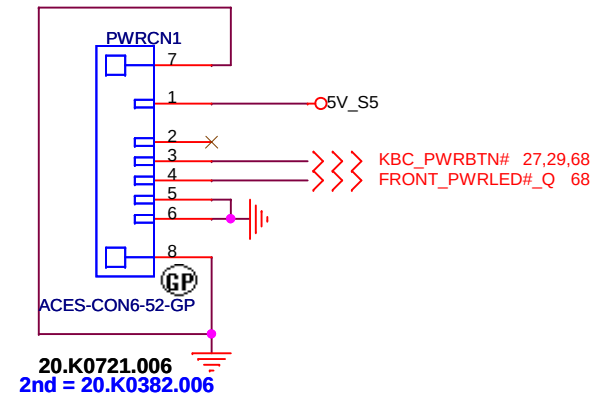
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緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title Reserved		
Size A4	Document Number Petra Uma	Rev -1
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緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
Reserved			
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<Core Design>

緯創資通 Wistron Corporation
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Title			IO Board Connector		
Size	Document Number				Rev
A4	Petra Uma				-1
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<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title GPU PCIE/STRAPPING(1/5)		
Size A4	Document Number Petra Uma	Rev -1
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<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title GPU DPPWR/GND(5/5)		
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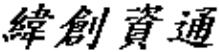
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		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
RT8208F +VGA CORE			
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<Core Design>

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<i>DISCRETE VGA POWER</i>			
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LVDS Channel A

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Panel BL brightness/Power En/BL En

<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title LVDS Switch		
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<Core Design>

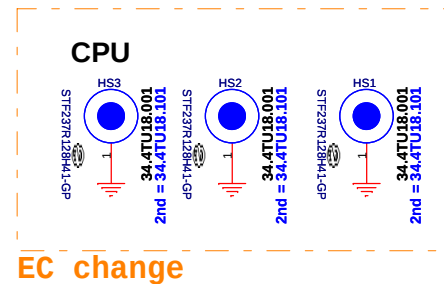
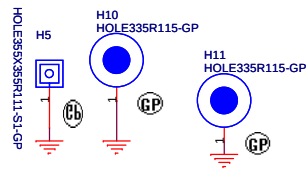
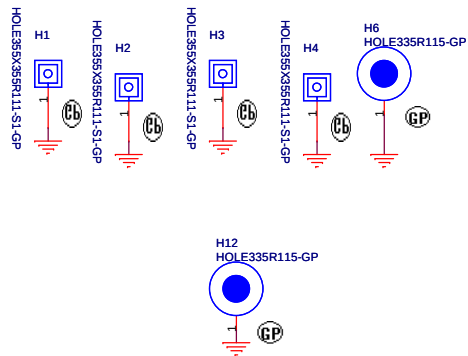
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title CRT Switch		
Size A4	Document Number Petra Uma	Rev -1
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SSID = SDIO

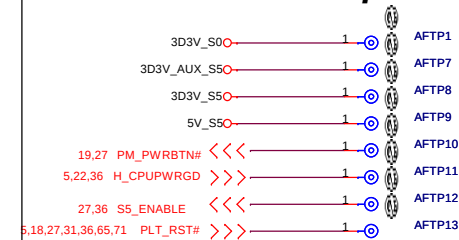
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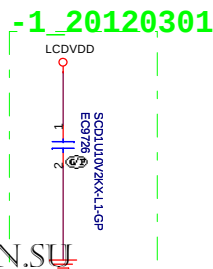
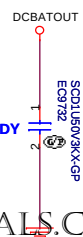
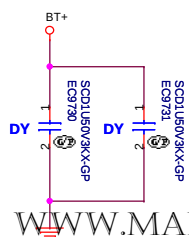
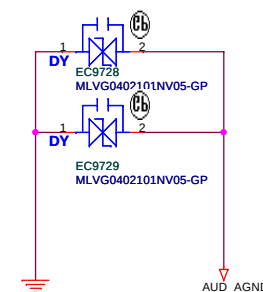
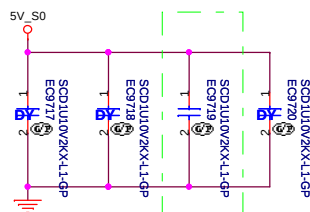
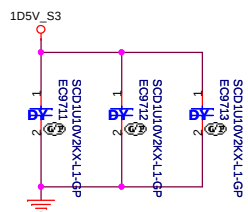
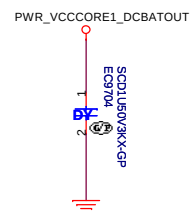
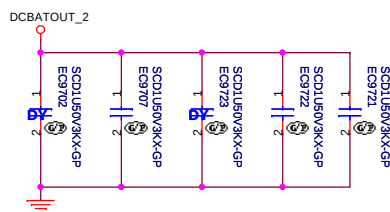
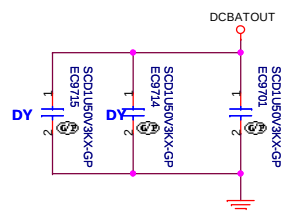
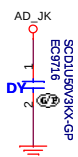
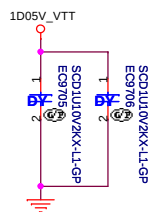
緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
TOUCH PANEL			
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Check test point



Test Point放在Dimm Door打開可量測處



-1_20120301

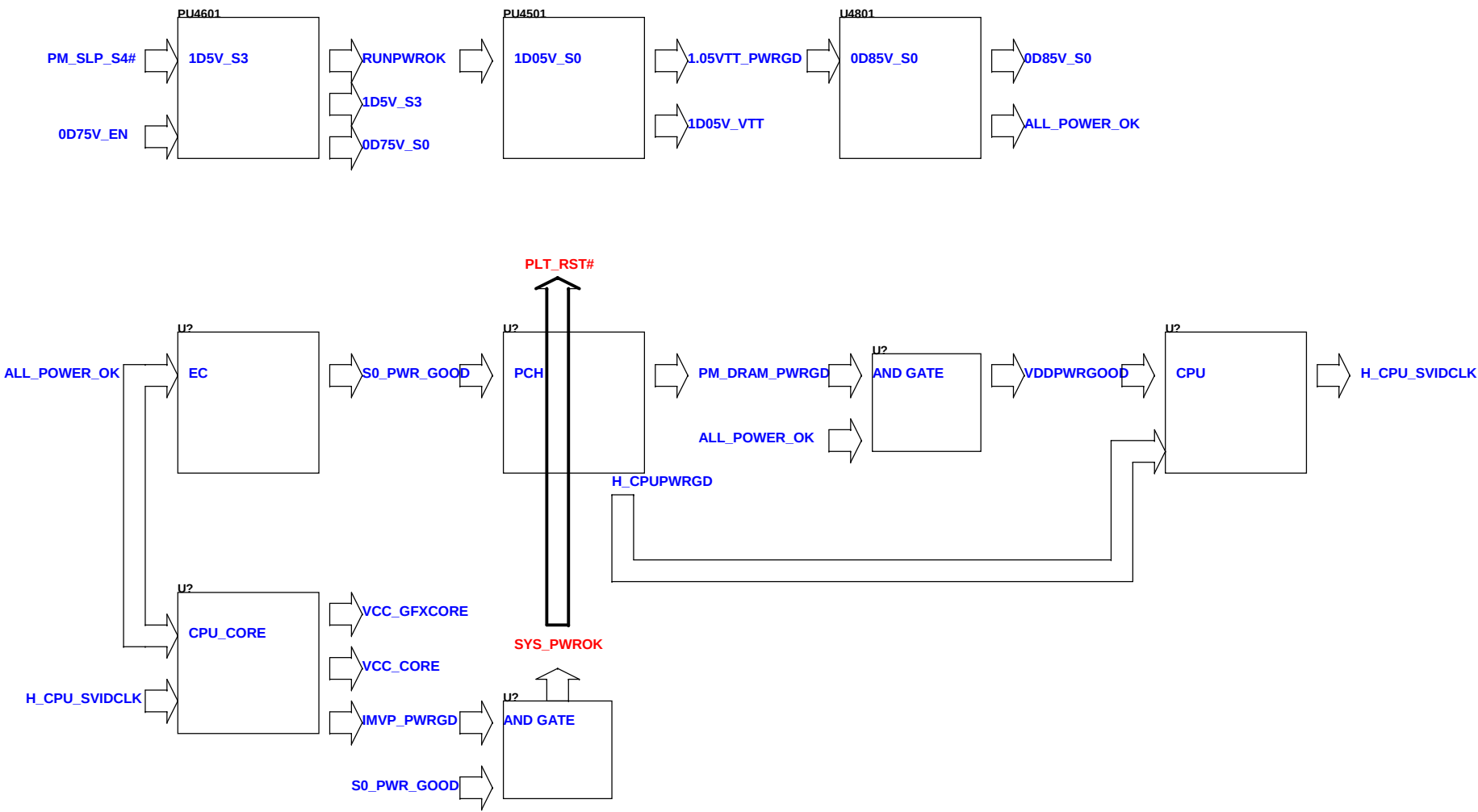
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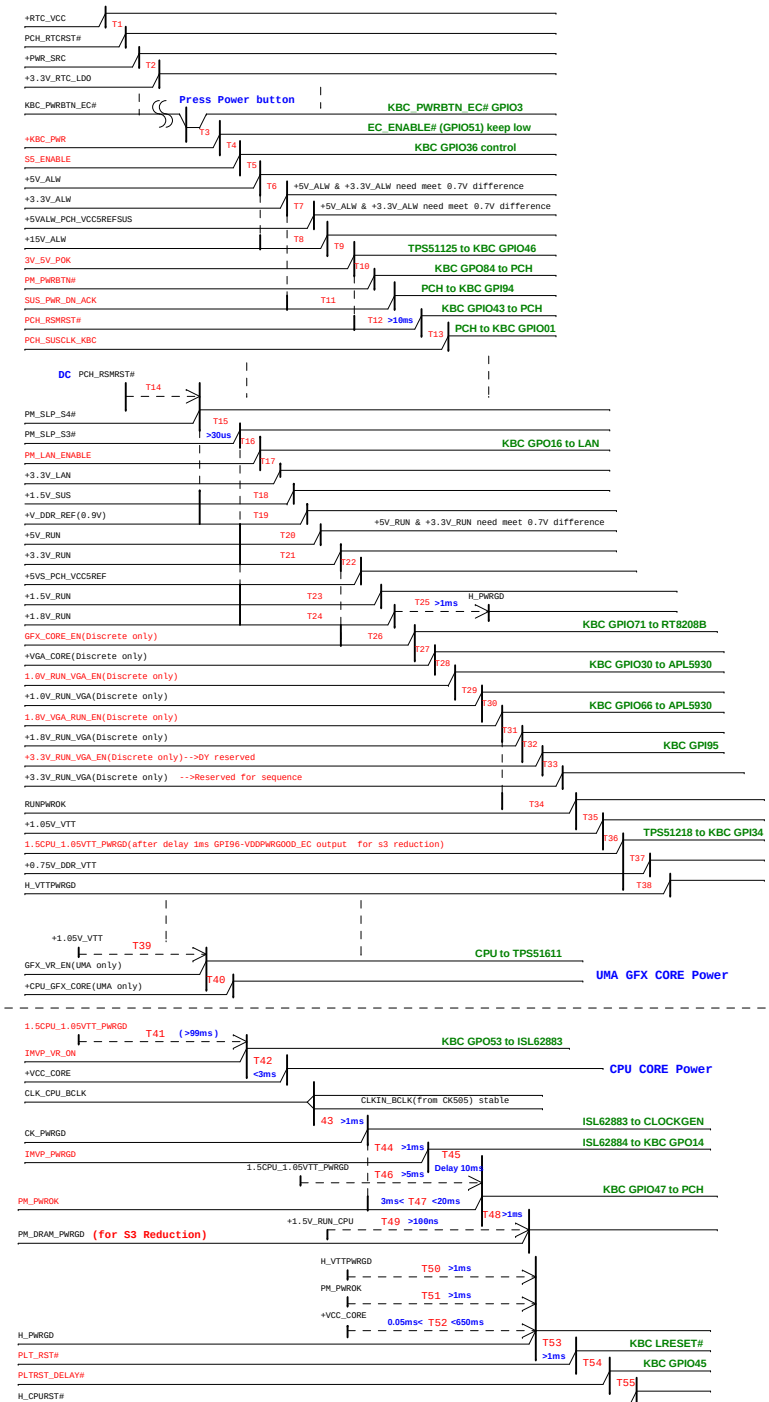
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UNUSED PARTS/EMI Capacitors		
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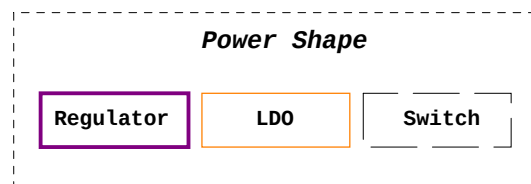
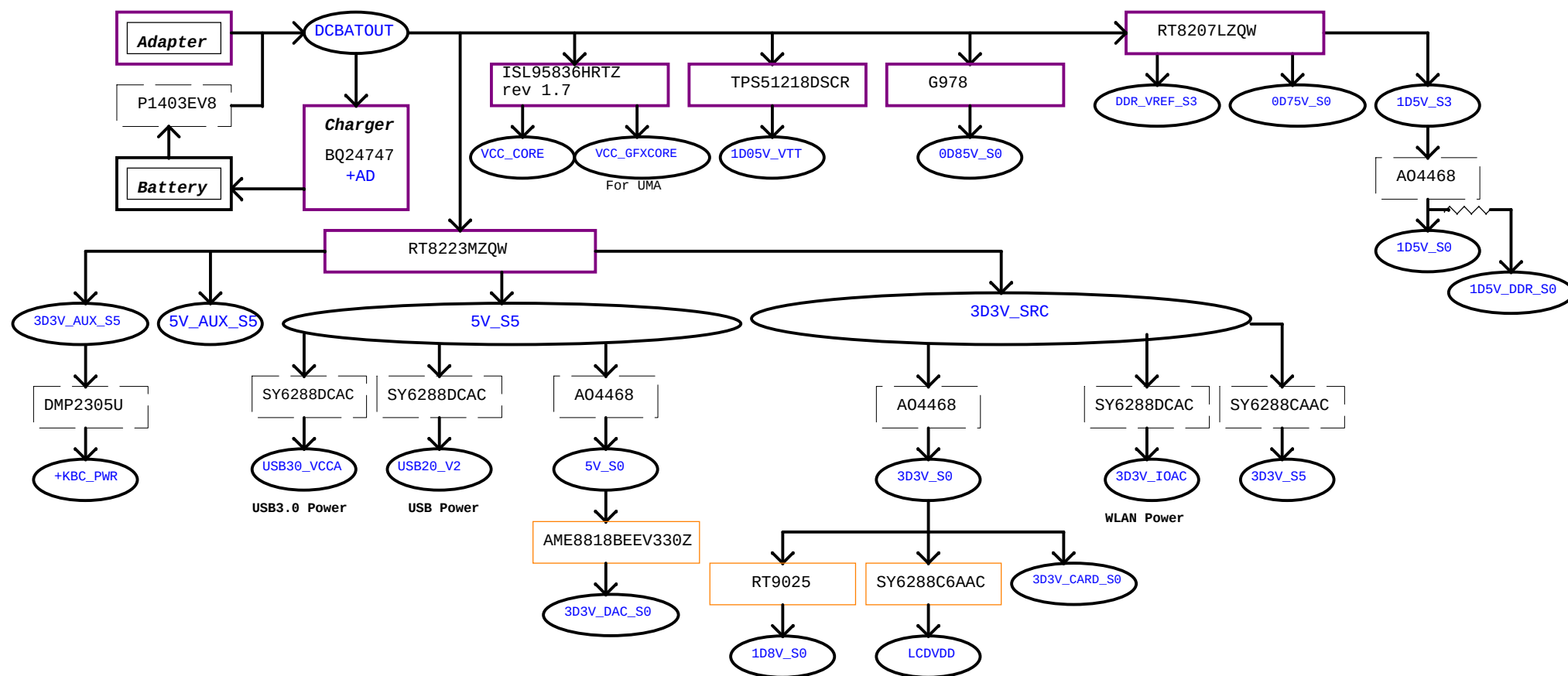
Power Sequence



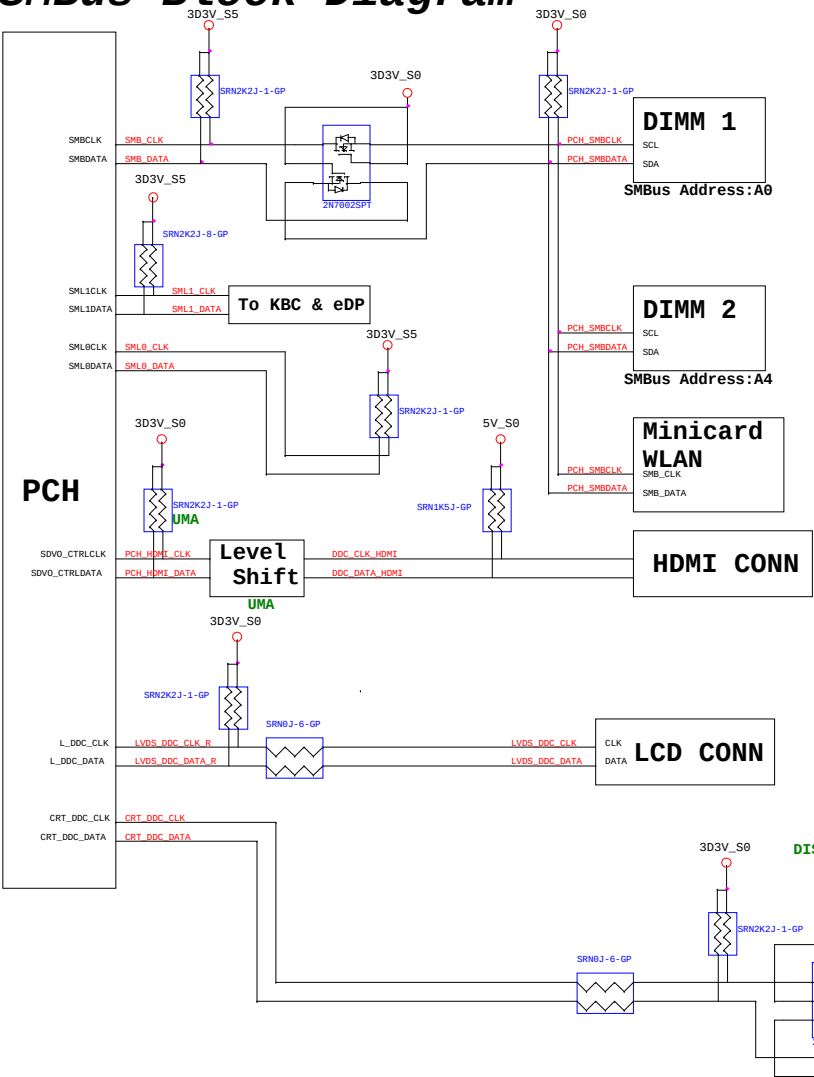
(AC mode)

red word: KBC GPIO

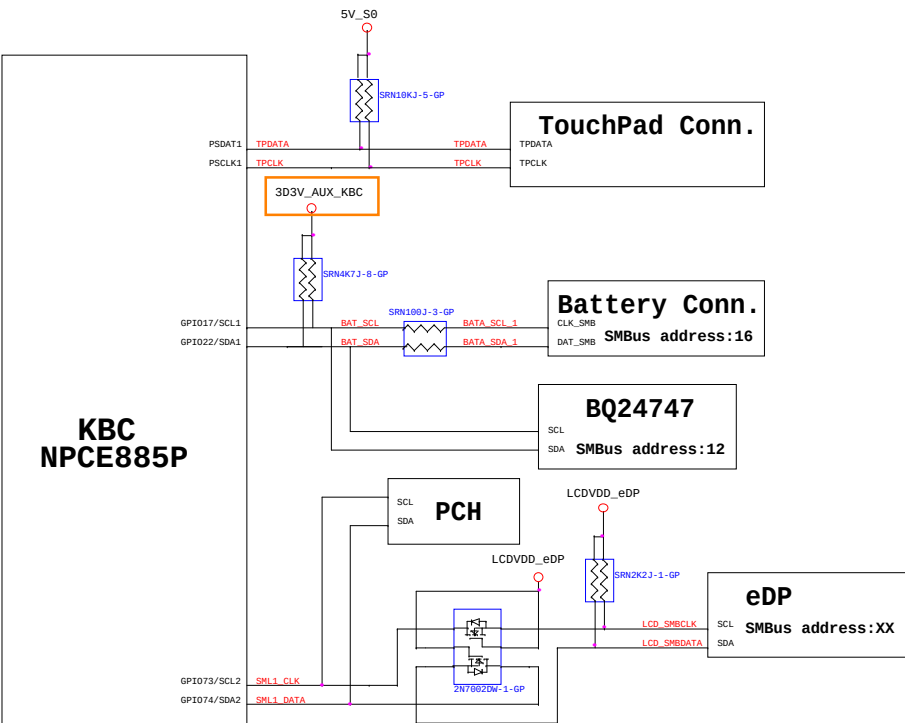




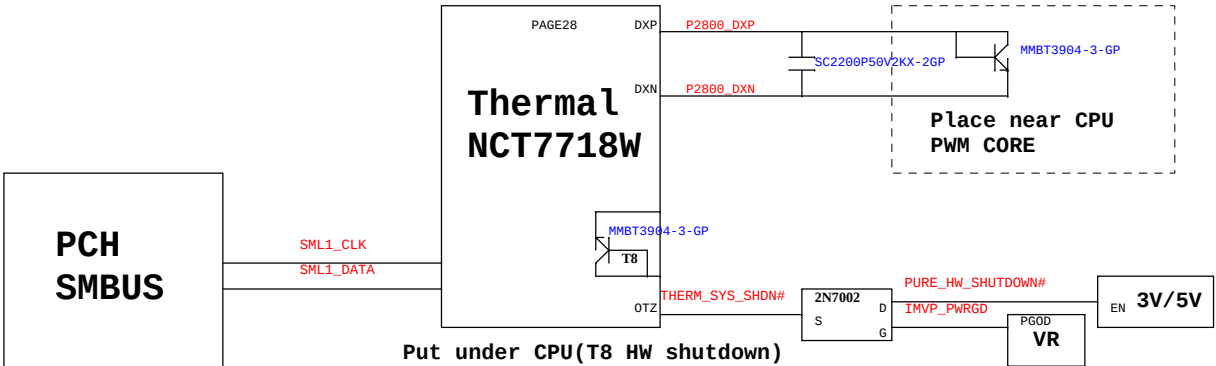
PCH SMBus Block Diagram



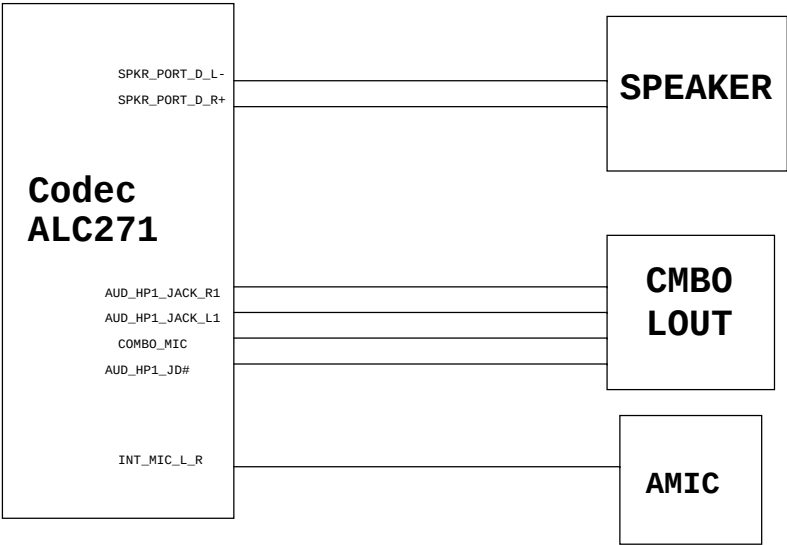
KBC SMBus Block Diagram



Thermal Block Diagram



Audio Block Diagram



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Title USB charger	
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