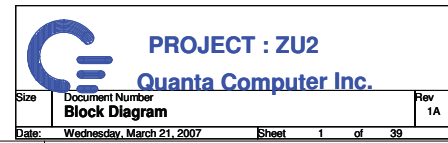
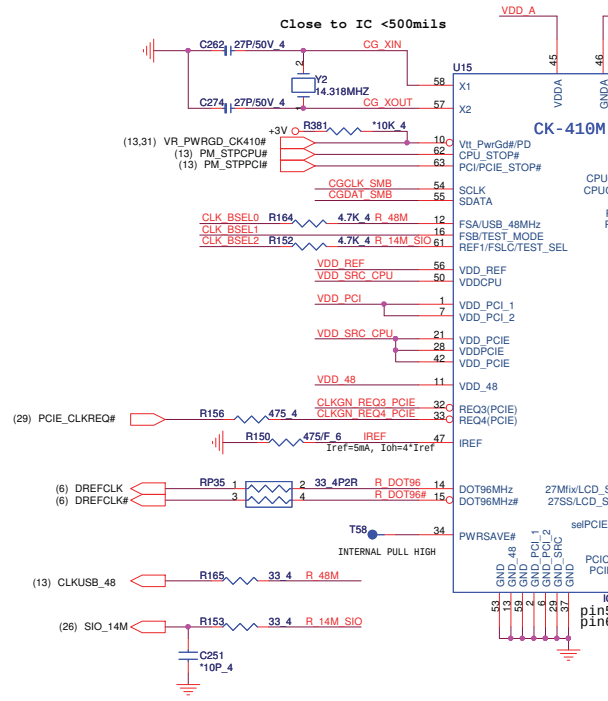
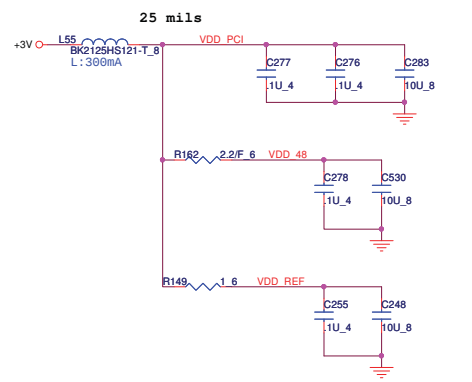


PCI DEVICE	IDSEL#	REQ# / GNT#	Interrupts	CLOCK
CB1410	AD17	REQ0# / GNT0#	INTE#	CK410/PCI3
TIAB23	AD25	REQ2# / GNT2#	INTF#	CK410/PCI8
MR510	AD18	REQ1# / GNT1#	INTG#	CK410/PCI4





REF0 60 14M_REF R373 3 4 14M_ICH (13)

CPUCLK0 52 R_HCLK_CPU RP31 1 2 33 4P2R CLK_CPU_BCLK (3)
CPUCLK0 51 R_HCLK_CPU RP31 3 4 CLK_CPU_BCLK# (3)

CPUCLK1 49 R_HCLK_MCH RP32 1 2 33 4P2R CLK_MCH_BCLK (5)
CPUCLK1 48 R_HCLK_MCH# RP32 3 4 CLK_MCH_BCLK# (5)

CLKLT2/PCIE7 44 X
CLKLT2/PCIE8 43 X

REQ1#/PCIE7 41 X
REQ2#/PCIE7 40 X

PCIE16 39 R_CLK_PCIE_3GPLL RP33 1 2 33 4P2R CLK_PCIE_3GPLL (6)
PCIE6 38 R_CLK_PCIE_3GPLL# RP33 3 4 CLK_PCIE_3GPLL# (6)

PCIE15 36 R_CLK_PCIE_DOCK RP34 1 2 33 4P2R CLK_PCIE_DOCK (29)
PCIE5 35 R_CLK_PCIE_DOCK# RP34 3 4 CLK_PCIE_DOCK# (29)

PCIE14 30 R_CLK_PCIE_ICH RP40 1 2 33 4P2R CLK_PCIE_ICH (12)
PCIE4 31 R_CLK_PCIE_ICH# RP40 3 4 CLK_PCIE_ICH# (12)

SATA_OKT 26 R_CLK_PCIE_SATA RP39 1 2 33 4P2R CLK_PCIE_SATA (11)
SATA_OKC 27 R_CLK_PCIE_SATA# RP39 3 4 CLK_PCIE_SATA# (11)

PCIE13 24 X
PCIE3 23 X

PCIE12 22 R_CLK_PCIE_MINI1 RP38 1 2 33 4P2R CLK_PCIE_MINI1 (23)
PCIE2 23 R_CLK_PCIE_MINI1# RP38 3 4 CLK_PCIE_MINI1# (23)

PCIE11 19 R_CLK_PCIE_LAN RP37 1 2 33 4P2R CLK_PCIE_LAN (14)
PCIE1 20 R_CLK_PCIE_LAN# RP37 3 4 CLK_PCIE_LAN# (14)

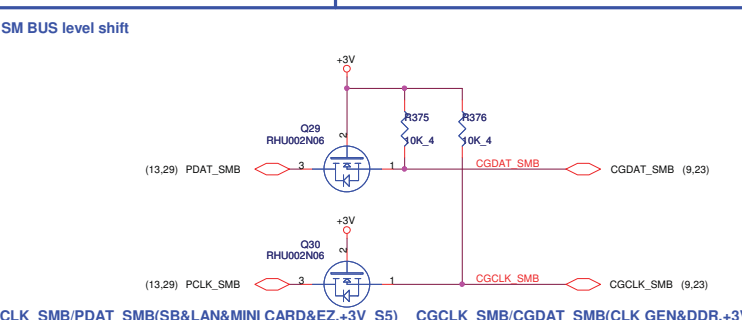
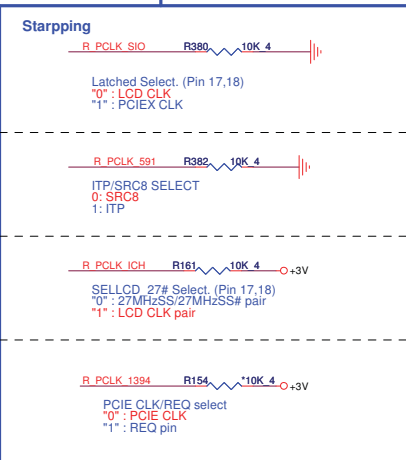
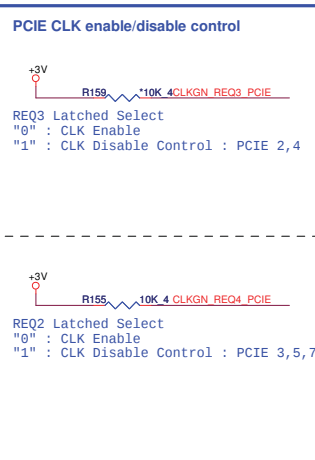
SGSGT/PCIE0T 17 R_DREFSSCLK RP36 1 2 33 4P2R DREFSSCLK (6)
SGSGG/PCIE0C 18 R_DREFSSCLK# RP36 3 4 DREFSSCLK# (6)

XC0_LD#/PC15 4 R_PCLK_SIO R385 33 4 PCI_CLK_SIO (26)
PC14 4 R_PCLK_SIO_110 R160 33 4 PCI_CLK_SIO_110 (26)
PC13 3 R_PCLK_PGM R279 33 4 PCI_CLK_PGM (21)
PC12 64 R_PCLK_1384 R151 33 4 PCI_CLK_CB714 (18)
CLK2/REG_SEL F116/LCD_27# R163 33 4 PCI_CLK_1384 (21)
PC10#/ITP_EN 8 R_PCLK_S91 R386 33 4 PCLK_ICH (12)
PCLK_591 (24)

CS954310BGLF
5, pin9, pin32, pin33, pin34 internal PU
34 internal PD

"EA report, fail in PCI_CLK_S0. It is too fast.
BOM change. R385 change to 33 ohm and unstuff R384."

R_PCLK_SIO R384 *10.4 PCLK_DEBUG_SW (24)
R383 *10.4 PCLK_DEBUG_SW (24)



SEL2 SEL1 SEL0

Frequency select

FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	200	100	33

Default

BSEL strappings need to be set for 533MHz Moby Dick
(Intel79150M - Calistoga Interposer)
(if Calistoga is designed for 667MHz board).

(3) CPU_BSEL0

(6) MCH_BSEL0

(3) CPU_BSEL1

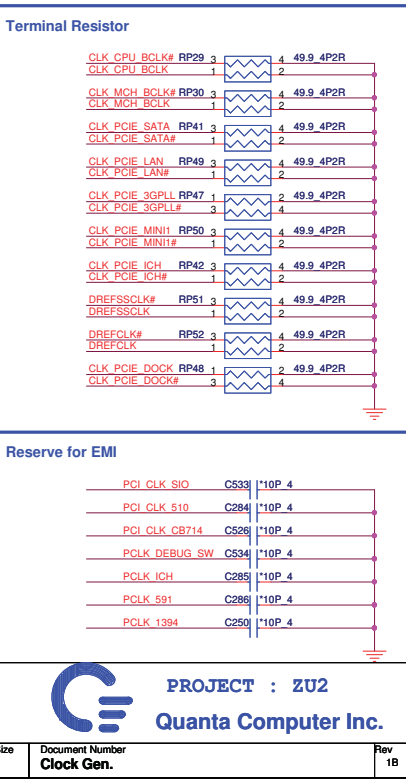
(6) MCH_BSEL1

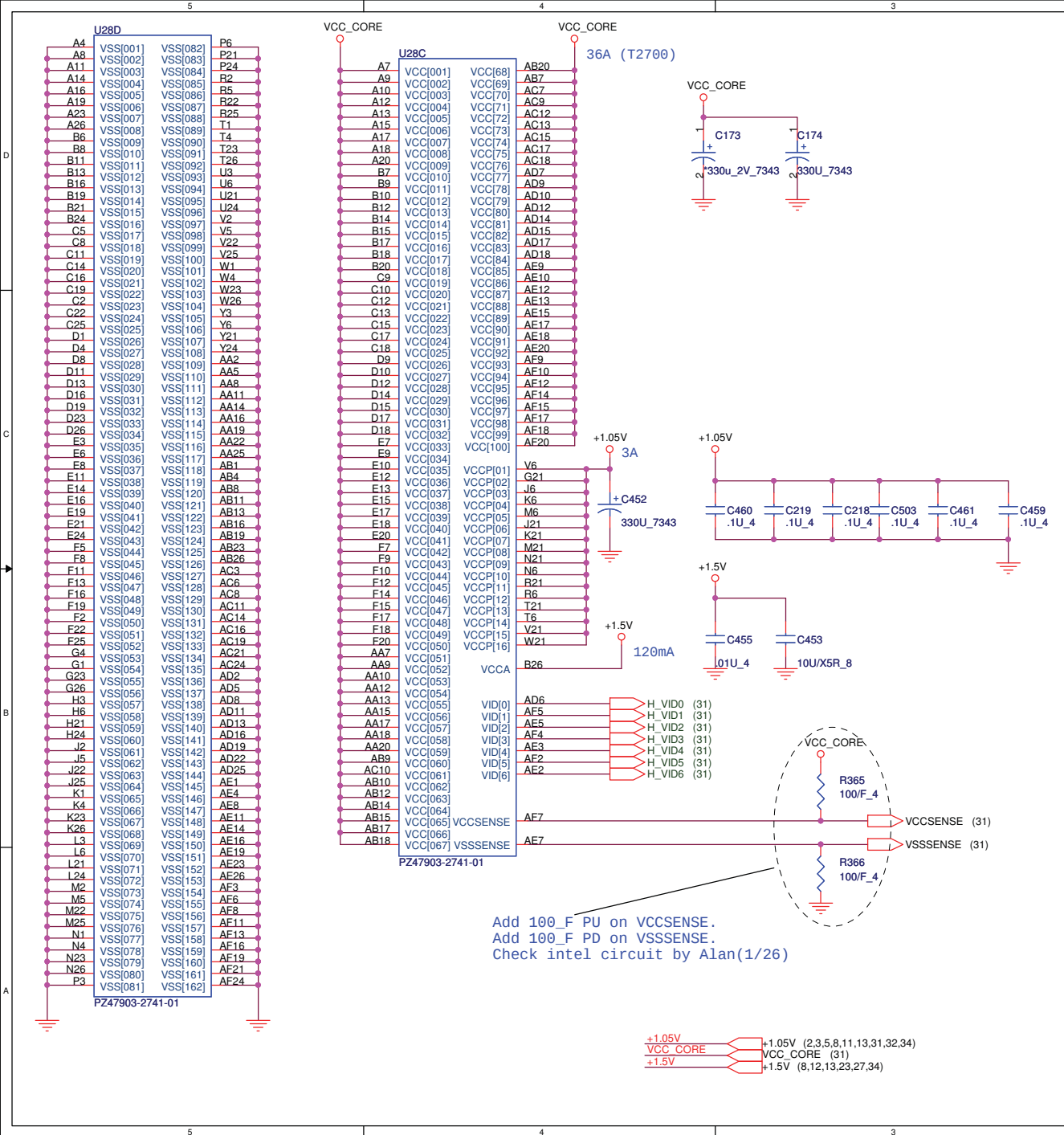
(3) CPU_BSEL2

(6) MCH_BSEL2

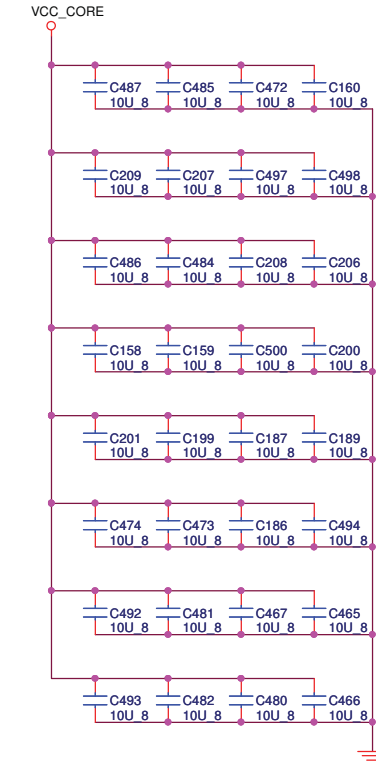
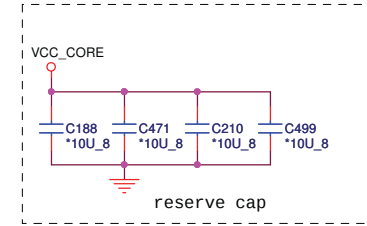
Power check

+3V (3,6,8,9,11,12,13,14,15,16,17,18,19,20,21,22,23,24,25,26,27,29,30,31,32,34)
 +1.05V (3,4,5,8,11,13,31,32,34)



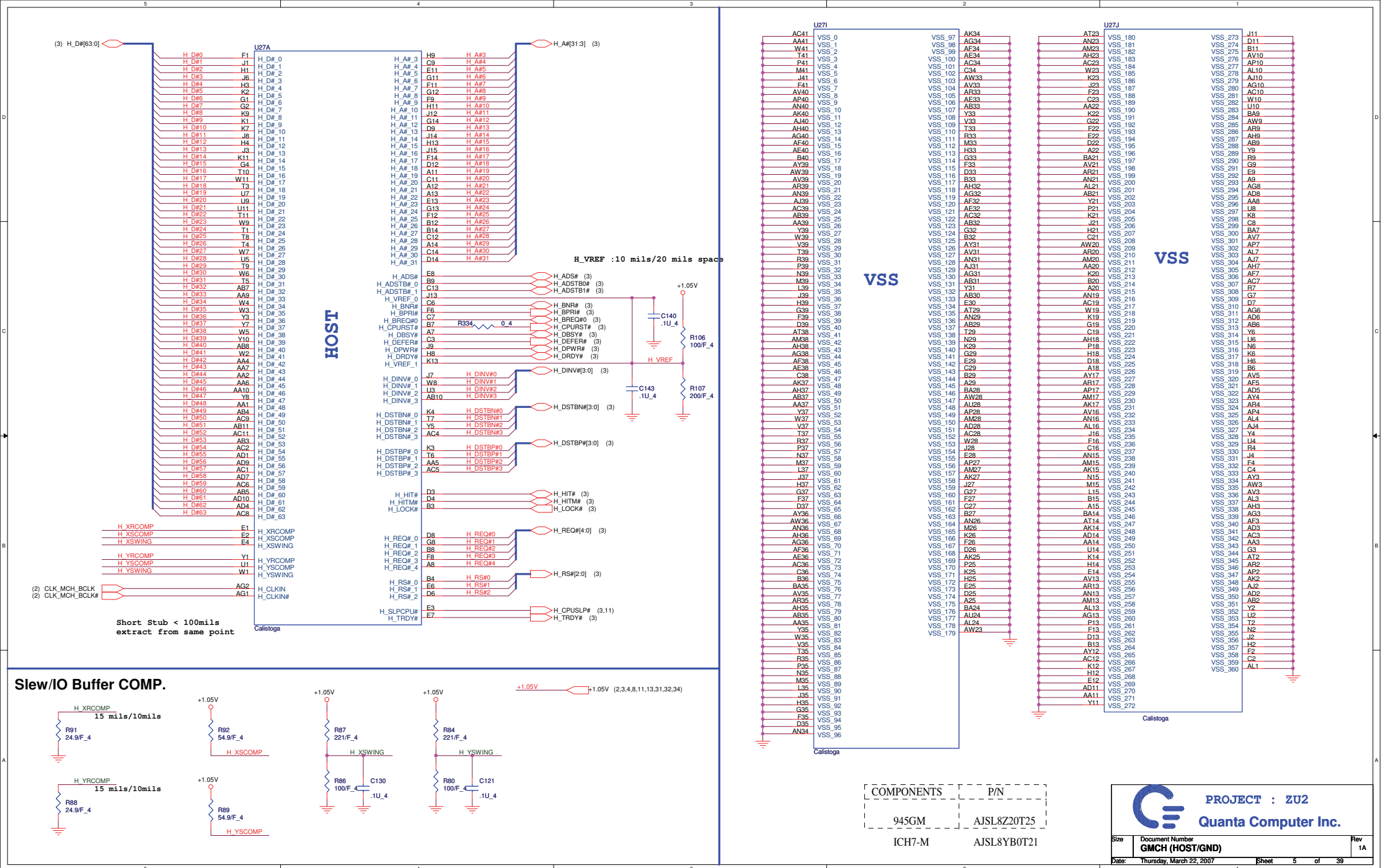


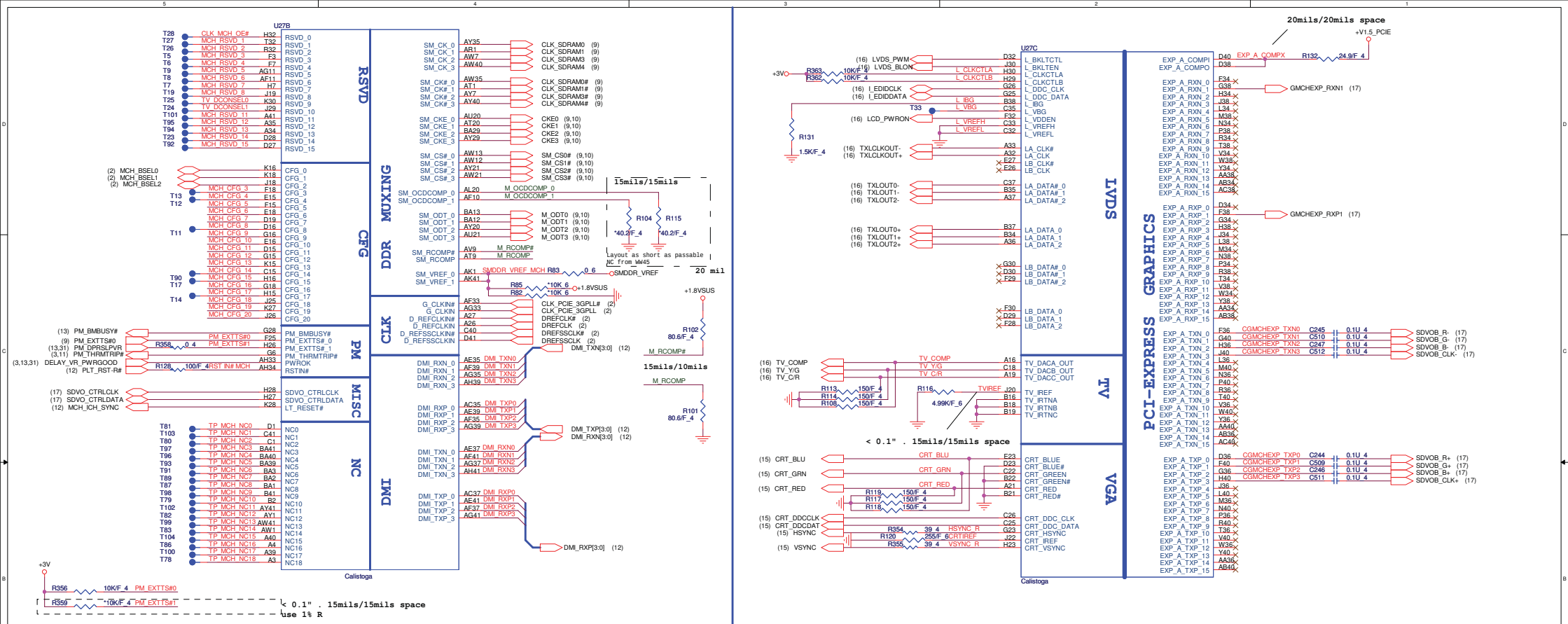
VCC_CORE CAP.

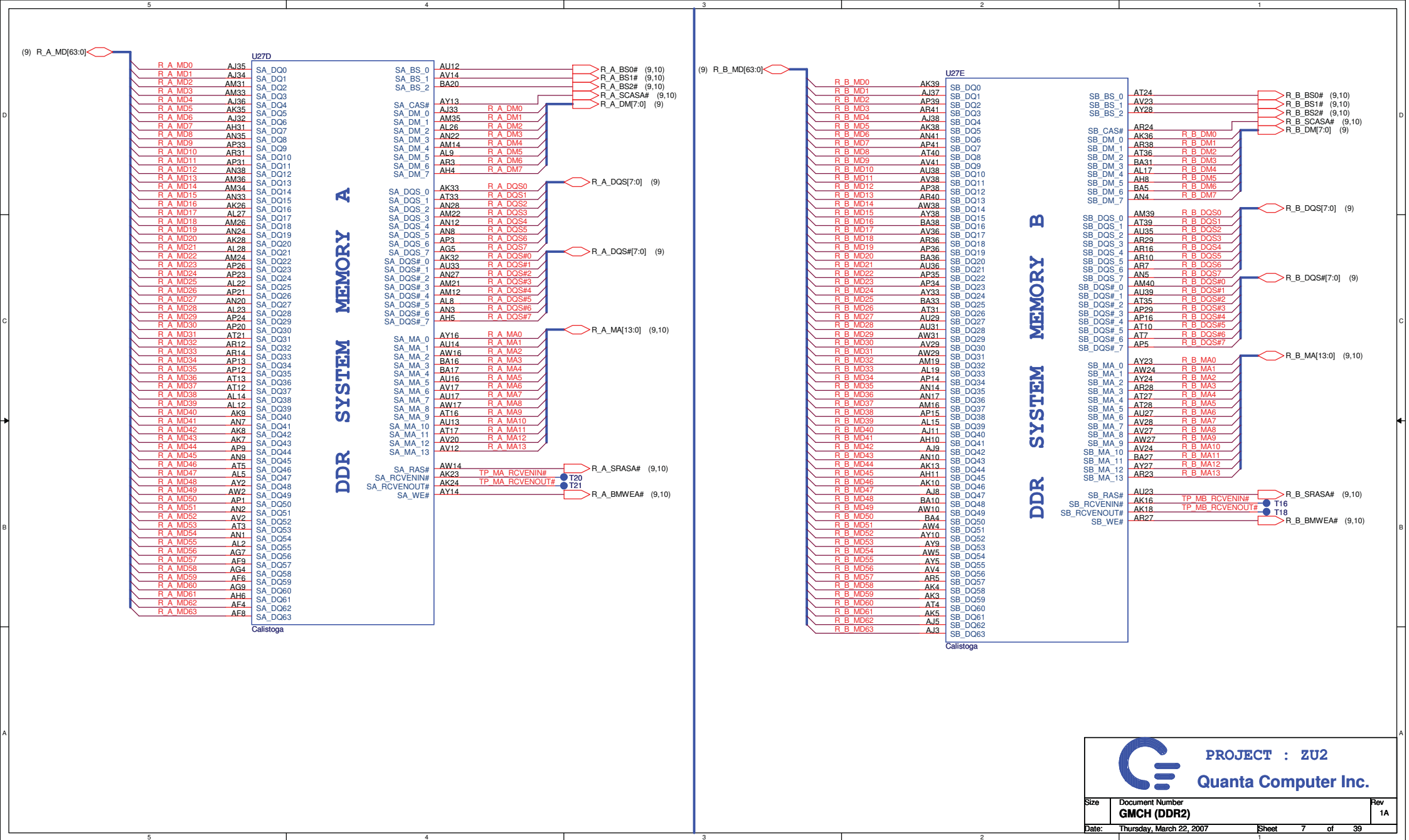


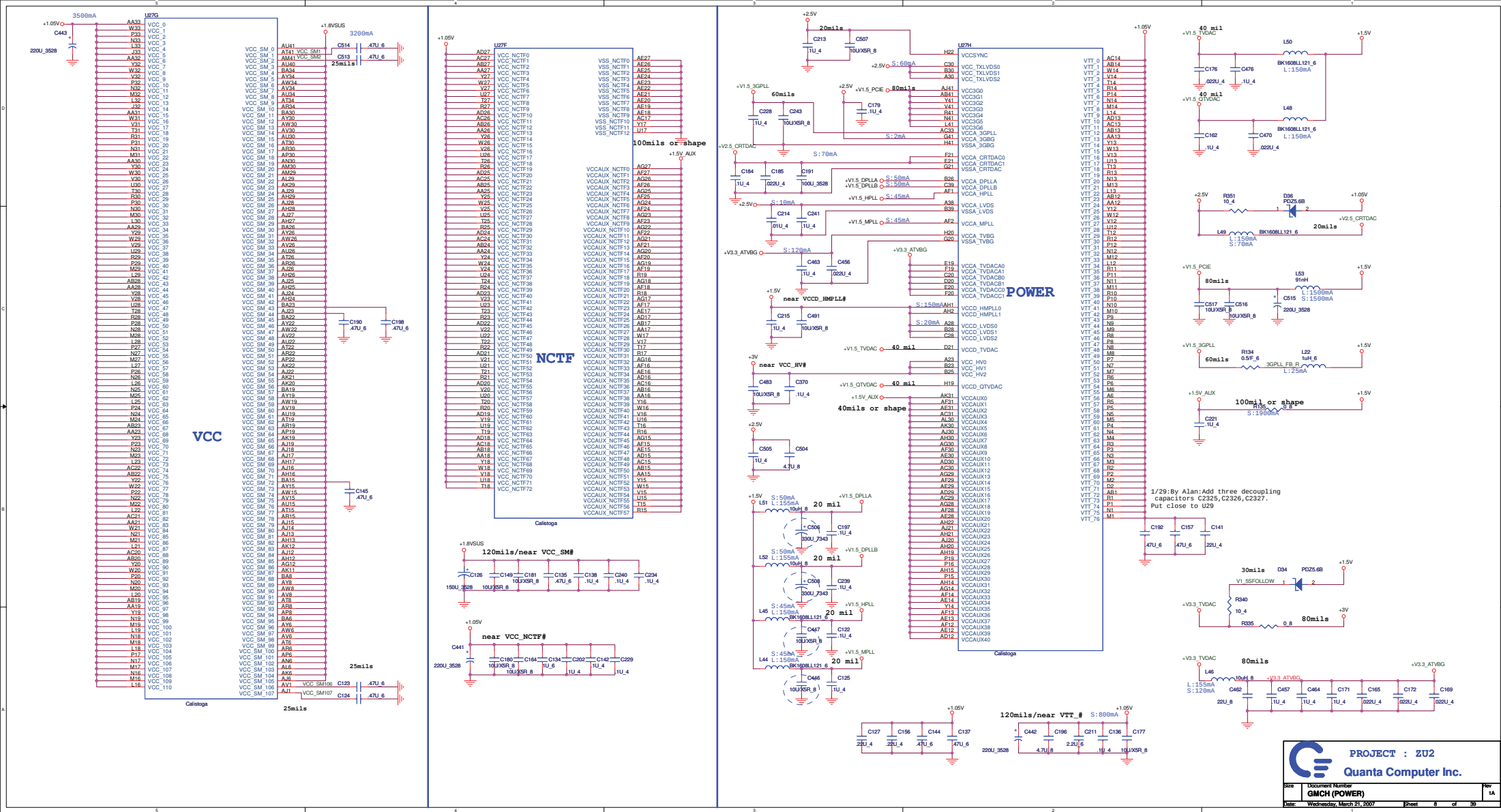
 PROJECT : ZU2
Quanta Computer Inc.

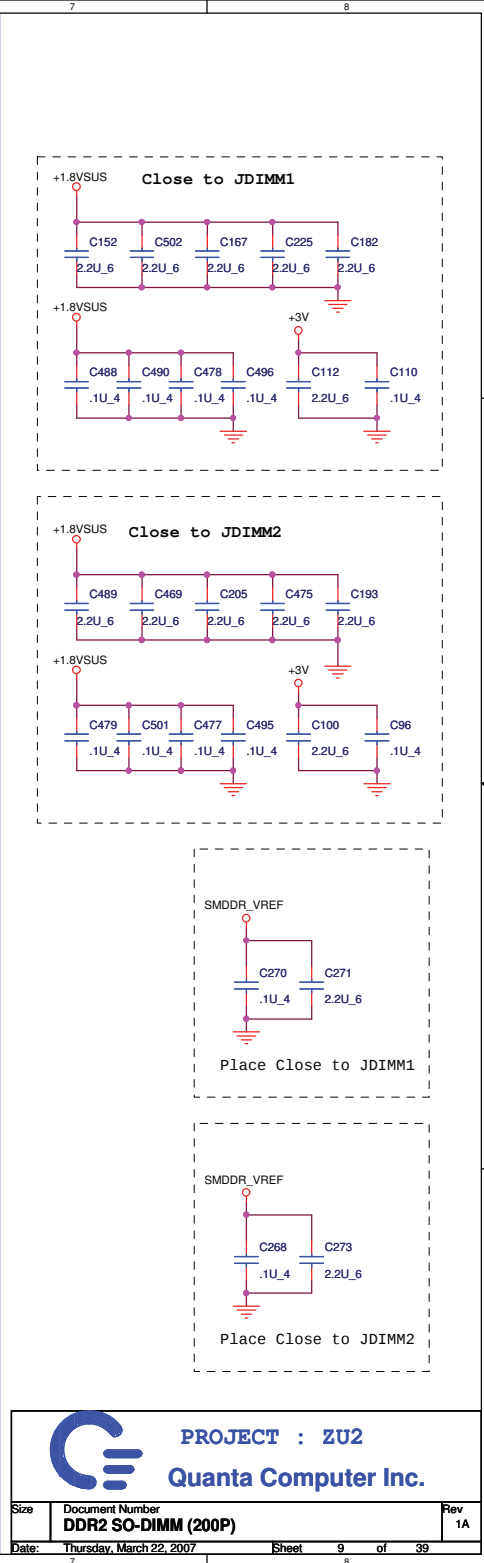
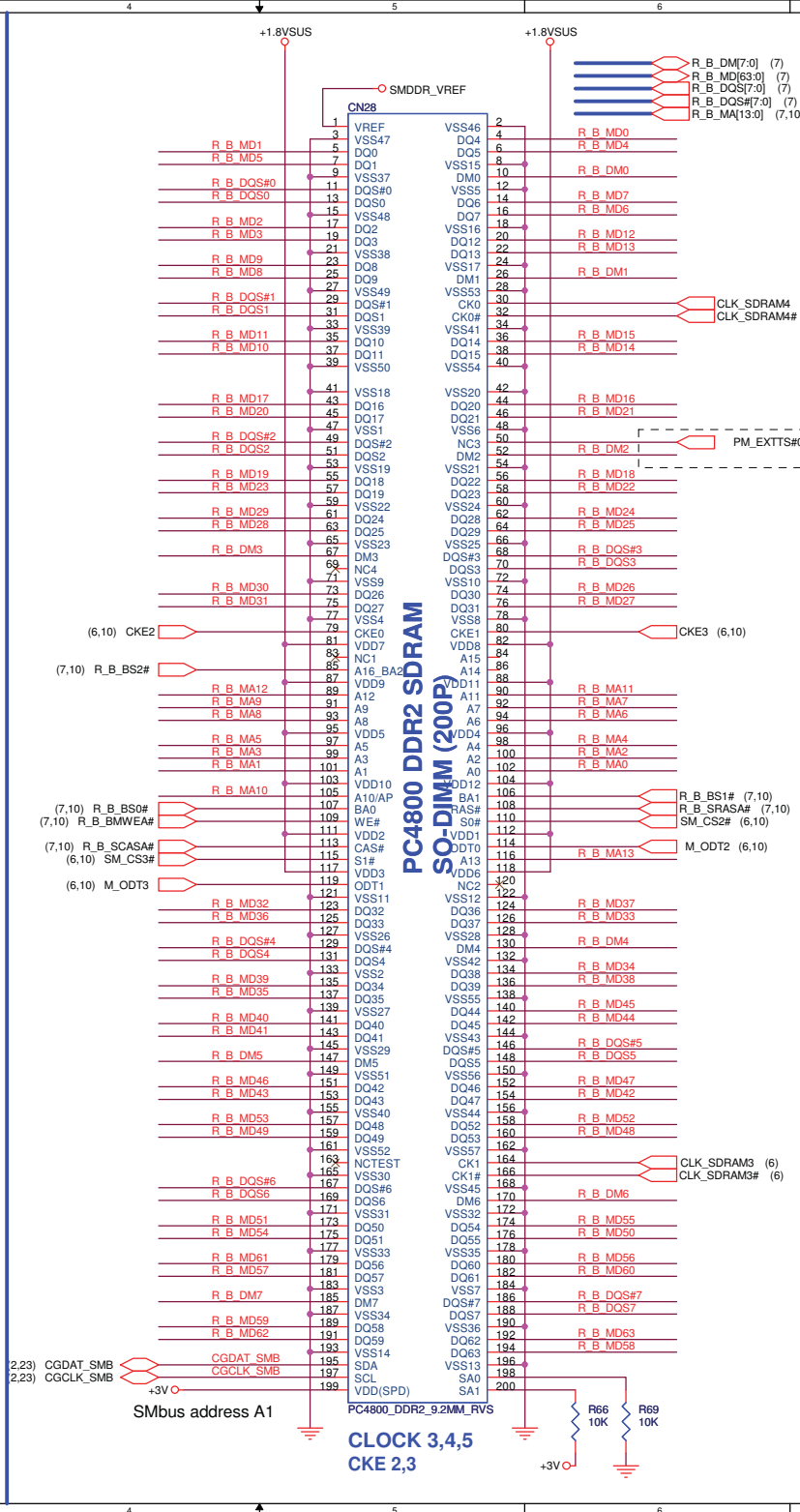
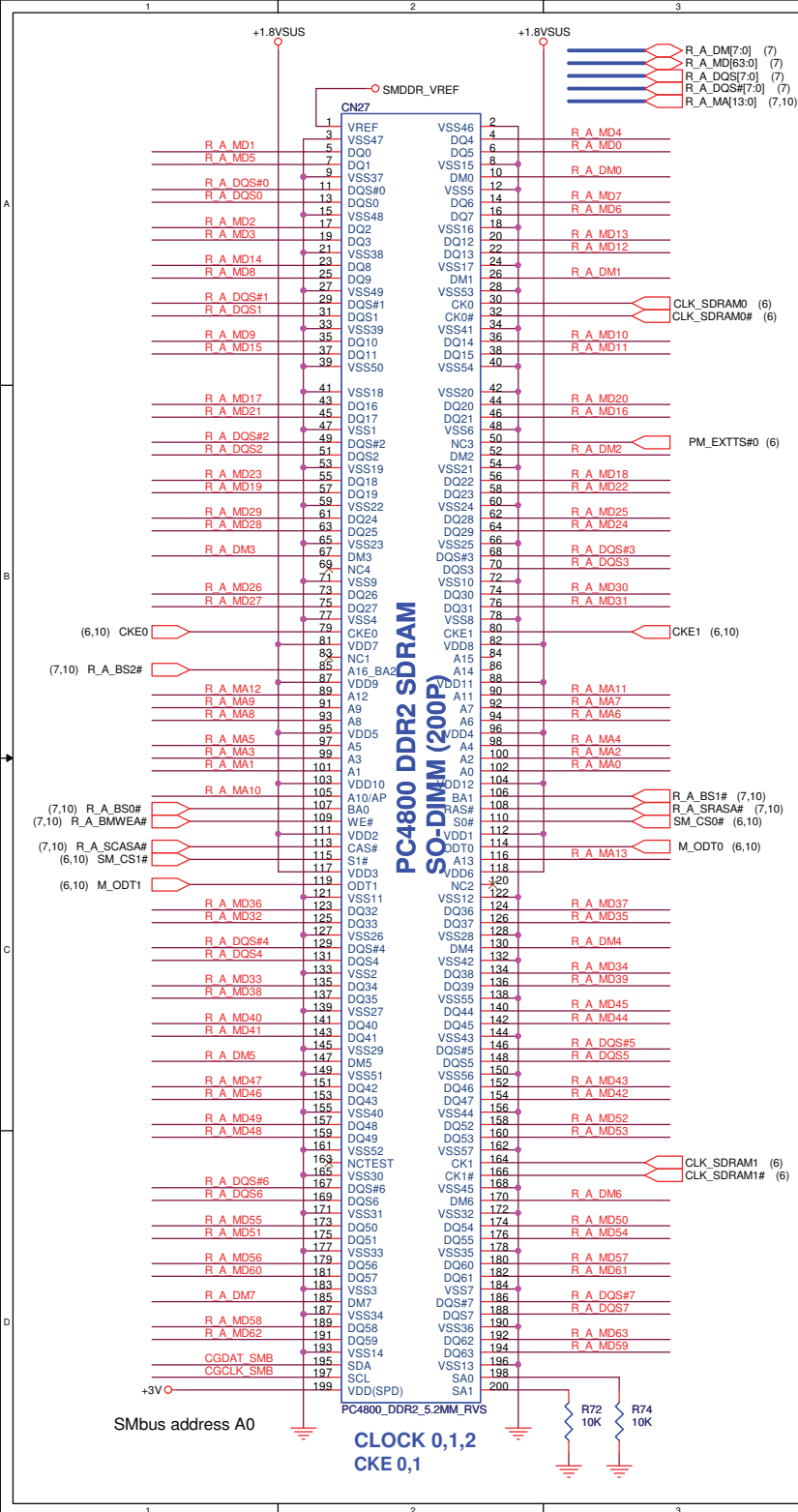
Size	Document Number	Rev
	Yonah/Merom CPU(POWER/GND)-2	1A
Date:	Thursday, March 22, 2007	Sheet 4 of 39



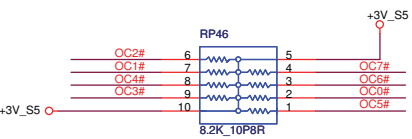
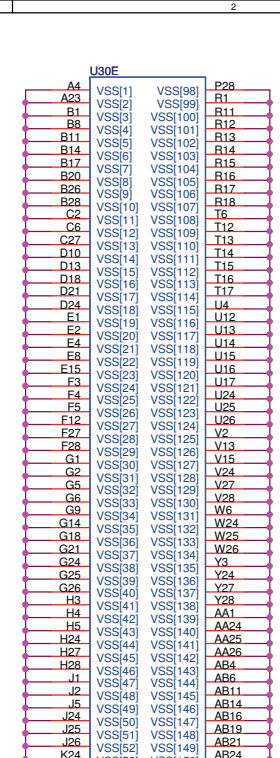




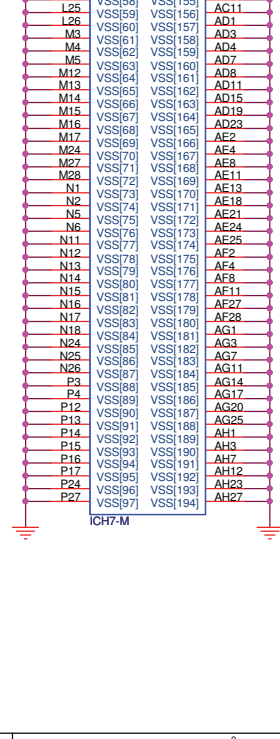


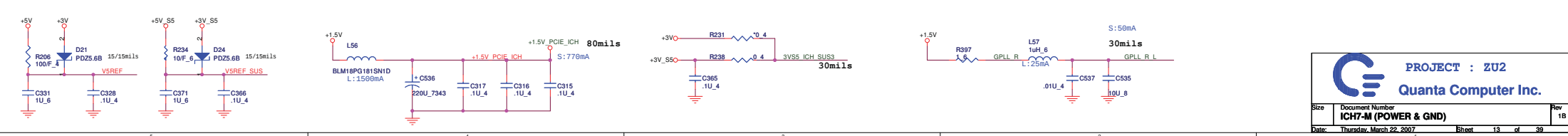
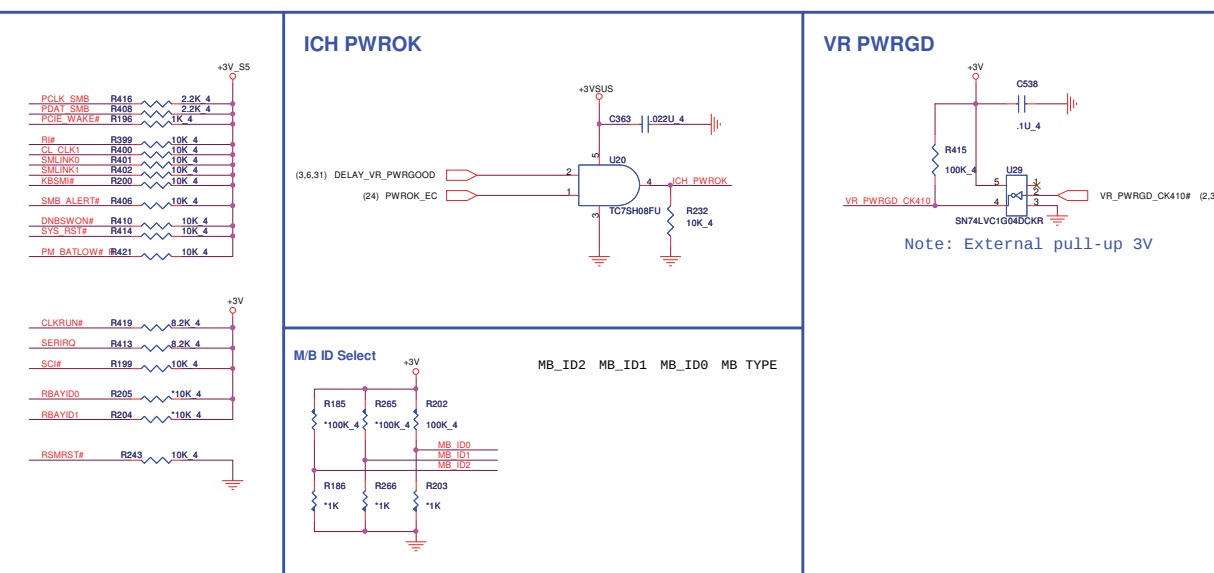
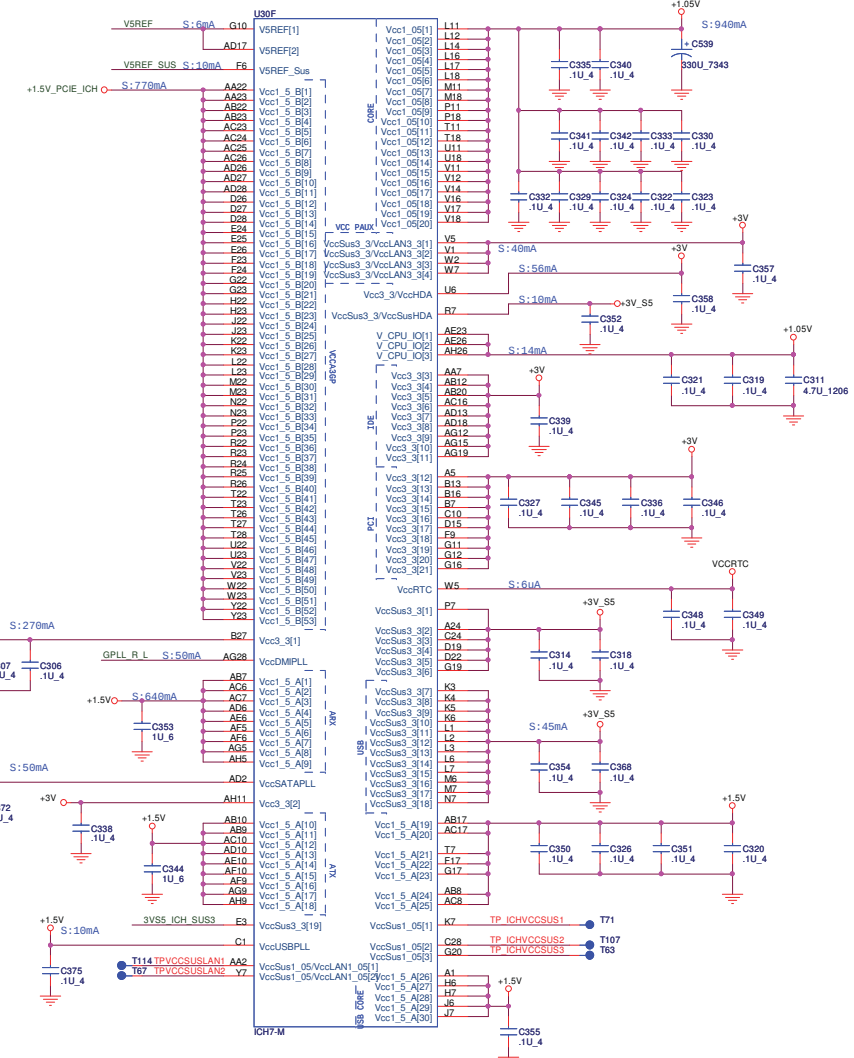


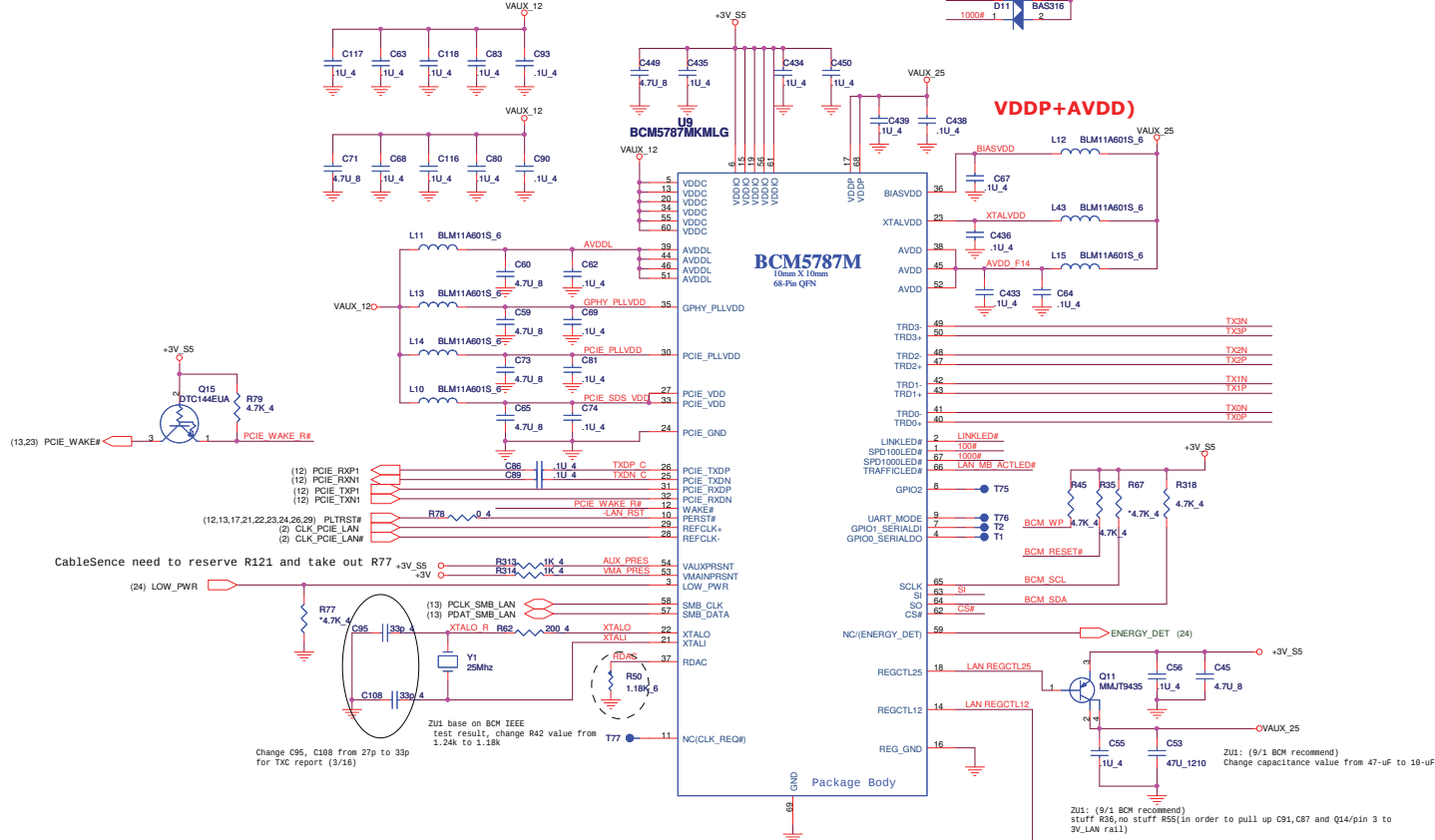




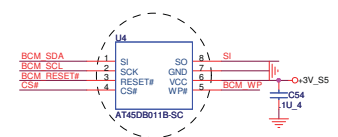
ICH7 Boot BIOS select			
	STRAP	GNT5# R1	GNT4# R2
LPC (default)	11	UNSTUFF	UNSTUFF
PCI	10	UNSTUFF	STUFF
SPI	01	STUFF	UNSTUFF





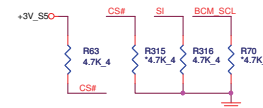


Flash ROM/EEPROM

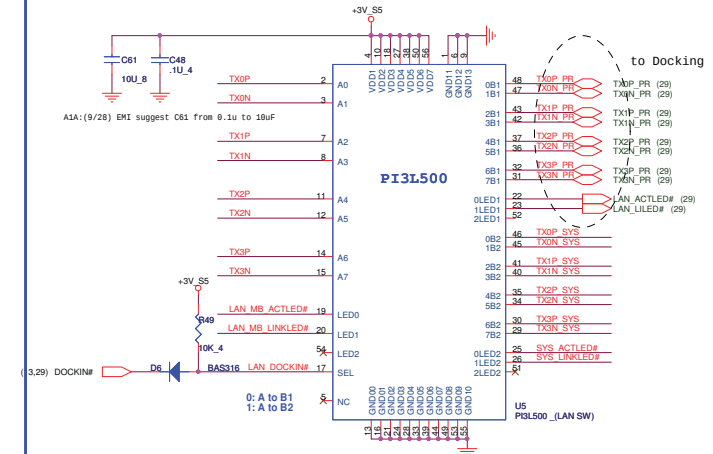


EEPROM Strapping

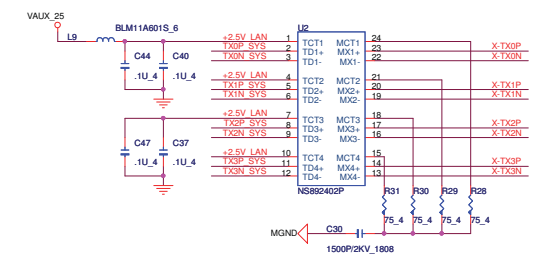
	S0	SI	CS#	SCLK
24c64	1	1	0	1
AT45DB011B	1	0	1	1



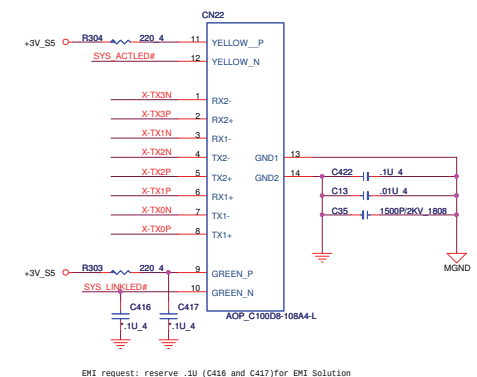
LAN Switch



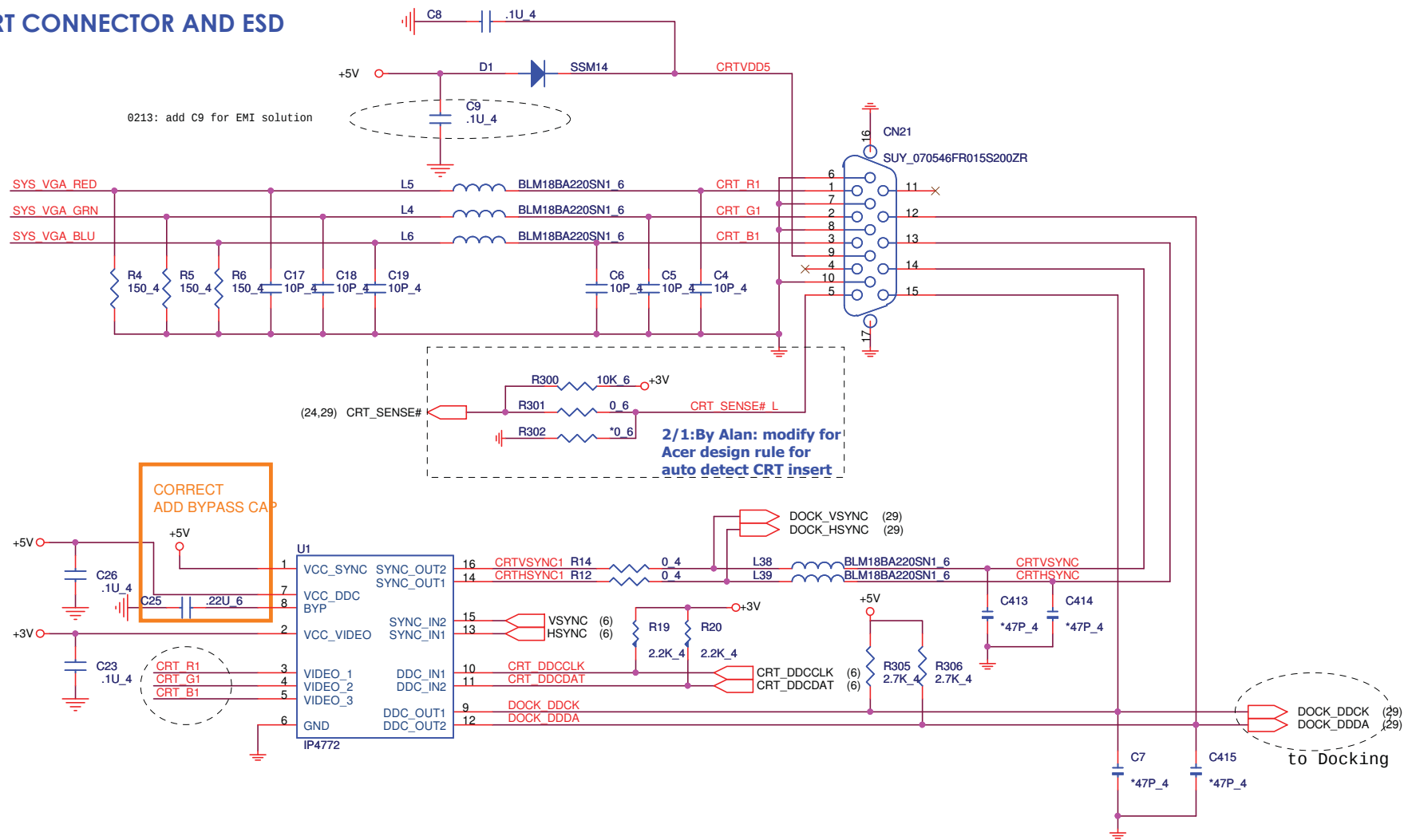
LAN Transformer



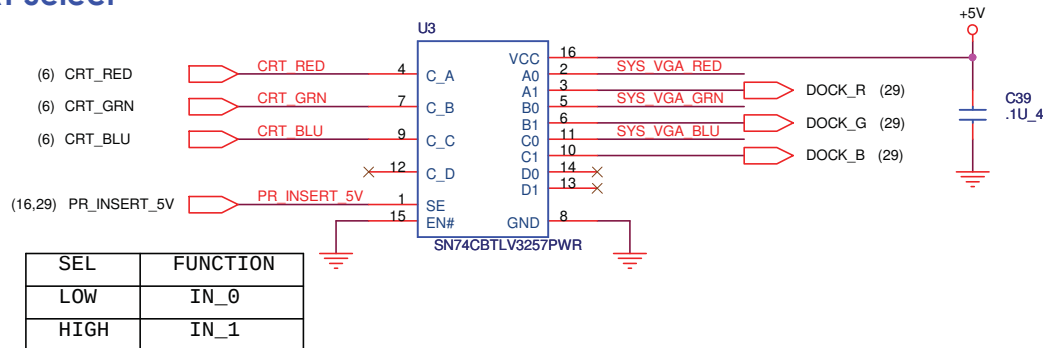
RJ45



CRT CONNECTOR AND ESD

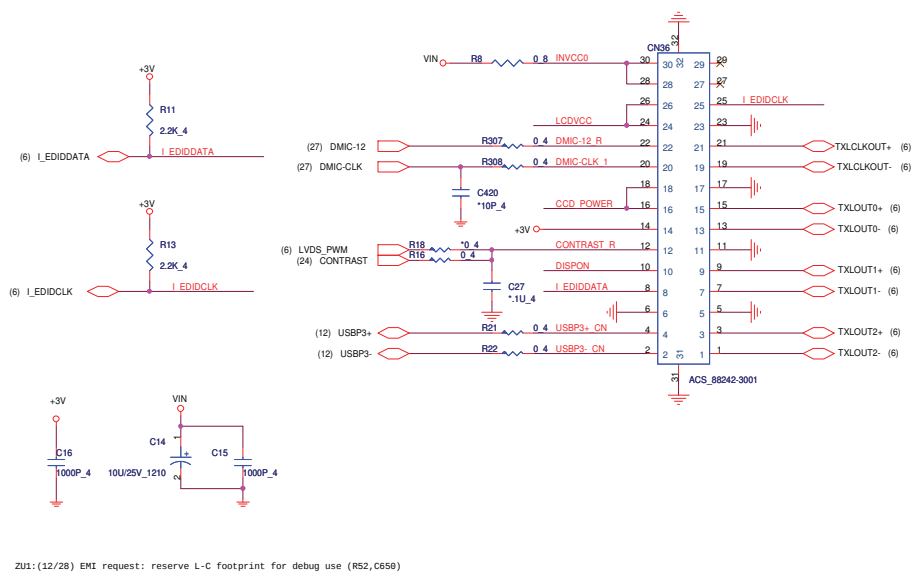


CRT Select



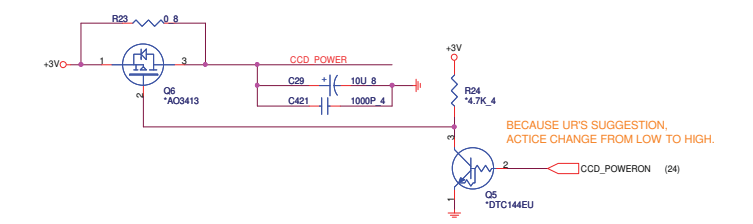
PROJECT : ZU2
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LVDS

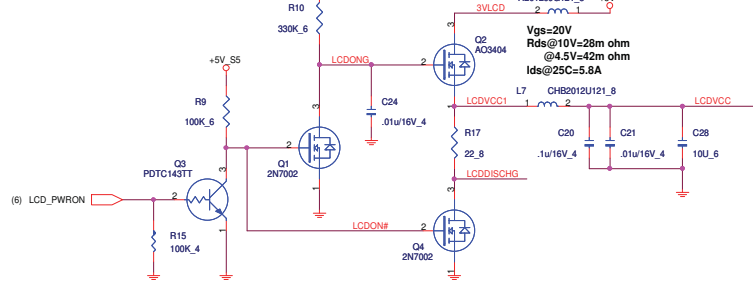


(3/19)Change MB LCD connector pin define(CN3) and LCD cable pin define to cover production line issue
(Inverter short with signal to burn system)->ZR1 issue
(1)pin 27,29->NC
(2)pin 8->INT_LVDS_EDIDDATA

CAMERA MODULE

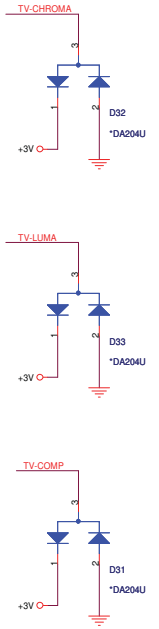
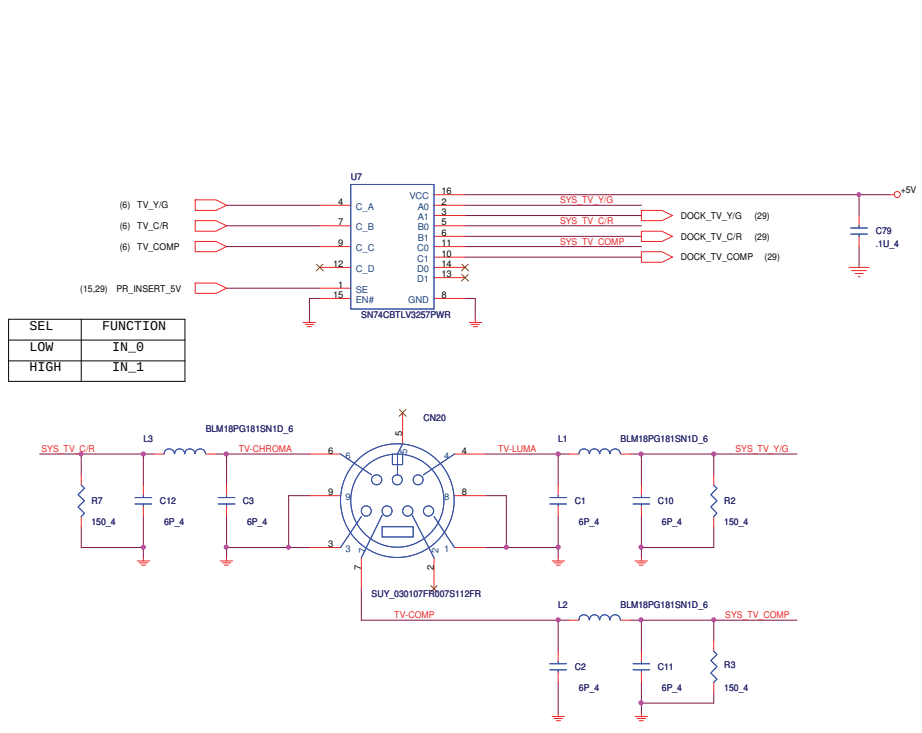


LCD Power

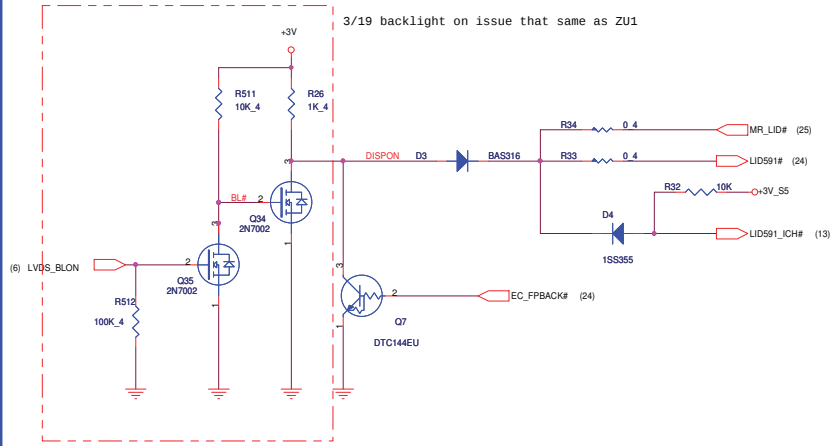


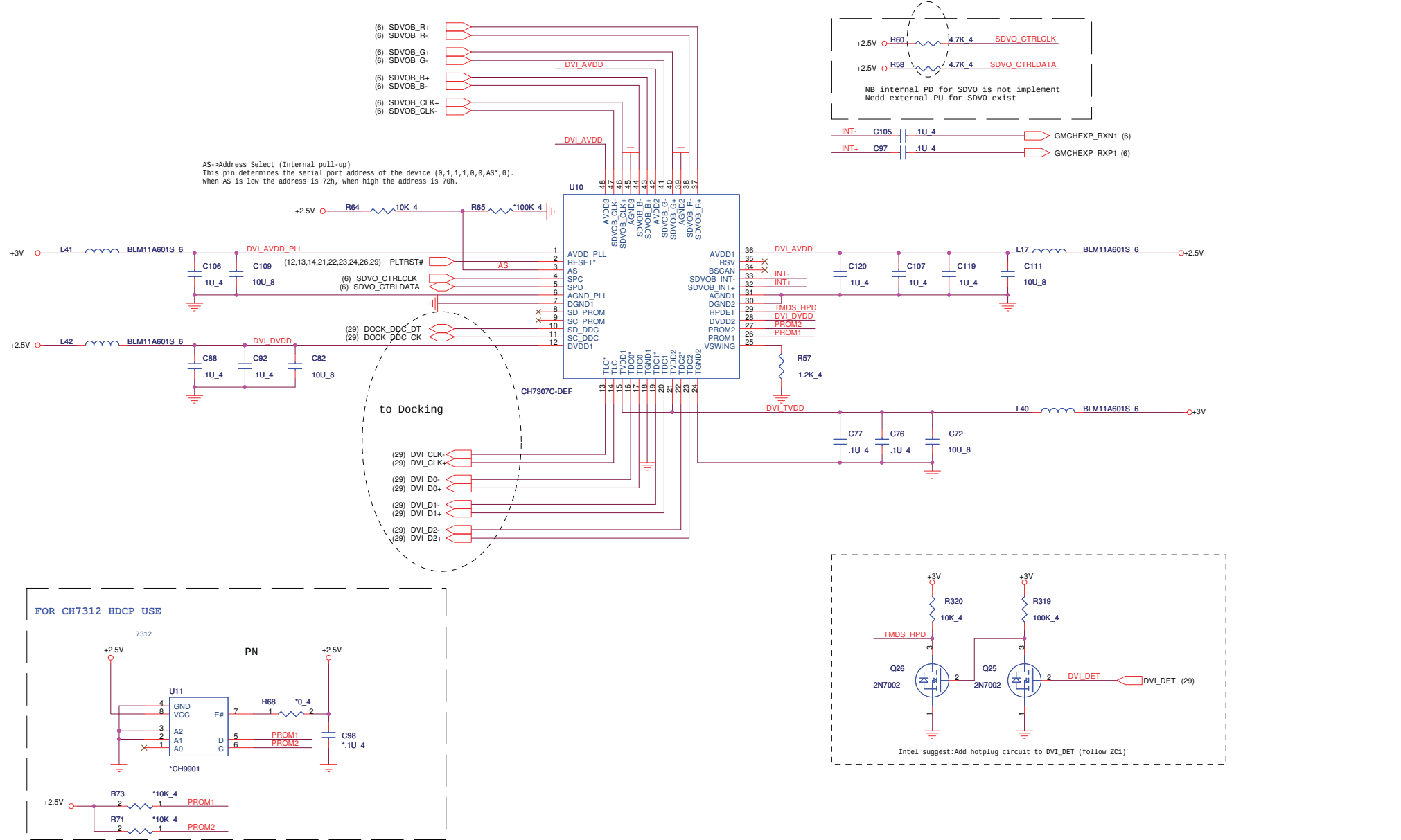
1/29:By Alan: AAT4280 fail on power ON rising time and falling time. EA.Additionally, some LCD panels will have garbage. follow ZR1 circuit.

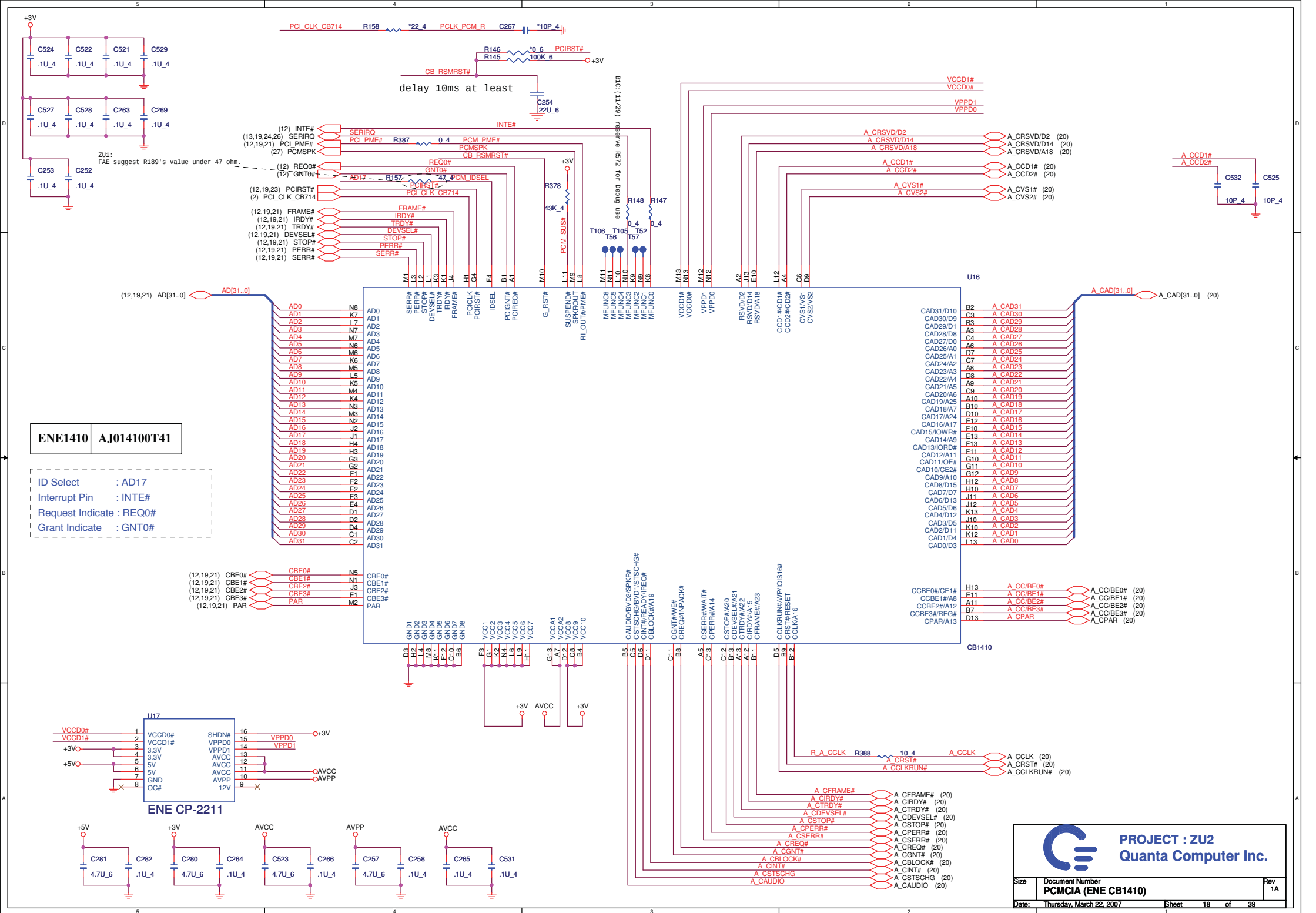
TV Out (SVHS) MiniDIN 7-pin



MR Sensor

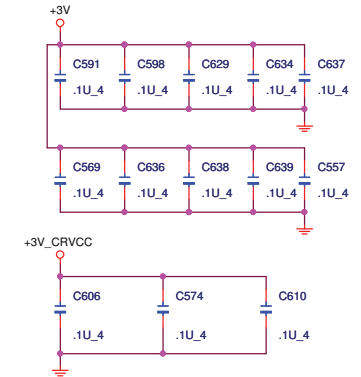




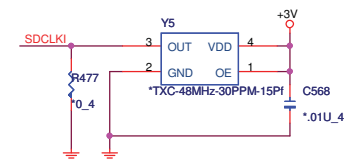


ZU1:no stuff R496,R522

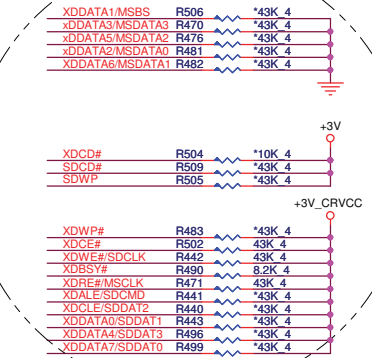
GRST# should
connect to Power
On reset if
support S3



48MHz CLK



PU/PD

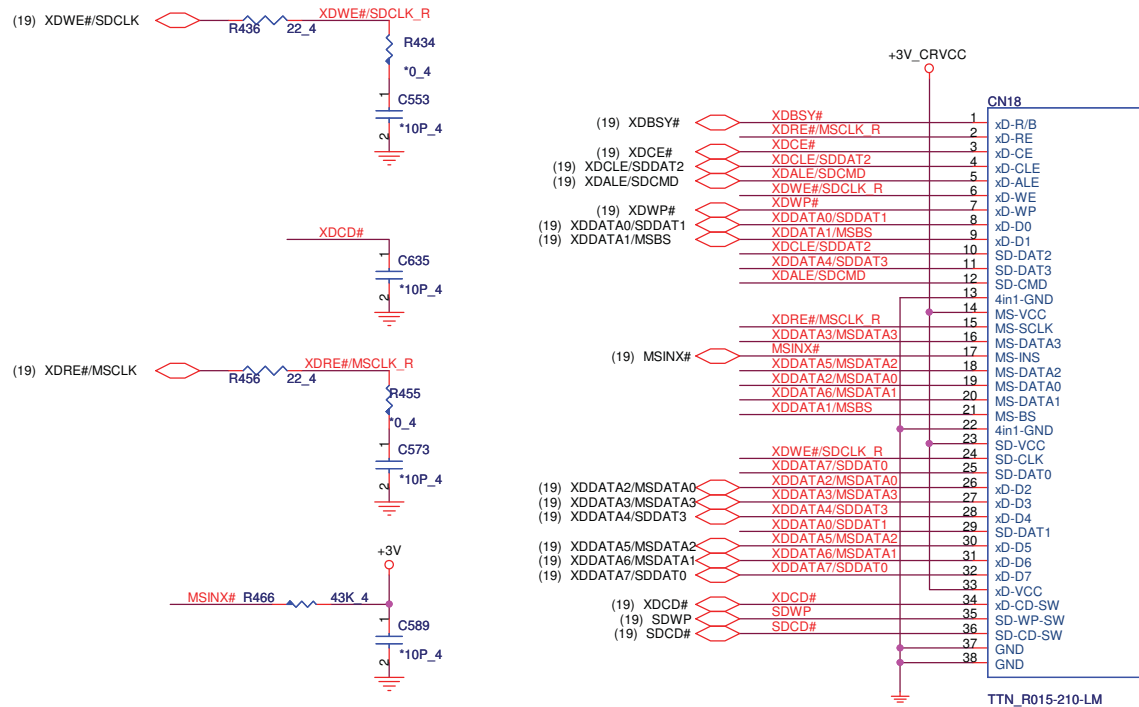


0213: follow ZU1 for cost down issue with FAE

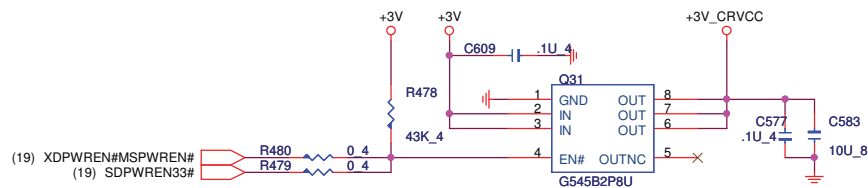


PROJECT : ZU2
Quanta Computer Inc.

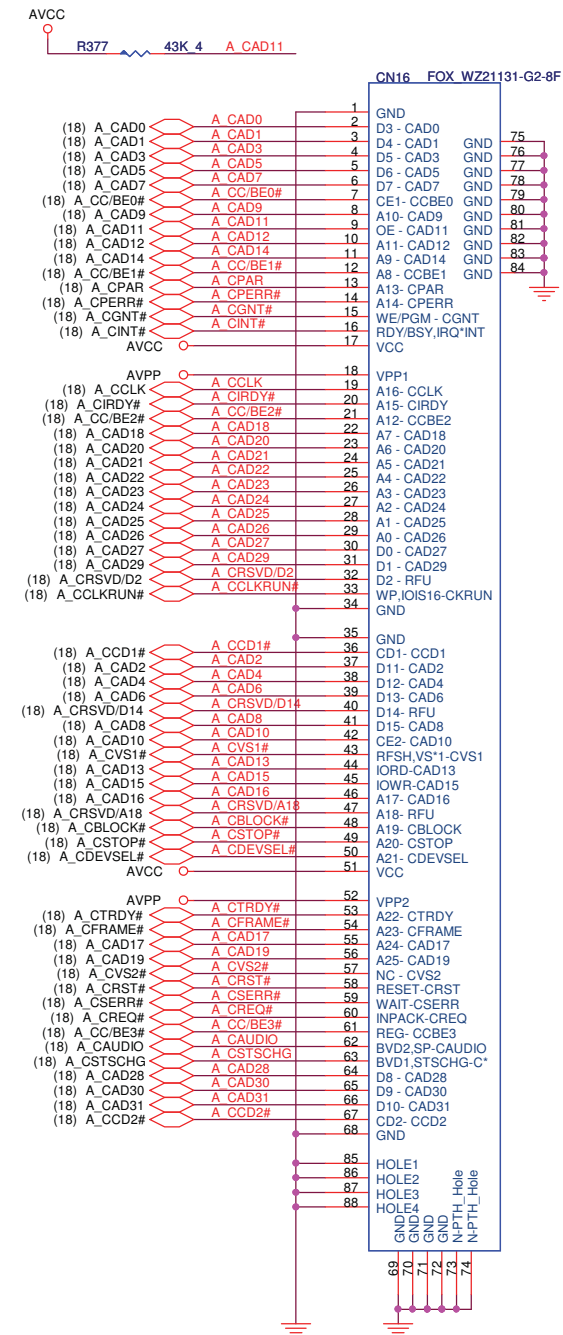
CardReader



CardReader Power switch

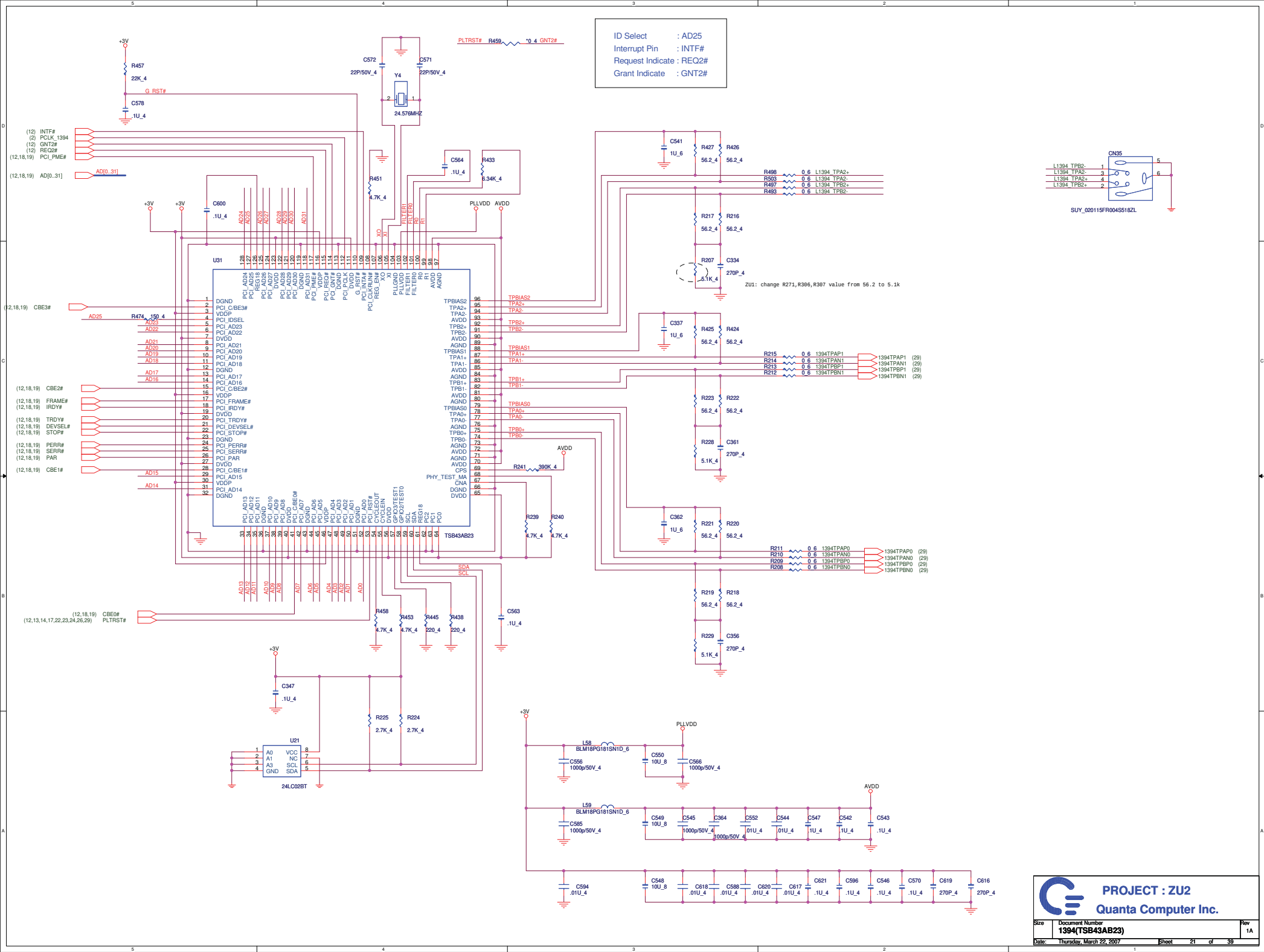


PCMCIA

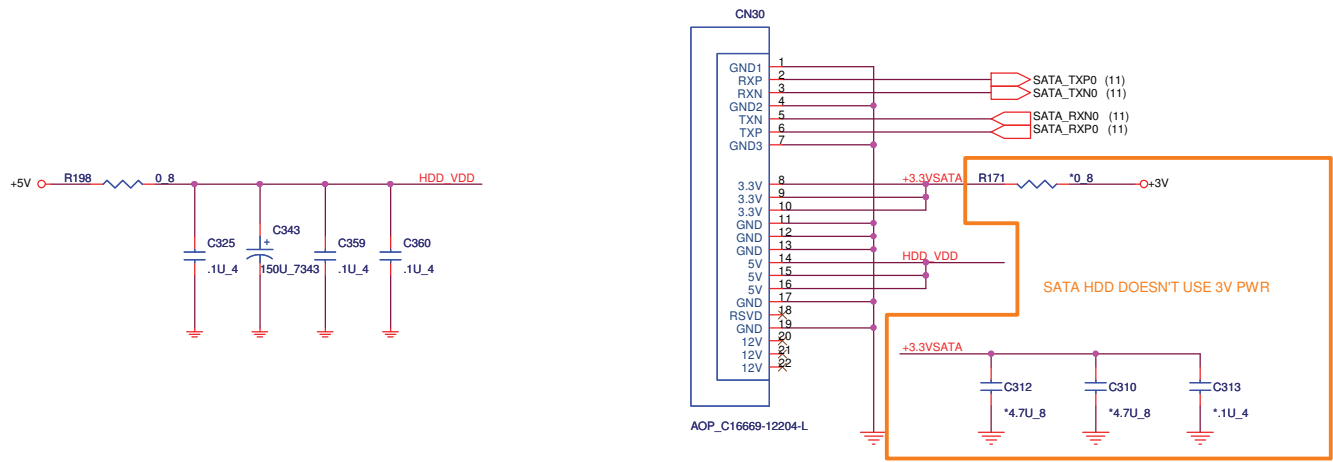


PROJECT : ZU2
Quanta Computer Inc.

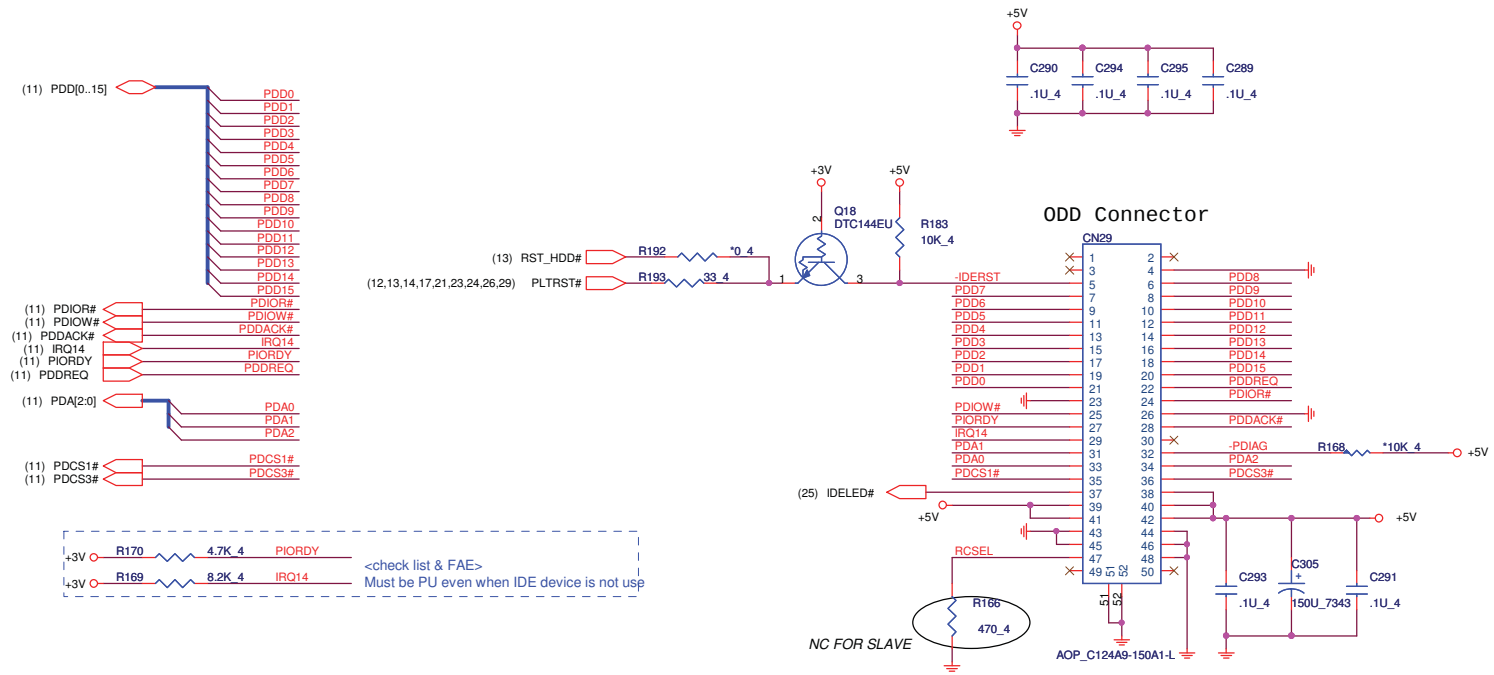
Size	Document Number CARD Reader & PCMCIA SLOT	Rev 1A
Date:	Thursday, March 22, 2007	Sheet 20 of 39



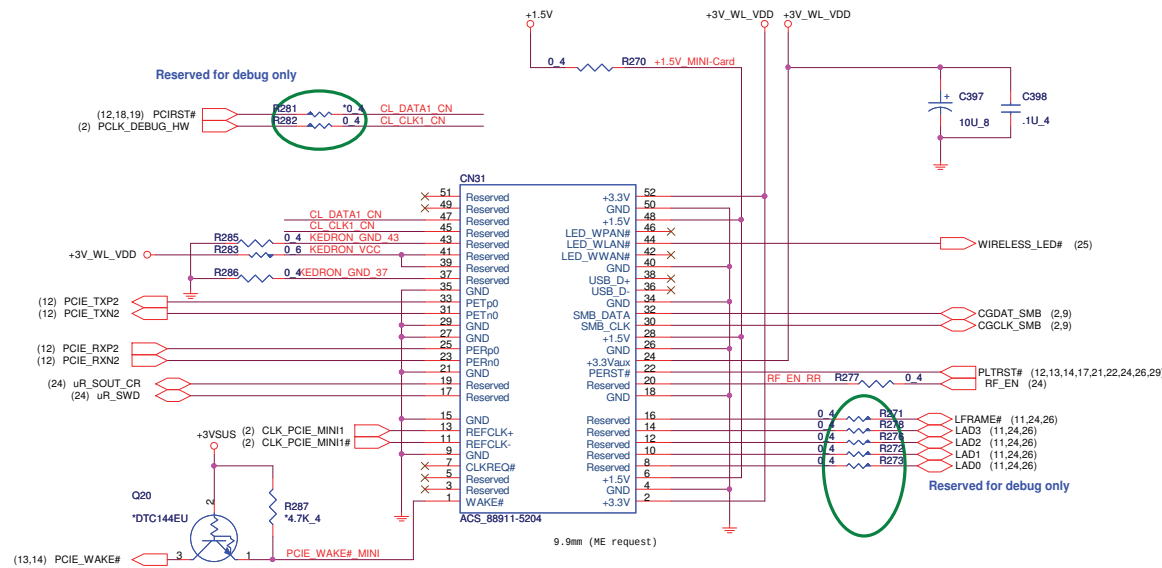
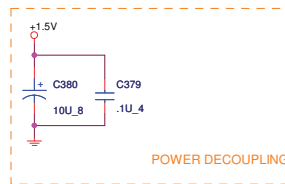
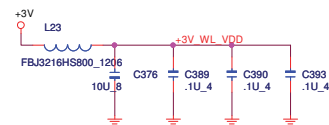
SATA HDD



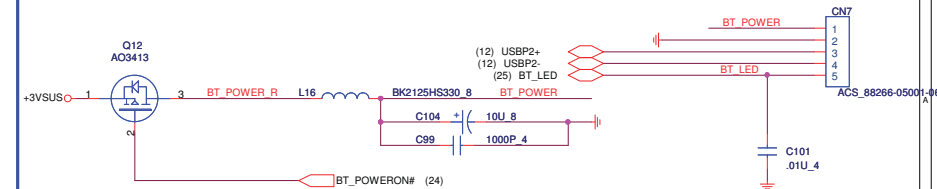
PATA ODD



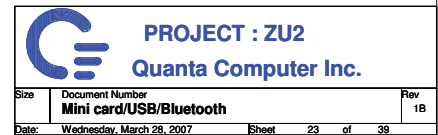
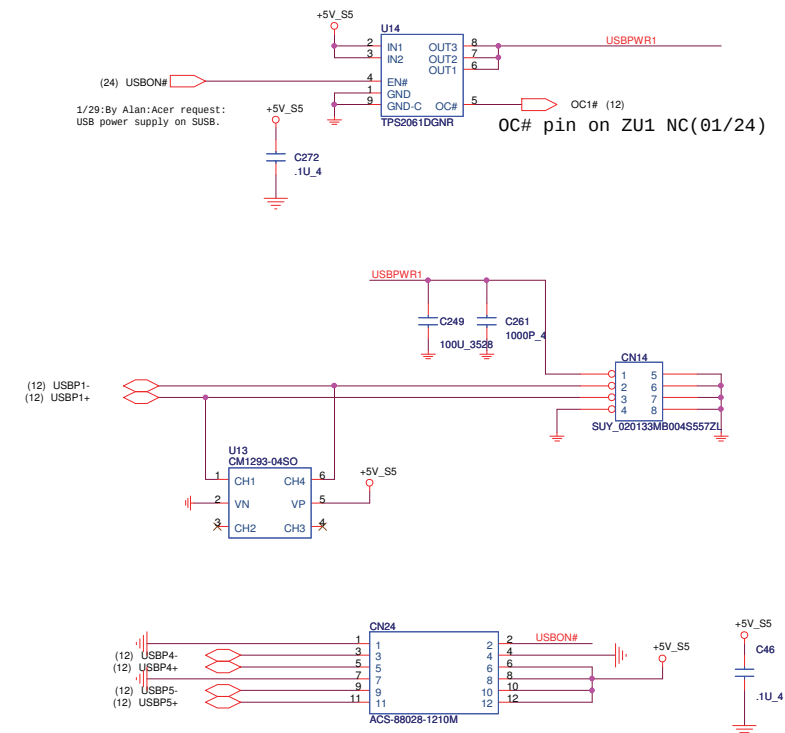
MINI-Card



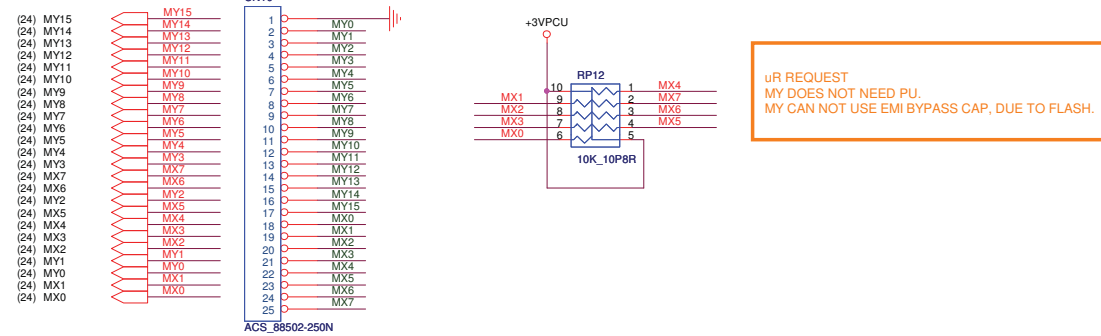
BLUETOOTH MODULE CONNECTOR



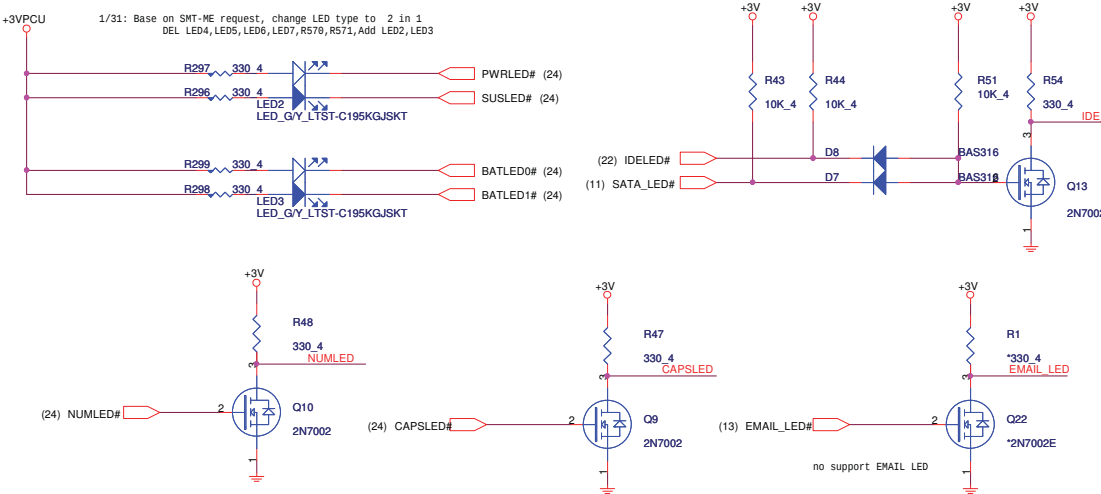
System USB



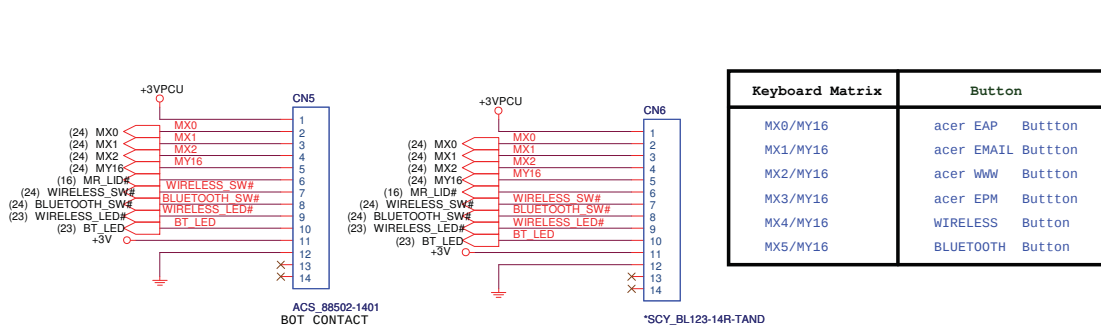
INT K/B



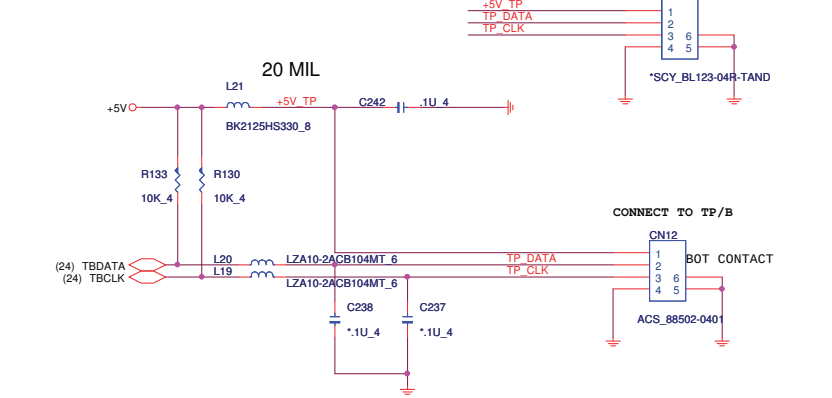
LED



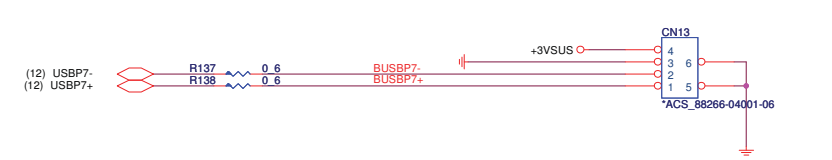
Function Board



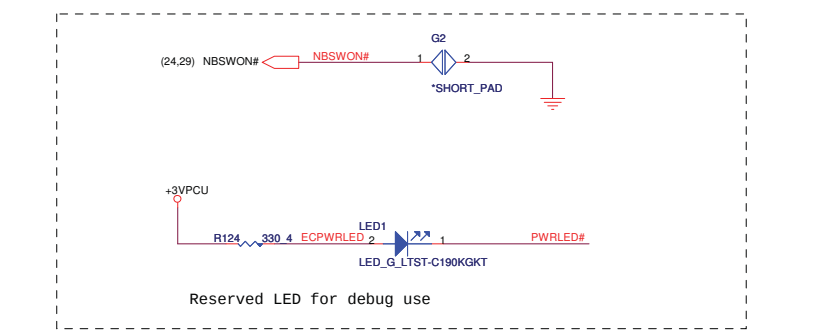
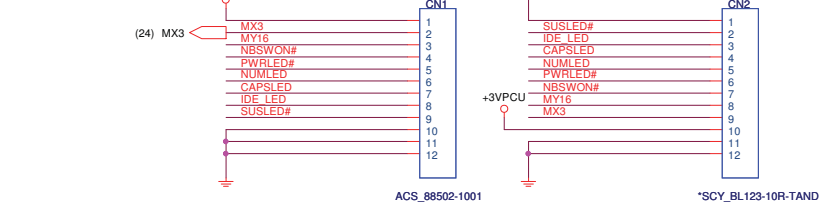
TOUCH PAD

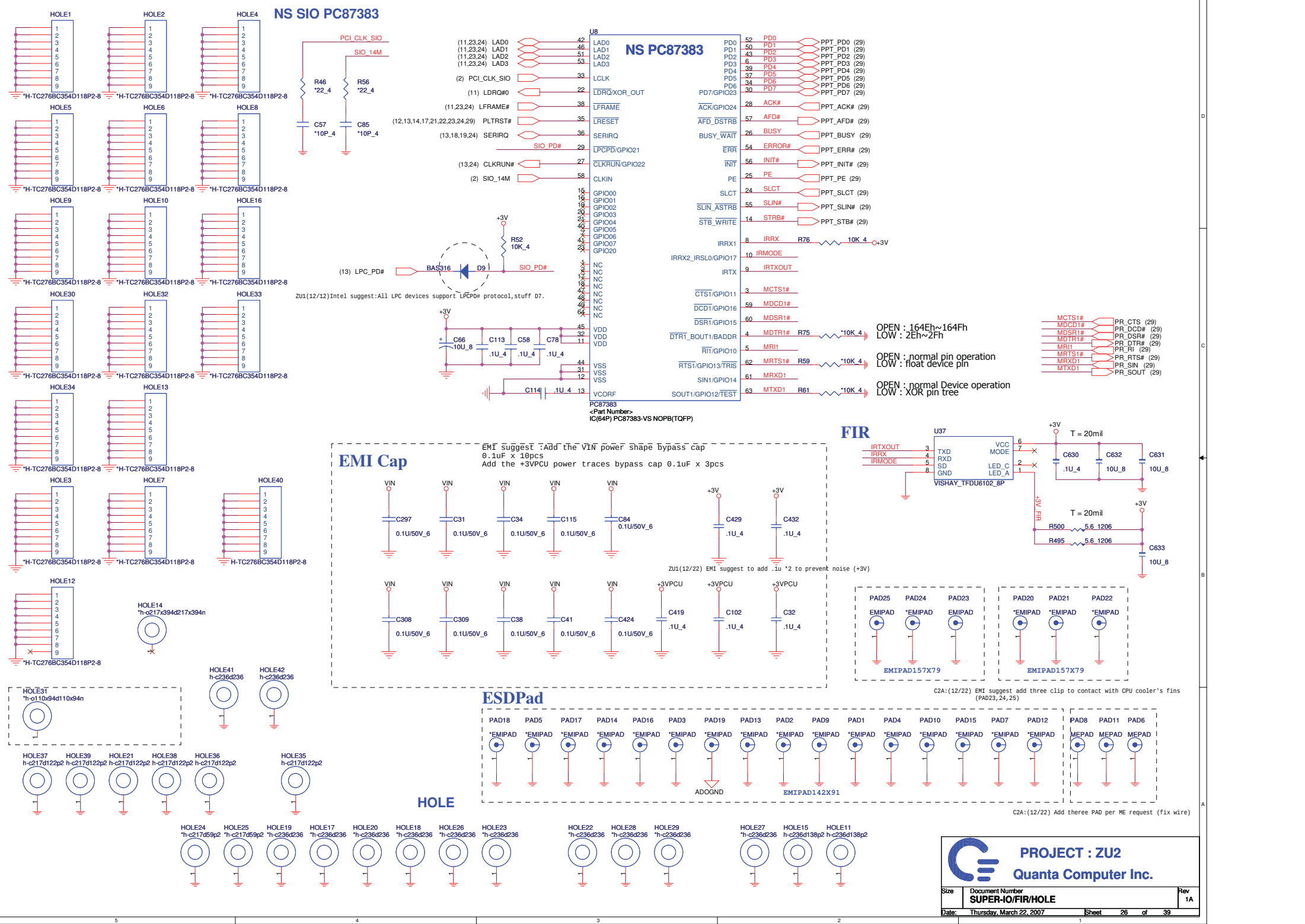


Finger Printer

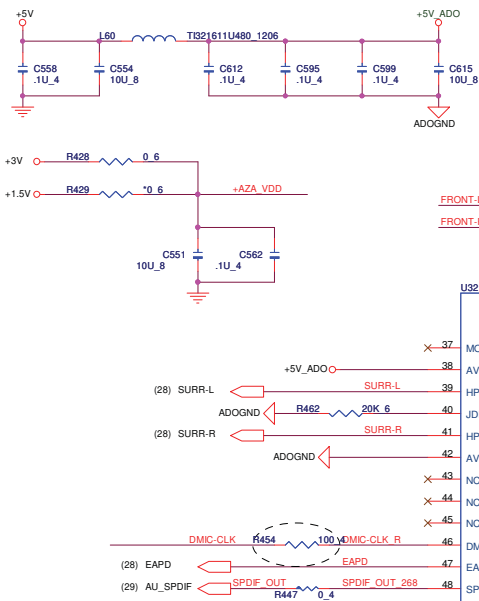


LED Board





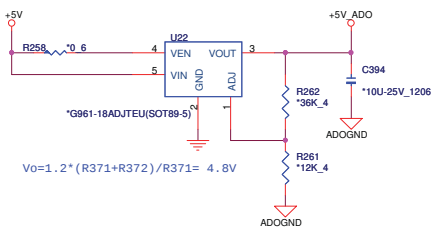
CODEC (ALC268)



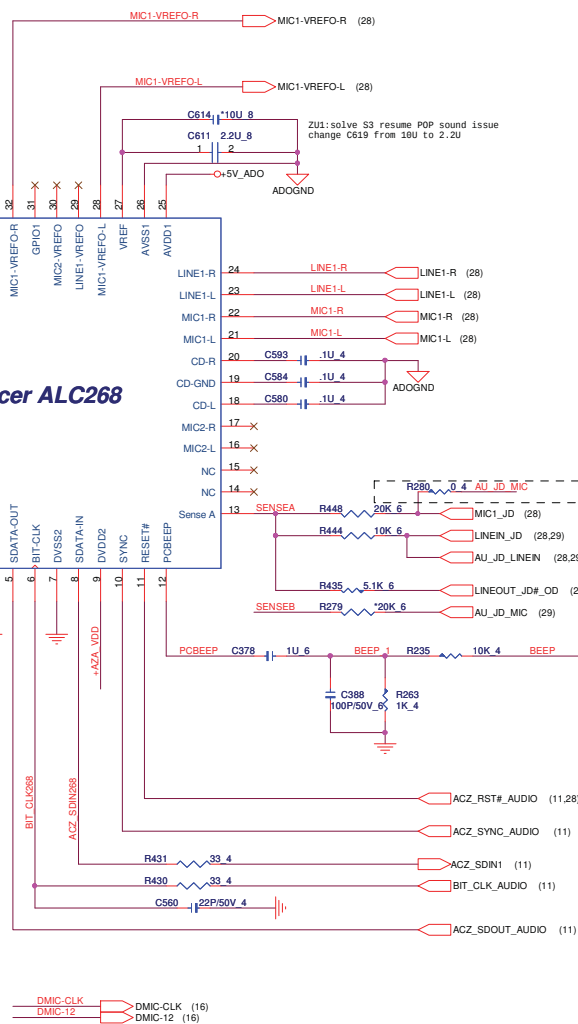
EMI suggest:
Add Additional two more bridge resistor
between ADDGND and GND,stuff R330 for Int-SPK issue

Tied at one point only
under the codec or
near the codec

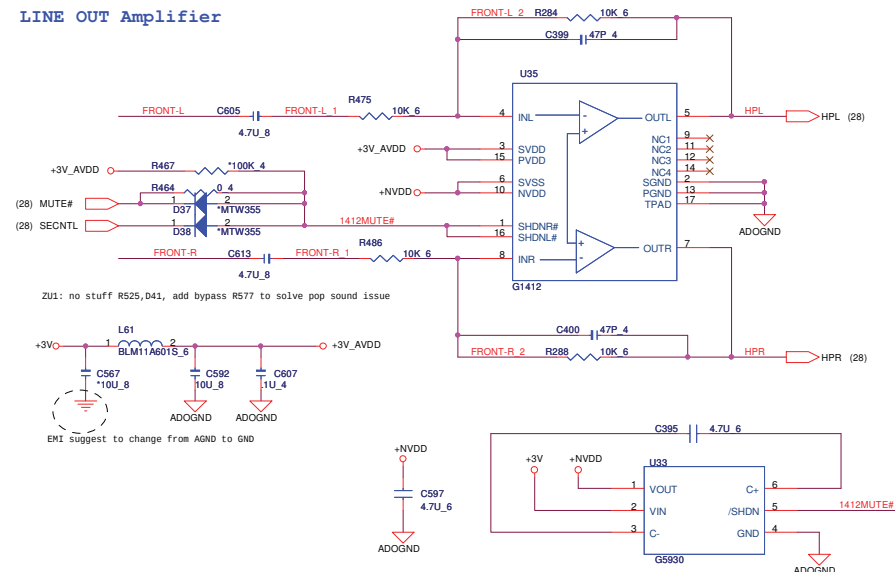
Reserve Audio power


$$V_0 = 1.2 * (R_{371} + R_{372}) / R_{371} = 4.8V$$

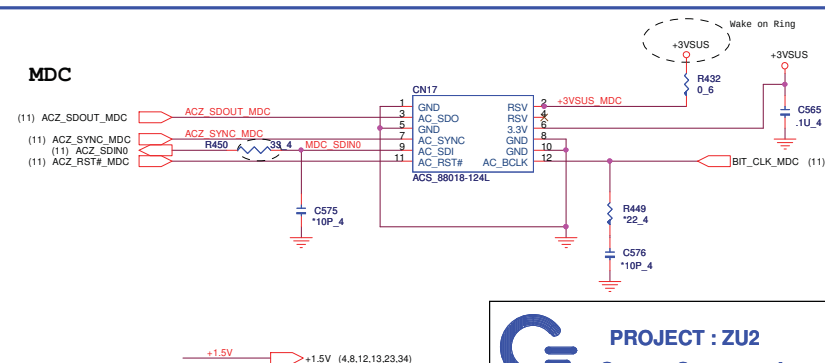
Acer ALC268



LINE OUT Amplifier

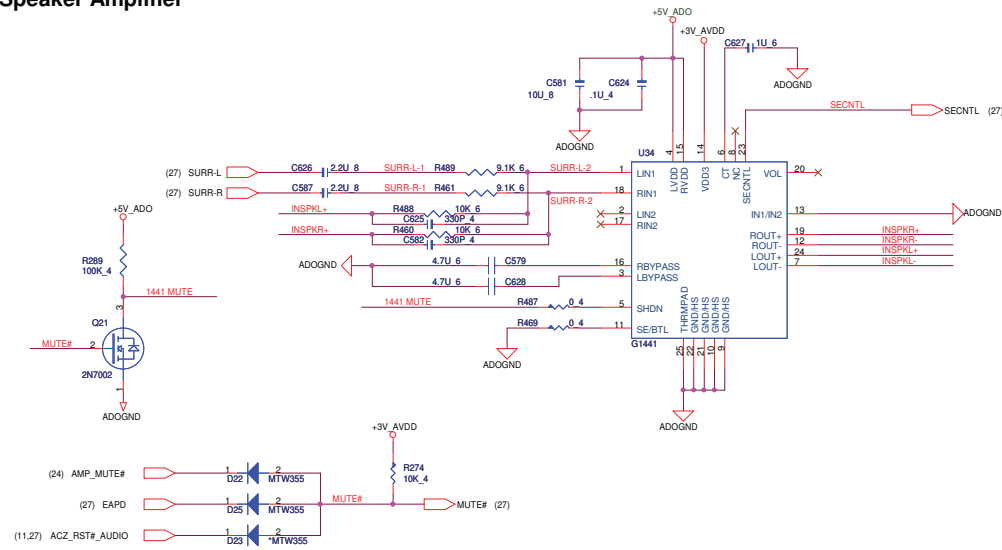


MDC

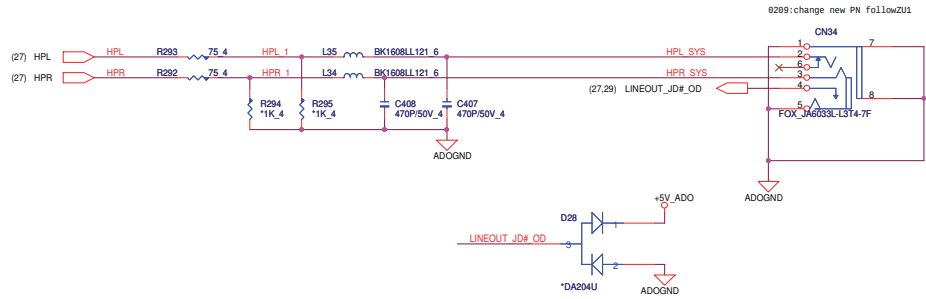


$+1.5V$  $+1.5V$ (4,8,12,13,23,34)

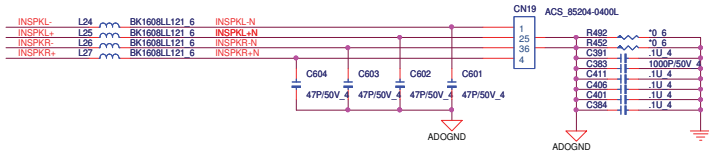
Speaker Amplifier



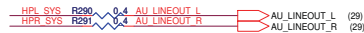
SYSTEM LINE OUT



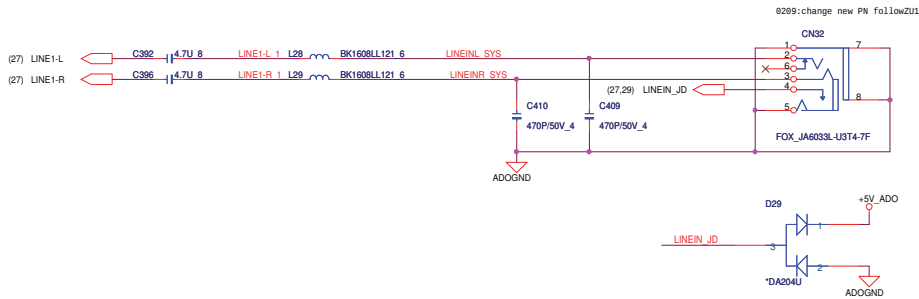
SPEAKER



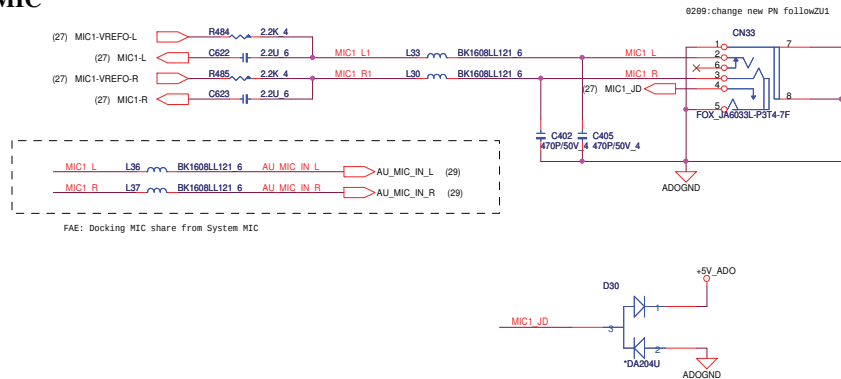
Docking LINE OUT/SPDIF



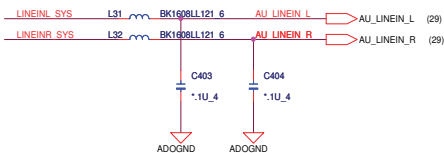
SYSTEM LINE IN

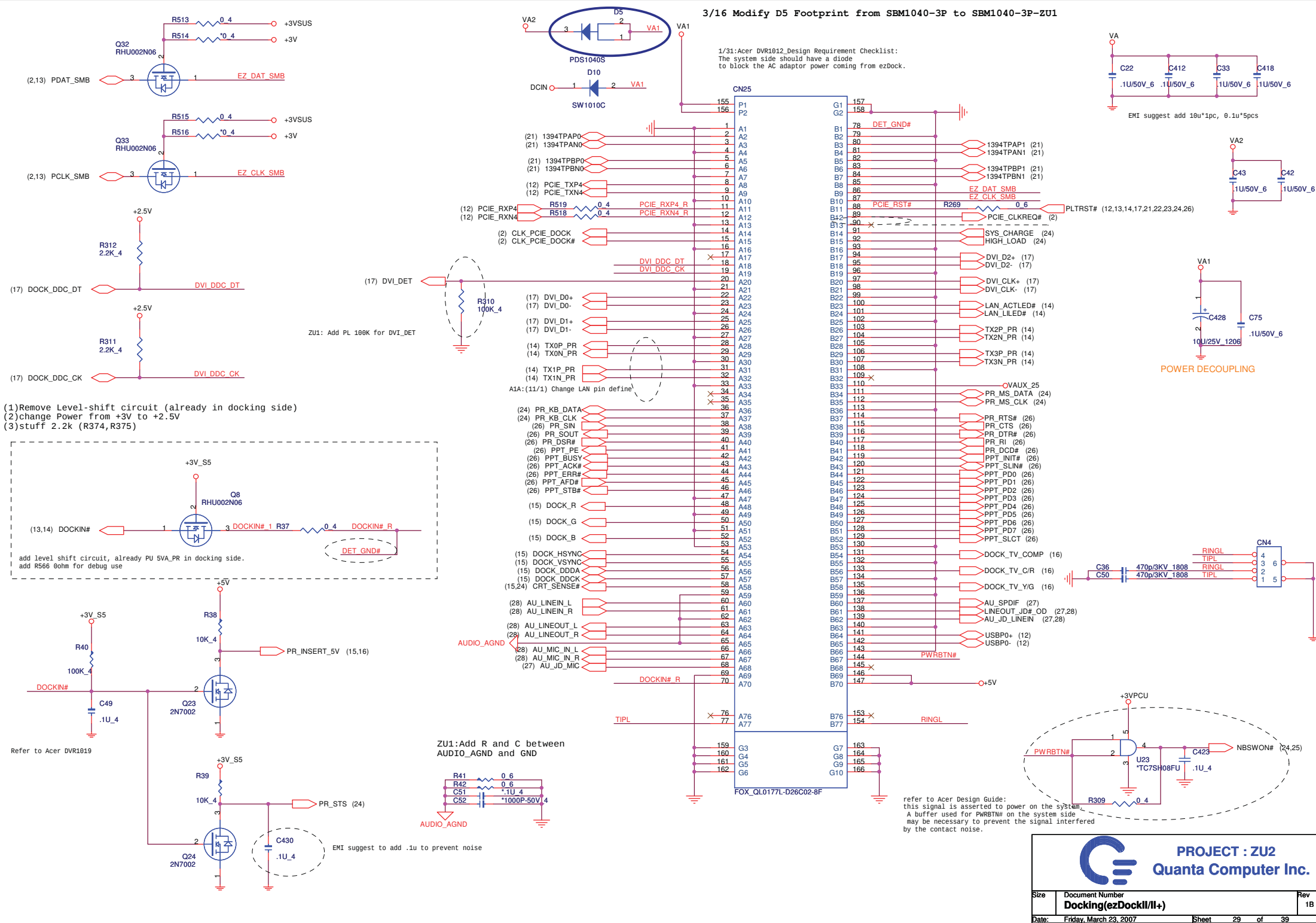


SYSTEM MIC



Docking LINE IN





3/16 Modify D5 Footprint from SBM1040-3P to SBM1040-3P-ZU1

1/31: Acer DVR1012 Design Requirement Checklist:
The system side should have a diode to block the AC adaptor power coming from ezDock.

EMI suggest add 10u*1pc, 0.1u*5pcs

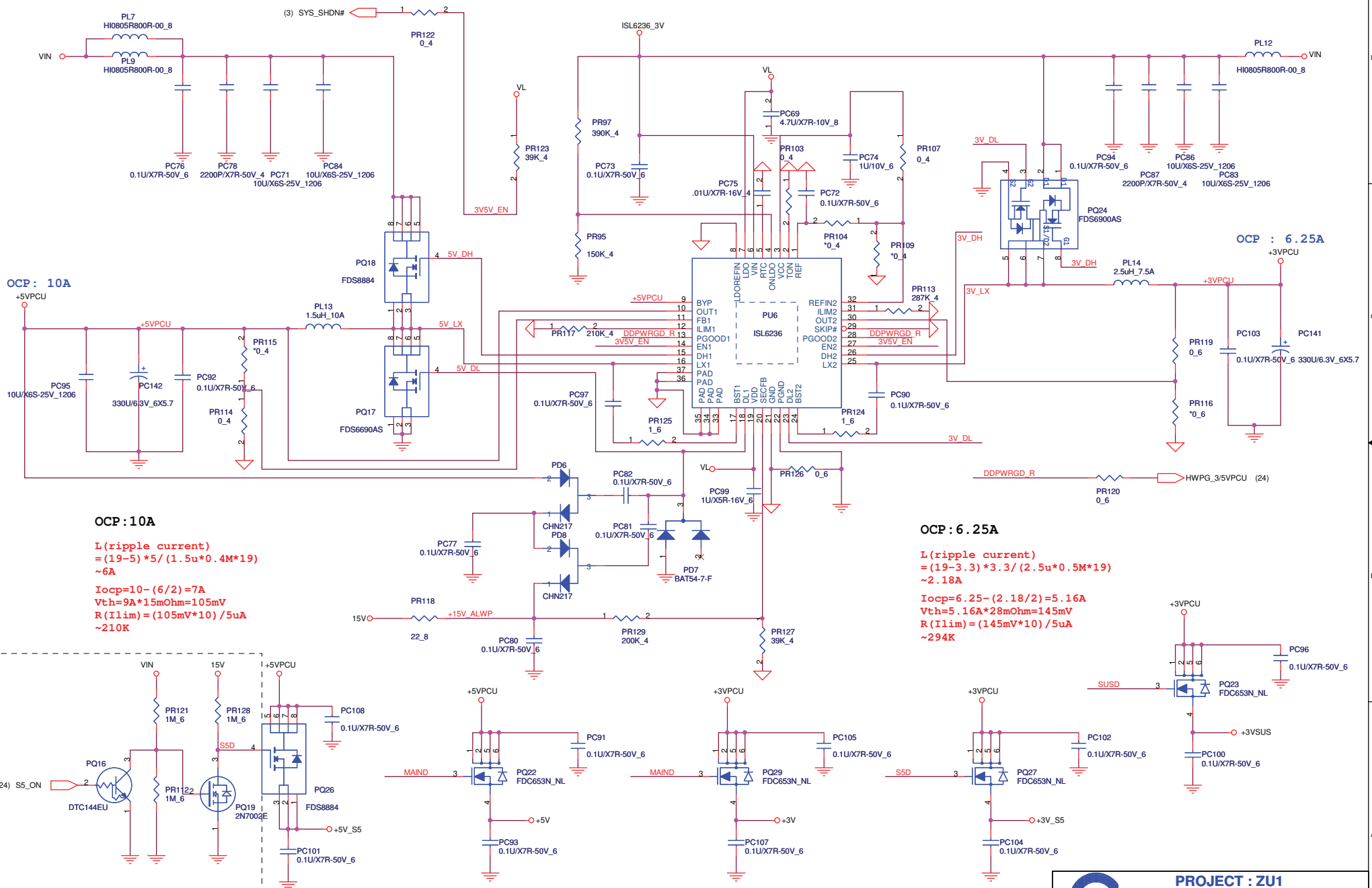
POWER DECOUPLING

refer to Acer Design Guide:
this signal is asserted to power on the system.
A buffer used for PWRBTN# on the system side
may be necessary to prevent the signal interfered
by the contact noise.



MAIND (34)

SUSD (34)



C2A: (12/18) change S5_On control circuit

B1C: (11/29) change P26 from FDS6690AS (BAM6690022) to FDS8884 (BAM8884006)

