

ICH6 GPIO SETTING

Pin	Pin Name	Connect to	Type	Input/Output Set
B7	GPI0/REQ6#	10K Pull +3V	I	fixed as Input only
E8	GPI1 / REQ5#	10K Pull +3V	I	fixed as Input only
D9	GPI2 / PIRQE#	10K Pull +3V	I	fixed as Input only
C7	GPI3 / PIRQF#	10K Pull +3V	I	fixed as Input only
C6	GPI4 / PIRQG#	10K Pull +3V	I	fixed as Input only
M3	GPI5 / PIRQH#	10K Pull +3V	I	fixed as Input only
AD19	GPI6 / BMBUSY#	NB BMBUSY#	I	Input
AE19	GPI7	NC	GPI	fixed as Input only
R1	GPI8	EC KBC_SCI#	GPI	fixed as Input only
C23	GPI9/OC4#	10K Pull +3V	I	Input
D23	GPI10/OC5#	10K Pull +3V	I	Input
W6	GPI11 / SMBALERT#	10K Pull +3V	I	Input
M2	GPI12	NC	GPI	fixed as Input only
R6	GPI13	EC EXTSMI#	GPI	fixed as Input only
C25	GPI14/OC6#	10K Pull +3V	I	Input
C24	GPI15 /OC7#	10K Pull +3V	I	Input
D8	GPO16/GTN6#	NC	O	Output
F6	GPO17 / GNT5#	NC	O	Output
AC21	GPO18 / STP_PC#	Clock GEN STP_PC#	O	Output
AB21	GPO19	WLAN_LED#	GPO	fixed as Output only
AD22	GPO20 / STP_CPU#	STP_CPU#	O	Output
AD20	GPO21	NC	GPO	
NA	GPIO22	NA	NA	NA
AD21	GPO23	NC	GPO	fixed as Output only
V3	GPIO24	3G	I/O	Output
P5	GPIO25	NC	I/O	Output

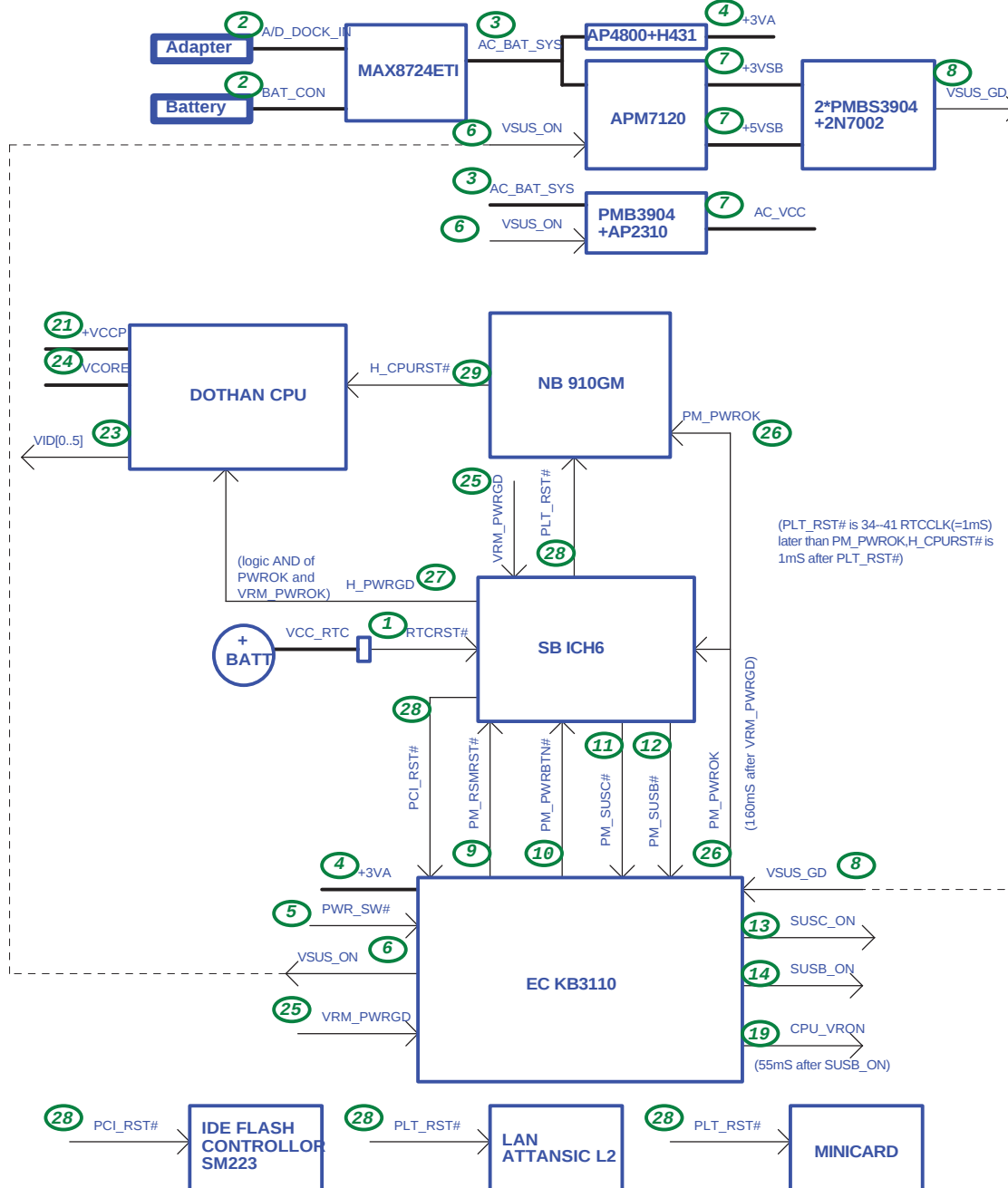
Pin	Pin Name	Connect to	Type	Input/Output Set
AF17	GPI26/SATA0GP	NC	GPI	(GPI)Input
R3	GPIO27	NC	I/O	Output
T3	GPIO28	NC	I/O	Output
AE18	GPI29 / SATA1GP	PCBVER0	GPI	(GPI)Input
AF18	GPI30 / SATA2GP	NC	GPI	(GPI)Input
AG18	GPI31 / SATA3GP	PCBVER1	GPI	(GPI)Input
AF19	GPIO32 / CLKRUN#	10K Pull +3V	I/O	Input
AF20	GPIO33	NC	I/O	Output
AC18	GPIO34	NC	I/O	Output
NA	GPIO35	NA	NA	NA
NA	GPIO36	NA	NA	NA
NA	GPIO37	NA	NA	NA
NA	GPIO38	NA	NA	NA
NA	GPIO39	NA	NA	NA
F7	GPI40 / REQ4#	10K Pull +3V	I	Input
P4	GPI41 / LDRQ1#	NC	I	Input
NA	GPIO42	NA	NA	NA
NA	GPIO43	NA	NA	NA
NA	GPIO44	NA	NA	NA
NA	GPIO45	NA	NA	NA
NA	GPIO46	NA	NA	NA
NA	GPIO47	NA	NA	NA
E7	GPO48 /GNT4#	NC	O	Output
AC25	GPO49 / CPUPWRGD	CPU Power Ok	O	Output

fixed as Output only

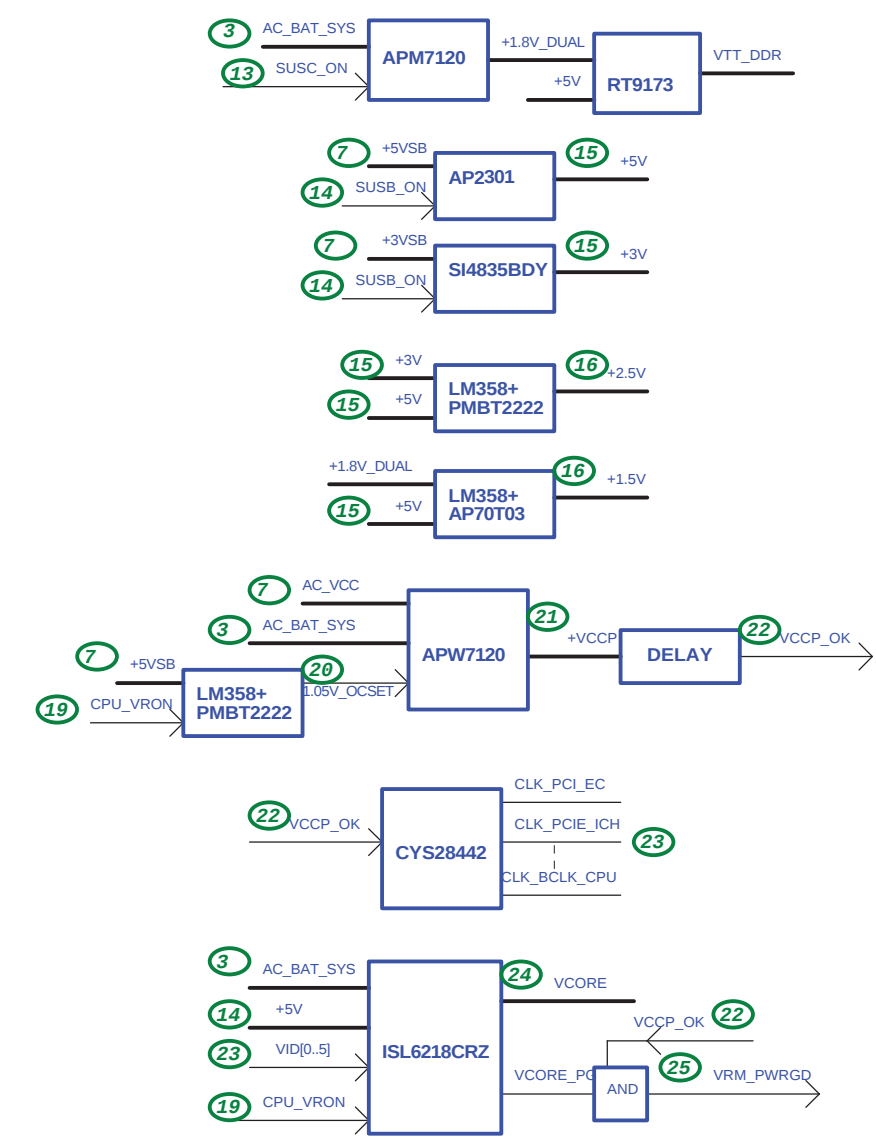
Default Group-A7V8X MX

		Title : System Setting	
ASUSTek Computer INC.		Engineer: Kell_Huang	
Size A3	Project Name 900SD_MB	Rev R1.0G	
Date: Monday, November 10, 2008		Sheet 2	of 51

*This sequence is for Battery Plug-in and no Adapter,
if Adapter Plug-in, the sequence change to:
A/D_DOCK_IN--->AC_BAT_SYS--->+3VA--->VSUS_ON--->+3VSB & +5VSB
--->VSUS_GD--->PM_REMRST#--->PWR_SW#--->PM_PWRBTN--->PM_SUSC#--->PM_SUSB#



	Signal	S0/S1	S3	S4/S5	Power
Only Battery	VSUS_ON	H	H	L	VSUS
Adapter In	VSUS_ON	H	H	H	VSUS
	SUSB_ON	H	L	L	Main
	SUSC_ON	H	H	L	DUAL



EC KB3310 GPIO SETTING

Pin No.	Pin Name	Signal Name	Type	NOTE
1	GA20	A20GATE	O	A20GATE
2	KBRST#	RC_IN#	O	KBRST#
6	GPIO04	CTRL_CAMER_PWR	I	Default : High
13	PCIRST#	PCI_RST#	I	PCI Reset
14	GPIO07	N.C	O	Reserved
15	GPIO08	EXTSMIH#	O	EXTSMIH#, 10K Pull +3VSUS
16	GPIO0A	LID_EC#	I	LID_EC#, *
17	GPIO0B	LCD_CSB	O	LCD chip select
18	GPIO0C	LCD_SDA	I/O	LCD Data
19	GPIO0D	DISTP_SW#	I	Touch Pad Disabled,*
20	SCI#	KBC_SCI#	O	KBC_SCI#, 10K Pull +3VSUS
21	PWM1	BL_PWM_DA	O	LCD Light Switch
23	PWM2	LCD_SCL	O	LCD clock
25	GPIO11	PM_PWRBTN#	OD	Power Button to SB,*
26	FANPWM1	FAN0_PWM	O	CPU Fan(Unused)
27	FANPWM2	FAN1_PWM	O	VGA Fan(Unused)
28	FANFB1	FAN0_TACH	I	CPU FanTach(Unused)
29	FANFB2	FAN1_TACH	I	VGA FanTach(Unused)
30	GPIO16	E51_TX	O	RS232 debug port
31	GPIO17	N.C	O	Reserved
32	GPIO18	PWR_SW#	I	power button,*
34	GPIO19	MAIL_LED#	O	Mail LED(Unused)
36	GPIO1A	CTRL_Mincard_PWR	O	Default : High
38	CLKRUN#	N.C	O	Reserved
39	KSO0	KSO0	O	For Keyboard interface
40	KSO1	KSO1	O	For Keyboard interface
41	KSO2	KSO2	O	For Keyboard interface
42	KSO3	KSO3	O	For Keyboard interface
43	KSO4	KSO4	O	For Keyboard interface
44	KSO5	KSO5	O	For Keyboard interface
45	KSO6	KSO6	O	For Keyboard interface
46	KSO7	KSO7	O	For Keyboard interface
47	KSO8	KSO8	O	For Keyboard interface
48	KSO9	KSO9	O	For Keyboard interface
49	KSO10	KSO10	O	For Keyboard interface
50	KSO11	KSO11	O	For Keyboard interface
51	KSO12	KSO12	O	For Keyboard interface
52	KSO13	KSO13	O	For Keyboard interface
53	KSO14	KSO14	O	For Keyboard interface
54	KSO15	KSO15	O	For Keyboard interface
55	KSI0	KSI0	I	For Keyboard interface
56	KSI1	KSI1	I	For Keyboard interface
57	KSI2	KSI2	I	For Keyboard interface
58	KSI3	KSI3	I	For Keyboard interface
59	KSI4	KSI4	I	For Keyboard interface
60	KSI5	KSI5	I	For Keyboard interface
61	KSI6	KSI6	I	For Keyboard interface
62	KSI7	KSI7	I	For Keyboard interface
63	AD0	P_PMON_10	I	Sense Power Loading
64	AD1	BAT_IN	I	sense Battery
65	AD2	N.C	I	Reserved
66	AD3	N.C	I	Reserved
68	GPO3C	DOC	O	Trigger Clock Gen

Pin No.	Pin Name	Signal Name	Type	NOTE
70	GPO3D	LCD_BACKOFF#	O	LCD_BACKOFF#
71	GPO3E	CLK_PWRSERVE#	O	Active when BAT_IN=1 and AC_OK=0(Unused)
72	GPO3F	BAT_LL#	O	Battery Low Low
73	GPIO40	AC_OK	I	AC Adaptor Plug in
74	GPIO41	PM_RSMRST#	O	10K Pull GND
75	GPIO42	N.C	O	Reserved
76	GPIO43	N.C	O	Reserved
77	SCL1	SMB0_CLK	I/OD	4.7K Pull +3VA_EC
78	SDA1	SMB0_DAT	I/OD	4.7K Pull +3VA_EC
79	SCL2	SMB1_CLK	I/OD	10K Pull +3VS
80	SDA2	SMB1_DAT	I/OD	10K Pull +3VS
81	KSO16	N.C	O	Reserved
82	KSO17	N.C	O	Reserved
83	PSCLK1	N.C	O	Reserved
84	PSDAT1	N.C	O	Reserved
85	PSCLK2	N.C	O	Reserved
86	PSDAT2	N.C	O	Reserved
87	PSCLK3	TP_CLK	I/OD	10K Pull +3VS
88	PSDAT3	TP_DAT	I/OD	10K Pull +3VS
89	GPIO50	BATSEL_3S	O	Battery series. Hi:3S, Lo:4S(Unused)
90	GPIO52	CHG_LED_UP#	O	charger LED
91	GPIO53	CTRL_L2_PWR	O	Default : High
92	GPIO54	PWR_LED_UP	O	EC H/W blinking
93	GPIO55	SCRL_LED#	O	EC H/W controls
95	GPIO56	PWR4G_SW#	I	*
97	GPXOA00	SPI_MODE#	O	*HW Strap for SPI Flash deExternal Pull Down 100K ohm to GND"
98	GPXOA01	SUSC_ON	O	
99	GPXOA02	VSUS_ON	O	
100	GPXOA03	CPU_VRON	O	
101	GPXOA04	SUSB_ON	O	
102	GPXOA05	ICH8_PWROK	O	
103	GPXOA06	N.C	O	Reserved
104	GPXOA07	CHG_EN#	O	Battery charging enabled
105	GPXOA08	PRECHG	O	
106	GPXOA09	SPI_WP#	O	
107	GPXOA10	OP_SD#	O	Audio OP
108	GPXOA11	BAT_LEARN	O	
109	GPXID0	BATSEL_2P#	O	Battery parallel. Hi:1P, Lo:2P-3P
110	GPXID1	N.C	O	Reserved
112	GPXID2	THRO_CPU	O	Active if Battery Temperature Is
114	GPXID3	SUSB#	I	Pull Down 100K ohm to GND
115	GPXID4	SUSC#	I	Pull Down 100K ohm to GND
116	GPXID5	CPUPWR_GD	I	10K Pull +3VS
117	GPXID6	VSUS_GD	I	Disabled **
118	GPXID7	N.C	O	Reserved
121	GPIO57	INTERNET#	I	*
126	SPICLK	SPI_CLK	O	SPI Clock
127	GPIO59	N.C	O	Reserved

EC KB3310 Other Pin SETTING

Pin No.	Pin Name	Signal Name	Type	NOTE
3	SERIRQ	INT_SERIRQ	I/OD	8.2K Pull +3VS
4	LFRAME#	LPC_FRAME#	I	
5	LAD3	LPC_AD3	I/O	
7	LAD2	LPC_AD2	I/O	
8	LAD1	LPC_AD1	I/O	
9	VCC	+3VA_EC	P	
10	LAD0	LPC_AD0	I/O	
11	GND	GND	P	
12	PCICLK	CLK_PCI_EC	I	
22	VCC	+3VA_EC	P	
24	GND	GND	P	
33	VCC	+3VA_EC	P	
35	GND	GND	P	
37	ECRST#	EC_RST#	I	Add 100K ohm to GND
67	AVCC	+3VACC	P	
69	AGND	AGND	P	
94	GND	GND	P	
96	VCC	+3VA_EC	P	
111	VCC	+3VA_EC	P	
113	GND	GND	P	
119	RD#	SPI_SO	I	
120	WR#	SPI_SI	O	
112	XCLKI	32KXCLKI	I	
123	XCLKO	32KXCLKO	O	
124	V18R	K_V18R		Reserved 1uF to GND
125	VCC	+3VA_EC	P	
128	SPICS#	SPI_CE#	O	

Default Group-A7V8X MX



Title : EC Pin Define

ASUSTek Computer INC.

Engineer: Kell_Huang

Size A3

Project Name 900SD_MB

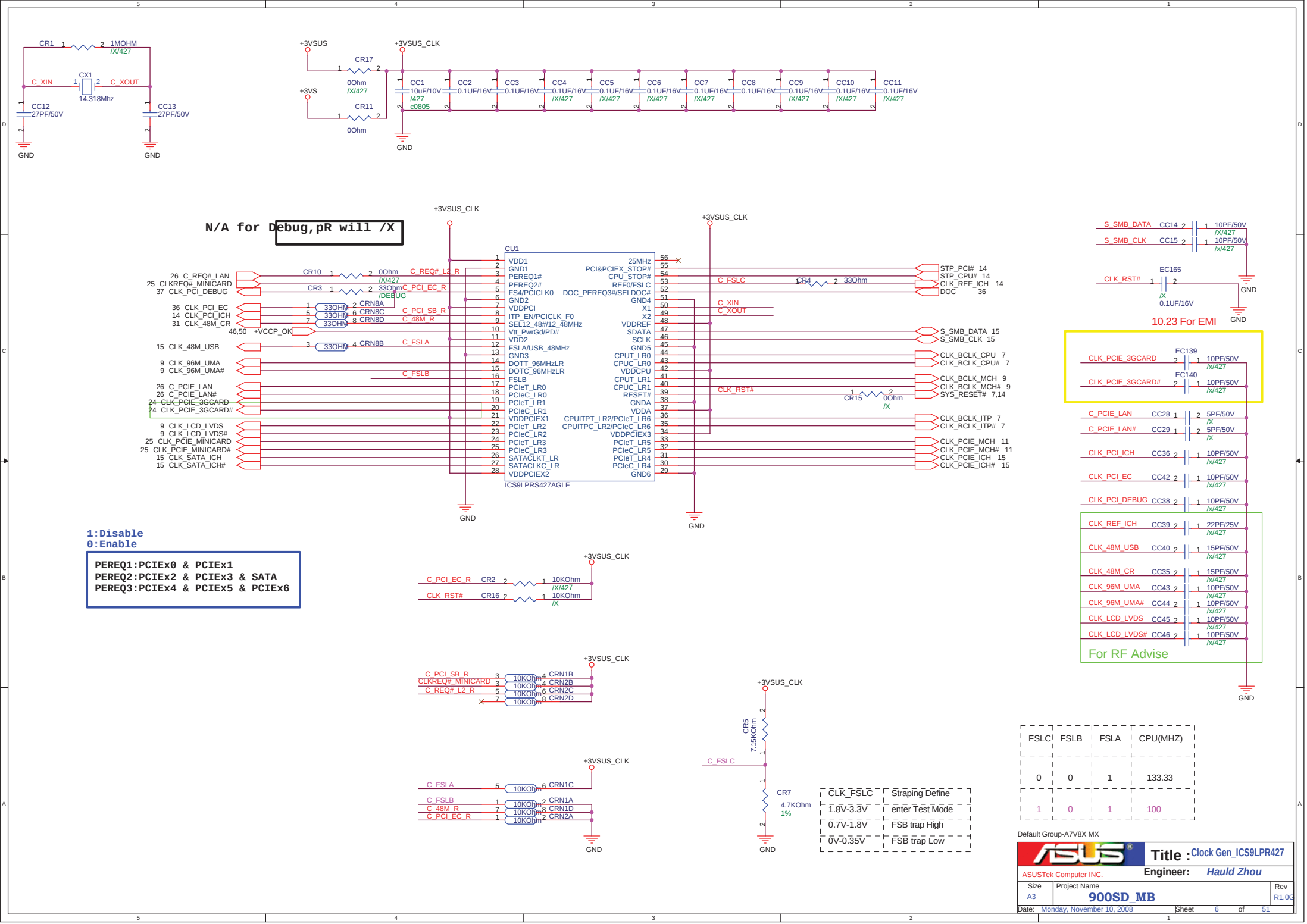
Rev R1.0G

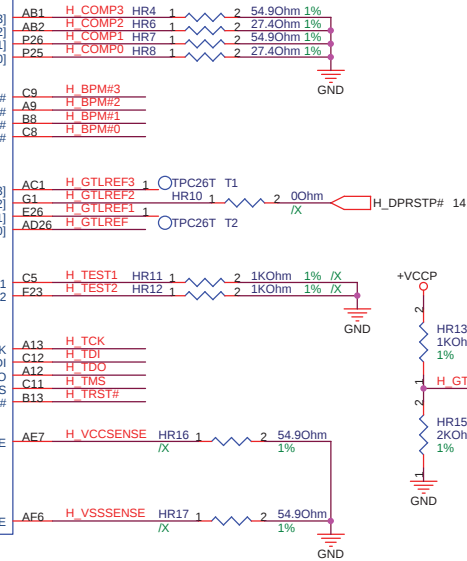
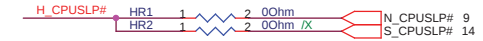
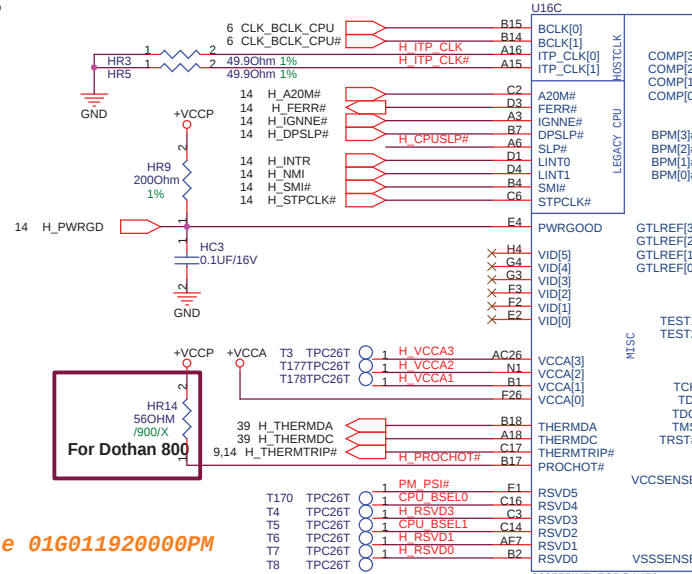
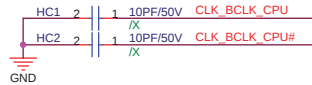
Date: Monday, November 10, 2008

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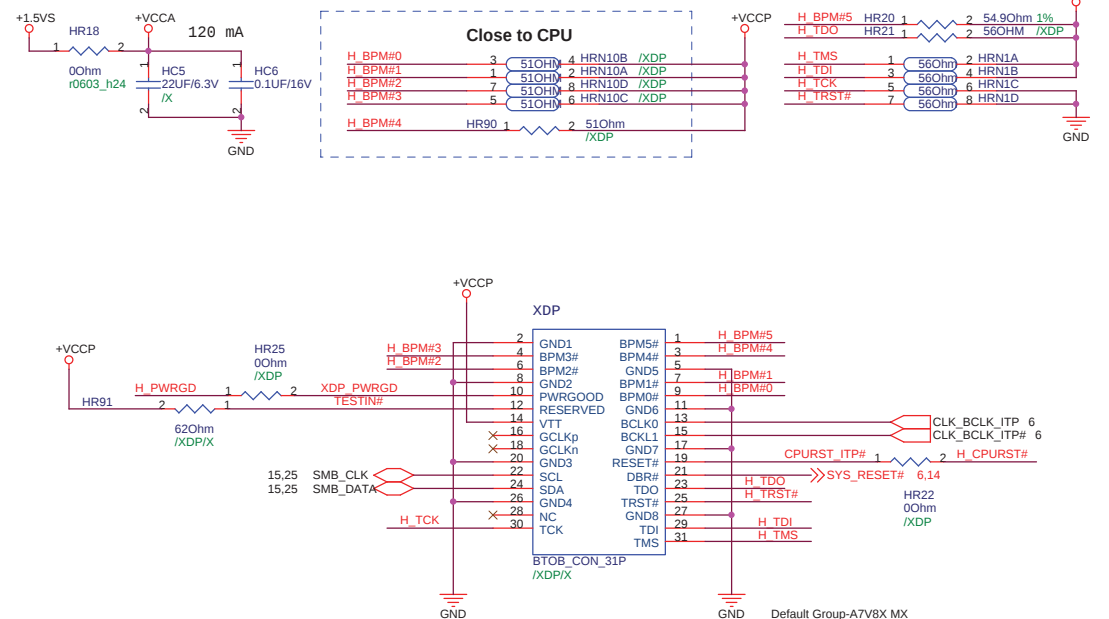
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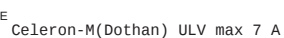
		Title : History	
ASUSTek Computer INC.		Engineer: Hauld Zhou	
Size A3	Project Name 900SD_MB	Rev R1.0	
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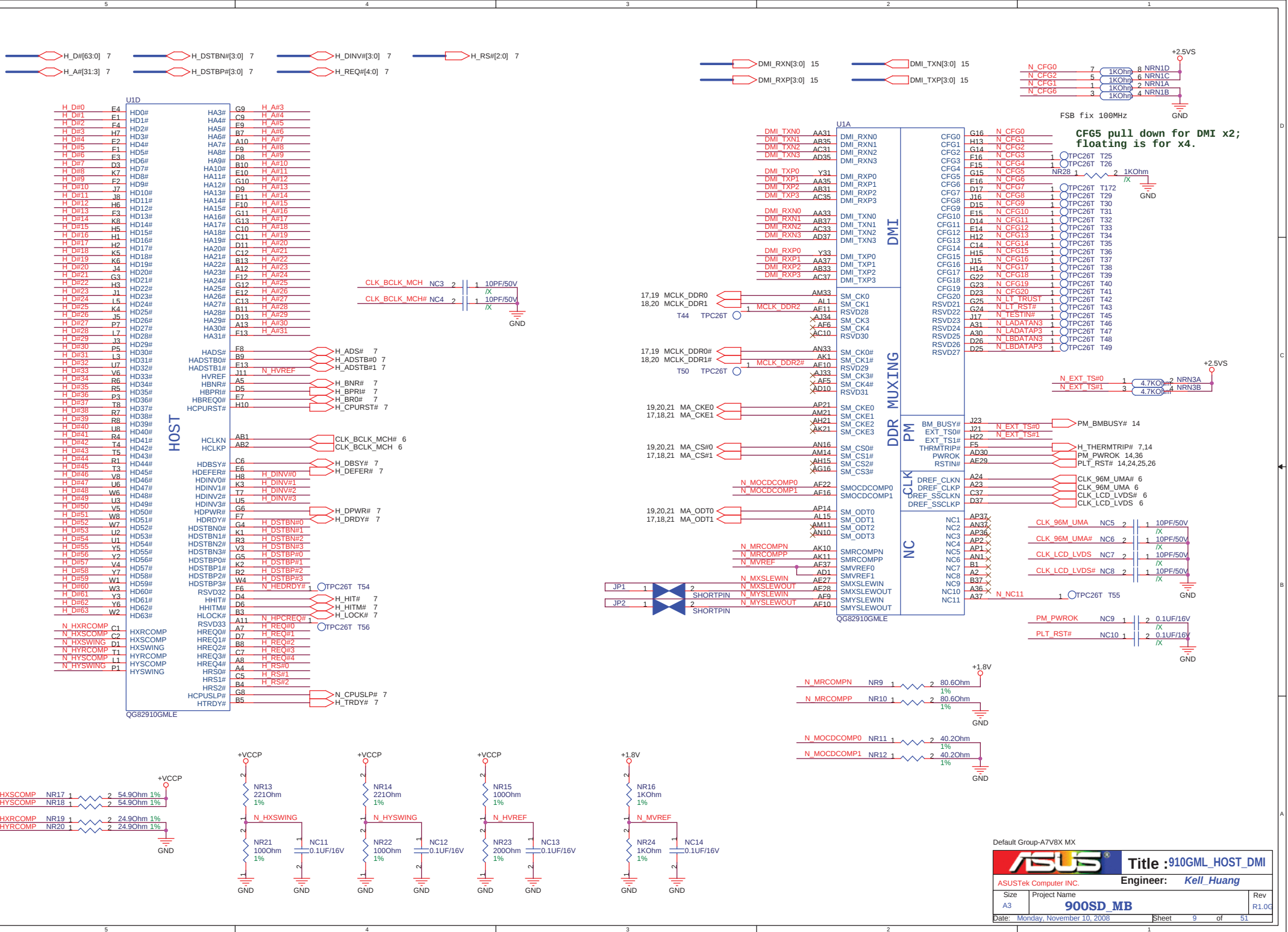


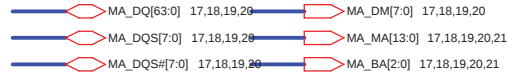


U16 use 01G011920000PM









U1B			
MA DQ0	AG35	SA DQ0	SA_BS0
MA DQ1	AH35	SA DQ1	SA_BS1
MA DQ2	AL35	SA DQ2	SA_BS2
MA DQ3	AL37	SA DQ3	
MA DQ4	AH36	SA DQ4	SA_DM0
MA DQ5	AJ35	SA DQ5	SA_DM1
MA DQ6	AK37	SA DQ6	SA_DM2
MA DQ7	AL34	SA DQ7	SA_DM3
MA DQ8	AM36	SA DQ8	SA_DM4
MA DQ9	AN35	SA DQ9	SA_DM5
MA DQ10	AP32	SA DQ10	SA_DM6
MA DQ11	AM31	SA DQ11	SA_DM7
MA DQ12	AM34	SA DQ12	
MA DQ13	AM35	SA DQ13	SA_DQ0
MA DQ14	AL32	SA DQ14	SA_DQ1
MA DQ15	AM32	SA DQ15	SA_DQ2
MA DQ16	AN31	SA DQ16	SA_DQ3
MA DQ17	AP31	SA DQ17	SA_DQ4
MA DQ18	AN28	SA DQ18	SA_DQ5
MA DQ19	AP28	SA DQ19	SA_DQ6
MA DQ20	AL30	SA DQ20	SA_DQ7
MA DQ21	AM30	SA DQ21	
MA DQ22	AM28	SA DQ22	SA_DQ0#
MA DQ23	AL28	SA DQ23	SA_DQ1#
MA DQ24	AP27	SA DQ24	SA_DQ2#
MA DQ25	AM27	SA DQ25	SA_DQ3#
MA DQ26	AM23	SA DQ26	SA_DQ4#
MA DQ27	AM22	SA DQ27	SA_DQ5#
MA DQ28	AL23	SA DQ28	SA_DQ6#
MA DQ29	AM24	SA DQ29	SA_DQ7#
MA DQ30	AN22	SA DQ30	
MA DQ31	AP22	SA DQ31	SA_MA0
MA DQ32	AL9	SA DQ32	SA_MA1
MA DQ33	AL6	SA DQ33	SA_MA2
MA DQ34	AP7	SA DQ34	SA_MA3
MA DQ35	AP11	SA DQ35	SA_MA4
MA DQ36	AP10	SA DQ36	SA_MA5
MA DQ37	AL7	SA DQ37	SA_MA6
MA DQ38	AM7	SA DQ38	SA_MA7
MA DQ39	AN5	SA DQ39	SA_MA8
MA DQ40	AN6	SA DQ40	SA_MA9
MA DQ41	AN3	SA DQ41	SA_MA10
MA DQ42	AP3	SA DQ42	SA_MA11
MA DQ43	AP6	SA DQ43	SA_MA12
MA DQ44	AM6	SA DQ44	SA_MA13
MA DQ45	AL4	SA DQ45	
MA DQ46	AM3	SA DQ46	SA_CAS#
MA DQ47	AK2	SA DQ47	SA_RAS#
MA DQ48	AK3	SA DQ48	SA_RCVENIN#
MA DQ49	AG2	SA DQ49	SA_RCVENOUT#
MA DQ50	AG1	SA DQ50	SA_WE#
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MA DQ52	AM2	SA DQ52	
MA DQ53	AH3	SA DQ53	
MA DQ54	AG3	SA DQ54	
MA DQ55	AE3	SA DQ55	
MA DQ56	AD6	SA DQ56	
MA DQ57	AC4	SA DQ57	
MA DQ58	AE2	SA DQ58	
MA DQ59	AD4	SA DQ59	
MA DQ60	AD5	SA DQ60	
MA DQ61		SA DQ61	
MA DQ62		SA DQ62	
MA DQ63		SA DQ63	

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U1C			
SB DQ0	SB_BS0	AJ15	
SB DQ1	SB_BS1	AG11	
SB DQ2	SB_BS2	AG21	
SB DQ3			
SB DQ4	SB_DM0	AF32	
SB DQ5	SB_DM1	AK34	
SB DQ6	SB_DM2	AK27	
SB DQ7	SB_DM3	AK24	
SB DQ8	SB_DM4	AK5	
SB DQ9	SB_DM5	AE7	
SB DQ10	SB_DM6	AB7	
SB DQ11	SB_DM7		
SB DQ12			
SB DQ13	SB_DQ0	AE34	
SB DQ14	SB_DQ1	AK32	
SB DQ15	SB_DQ2	AJ28	
SB DQ16	SB_DQ3	AK23	
SB DQ17	SB_DQ4	AM10	
SB DQ18	SB_DQ5	AH6	
SB DQ19	SB_DQ6	AF8	
SB DQ20	SB_DQ7	AB4	
SB DQ21			
SB DQ22	SB_DQ0#	AF35	
SB DQ23	SB_DQ1#	AK33	
SB DQ24	SB_DQ2#	AK28	
SB DQ25	SB_DQ3#	AJ23	
SB DQ26	SB_DQ4#	AL10	
SB DQ27	SB_DQ5#	AH7	
SB DQ28	SB_DQ6#	AE7	
SB DQ29	SB_DQ7#	AB5	
SB DQ30			
SB DQ31	SB_MA0	AH17	
SB DQ32	SB_MA1	AK17	
SB DQ33	SB_MA2	AH18	
SB DQ34	SB_MA3	AK18	
SB DQ35	SB_MA4	AJ19	
SB DQ36	SB_MA5	AK19	
SB DQ37	SB_MA6	AH15	
SB DQ38	SB_MA7	AJ20	
SB DQ39	SB_MA8	AH20	
SB DQ40	SB_MA9	AI16	
SB DQ41	SB_MA10	AG19	
SB DQ42	SB_MA11	AG20	
SB DQ43	SB_MA12	AG15	
SB DQ44	SB_MA13		
SB DQ45			
SB DQ46	SB_CAS#	AH14	
SB DQ47	SB_RAS#	AK14	
SB DQ48	SB_RCVENIN#	AE15	
SB DQ49	SB_RCVENOUT#	AF14	
SB DQ50	SB_WE#	AH16	
SB DQ51			
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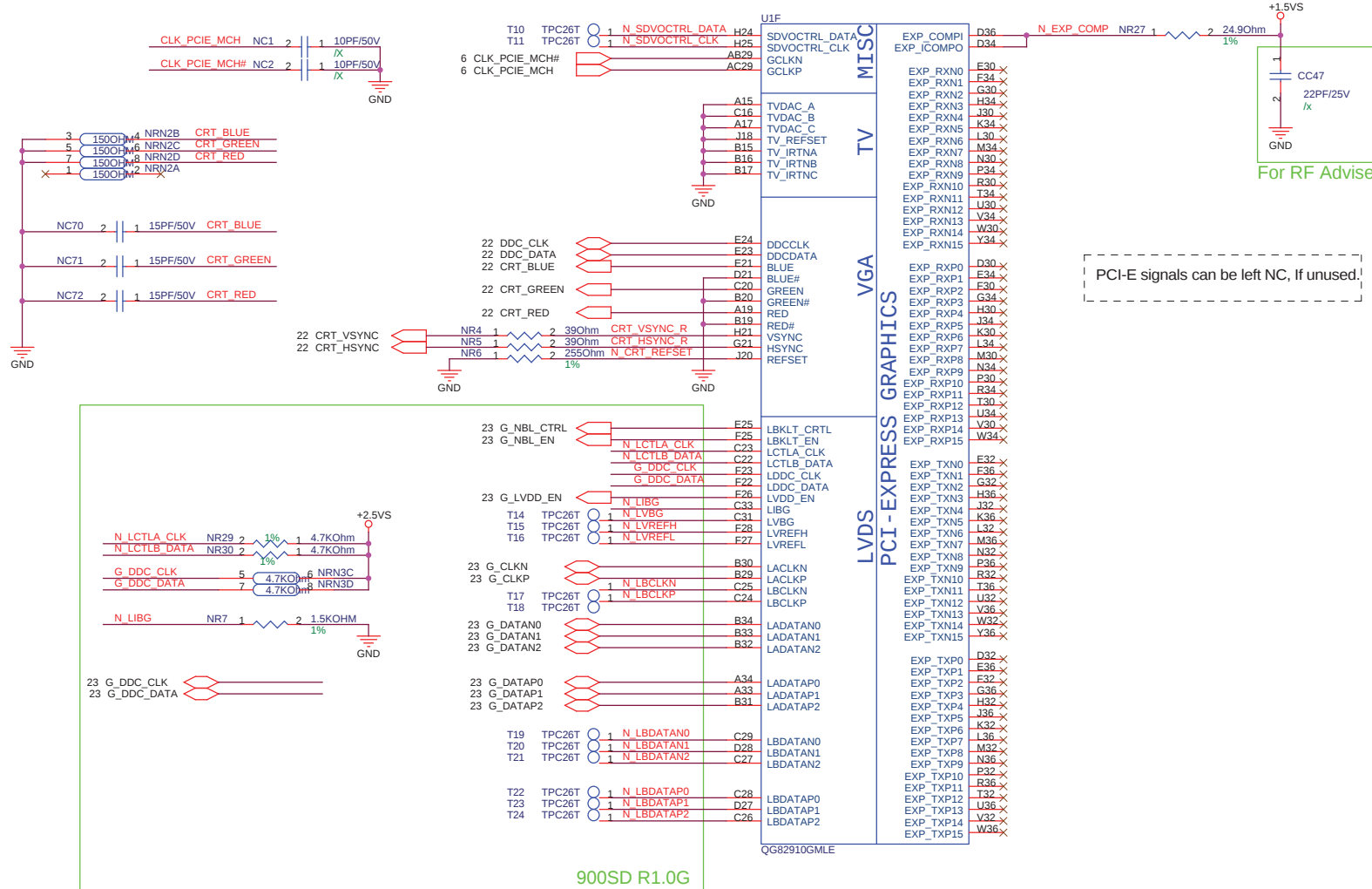
Default Group-A7V8X M2

ASUS		Title : 910GML_DRAM	
ASUSTek Computer INC.		Engineer: Kell_Huang	
Size	Project Name		Rev
A3	900SD_MB		R1.0G
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SDVO SMBus have
internal pull down

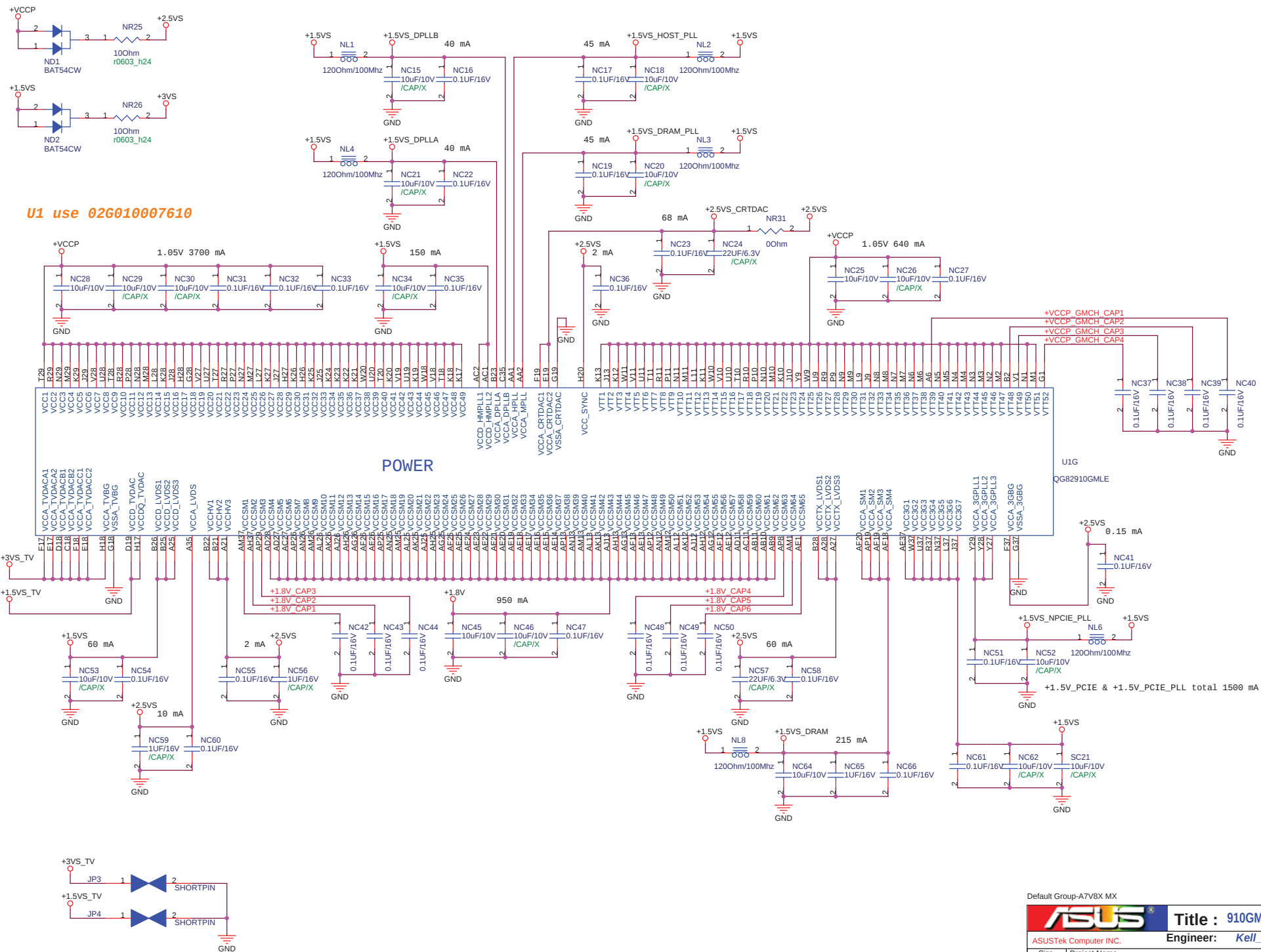
SDVOCTRL_DATA Int PD
0 : No SDVO device
1 : SDVO device present

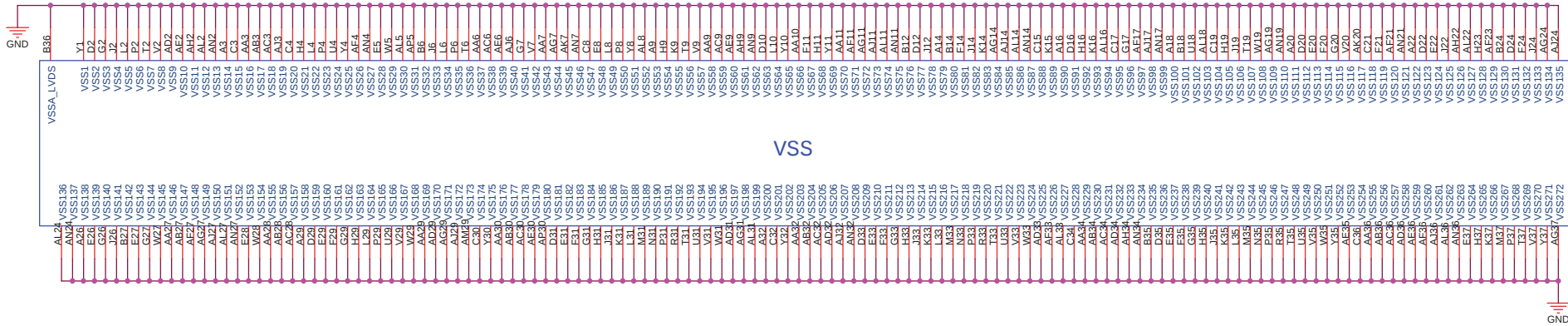
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Default Group-A7V8X MX

ASUS		Title :910GML_VGA_LVDS	
ASUSTek Computer INC.		Engineer: Hauld_Zhou	
Size	Project Name		Rev
A3	900SD_MB		R1.0G
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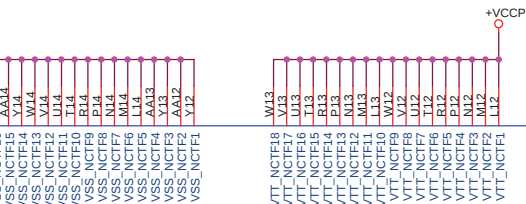




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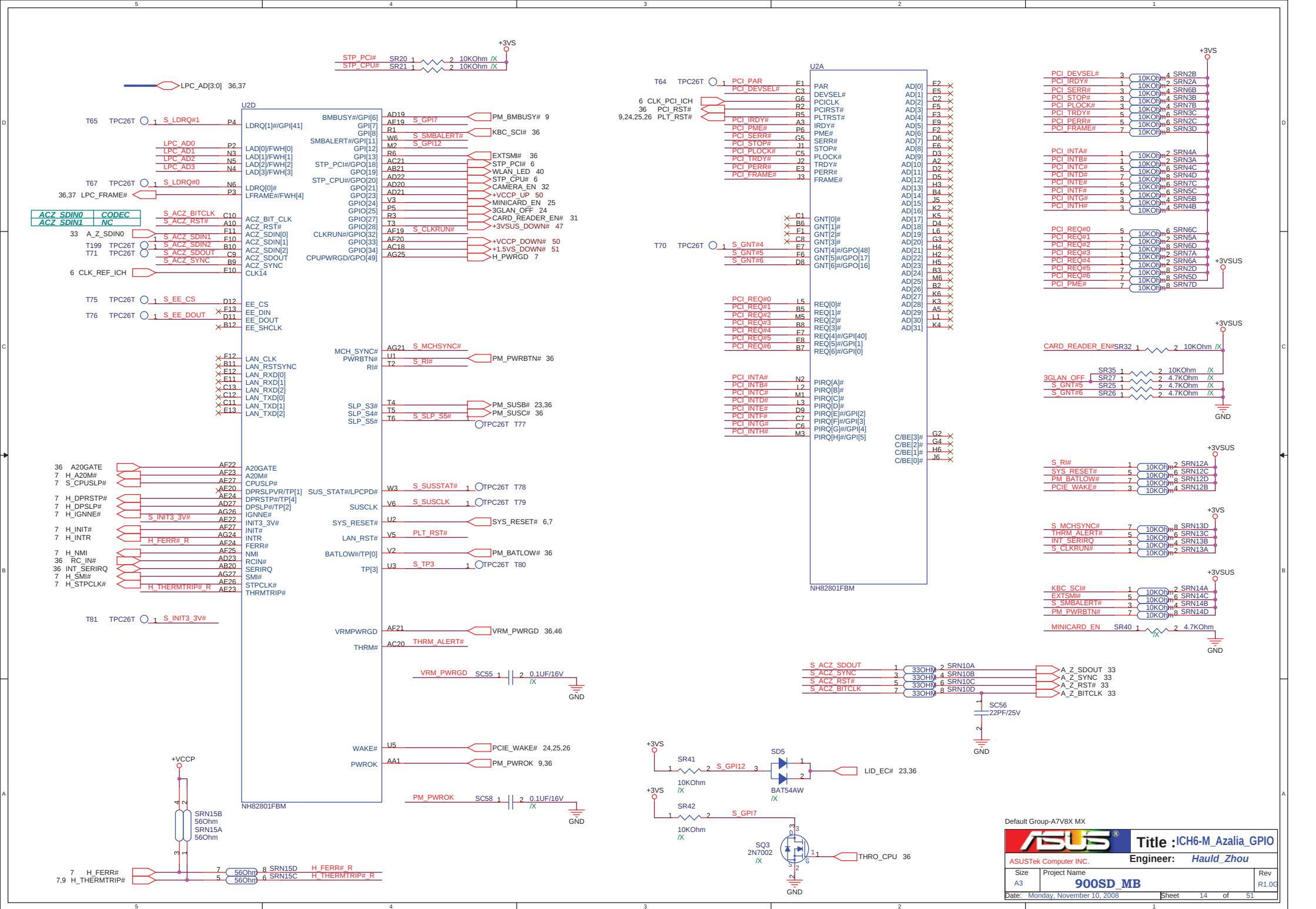
NCTF

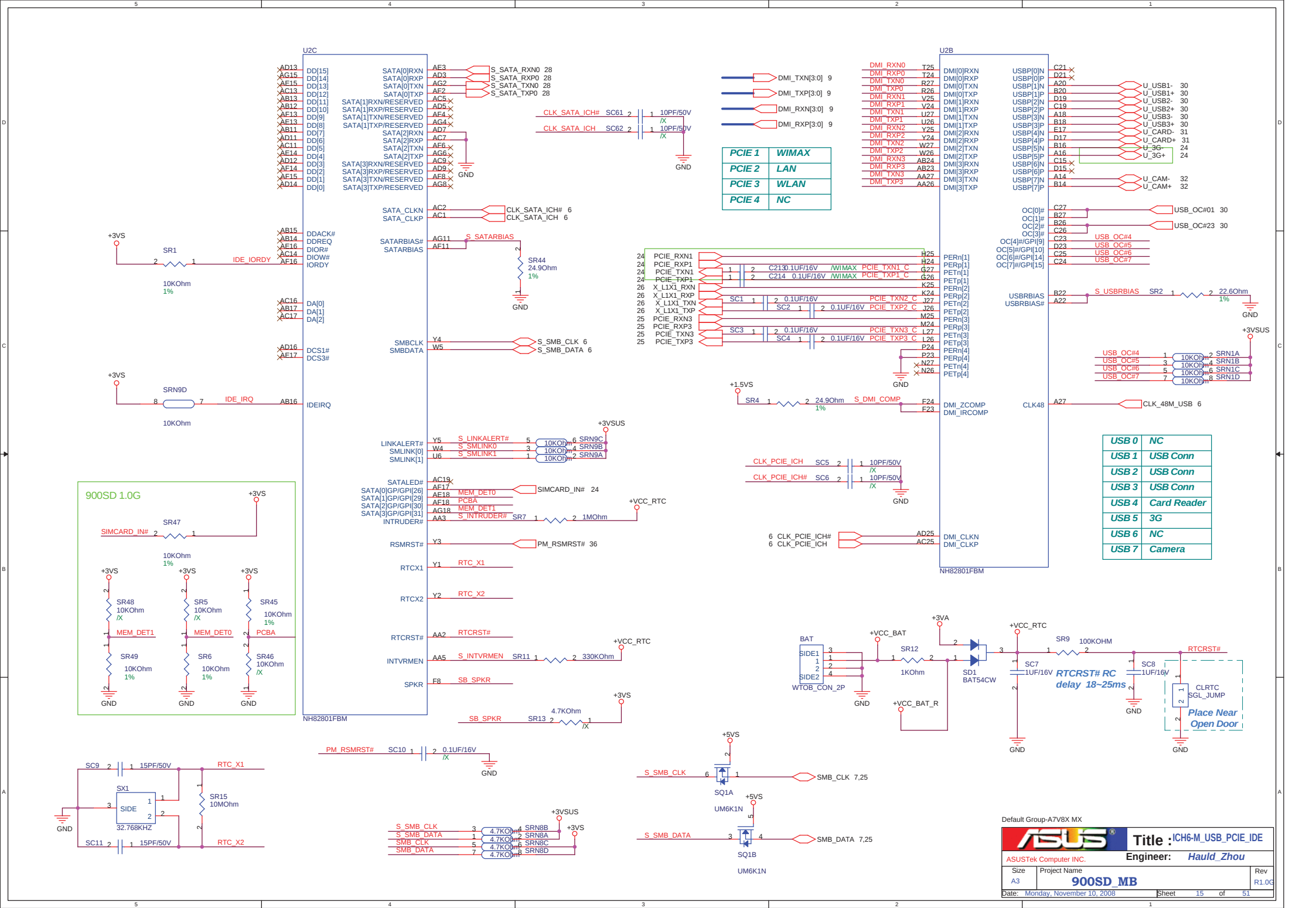
VSS

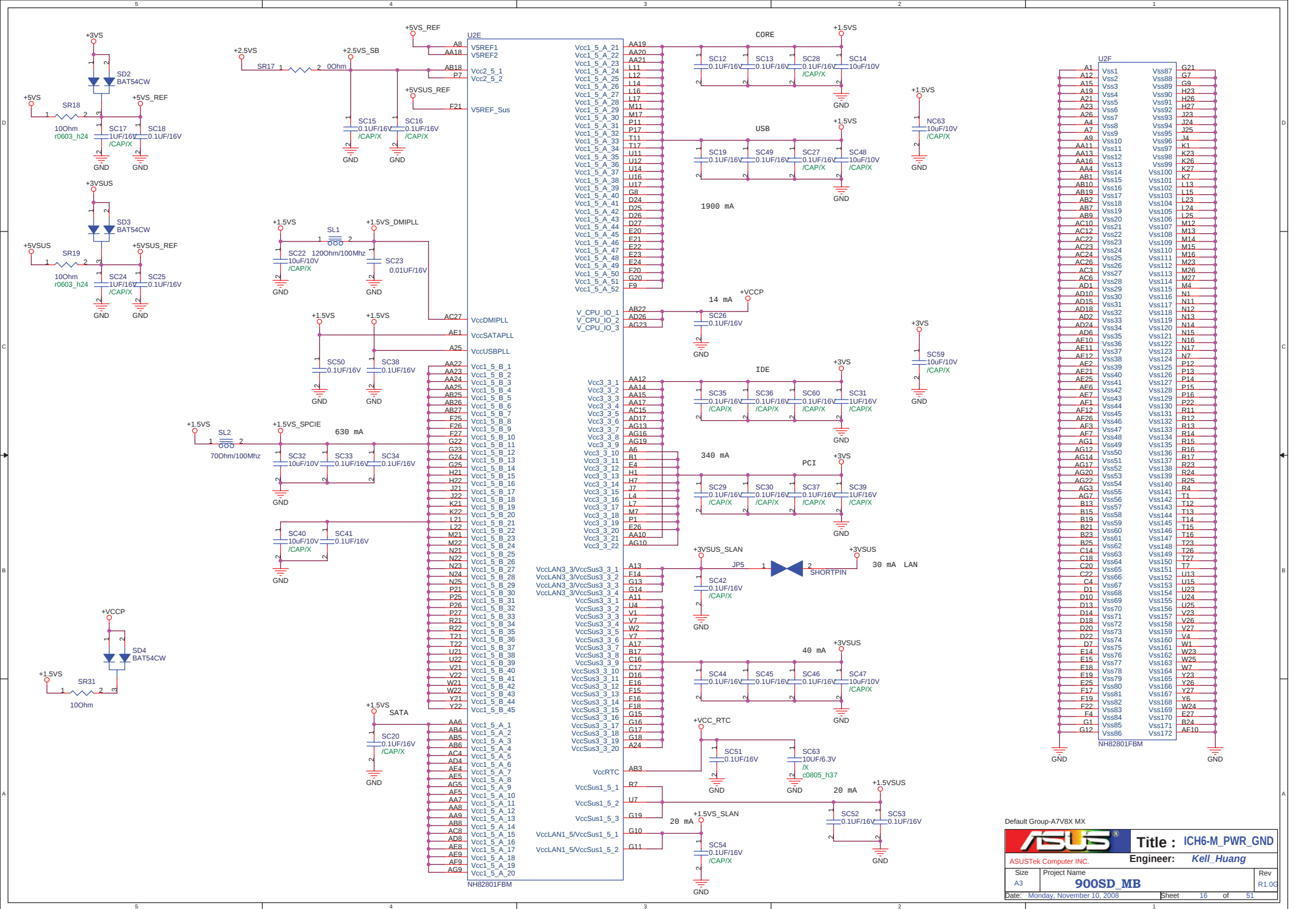


Default Group-A7V8X MX

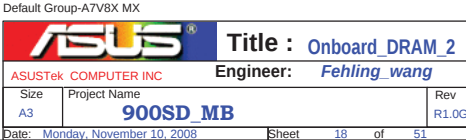
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ASUSTek Computer INC.		Engineer: Kell Huang	
Size	Project Name		Rev
A3	900SD_MB		R1.0G
Date: Monday, November 10, 2008		Sheet	13 of 51

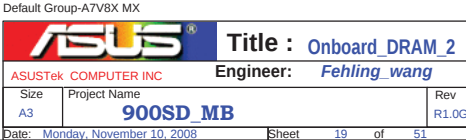












D

D

C

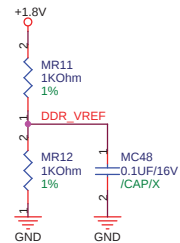
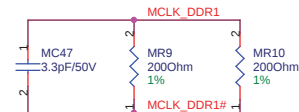
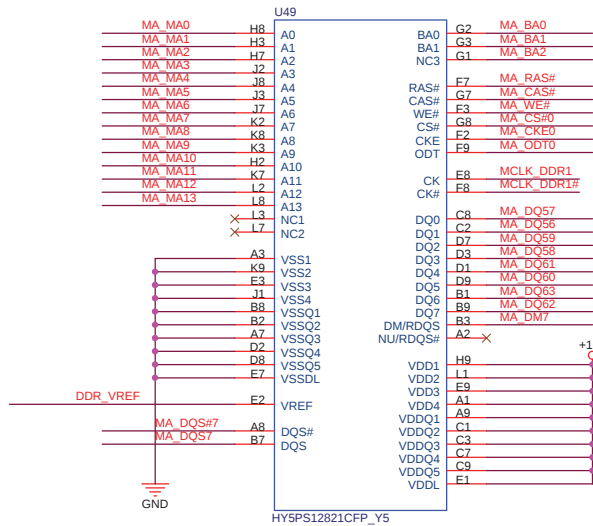
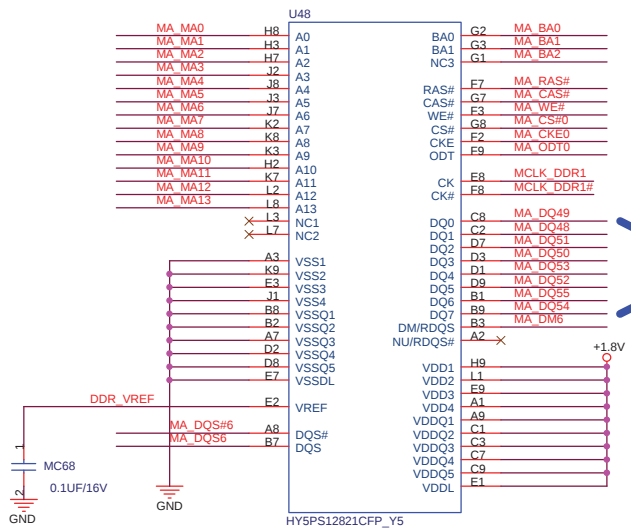
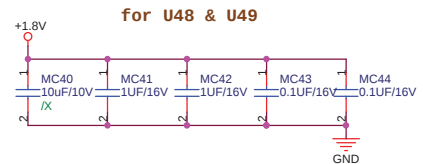
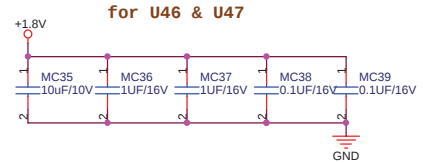
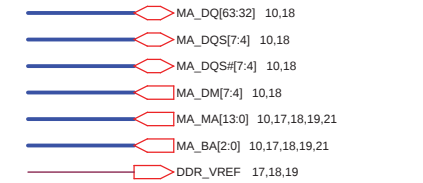
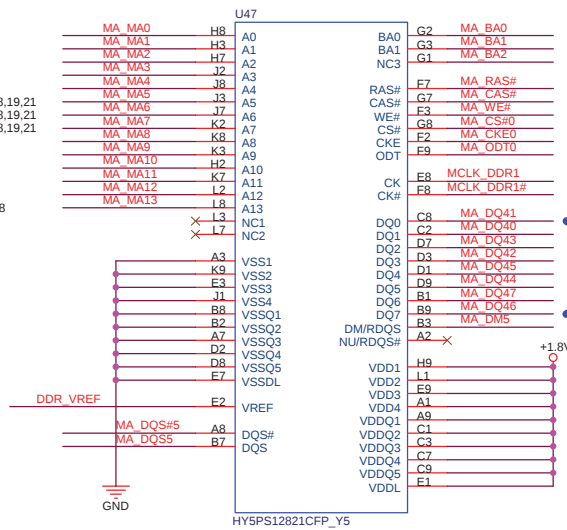
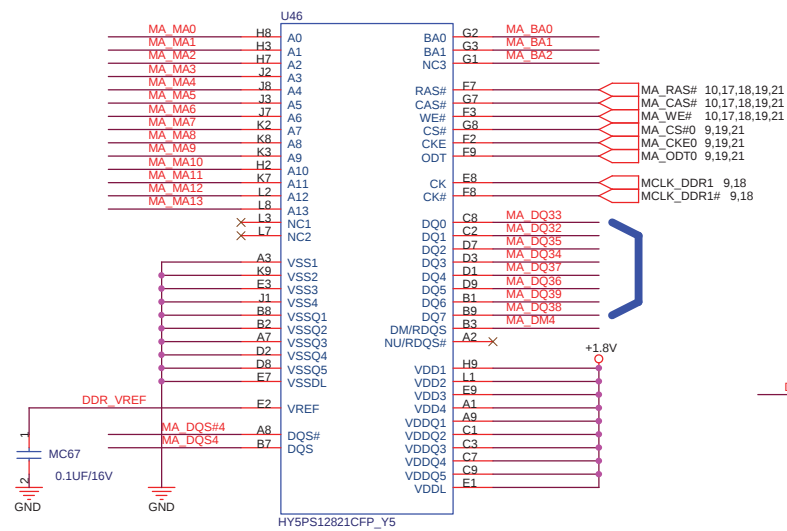
C

B

B

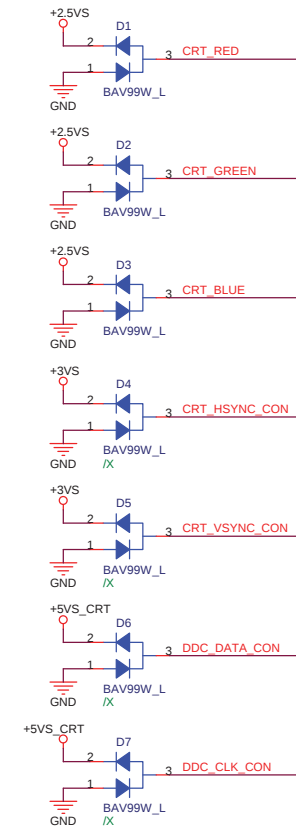
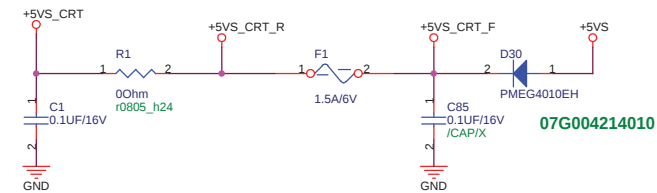
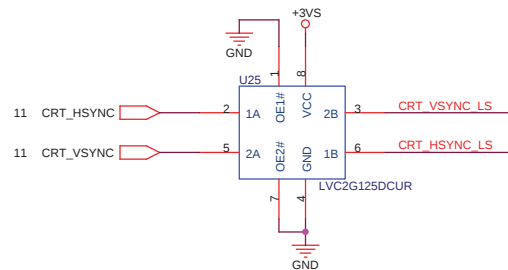
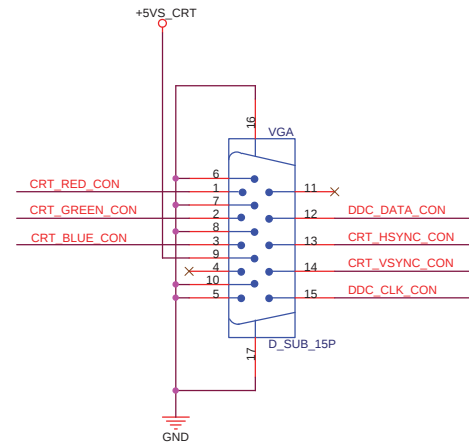
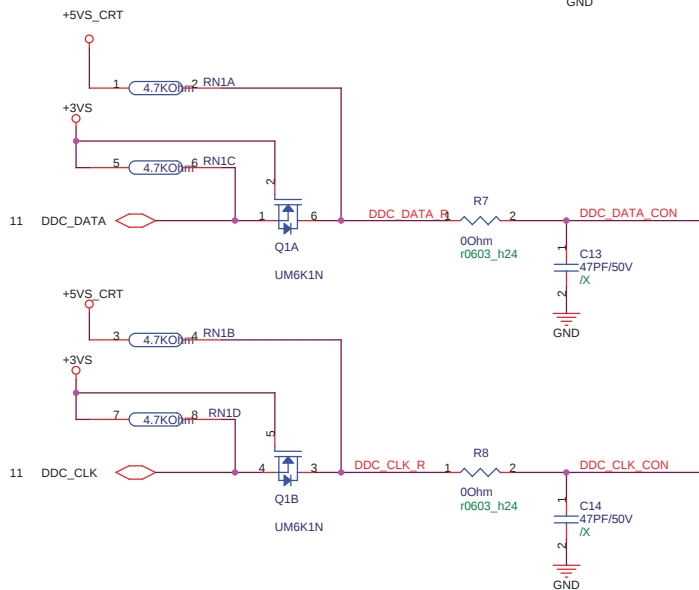
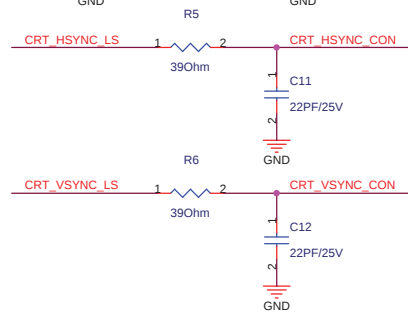
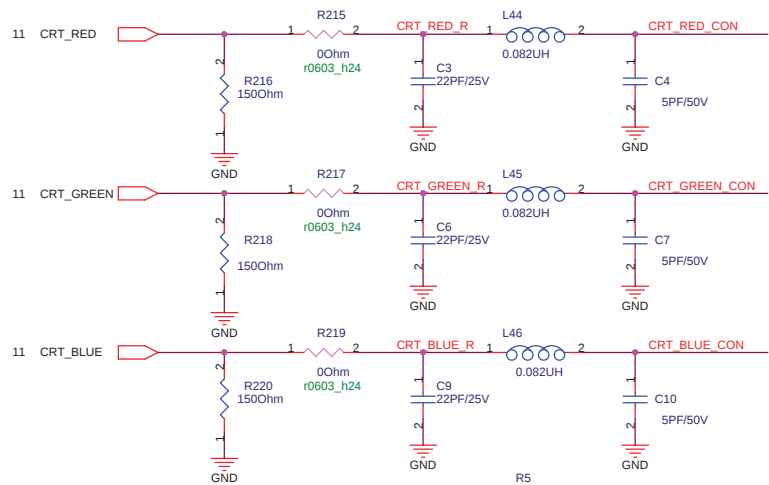
A

A



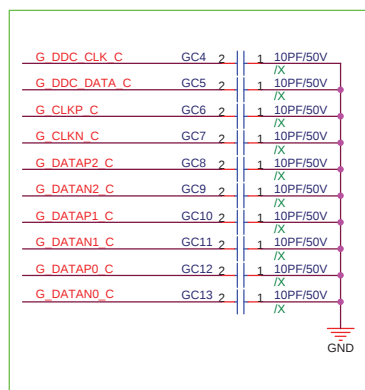
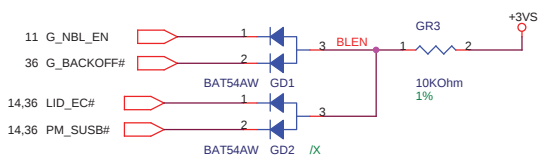
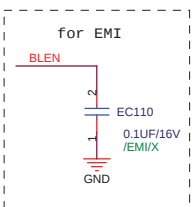
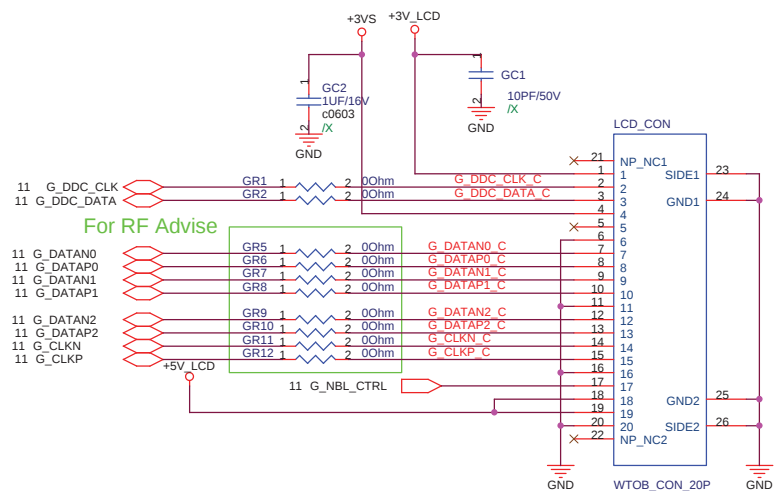
Default Group-A7V8X MX

ASUS		Title : Onboard_DRAM_1	
ASUSTek COMPUTER INC		Engineer: Fehling_wang	
Size	Project Name	Rev	
A3	900SD_MB	R1.0C	
Date: Monday, November 10, 2008		Sheet 20 of 51	

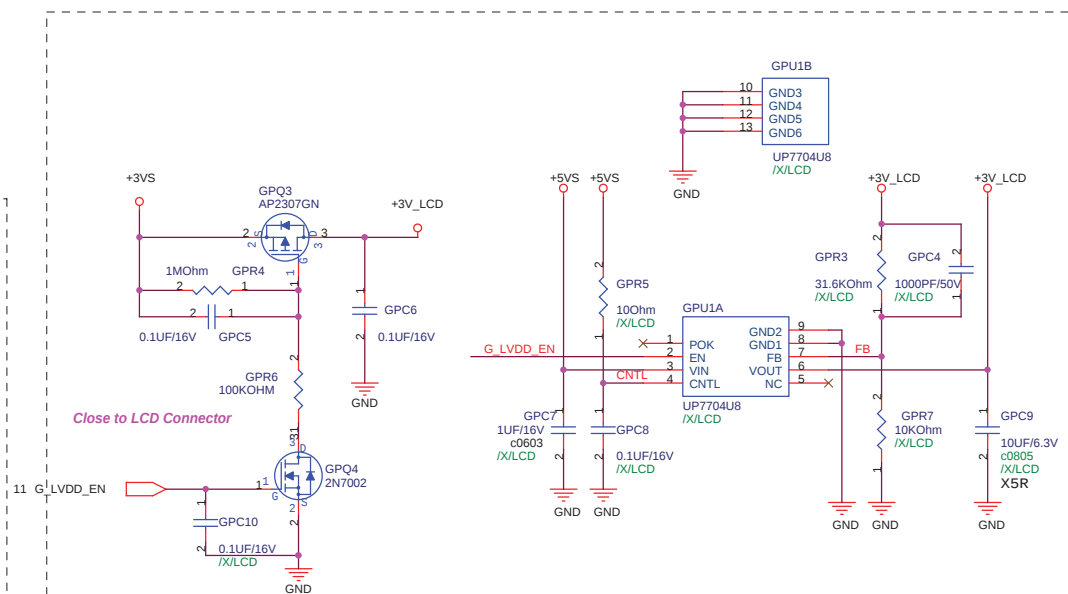
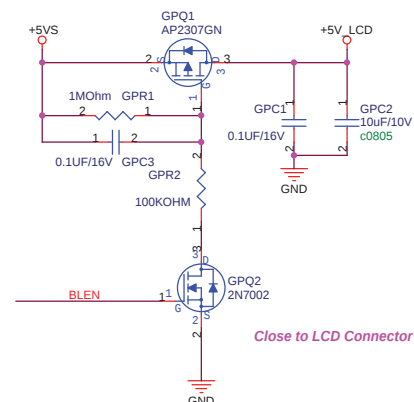
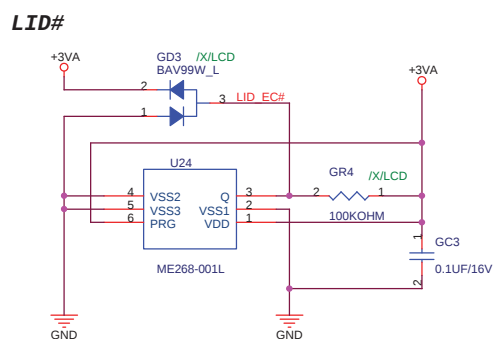


Default Group-A7V8X MX

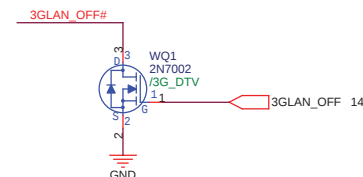
ASUS		Title : Onboard VGA	
ASUSTek Computer INC.		Engineer: Kell_Huang	
Size	Project Name		Rev
A3	900SD_MB		R1.0G
Date: Monday, November 10, 2008		Sheet	22 of 51

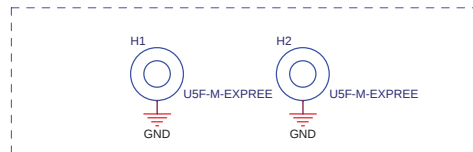
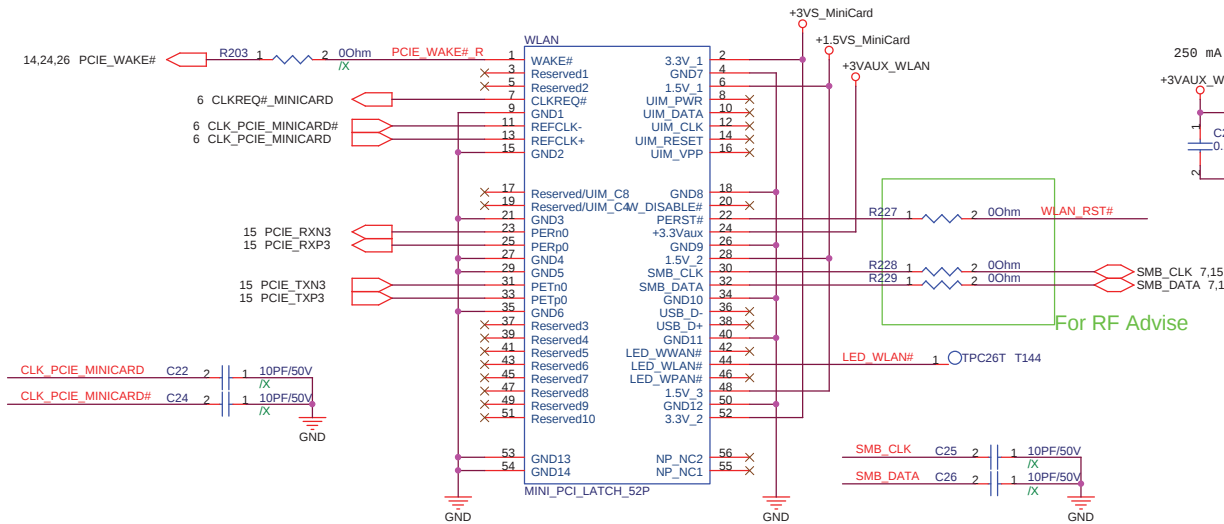


For RF Advise(Close to LCD_CON)

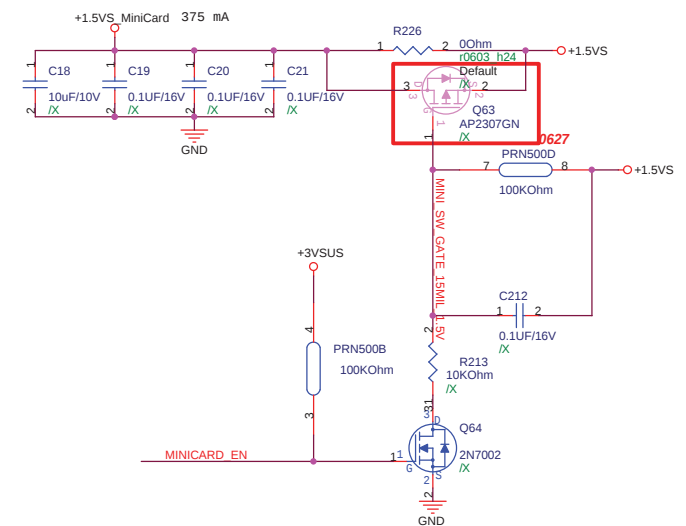
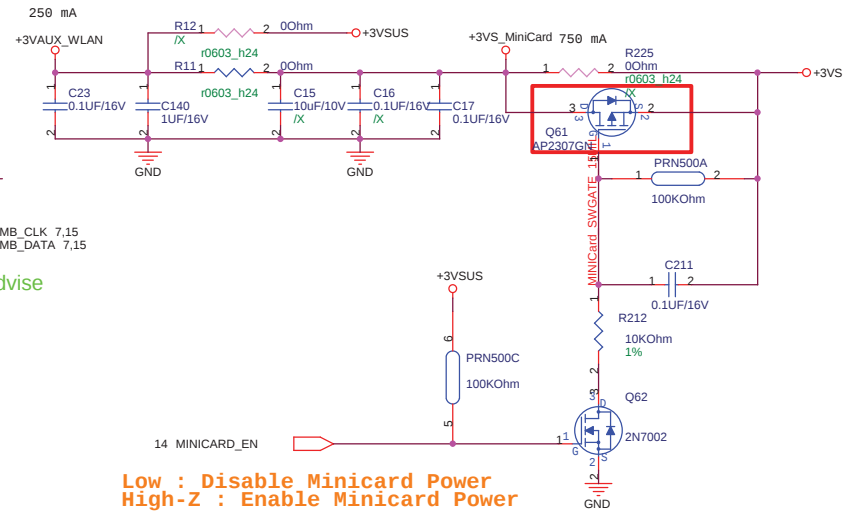
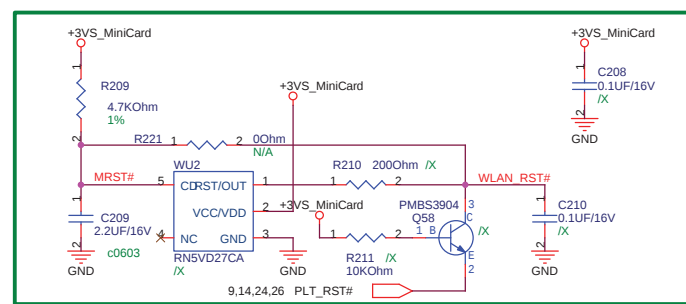
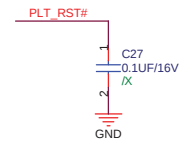


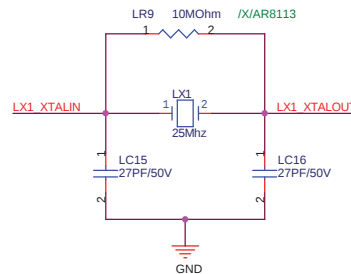
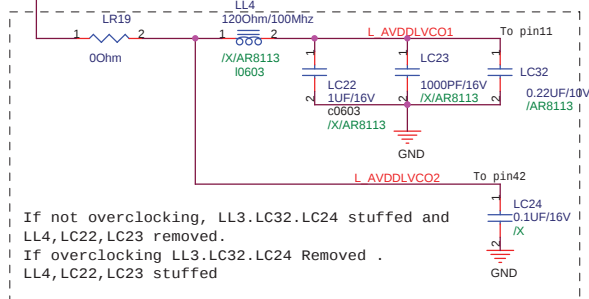
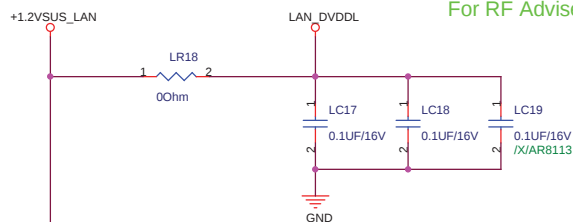
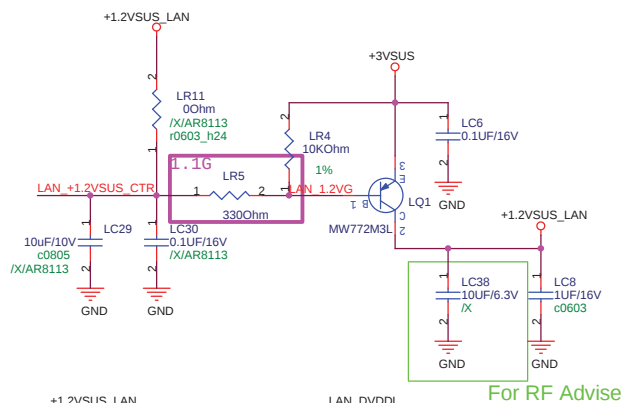
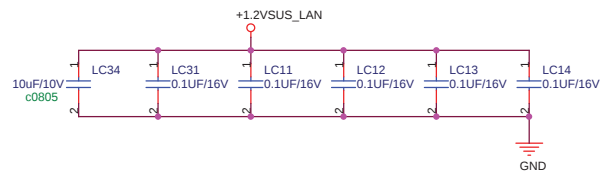
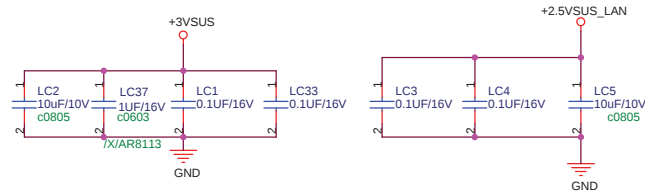
Default Group-A7V8X MX



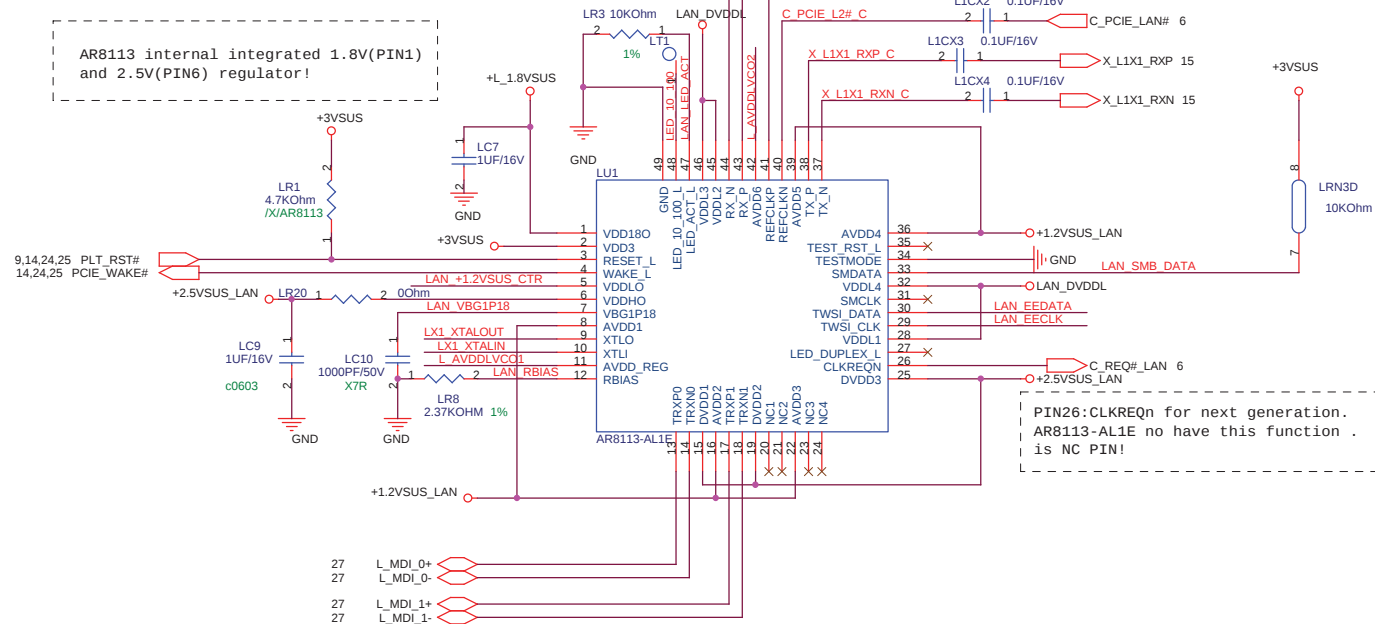


MINI CARD NUT(1.6mm) *2

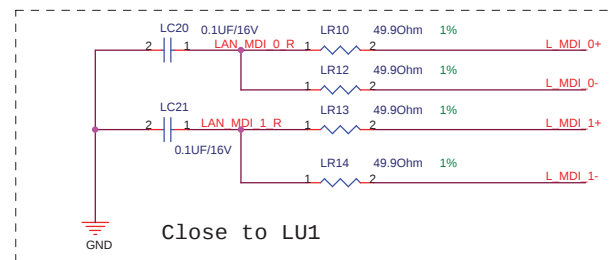




AR8113 internal integrated 1.8V(PIN1) and 2.5V(PIN6) regulator!



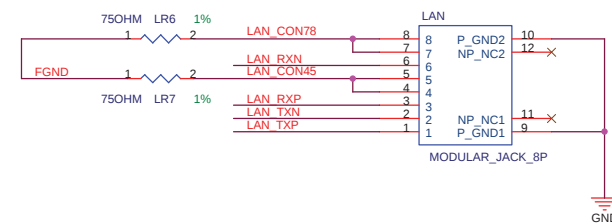
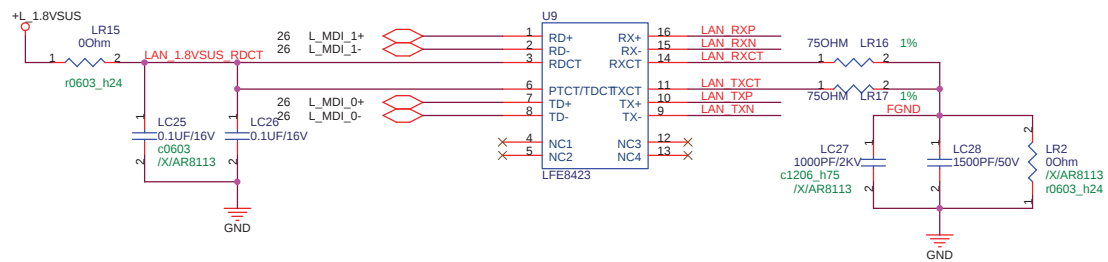
PIN26:CLKREQn for next generation.
AR8113-AL1E no have this function .
is NC PIN!

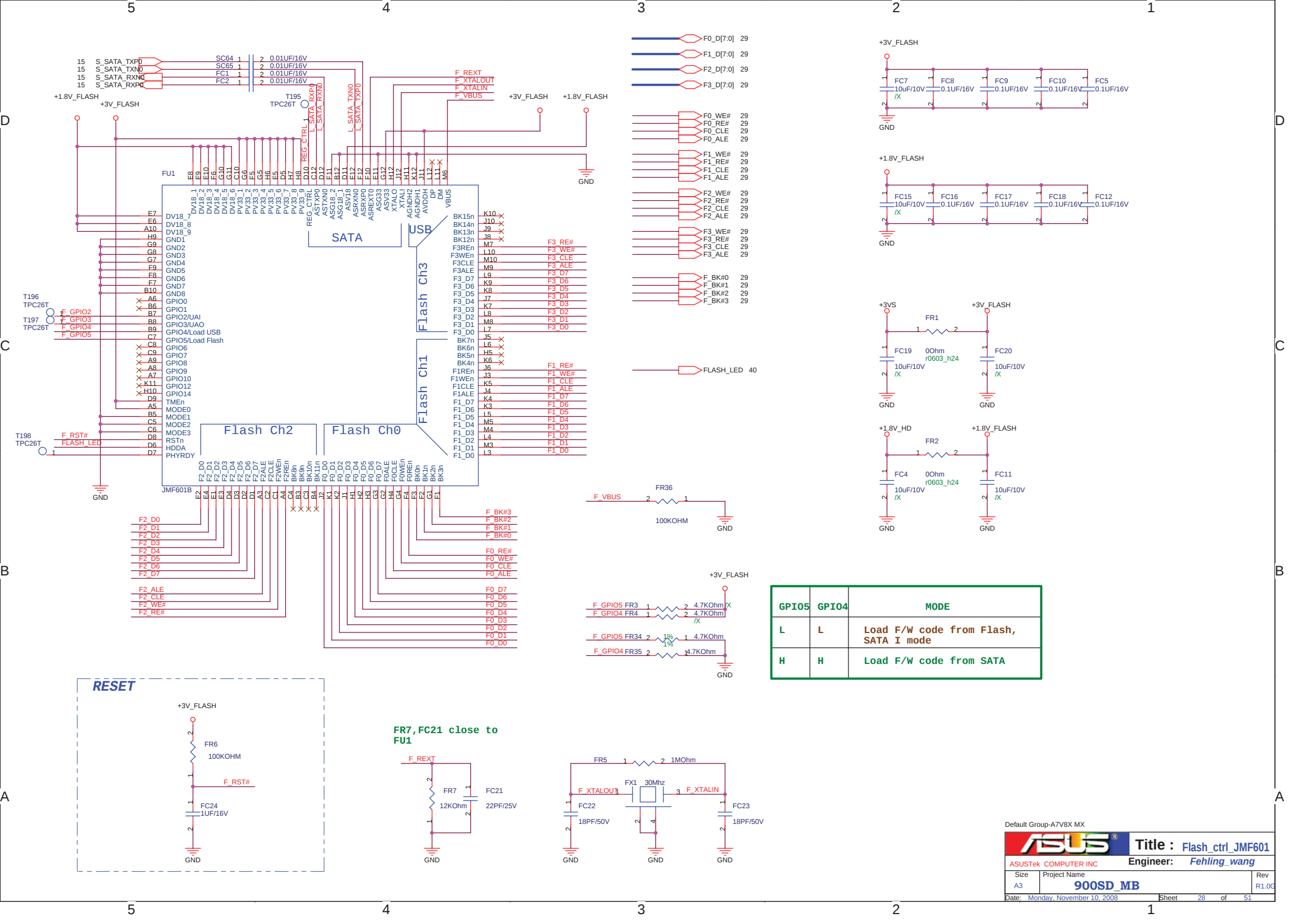


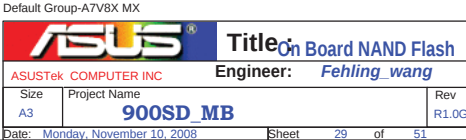
Default Group-A7V8X MX

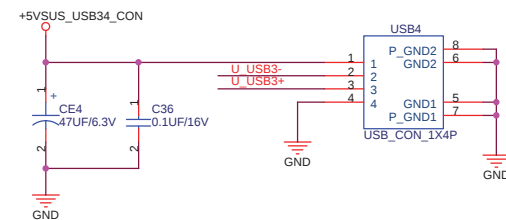
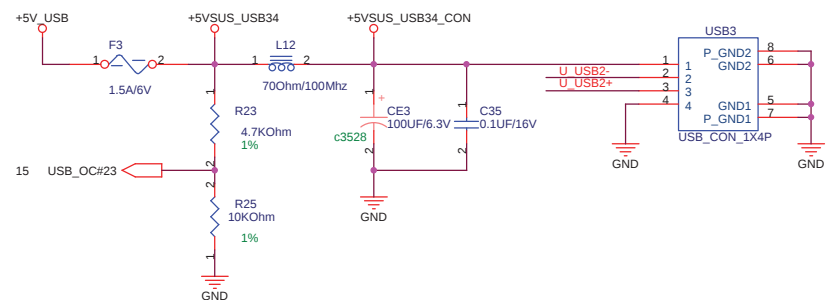
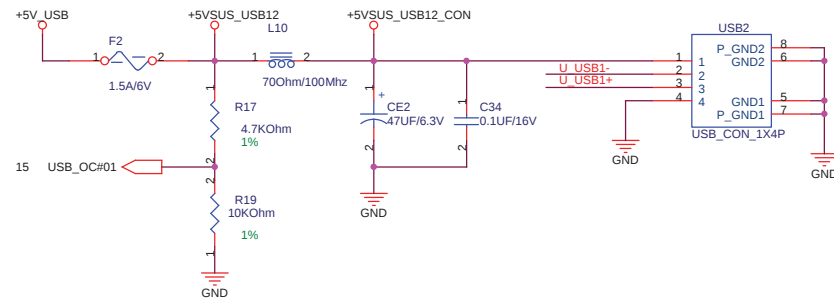
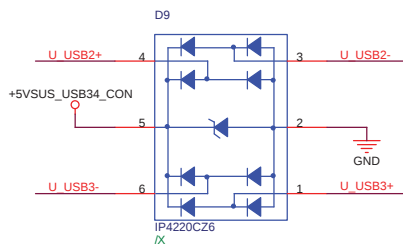
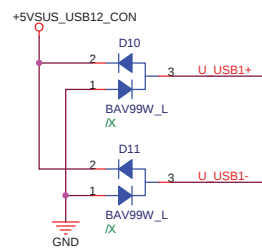
ASUS		Title : LAN_8113	
ASUSTek Computer INC.		Engineer: Hauld_Zhou	
Size A3	Project Name 900SD_MB	Rev R1.0G	
Date: Monday, November 10, 2008		Sheet 26	of 51

DELETE MODEM



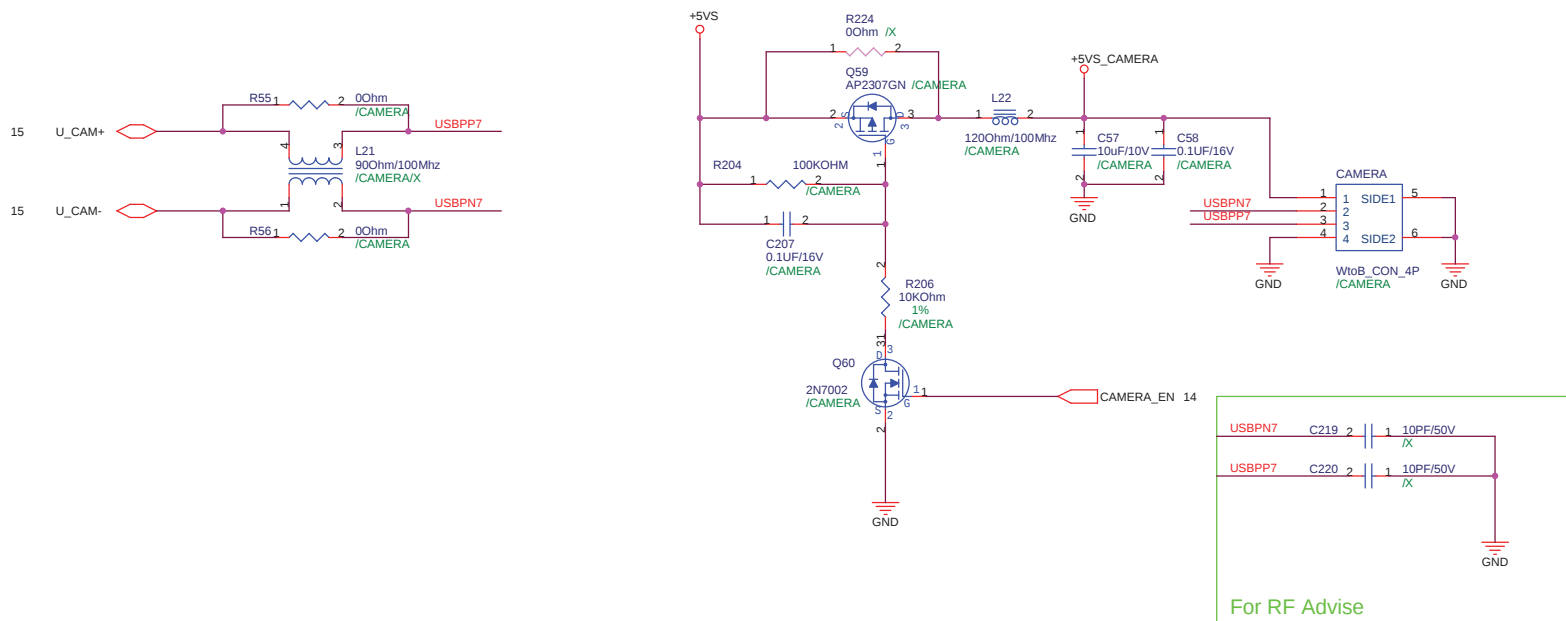






Default Group-A7V8X MX

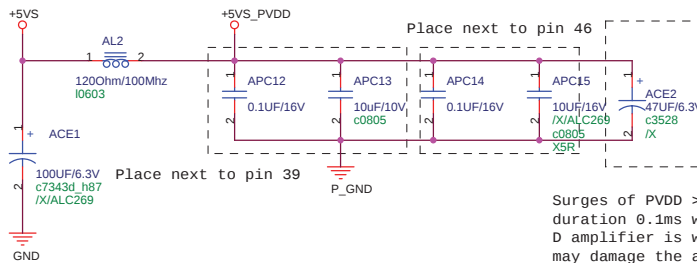
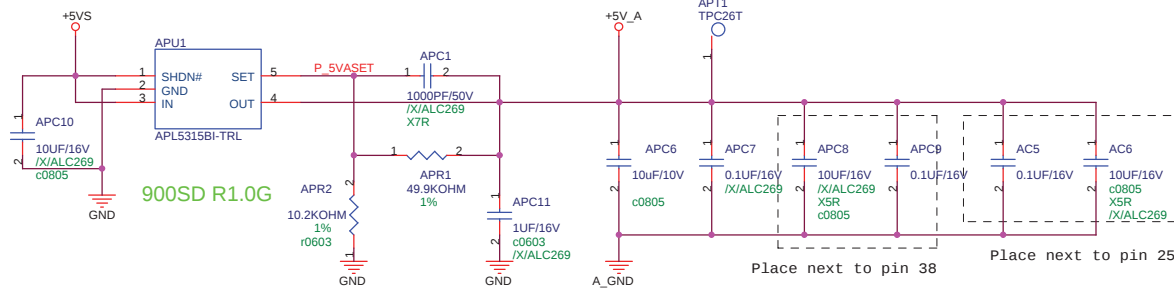
ASUS		Title : USB Port	
ASUSTek Computer INC.		Engineer: <i>Kell Huang</i>	
Size A3	Project Name 900SD_MB	Rev R1.0G	
Date: Monday, November 10, 2008		Sheet	30 of 51



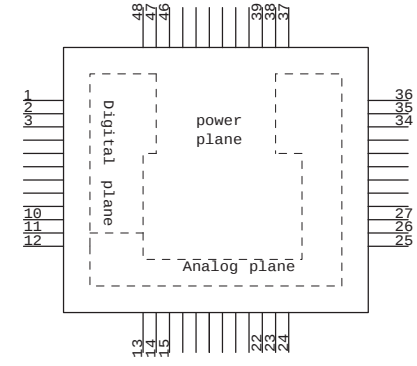
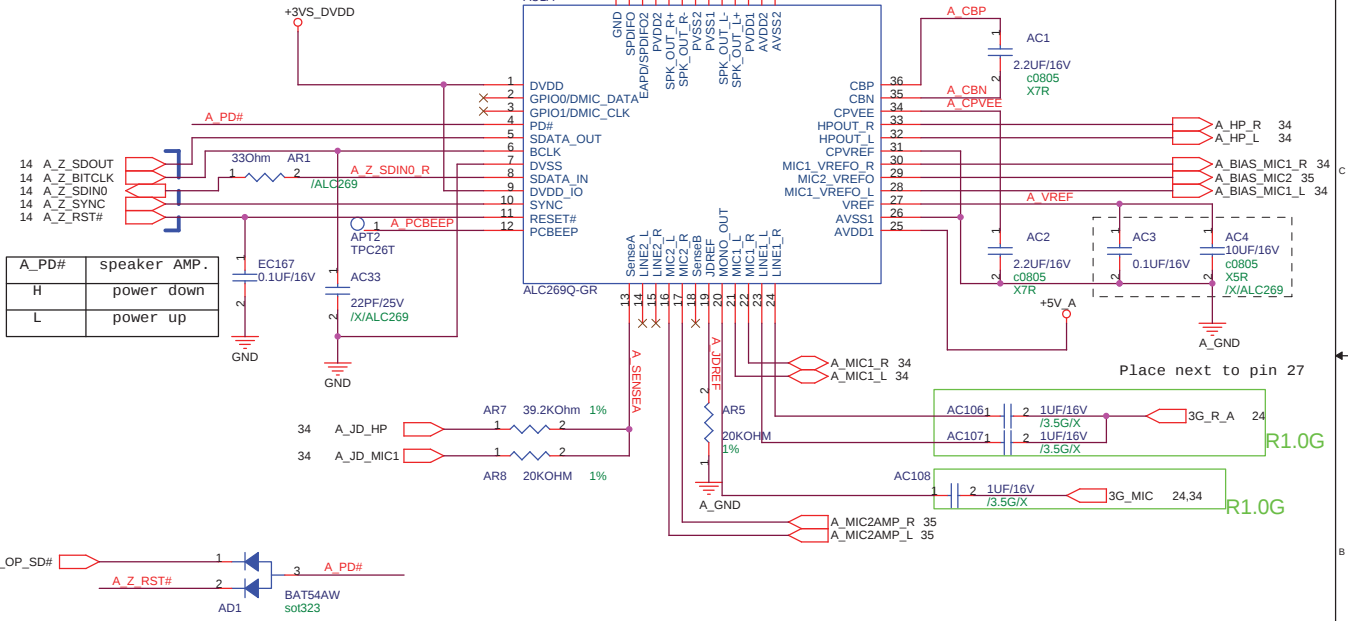
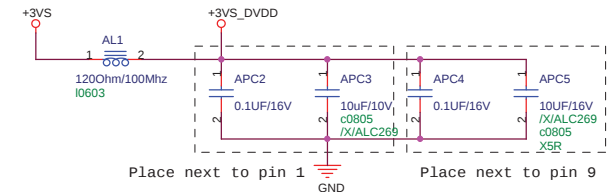
Default Group-A7V8X MX

ASUS		Title : Camera Conn	
ASUSTek Computer INC.		Engineer: <i>Kell Huang</i>	
Size A3	Project Name 900SD_MB	Rev R1.0G	
Date: Monday, November 10, 2008		Sheet	32 of 51

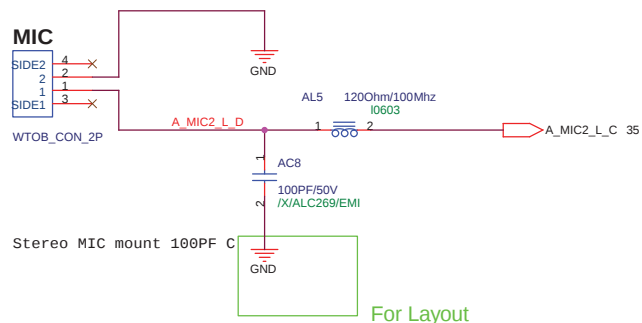
$$V_{out} = 0.8 * (1 + (49.9K / 10.2K))$$



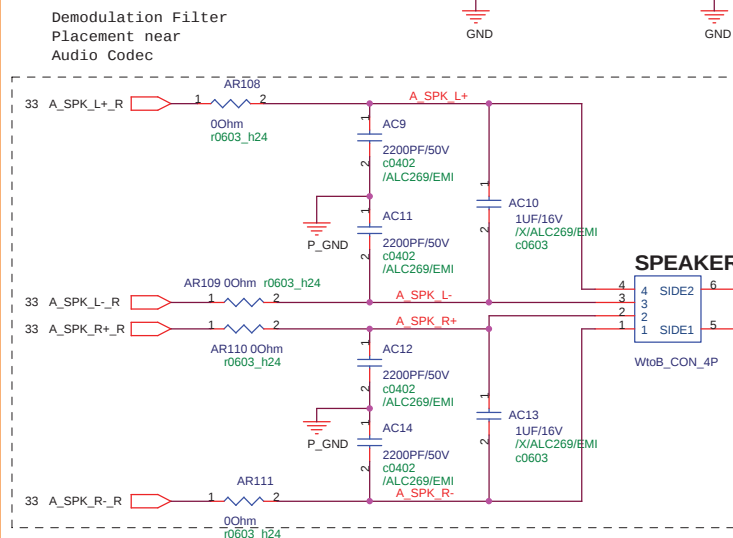
Surges of PVDD >7V duration 0.1ms when class D amplifier is working may damage the amplifier, 10uF tantalum capacitors are required at PVDD1 and PVDD2 to upress the surge.



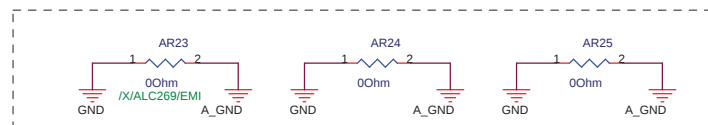
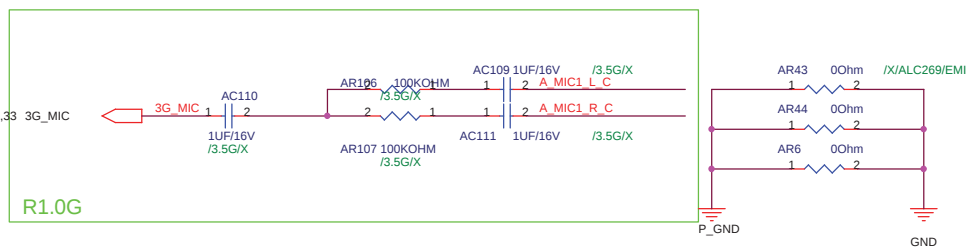
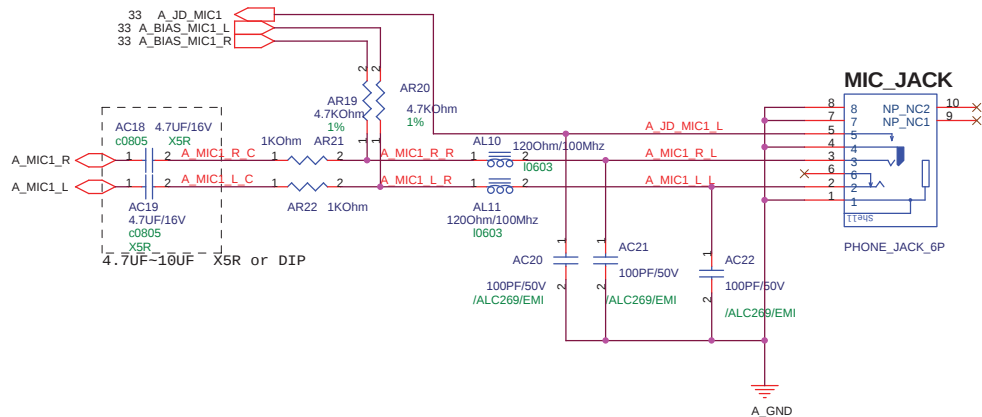
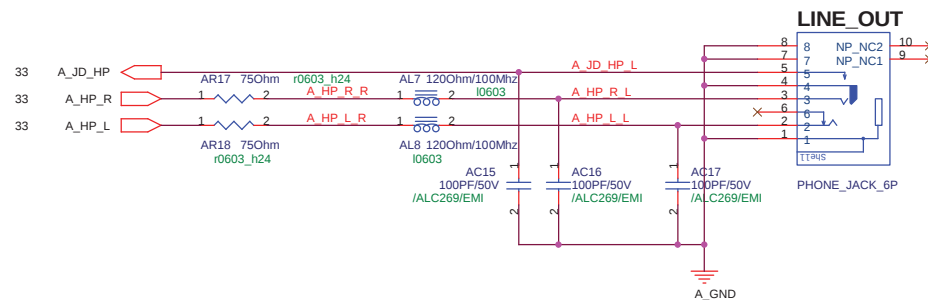
Internal MIC



SPEAKER

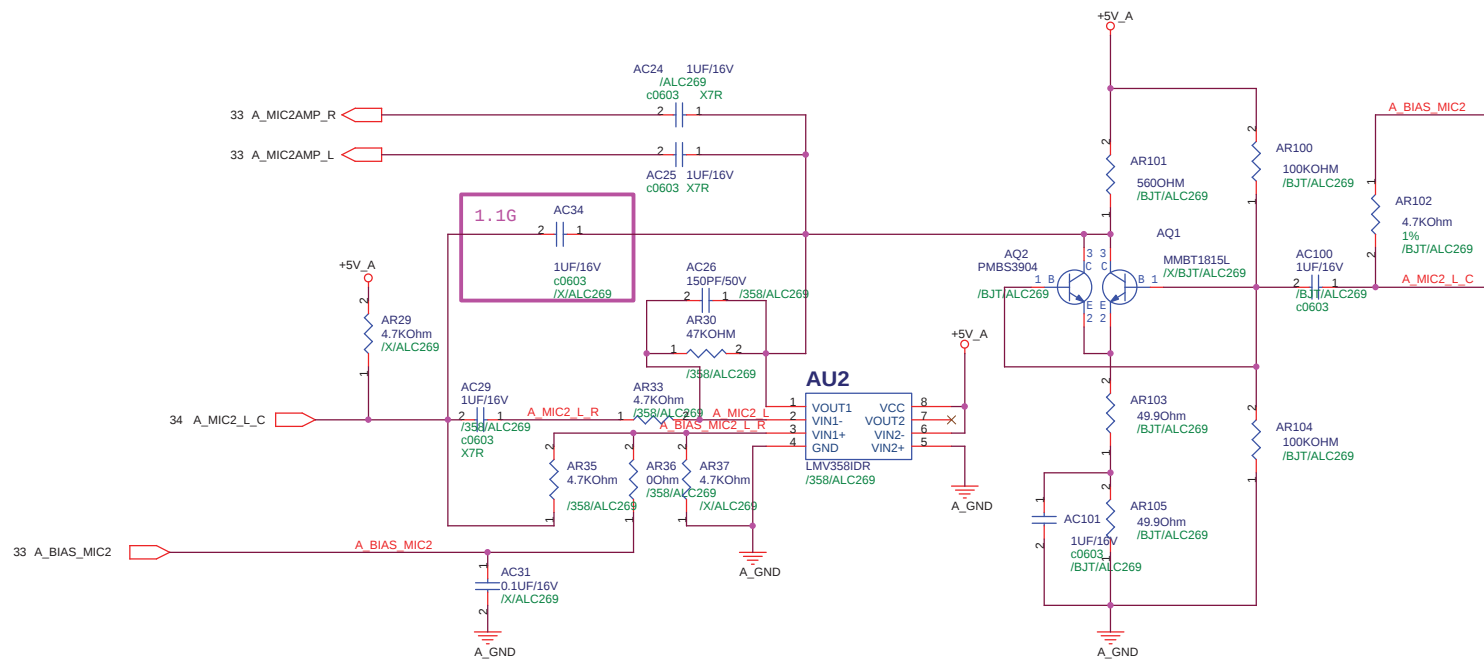


HP



Default Group-A7V8X MX

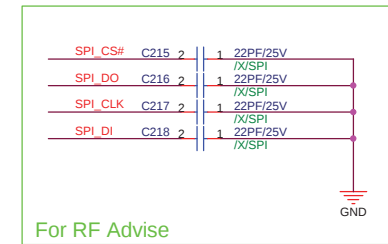
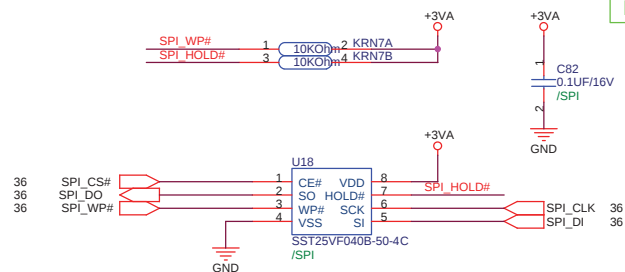
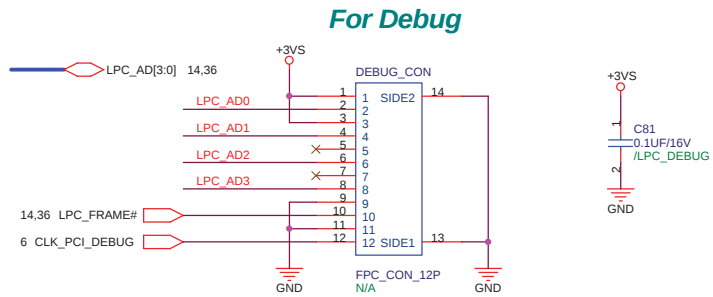
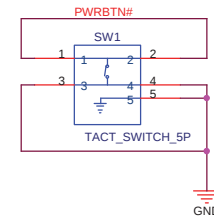
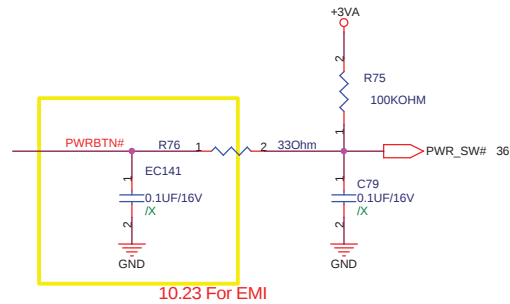
ASUS		Title : ALC269-2(I/O)	
ASUSTek Computer INC.		Engineer: Jeff Li	
Size A3	Project Name 900SD_MB	Rev R1.0G	
Date: Monday, November 10, 2008	Sheet 34 of 51		



Default Group-A7V8X MX

ASUS		Title : ALC269-3(MIC AMP)	
ASUSTek Computer INC.		Engineer: Jeff Li	
Size	Project Name		Rev
A3	900SD_MB		R1.0G
Date: Monday, November 10, 2008		Sheet	35 of 51

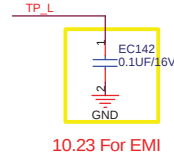
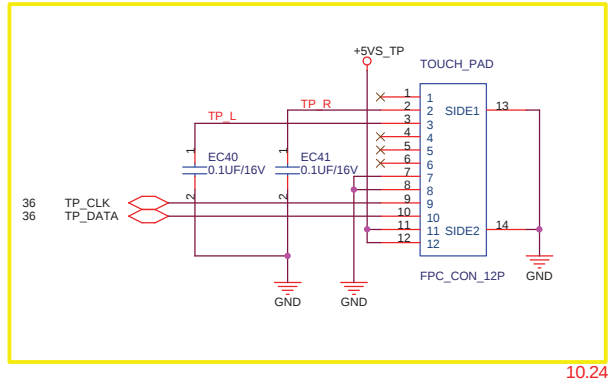
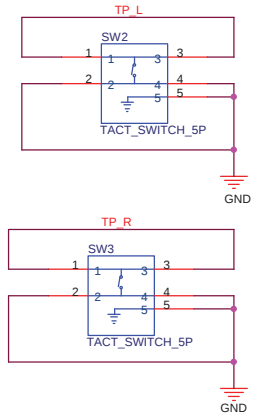




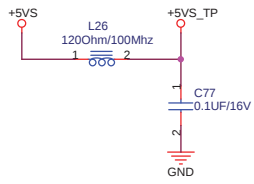
Default Group-A7V8X MX

ASUS		Title : Switch_SPI ROM	
ASUSTek Computer INC.		Engineer: Kell_Huang	
Size	Project Name		Rev
A3	900SD_MB		R1.0G
Date: Monday, November 10, 2008		Sheet	37 of 51

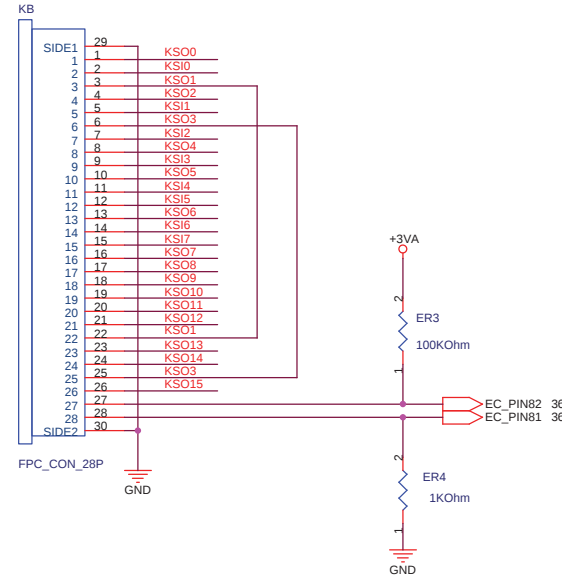
For Touch-Pad



For Keyboard

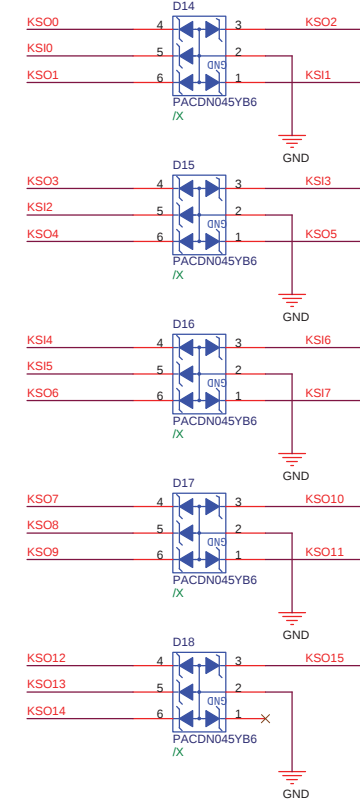


KSO15 1 TPC26T T188
KSI0 1 TPC26T T189
KSO3 1 TPC26T T190

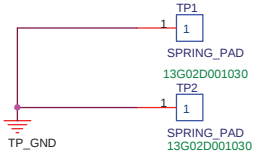
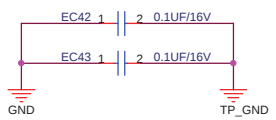
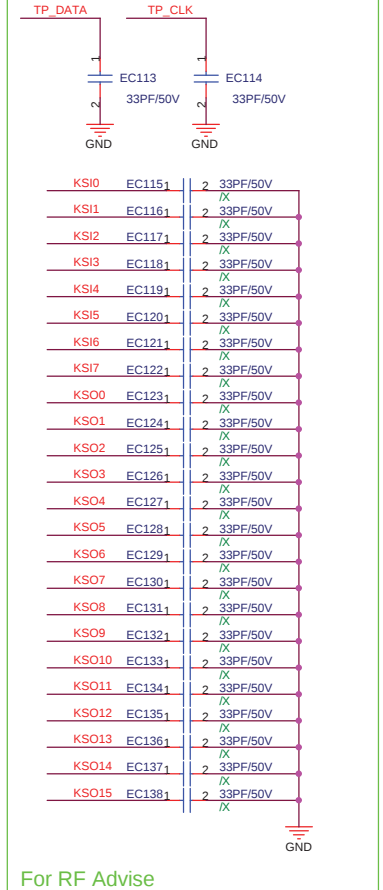


KSO[15:0] 36

KSI[7:0] 36

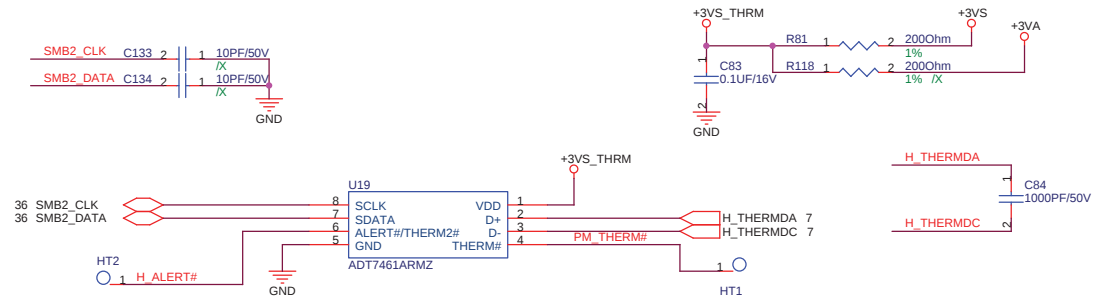


For RF Advise

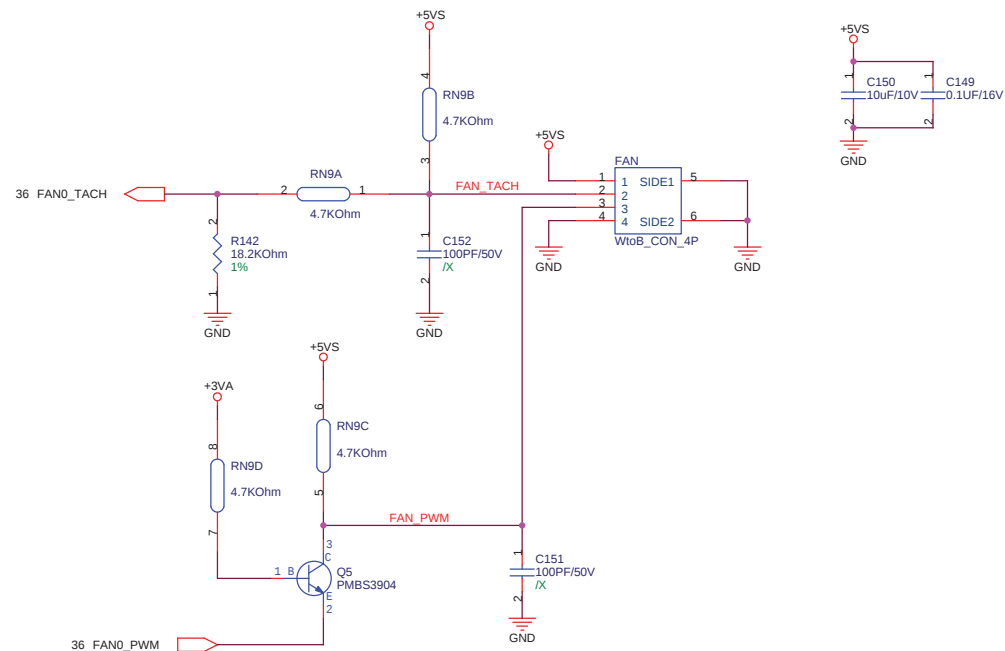


Default Group-A7V8X MX

ASUS		Title : KB_Touch Pad	
ASUSTek Computer INC.		Engineer: Kell_Huang	
Size	Project Name	Rev	
A3	900SD_MB	R1.0G	
Date: Monday, November 10, 2008		Sheet	38 of 51



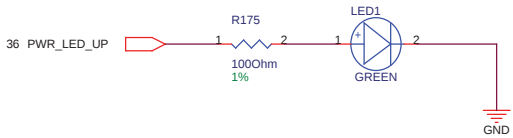
U19 use 06G023048021



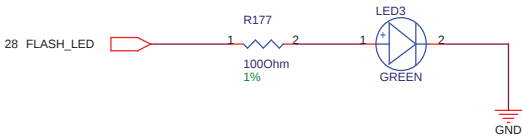
Default Group-A7V8X MX

ASUS		Title : Thermal Sensor_FAN	
ASUSTek Computer INC.		Engineer: <i>Kell Huang</i>	
Size	Project Name		Rev
A3	900SD_MB		R1.0G
Date: Monday, November 10, 2008		Sheet	39 of 51

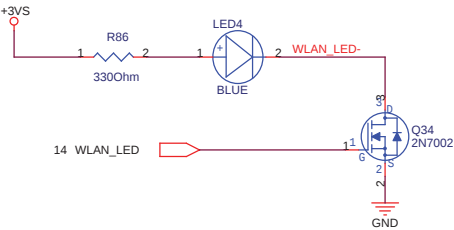
for POWER LED



for FLASH LED

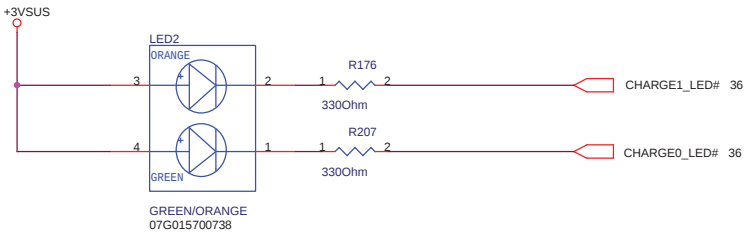


for WLAN LED



R1.0G

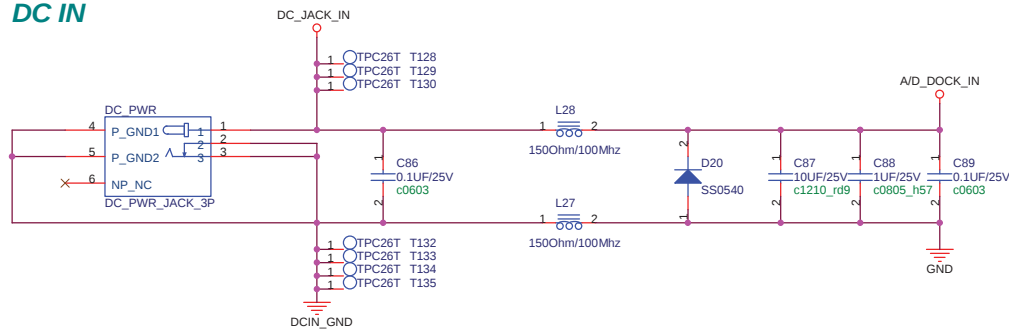
for CHARGE LED



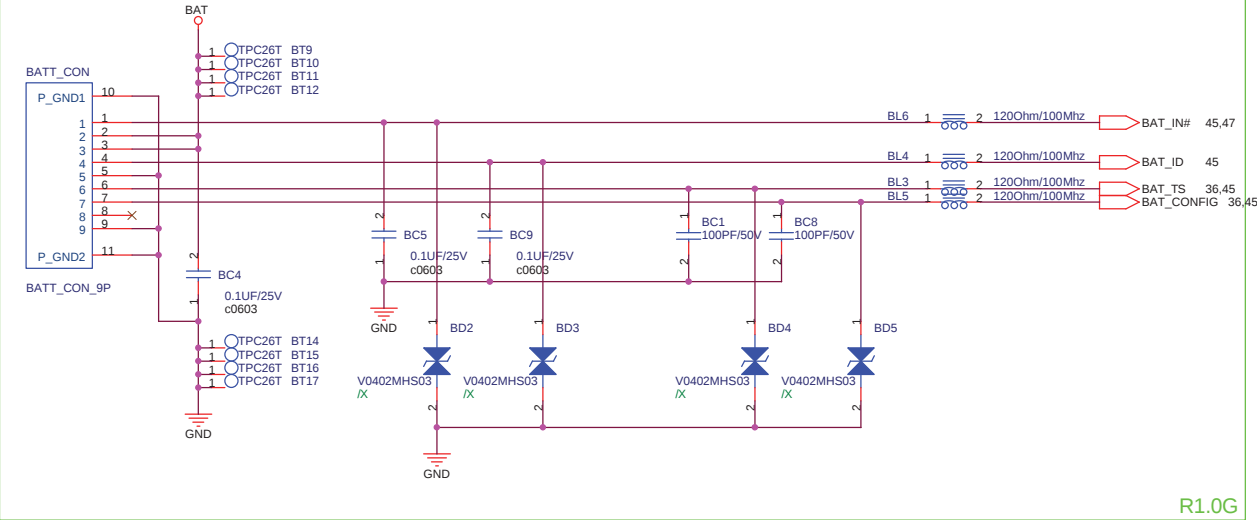
Default Group-A7V8X MX

ASUS		Title : LED	
ASUSTek Computer INC.		Engineer: Hauld_Zhou	
Size	Project Name		Rev
A3	900SD_MB		R1.0G
Date: Monday, November 10, 2008		Sheet	40 of 51

DC IN



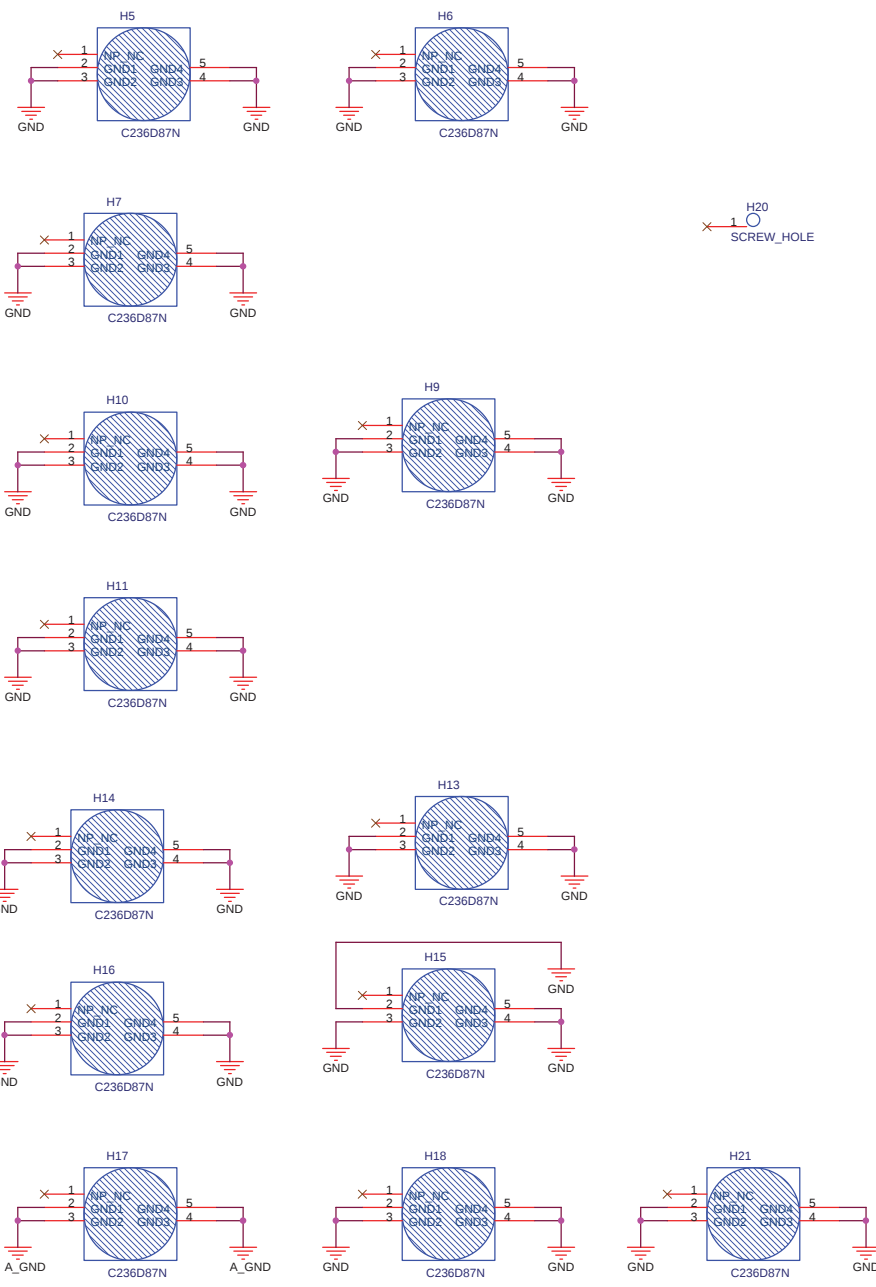
BAT IN

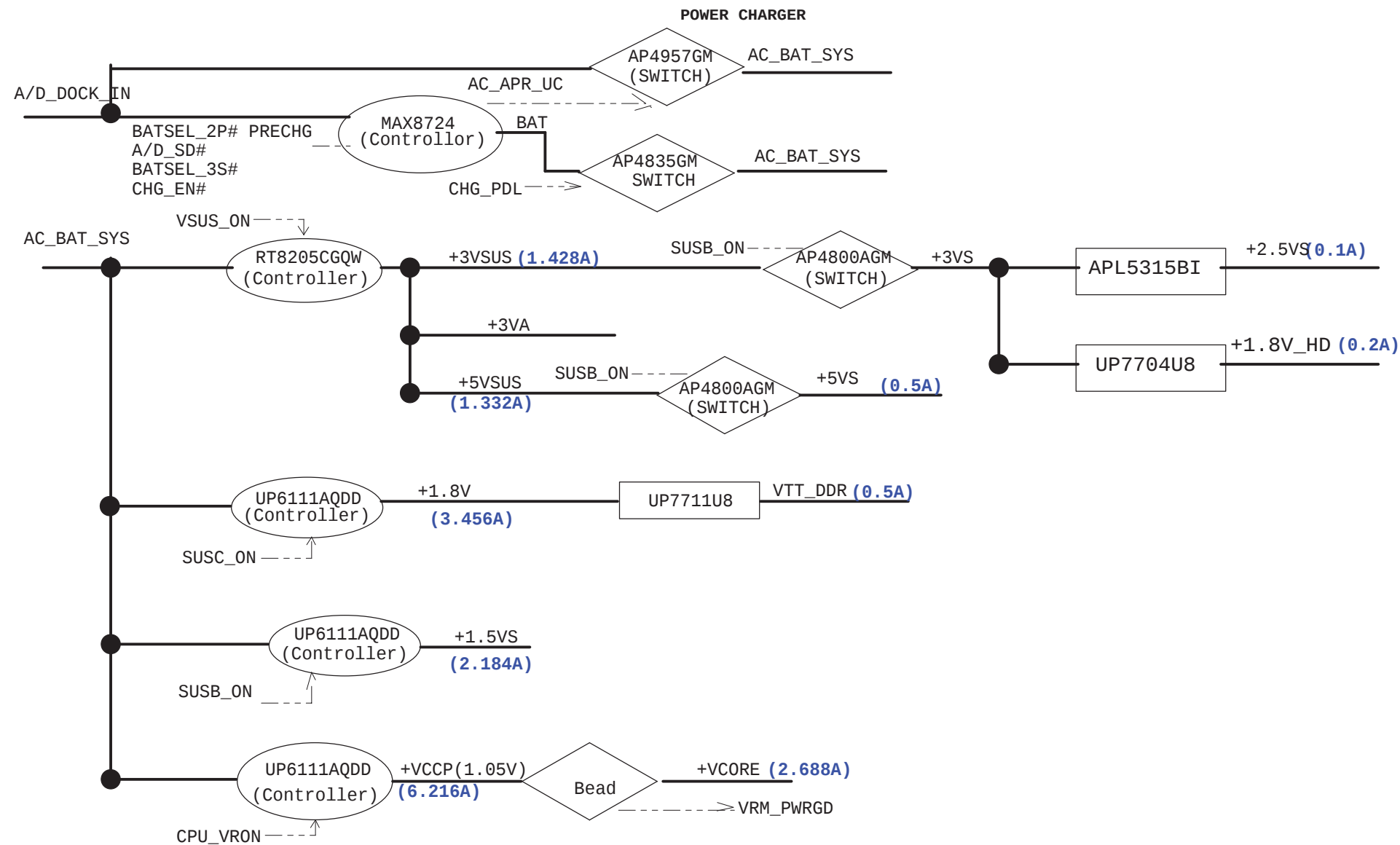


R1.0G

Default Group-A7V8X MX

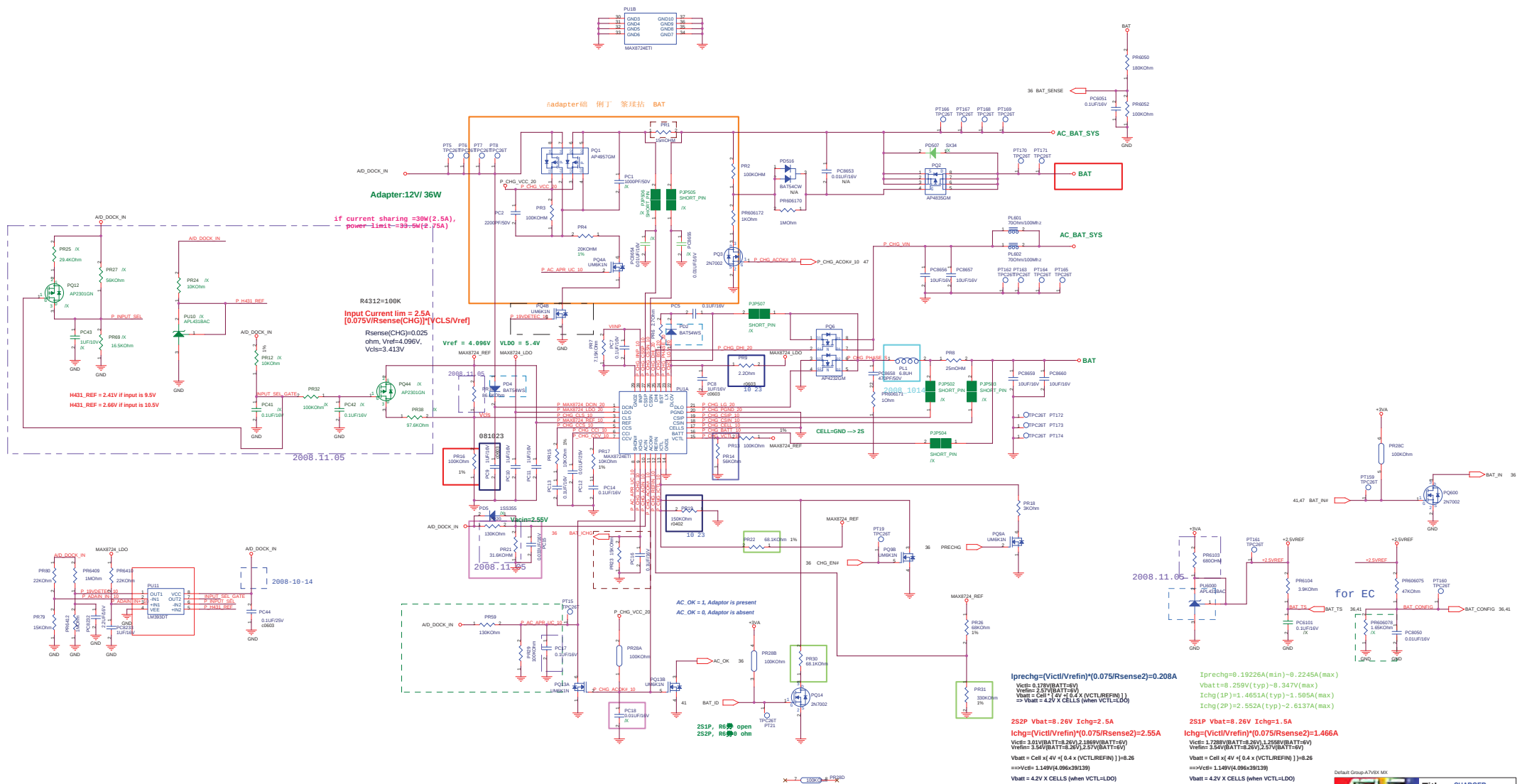
		Title : PWR Jack	
ASUSTek Computer INC.		Engineer: <i>Kell_Huang</i>	
Size A3	Project Name 900SD_MB		Rev R1.0G
Date: Monday, November 10, 2008	Sheet	41	of 51



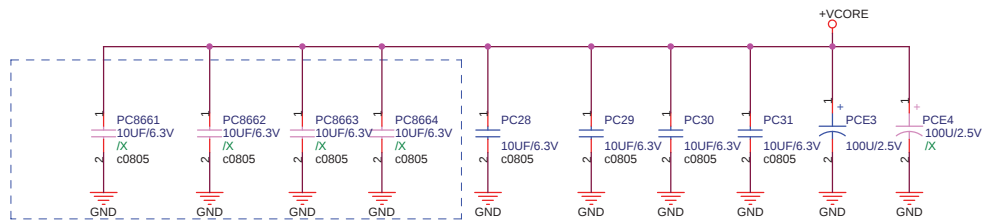


Default Group-A7V8X MX

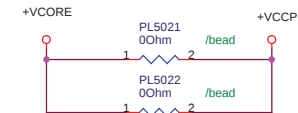
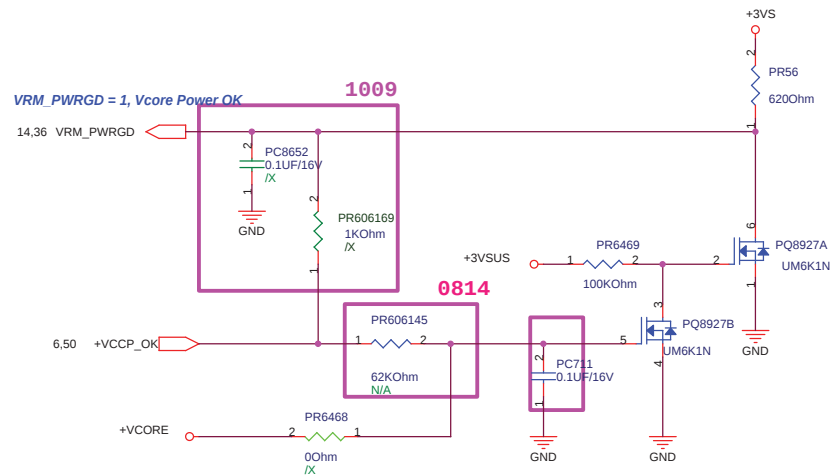
ASUS		Title : Power Flow	
ASUSTek Computer INC.		Engineer: Joy_Zhou	
Size	Project Name		Rev
A3	900SD MB		R1.0G
Date: Monday, November 10, 2008		Sheet	44 of 51



+Vcore / 7A

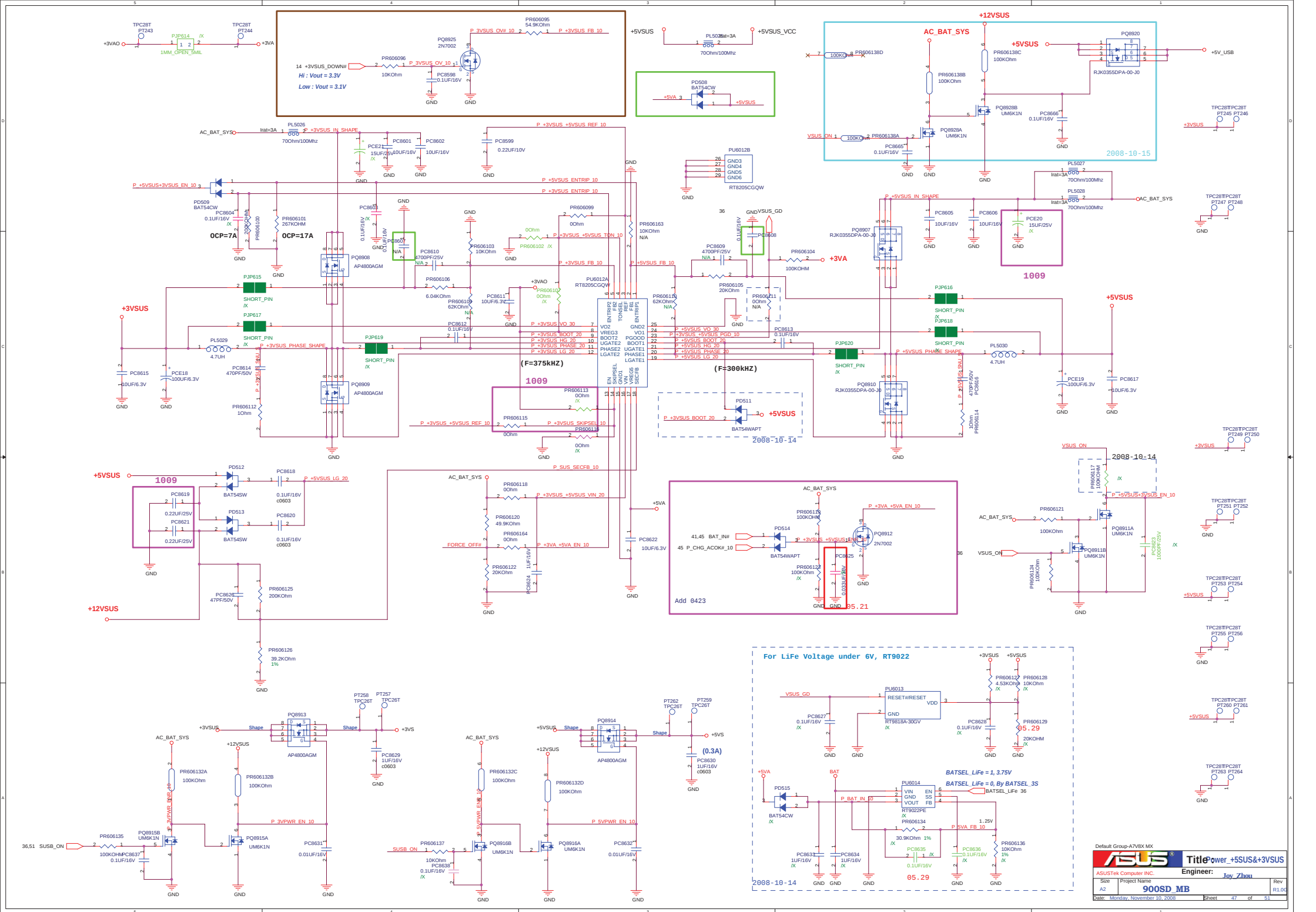


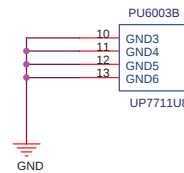
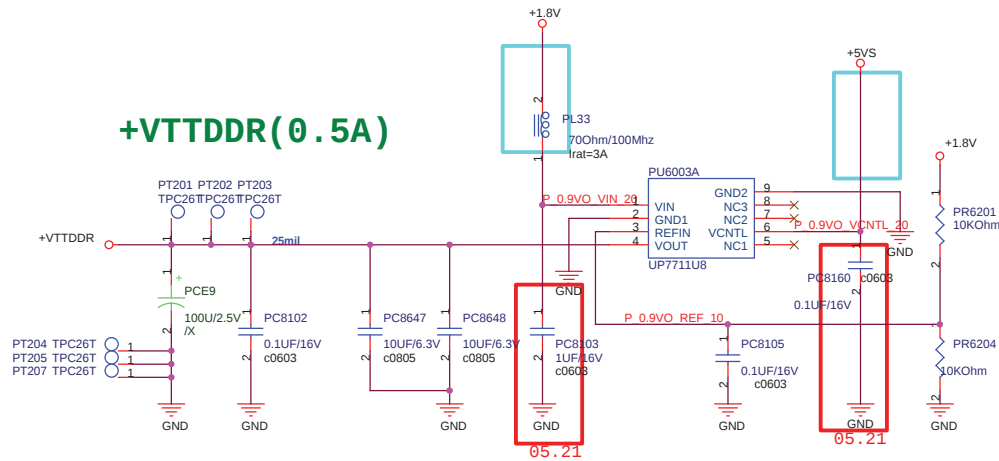
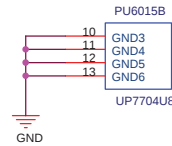
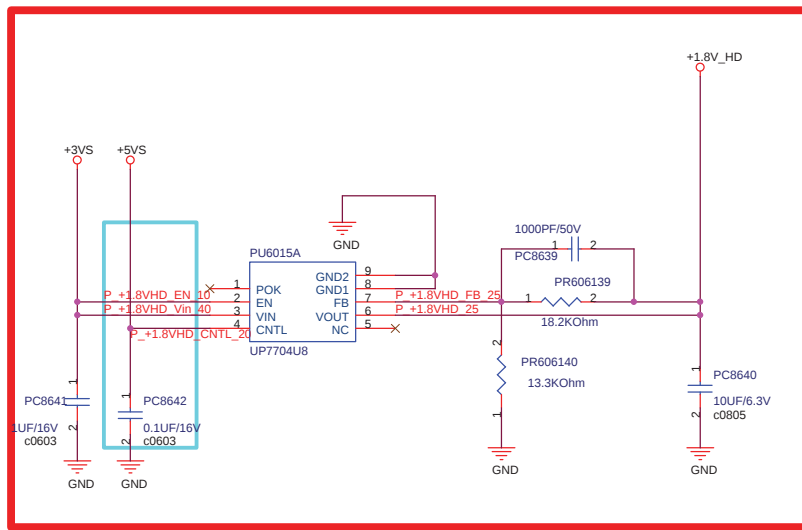
2008-10-15



Default Group-A7V8X MX







Default Group-A7V8X MX

ASUS		Title : 1.8VHD_VTT_DDR	
ASUSTek Computer INC.		Engineer: Joy Zhou	
Size	Project Name	Rev	
A3	900SD_MB	R1.0G	
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