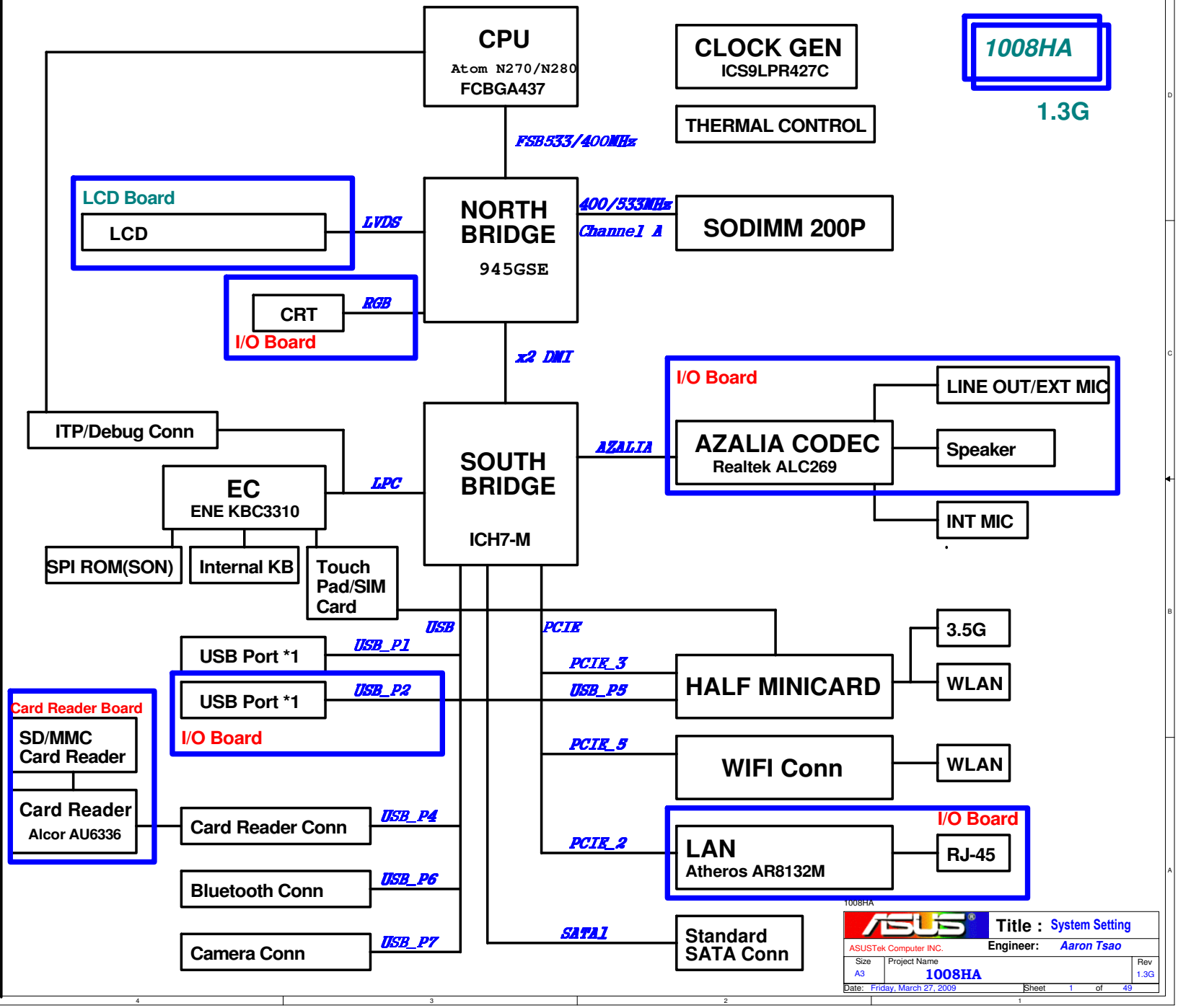


1008HA

Title : System Setting

ASUSTek Computer INC. Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 1 of 49

EEE PC 701 PCB version

GPI37	GPI38	GPI39	PCB version
0	0	0	
0	0	0	
0	0	1	
0	0	1	
0	1	0	
0	1	0	
0	1	1	
0	1	1	
1	0	0	
1	0	0	
1	0	1	
1	0	1	
1	1	0	
1	1	0	
1	1	1	
1	1	1	

USB

USB 0	USB Conn
USB 1	USB Conn
USB 2	NA
USB 3	NA
USB 4	Card Reader
USB 5	Minicard
USB 6	Bluetooth
USB 7	Camera

PCIE

PCIE 1	NC
PCIE 2	LAN
PCIE 3	Minicard
PCIE 4	SSD

Azalia

ACZ_SDIN0	CODEC
ACZ_SDIN1	NC
ACZ_SDIN2	NC

1008HA

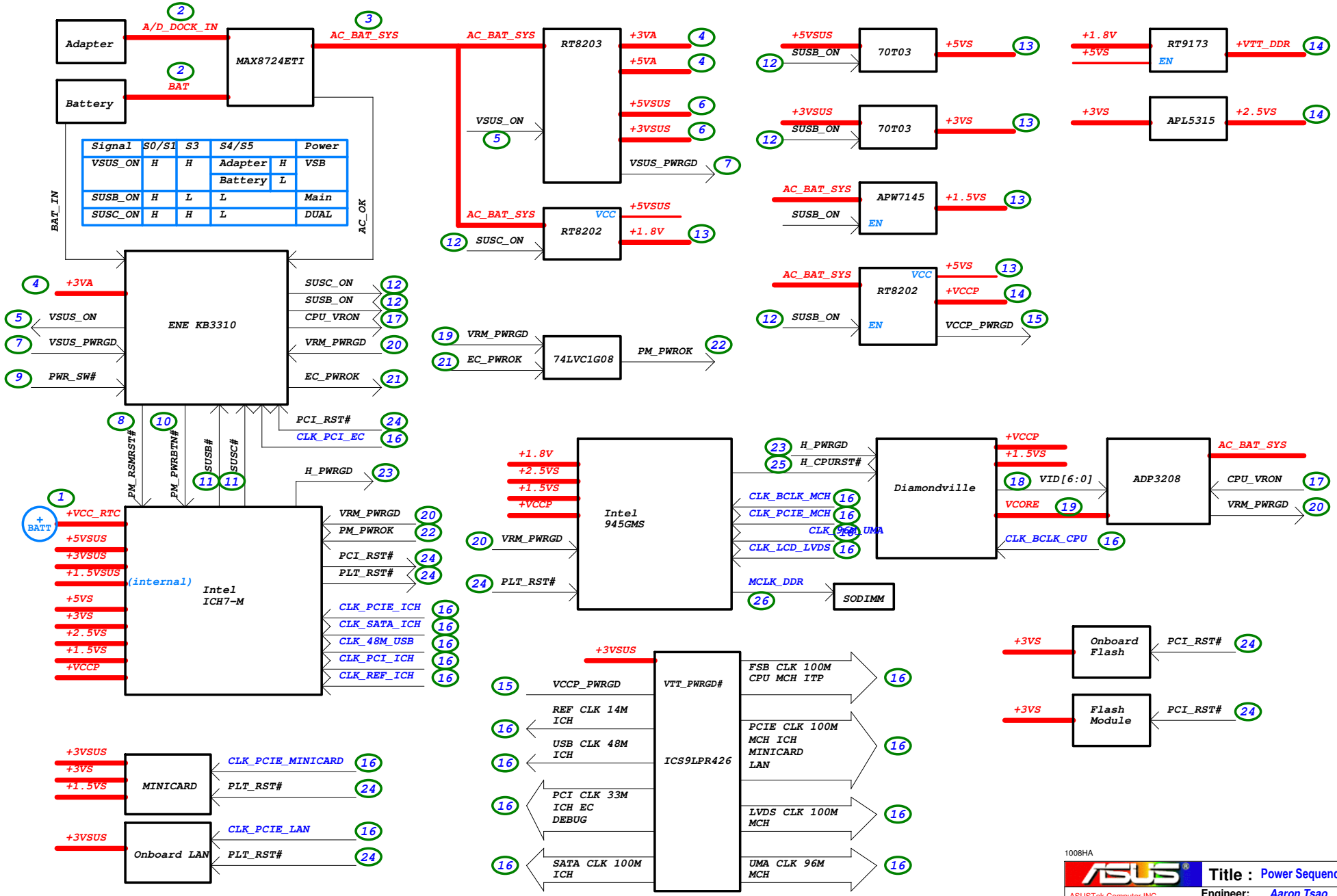
ASUSTek Computer INC.

Title : System Setting

Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 2 of 49



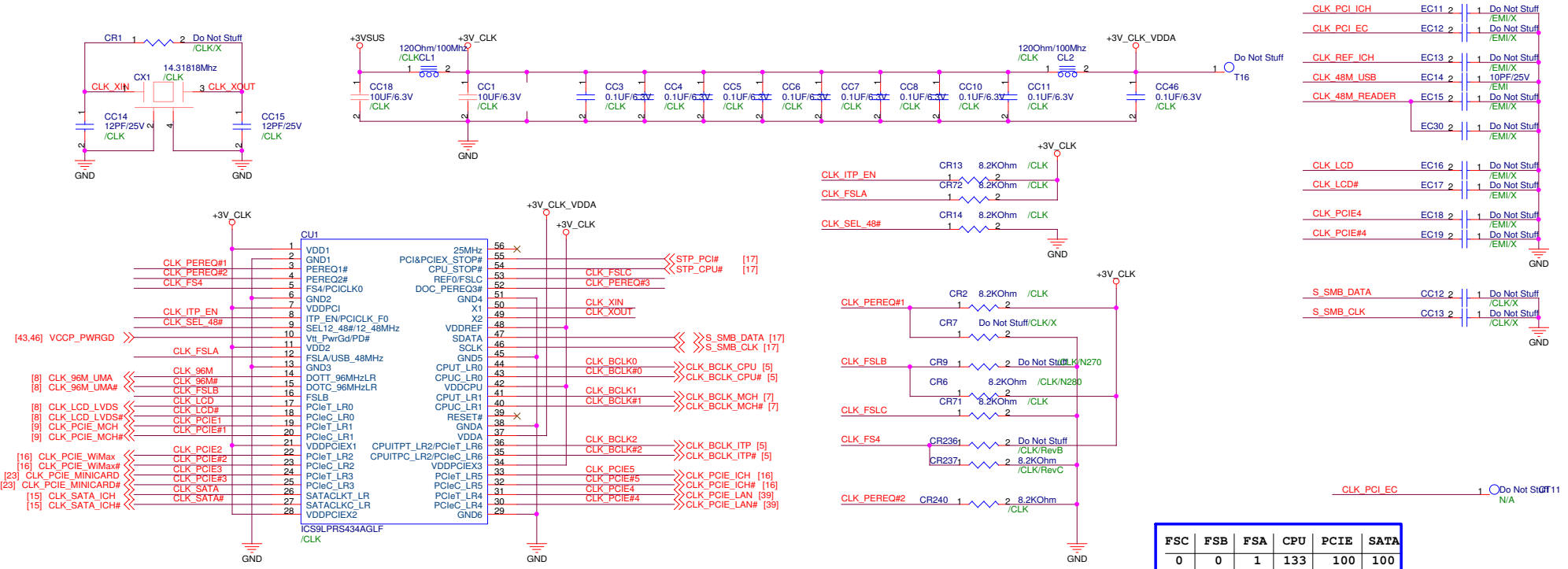
1008HA

Title : Power Sequence

ASUSTek Computer INC. Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 3 of 49



FSC	FSB	FSA	CPU	PCIE	SATA
0	0	1	133	100	100
1	0	1	100	100	100

DOC1	DOC2	Voltage	Status
L	L	2.3~2.52V	Performance
H	L	0V	Power Saving
H	H	0V	Power Saving
L	H	1.1~1.2V	Normal

1008HA

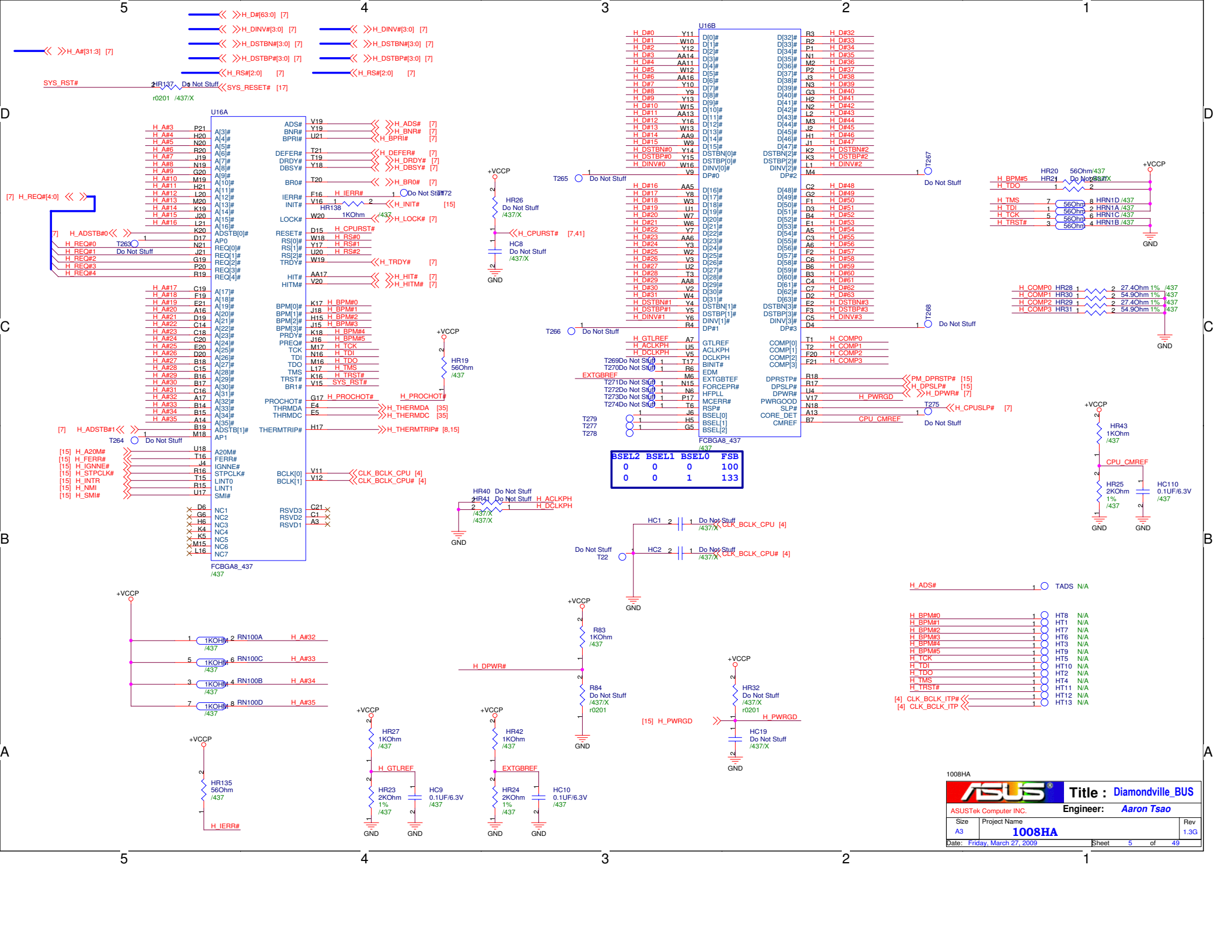
ASUSTek Computer INC.

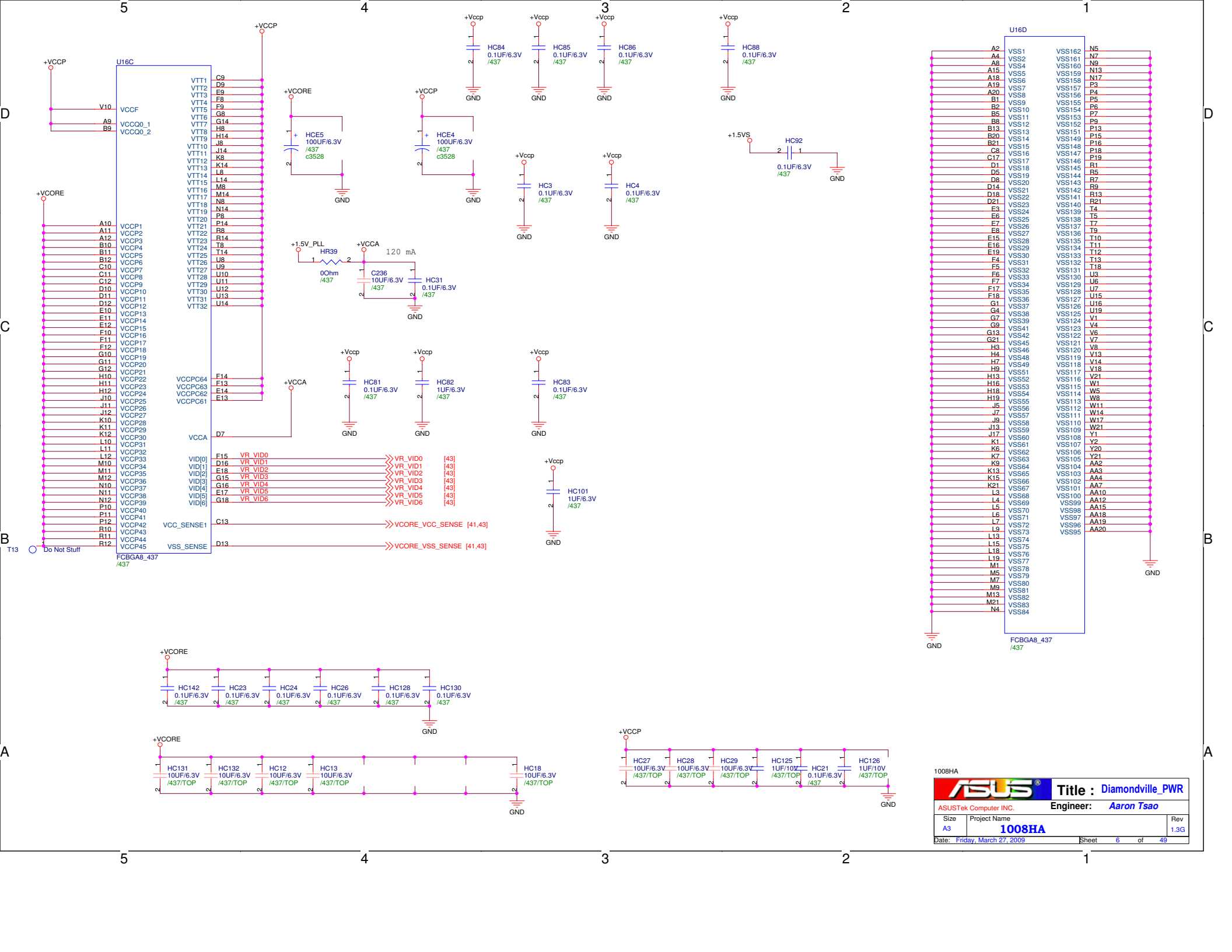
Title : Clock ICS9LPRS427

Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 4 of 49

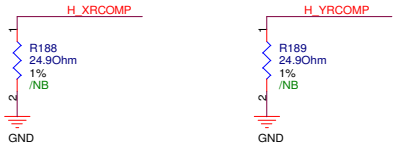

H_D#[63:0] [7]
H_DINV#[3:0] [7]
H_DSTBN#[3:0] [7]
H_DSTBP#[3:0] [7]
H_RS#[2:0] [7]
H_A#[31:3] [7]
SYS_RST#
HR137 Do Not Stuff
r0201 /437/X
SYS_RESET# [17]
U16A
FCBGA8_437
/437
U16B
FCBGA8_437
/437
[7] H_REQ#[4:0]
H_ADSTB#0
H_REQ#0
H_REQ#1
H_REQ#2
H_REQ#3
H_REQ#4
T263 Do Not Stuff
H_ADSTB#1
T264 Do Not Stuff
T265 Do Not Stuff
T266 Do Not Stuff
T267 Do Not Stuff
T268 Do Not Stuff
T275 Do Not Stuff
T269Do Not Stuff
T270Do Not Stuff
T271Do Not Stuff
T272Do Not Stuff
T273Do Not Stuff
T274Do Not Stuff
T279
T277
T278
T22 Do Not Stuff
HR26 Do Not Stuff /437/X
HC8 Do Not Stuff /437/X
H_CPURST# [7,41]
HR138 1KOhm /437
H_IERR# Do Not Stuff
H_INIT# [15]
H_LOCK# [7]
H_TRDY# [7]
H_HIT# [7]
H_HITM# [7]
H_ADS# [7]
H_BNR# [7]
H_BPRI# [7]
H_DEFER# [7]
H_DRDY# [7]
H_DBSY# [7]
H_BR0# [7]
H_RS#0
H_RS#1
H_RS#2
H_CPURST#
H_BPM#0
H_BPM#1
H_BPM#2
H_BPM#3
H_BPM#4
H_BPM#5
H_TCK
H_TDI
H_TDO
H_TMS
H_TRST#
SYS_RST#
H_PROCHOT#
H_THERMDA [35]
H_THERMDC [35]
H_THERMTRIP# [8,15]
CLK_BCLK_CPU [4]
CLK_BCLK_CPU# [4]
HR19 56Ohm /437
HR40 Do Not Stuff
HR41 Do Not Stuff
H_ACLKPH
H_DCLKPH
/437/X
HC1 Do Not Stuff /437/X
HC2 Do Not Stuff /437/X
CLK_BCLK_CPU [4]
CLK_BCLK_CPU# [4]
HR20 56Ohm/437 Do Not Stuff
H_TDO
H_TMS
H_TDI
H_TCK
H_TRST#
56Ohm
HRN1D /437
HRN1A /437
HRN1C /437
HRN1B /437
H_COMP0 HR28 27.4Ohm 1% /437
H_COMP1 HR30 54.9Ohm 1% /437
H_COMP2 HR29 27.4Ohm 1% /437
H_COMP3 HR31 54.9Ohm 1% /437
H_A20M# [15]
H_FERR# [15]
H_IGNNE# [15]
H_STPCLK# [15]
H_INTR [15]
H_NMI [15]
H_SMI# [15]
H_DPRSTP# [15]
H_DPSLP# [15]
H_DPWR# [7]
H_PWRGD
CPU_CMREF
H_CPUSLP# [7]
HR43 1KOhm /437
HR25 2KOhm 1% /437
HC110 0.1UF/6.3V /437
BSEL2 BSEL1 BSEL0 FSB
0 0 0 100
0 0 1 133
RN100A H_A#32
RN100C H_A#33
RN100B H_A#34
RN100D H_A#35
1KOHM /437
R83 1KOhm /437
R84 Do Not Stuff /437/X r0201
H_DPWR#
HR32 Do Not Stuff /437/X r0201
HC19 Do Not Stuff /437/X
[15] H_PWRGD
HR135 56Ohm /437
H_IERR#
HR27 1KOhm /437
HR23 2KOhm 1% /437
HC9 0.1UF/6.3V /437
HR42 1KOhm /437
HR24 2KOhm 1% /437
HC10 0.1UF/6.3V /437
H_ADS# TADS N/A
HT8 N/A
HT1 N/A
HT7 N/A
HT6 N/A
HT3 N/A
HT9 N/A
HT5 N/A
HT10 N/A
HT2 N/A
HT4 N/A
HT11 N/A
HT12 N/A
HT13 N/A
[4] CLK_BCLK_ITP#
[4] CLK_BCLK_ITP
+VCCP
GND
1008HA
Title : Diamondville_BUS
ASUSTek Computer INC.
Engineer: Aaron Tsao
Size A3
Project Name 1008HA
Rev 1.3G
Date: Friday, March 27, 2009
Sheet 5 of 49


U16C
FCBGA8_437
/437
U16D
FCBGA8_437
/437
+VCCP
+VCORE
+VCCA
+Vccp
+1.5V_PLL
+1.5VS
GND
HC84 0.1UF/6.3V /437
HC85 0.1UF/6.3V /437
HC86 0.1UF/6.3V /437
HC88 0.1UF/6.3V /437
HCE5 100UF/6.3V /437 c3528
HCE4 100UF/6.3V /437 c3528
HC92 0.1UF/6.3V /437
HC3 0.1UF/6.3V /437
HC4 0.1UF/6.3V /437
HR39 0Ohm /437
120 mA
C236 10UF/6.3V /437
HC31 0.1UF/6.3V /437
HC81 0.1UF/6.3V /437
HC82 1UF/6.3V /437
HC83 0.1UF/6.3V /437
HC101 1UF/6.3V /437
T13 Do Not Stuff
VR_VID0 [43]
VR_VID1 [43]
VR_VID2 [43]
VR_VID3 [43]
VR_VID4 [43]
VR_VID5 [43]
VR_VID6 [43]
VCORE_VCC_SENSE [41,43]
VCORE_VSS_SENSE [41,43]
HC142 0.1UF/6.3V /437
HC23 0.1UF/6.3V /437
HC24 0.1UF/6.3V /437
HC26 0.1UF/6.3V /437
HC128 0.1UF/6.3V /437
HC130 0.1UF/6.3V /437
HC131 10UF/6.3V /437/TOP
HC132 10UF/6.3V /437/TOP
HC12 10UF/6.3V /437/TOP
HC13 10UF/6.3V /437/TOP
HC18 10UF/6.3V /437/TOP
HC27 10UF/6.3V /437/TOP
HC28 10UF/6.3V /437/TOP
HC29 10UF/6.3V /437/TOP
HC125 1UF/10V /437/TOP
HC21 0.1UF/6.3V /437
HC126 1UF/10V /437/TOP
1008HA
Title : Diamondville_PWR
ASUSTek Computer INC.
Engineer: Aaron Tsao
Size A3
Project Name 1008HA
Rev 1.3G
Date: Friday, March 27, 2009
Sheet 6 of 49

Power:
+VCCP

RCOMP

For Calibrating the FSB I/O Buffer

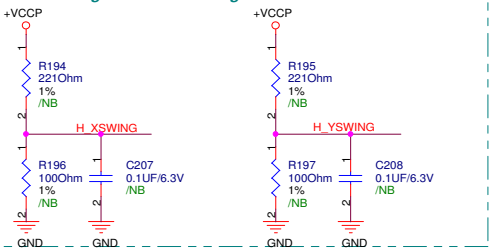


SCOMP

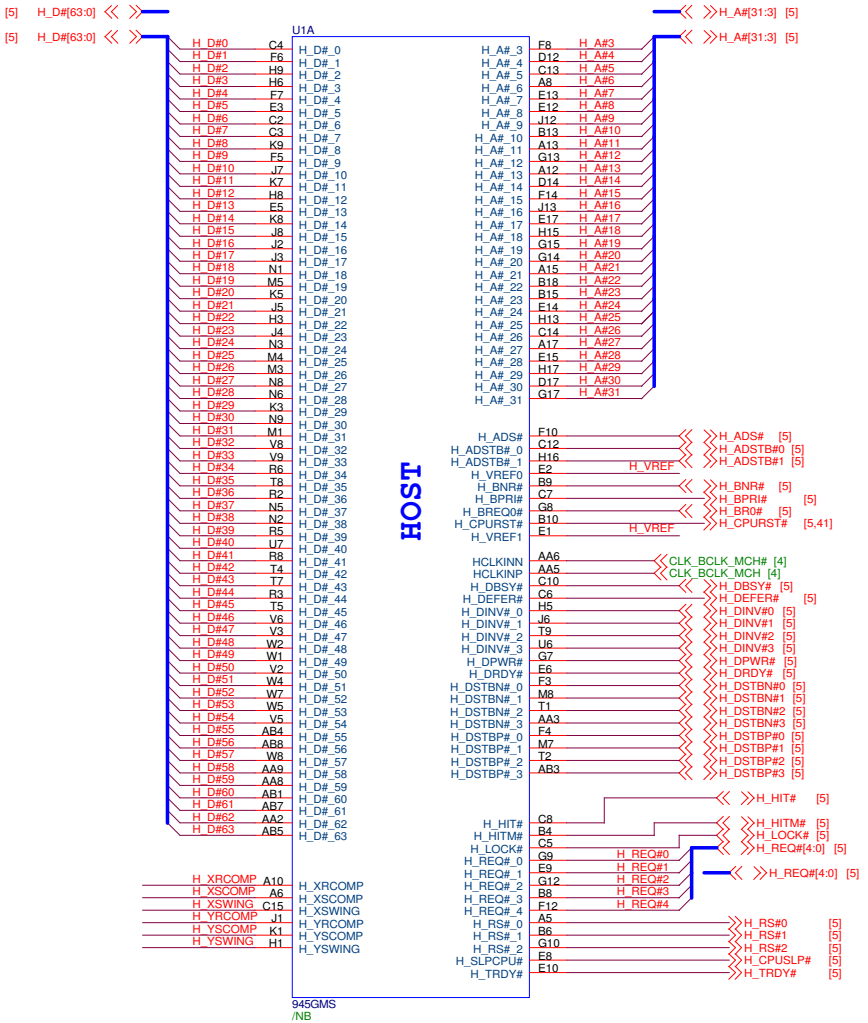
For Slew Rate Compenssation on the FSB

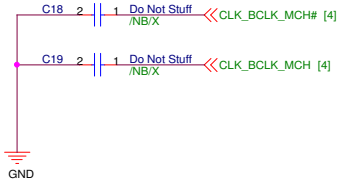
Voltage Swing

For Providing a Reference Voltage to The FSB RCOMP circuits

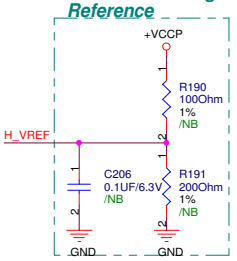


Signal voltage level = 0.3125*VCCP
Trace should be 10 mil wide with 20 mil spacing





AGTL+ I/O Voltage Reference



Layout Note:
0.1uF should be placed 100mils or less from GMCH pin.

1008HA

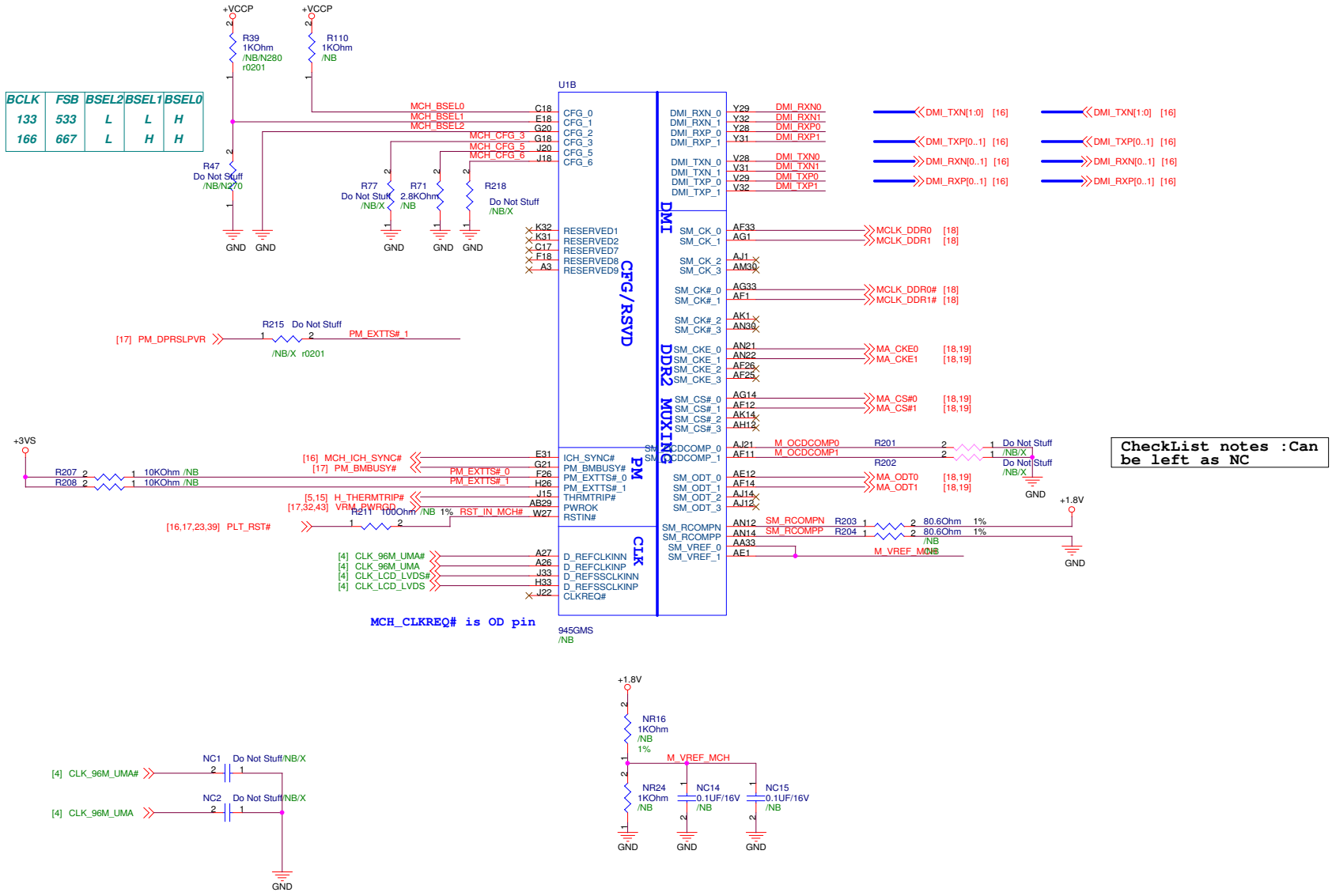
ASUSTeK COMPUTER INC.

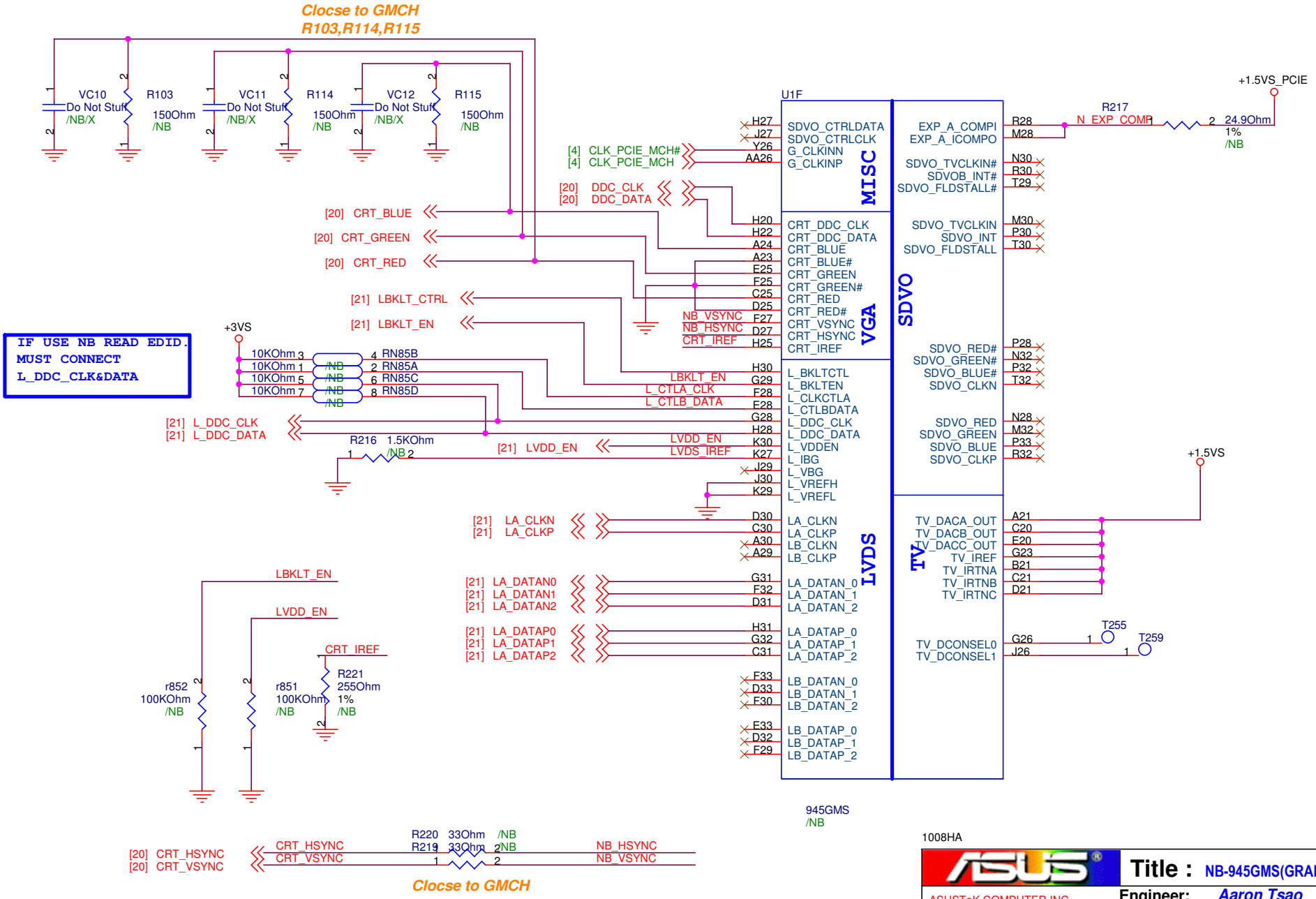
Title : NB-945GMS(HOST)

Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 7 of 49


BCLK FSB BSEL2 BSEL1 BSEL0
133 533 L L H
166 667 L H H
+VCCP
R39 1KOhm /NB/N280 r0201
R110 1KOhm /NB
R47 Do Not Stuff /NB/N270
R77 Do Not Stuff /NB/X
R71 2.8KOhm /NB
R218 Do Not Stuff /NB/X
GND
MCH_BSEL0
MCH_BSEL1
MCH_BSEL2
MCH_CFG_3
MCH_CFG_5
MCH_CFG_6
U1B
CFG_0 CFG_1 CFG_2 CFG_3 CFG_5 CFG_6
RESERVED1 RESERVED2 RESERVED7 RESERVED8 RESERVED9
CFG/RSVD
DMI_RXN_0 DMI_RXN_1 DMI_RXP_0 DMI_RXP_1
DMI_TXN_0 DMI_TXN_1 DMI_TXP_0 DMI_TXP_1
DMI_RXN0 DMI_RXN1 DMI_RXP0 DMI_RXP1
DMI_TXN0 DMI_TXN1 DMI_TXP0 DMI_TXP1
DMI_TXN[1:0] [16]
DMI_TXN[0..1] [16]
DMI_RXN[0..1] [16]
DMI_RXP[0..1] [16]
DMI
SM_CK_0 SM_CK_1 SM_CK_2 SM_CK_3
SM_CK#_0 SM_CK#_1 SM_CK#_2 SM_CK#_3
SM_CKE_0 SM_CKE_1 SM_CKE_2 SM_CKE_3
SM_CS#_0 SM_CS#_1 SM_CS#_2 SM_CS#_3
SM_ODT_0 SM_ODT_1 SM_ODT_2 SM_ODT_3
MCLK_DDR0 [18]
MCLK_DDR1 [18]
MCLK_DDR0# [18]
MCLK_DDR1# [18]
MA_CKE0 [18,19]
MA_CKE1 [18,19]
MA_CS#0 [18,19]
MA_CS#1 [18,19]
MA_ODT0 [18,19]
MA_ODT1 [18,19]
DDR2 MUXING
+3VS
R207 10KOhm /NB
R208 10KOhm /NB
[16] MCH_ICH_SYNC#
[17] PM_BMBUSY#
PM_EXTTS#_0
PM_EXTTS#_1
[5,15] H_THERMTRIP#
[17,32,43] VRM_PWRGD
[16,17,23,39] PLT_RST#
R211 100Ohm /NB 1%
RST_IN_MCH#
ICH_SYNC# PM_BMBUSY# PM_EXTTS#_0 PM_EXTTS#_1 THRMTRIP# PWROK RSTIN#
PM
SM_OCDCOMP_0 SM_OCDCOMP_1
M_OCDCOMP0 M_OCDCOMP1
R201 Do Not Stuff /NB/X
R202 Do Not Stuff /NB/X
SM_RCOMPN SM_RCOMPP SM_VREF_0 SM_VREF_1
R203 80.6Ohm 1%
R204 80.6Ohm 1%
/NB
M_VREF_MCH
+1.8V
[4] CLK_96M_UMA#
[4] CLK_96M_UMA
[4] CLK_LCD_LVDS#
[4] CLK_LCD_LVDS
D_REFCLKINN D_REFCLKINP D_REFSSCLKINN D_REFSSCLKINP CLKREQ#
CLK
MCH_CLKREQ# is OD pin
945GMS /NB
R215 Do Not Stuff /NB/X r0201
[17] PM_DPRSLPVR
PM_EXTTS#_1
NC1 Do Not Stuff/NB/X
NC2 Do Not Stuff/NB/X
NR16 1KOhm /NB 1%
NR24 1KOhm /NB
NC14 0.1UF/16V /NB
NC15 0.1UF/16V /NB
CheckList notes :Can be left as NC
1008HA
Title : NB-945GMS(DMI & CFG)
ASUSTeK COMPUTER INC.
Engineer: Aaron Tsao
Size A3
Project Name 1008HA
Rev 1.3G
Date: Friday, March 27, 2009
Sheet 8 of 49



1008HA

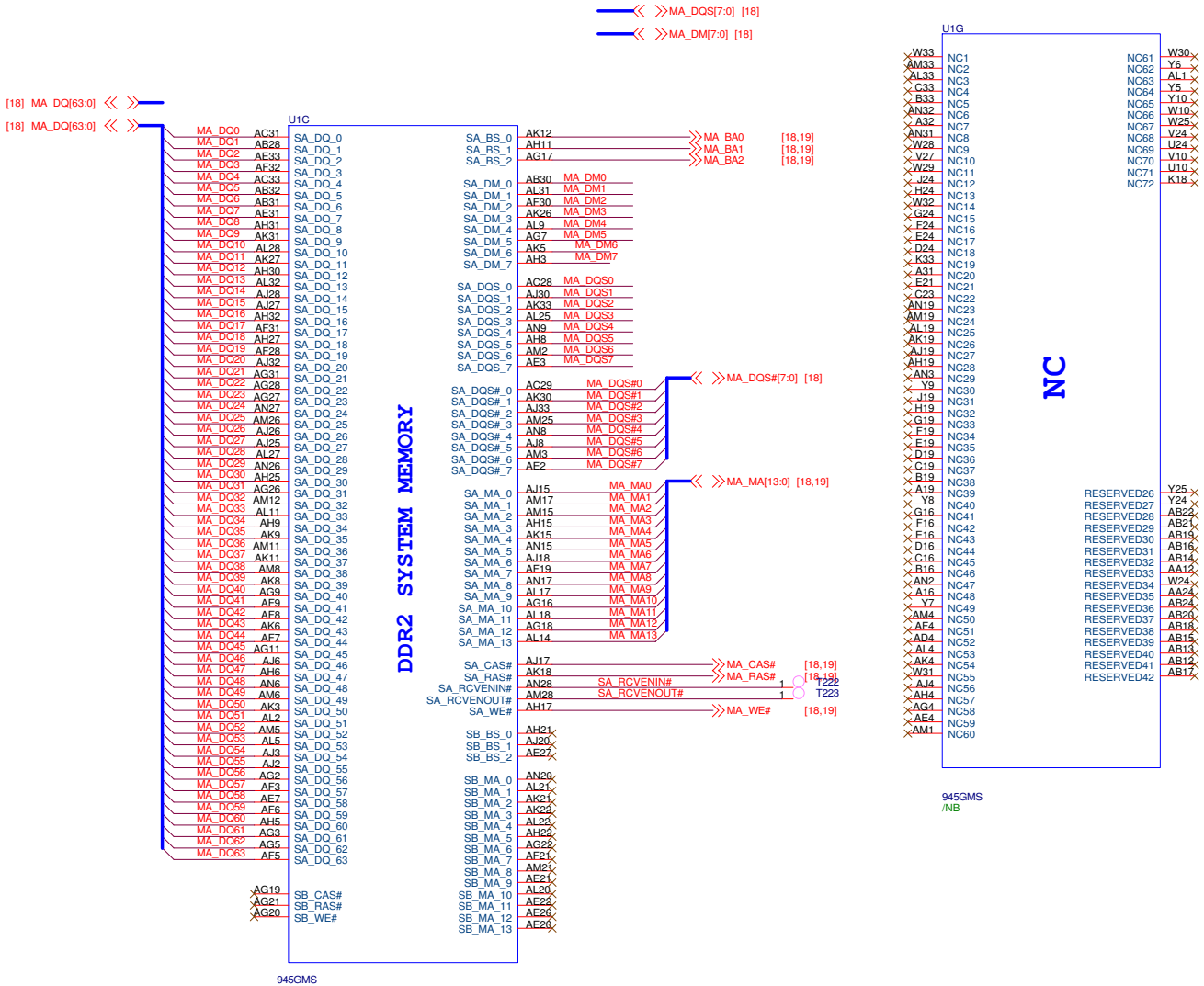
ASUSTeK COMPUTER INC.

Title : NB-945GMS(GRAPHIC)

Engineer: Aaron Tsao

Size	Project Name	Rev
A4	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 9 of 49



1008HA

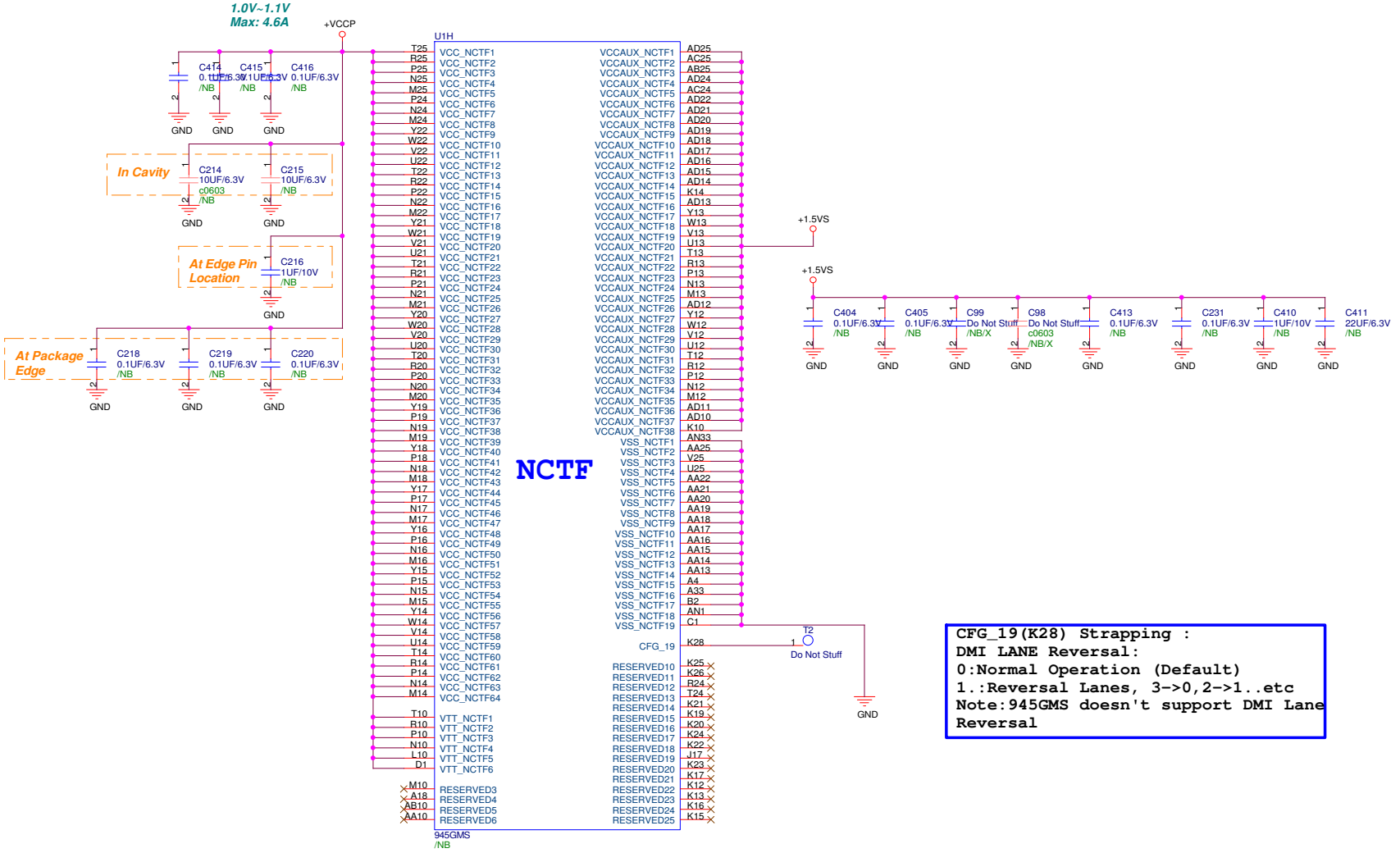
ASUSTeK COMPUTER INC.

Title : NB-945GMS(DDR2)

Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 10 of 49



1008HA

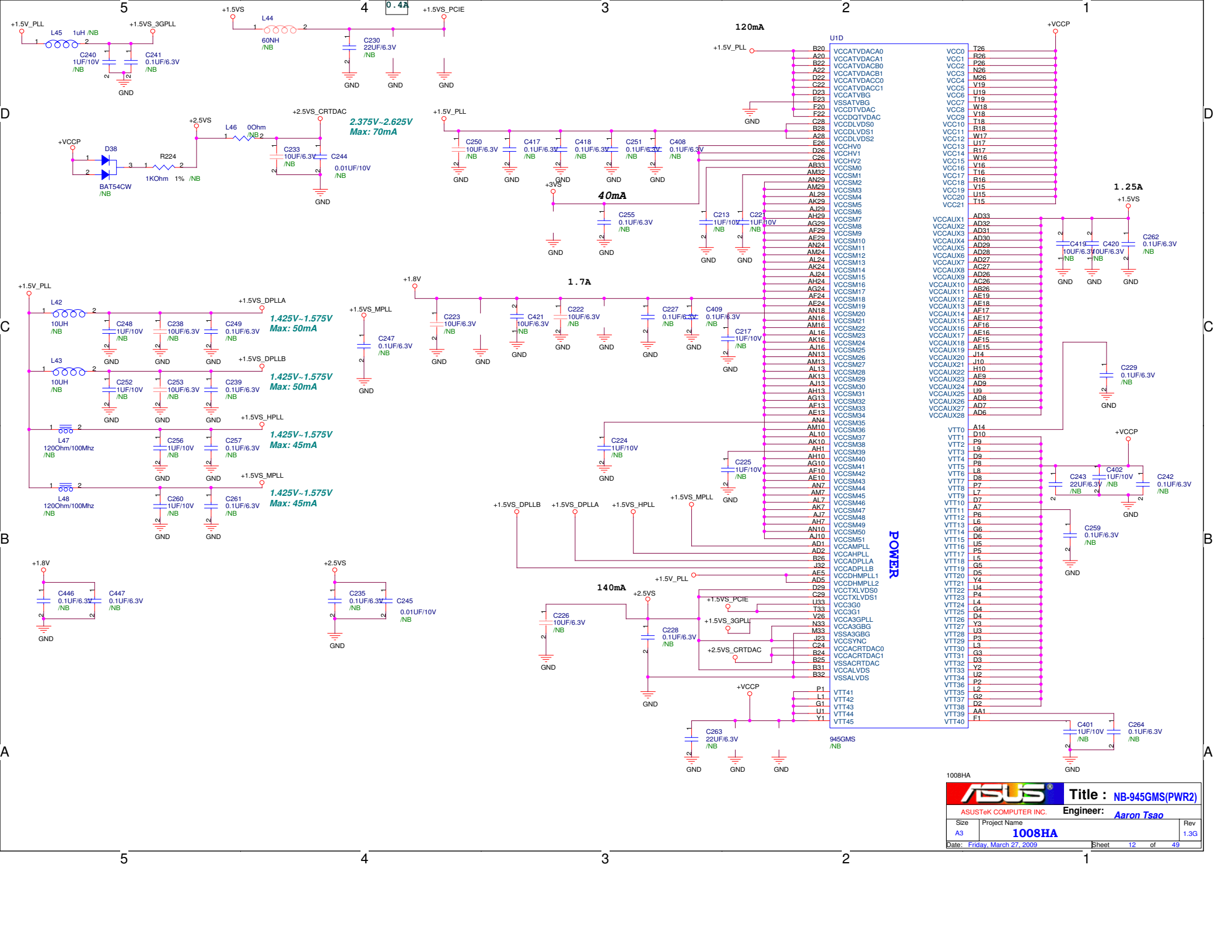
ASUSTeK COMPUTER INC.

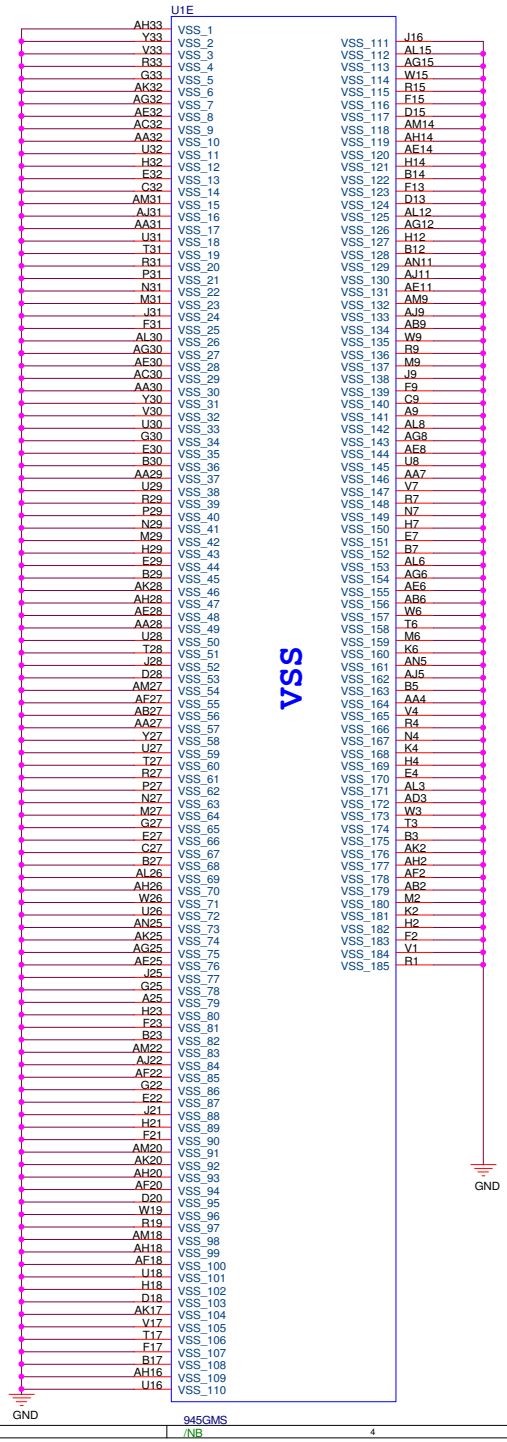
Title : NB-945GMs(PWR)

Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

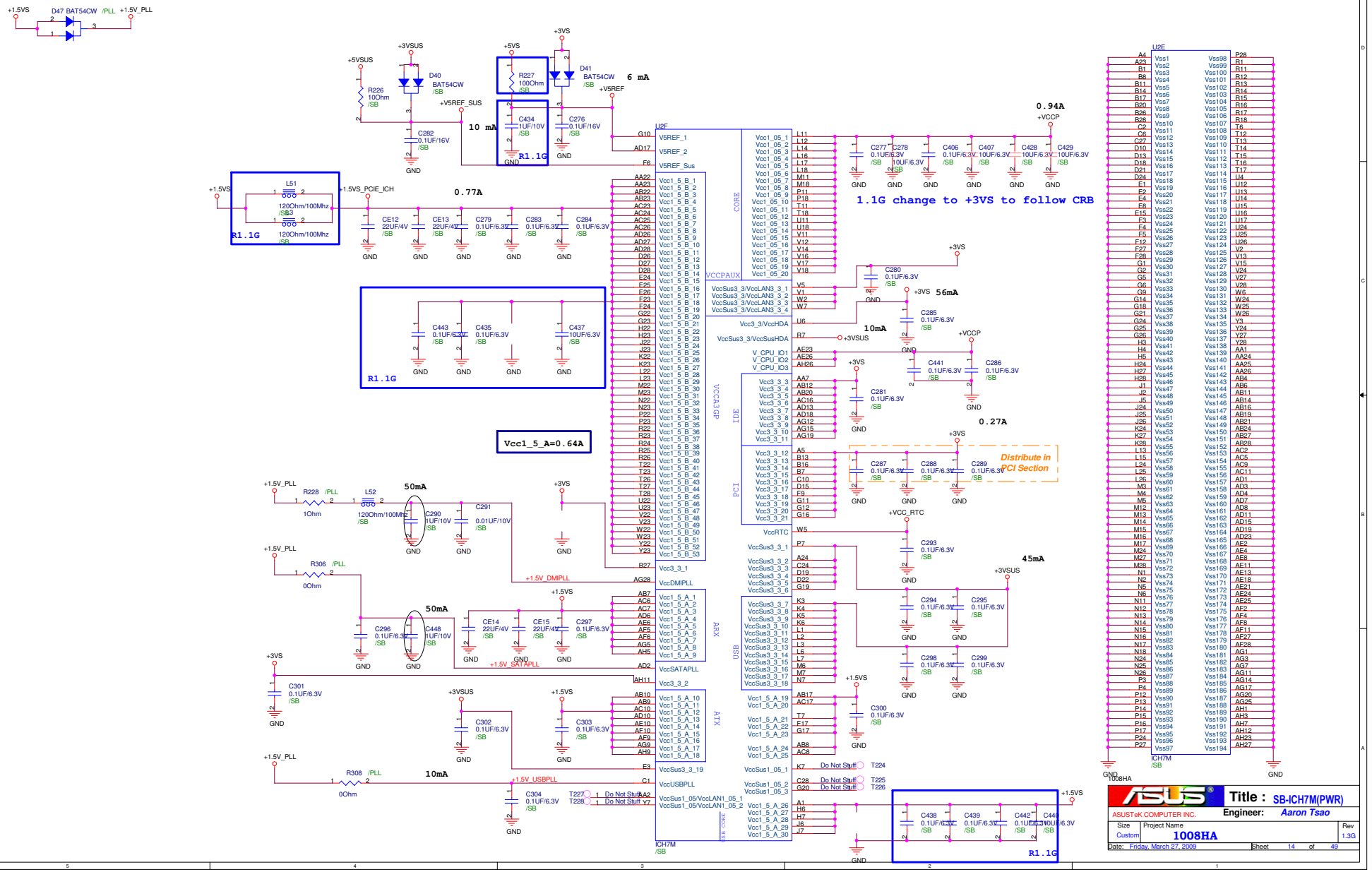
Date: Friday, March 27, 2009 Sheet 11 of 49


+1.5V_PLL
L45 1uH /NB
+1.5VS_3GPLL
C240 1UF/10V /NB
C241 0.1UF/6.3V /NB
GND
+1.5VS
L44 60NH /NB
+1.5VS_PCIE
C230 22UF/6.3V /NB
+2.5VS
L46 0Ohm /NB
+2.5VS_CRTDAC
2.375V~2.625V
Max: 70mA
+VCCP
D38 BAT54CW /NB
R224 1KOhm 1% /NB
C233 10UF/6.3V /NB
C244 0.01UF/10V /NB
120mA
U1D
C250 10UF/6.3V /NB
C417 0.1UF/6.3V /NB
C418 0.1UF/6.3V /NB
C251 0.1UF/6.3V /NB
C408 0.1UF/6.3V /NB
+3VS
40mA
C255 0.1UF/6.3V /NB
C213 1UF/10V /NB
C221 1UF/10V /NB
1.25A
C262 0.1UF/6.3V /NB
C419 10UF/6.3V /NB
C420 10UF/6.3V /NB
+1.8V
1.7A
+1.5VS_MPLL
C247 0.1UF/6.3V /NB
C223 10UF/6.3V /NB
C421 10UF/6.3V /NB
C222 10UF/6.3V /NB
C227 0.1UF/6.3V /NB
C409 0.1UF/6.3V /NB
C217 1UF/10V /NB
C229 0.1UF/6.3V /NB
L42 10UH /NB
+1.5VS_DPLLA
1.425V~1.575V
Max: 50mA
C248 1UF/10V /NB
C238 10UF/6.3V /NB
C249 0.1UF/6.3V /NB
L43 10UH /NB
+1.5VS_DPLLB
1.425V~1.575V
Max: 50mA
C252 1UF/10V /NB
C253 10UF/6.3V /NB
C239 0.1UF/6.3V /NB
L47 120Ohm/100Mhz /NB
+1.5VS_HPLL
1.425V~1.575V
Max: 45mA
C256 1UF/10V /NB
C257 0.1UF/6.3V /NB
L48 120Ohm/100Mhz /NB
+1.5VS_MPLL
1.425V~1.575V
Max: 45mA
C260 1UF/10V /NB
C261 0.1UF/6.3V /NB
C224 1UF/10V /NB
C225 1UF/10V /NB
C402 1UF/10V /NB
C243 22UF/6.3V /NB
C242 0.1UF/6.3V /NB
C259 0.1UF/6.3V /NB
140mA
C226 10UF/6.3V /NB
C228 0.1UF/6.3V /NB
C263 22UF/6.3V /NB
C401 1UF/10V /NB
C264 0.1UF/6.3V /NB
945GMS /NB
C446 0.1UF/6.3V /NB
C447 0.1UF/6.3V /NB
C235 0.1UF/6.3V /NB
C245 0.01UF/10V /NB
POWER
1008HA
Title : NB-945GMS(PWR2)
ASUSTeK COMPUTER INC.
Engineer: Aaron Tsao
Size A3
Project Name 1008HA
Rev 1.3G
Date: Friday, March 27, 2009
Sheet 12 of 49



1008HA

ASUSTeK COMPUTER INC.	Title : NB-945PMS(GND)	
	Engineer: Aaron Tsao	
Size: A3	Project Name: 1008HA	Rev: 1.3G
Date: Friday, March 27, 2009	Sheet 13 of 49	

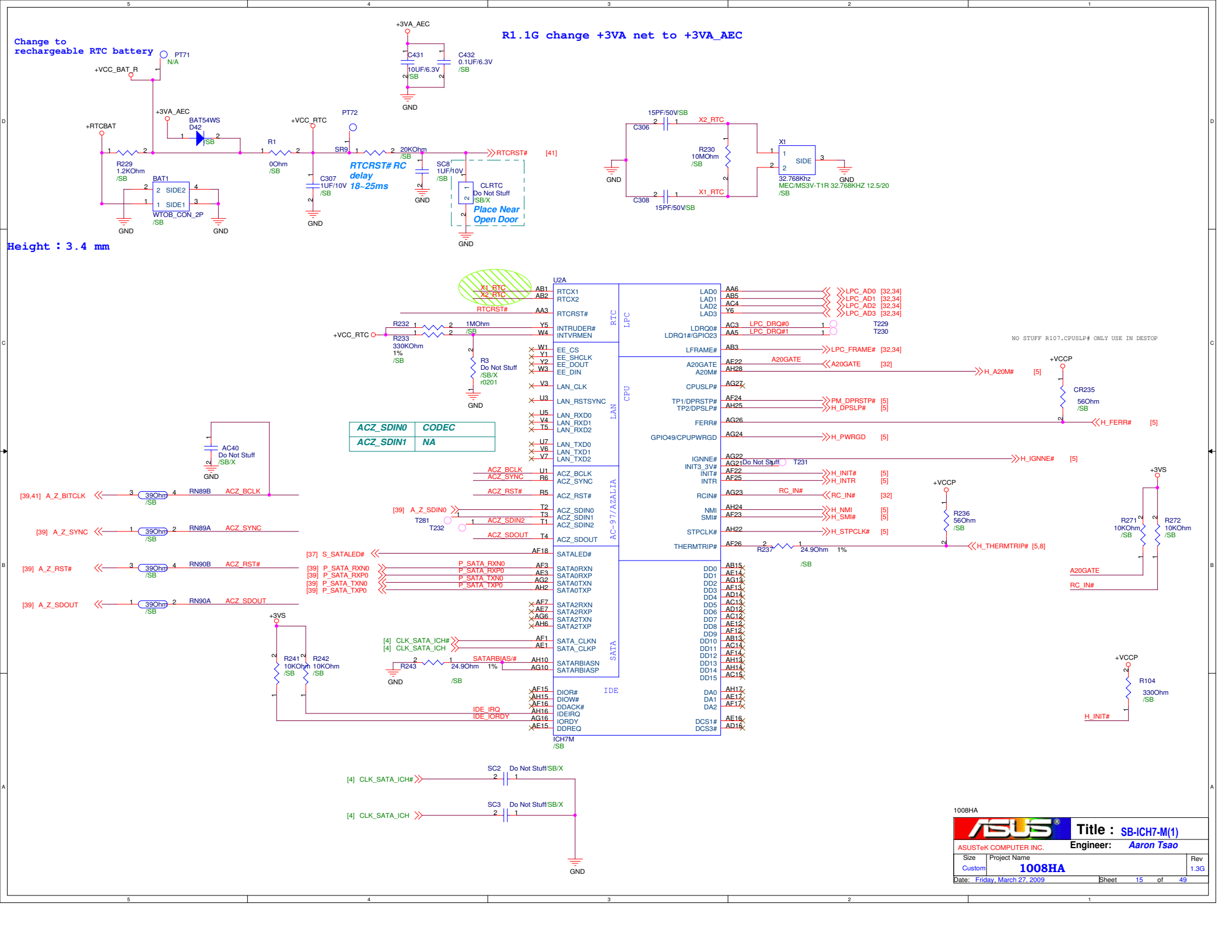


ASUSTeK COMPUTER INC.	Title : SB-ICH7M(PWR)		
	Engineer: Aaron Tsao		
Size: Custom	Project Name: 1008HA		Rev: 1.3G
Date: Friday, March 27, 2009	Sheet 14 of 49		

Change to
rechargeable RTC battery

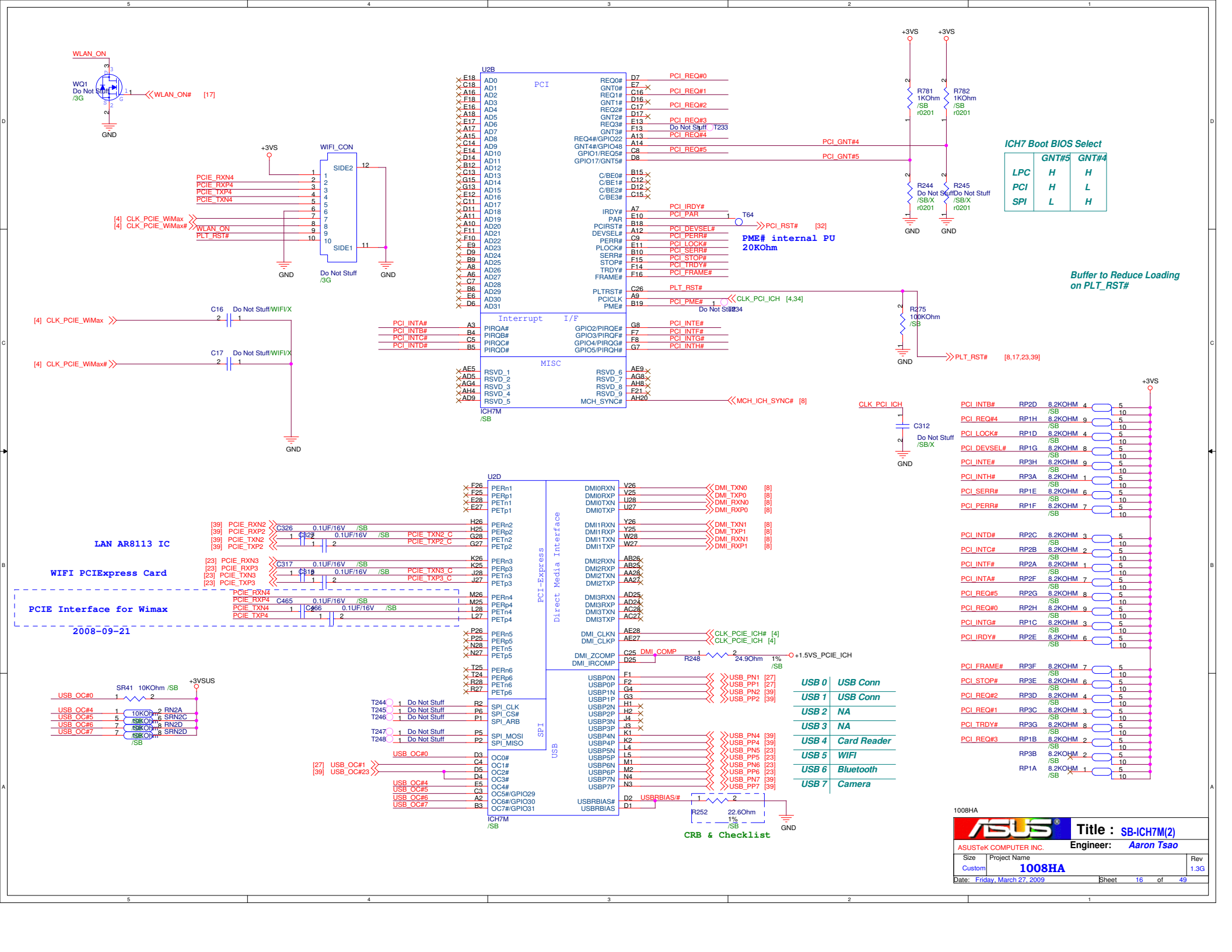
R1.1G change +3VA net to +3VA_AEC

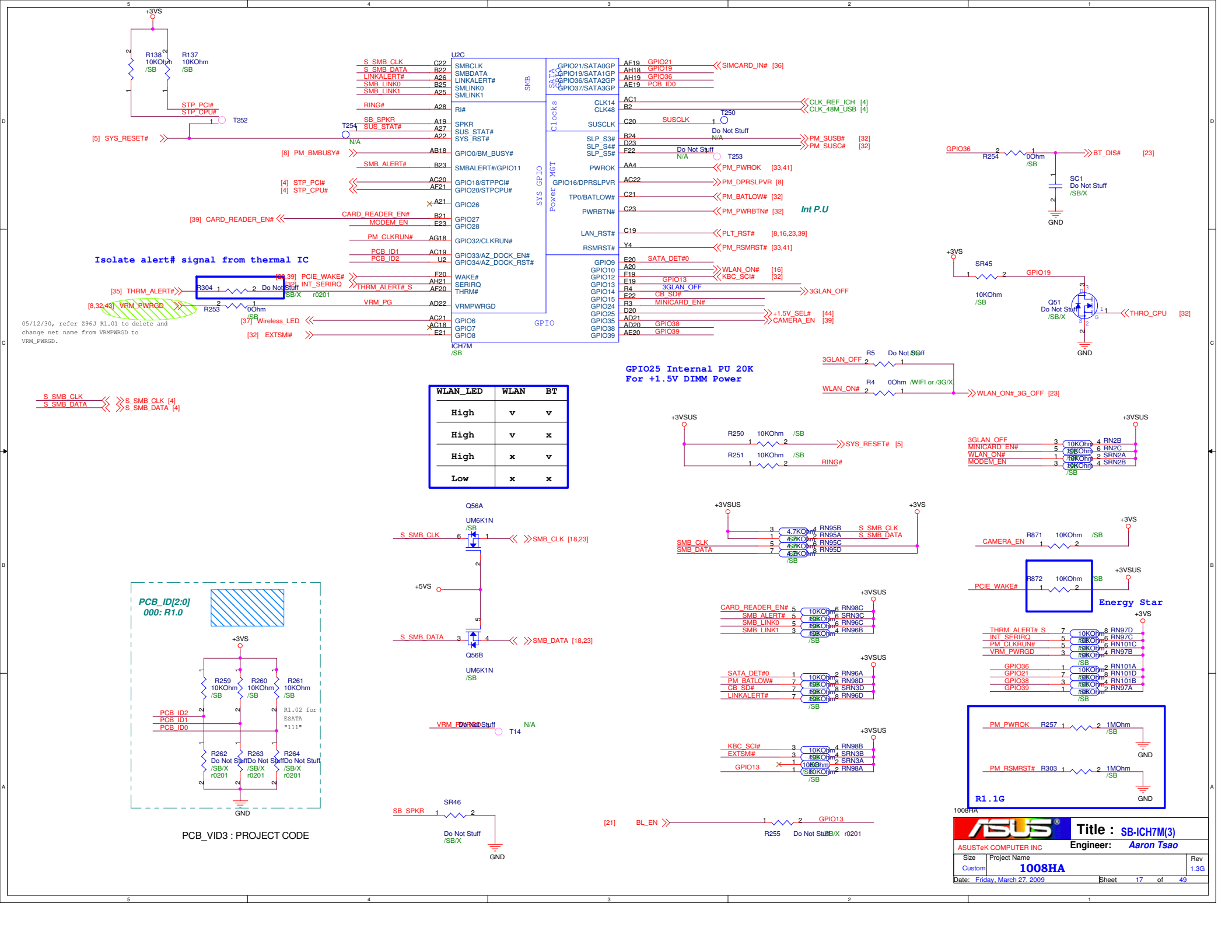
Height : 3.4 mm

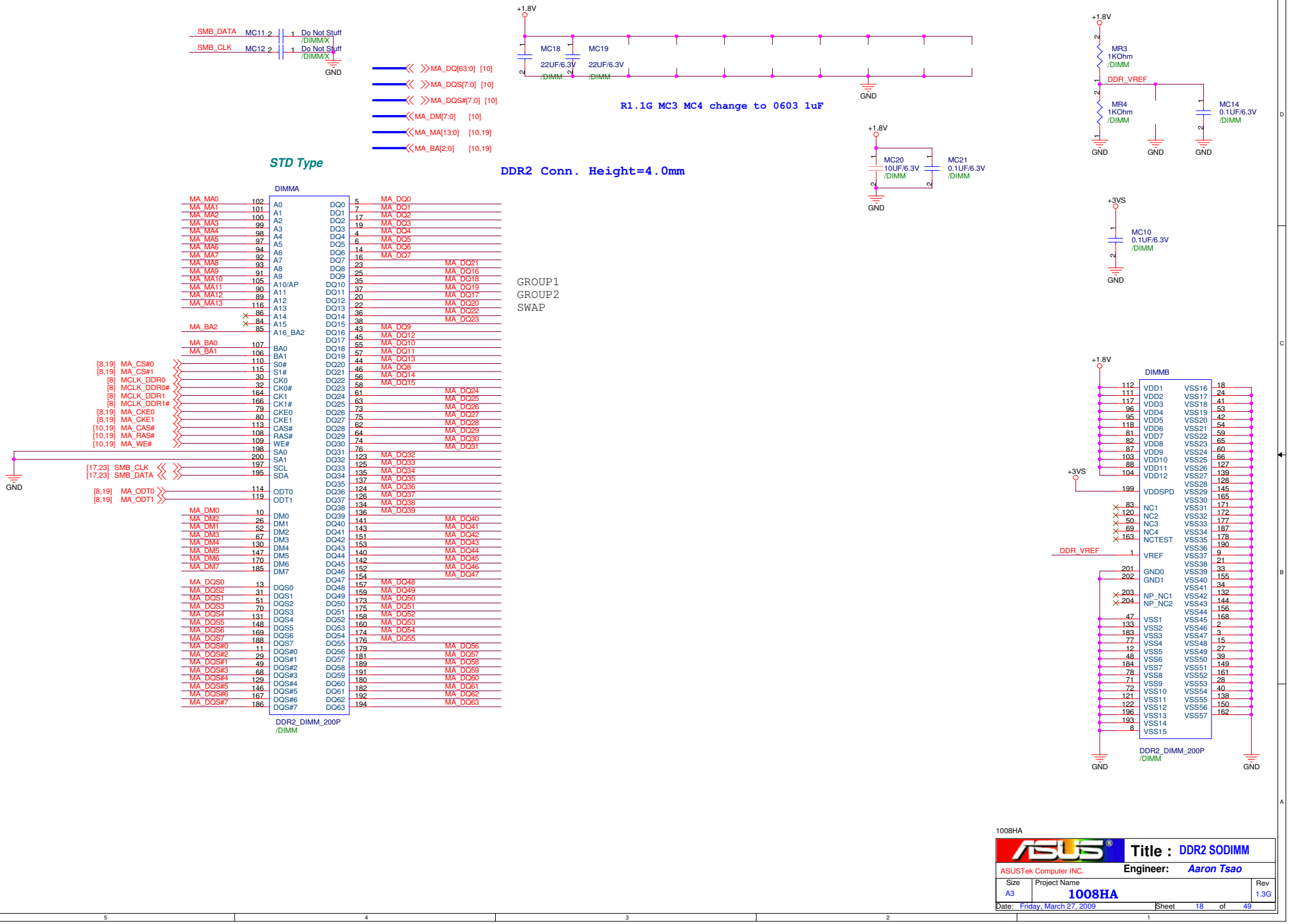


1008HA

ASUSTeK COMPUTER INC.	Title : SB-ICH7-M(1)	
	Engineer: Aaron Tsao	
Size: Custom	Project Name: 1008HA	Rev: 1.3G
Date: Friday, March 27, 2009	Sheet 15 of 49	

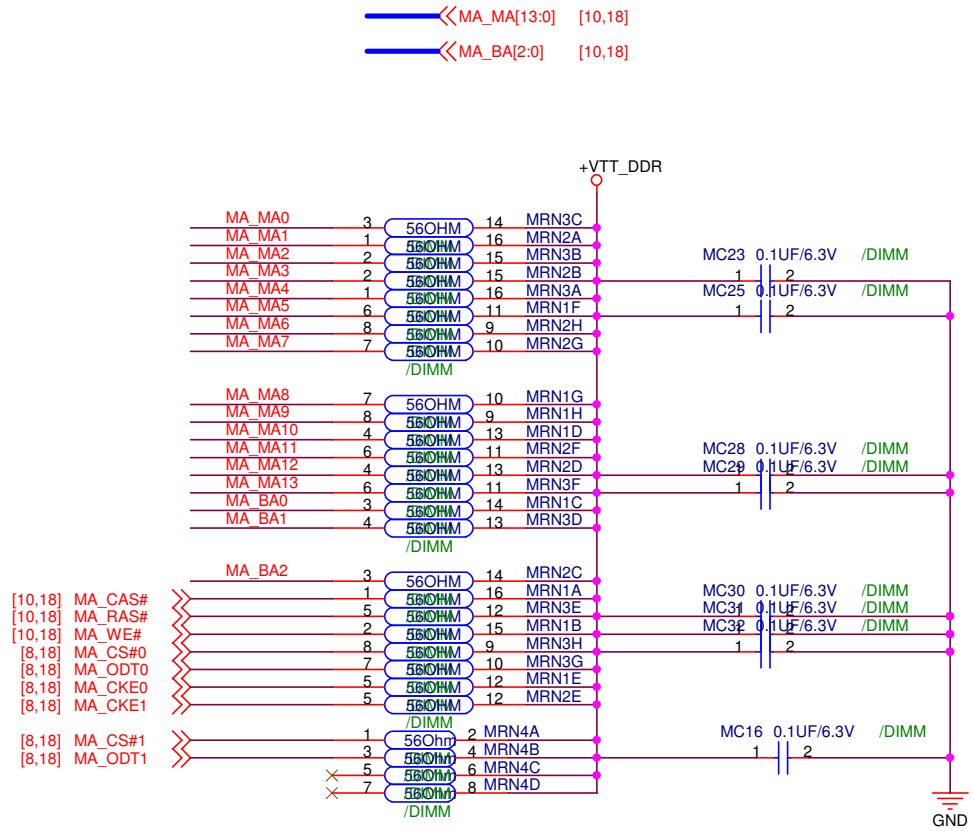

WLAN_ON
WQ1
Do Not Stuff
/3G
WLAN_ON# [17]
GND
+3VS
WIFI_CON
SIDE2
SIDE1
Do Not Stuff
/3G
PCIE_RXN4
PCIE_RXP4
PCIE_TXP4
PCIE_TXN4
[4] CLK_PCIE_WiMax
[4] CLK_PCIE_WiMax#
WLAN_ON
PLT_RST#
C16 Do Not Stuff/WIFI/X
C17 Do Not Stuff/WIFI/X
U2B
PCI
Interrupt I/F
MISC
ICH7M
/SB
REQ0#
REQ1#
REQ2#
REQ3#
GNT0#
GNT1#
GNT2#
GNT3#
REQ4#/GPIO22
GNT4#/GPIO48
GPIO1/REQ5#
GPIO17/GNT5#
PCI_REQ#0
PCI_REQ#1
PCI_REQ#2
PCI_REQ#3
PCI_REQ#4
PCI_REQ#5
Do Not Stuff T233
PCI_GNT#4
PCI_GNT#5
R781 1KOhm /SB r0201
R782 1KOhm /SB r0201
R244 Do Not Stuff /SB/X r0201
R245 Do Not Stuff /SB/X r0201
ICH7 Boot BIOS Select
LPC H H
PCI H L
SPI L H
PME# internal PU 20KOhm
PCI_PME# T64
PCI_RST# [32]
PLT_RST#
CLK_PCI_ICH [4,34]
Do Not Stuff T234
R275 100KOhm /SB
PLT_RST# [8,17,23,39]
Buffer to Reduce Loading on PLT_RST#
PCI_IRDY#
PCI_PAR
PCI_DEVSEL#
PCI_PERR#
PCI_LOCK#
PCI_SERR#
PCI_STOP#
PCI_TRDY#
PCI_FRAME#
PCI_INTA#
PCI_INTB#
PCI_INTC#
PCI_INTD#
PCI_INTE#
PCI_INTF#
PCI_INTG#
PCI_INTH#
RSVD_1
RSVD_2
RSVD_3
RSVD_4
RSVD_5
RSVD_6
RSVD_7
RSVD_8
RSVD_9
MCH_ICH_SYNC# [8]
C312 Do Not Stuff /SB/X
RP2D 8.2KOHM /SB
RP1H 8.2KOHM /SB
RP1D 8.2KOHM /SB
RP1G 8.2KOHM /SB
RP3H 8.2KOHM /SB
RP3A 8.2KOHM /SB
RP1E 8.2KOHM /SB
RP1F 8.2KOHM /SB
RP2C 8.2KOHM /SB
RP2B 8.2KOHM /SB
RP2A 8.2KOHM /SB
RP2F 8.2KOHM /SB
RP2G 8.2KOHM /SB
RP2H 8.2KOHM /SB
RP1C 8.2KOHM /SB
RP2E 8.2KOHM /SB
RP3F 8.2KOHM /SB
RP3E 8.2KOHM /SB
RP3D 8.2KOHM /SB
RP3C 8.2KOHM /SB
RP3G 8.2KOHM /SB
RP1B 8.2KOHM /SB
RP3B 8.2KOHM /SB
RP1A 8.2KOHM /SB
LAN AR8113 IC
WIFI PCIExpress Card
PCIE Interface for Wimax
2008-09-21
U2D
PCI-Express
Direct Media Interface
SPI
USB
PERn1
PERp1
PETn1
PETp1
PERn2
PERp2
PETn2
PETp2
PERn3
PERp3
PETn3
PETp3
PERn4
PERp4
PETn4
PETp4
PERn5
PERp5
PETn5
PETp5
PERn6
PERp6
PETn6
PETp6
C326 0.1UF/16V /SB
C327 0.1UF/16V /SB
C465 0.1UF/16V /SB
C466 0.1UF/16V /SB
PCIE_TXN2_C
PCIE_TXP2_C
PCIE_TXN3_C
PCIE_TXP3_C
DMI0RXN
DMI0RXP
DMI0TXN
DMI0TXP
DMI1RXN
DMI1RXP
DMI1TXN
DMI1TXP
DMI2RXN
DMI2RXP
DMI2TXN
DMI2TXP
DMI3RXN
DMI3RXP
DMI3TXN
DMI3TXP
DMI_TXN0
DMI_TXP0
DMI_RXN0
DMI_RXP0
DMI_TXN1
DMI_TXP1
DMI_RXN1
DMI_RXP1
DMI_CLKN
DMI_CLKP
CLK_PCIE_ICH# [4]
CLK_PCIE_ICH [4]
DMI_ZCOMP
DMI_IRCOMP
DMI_COMP
R248 24.9Ohm 1% /SB
+1.5VS_PCIE_ICH
SPI_CLK
SPI_CS#
SPI_ARB
SPI_MOSI
SPI_MISO
USBP0N
USBP0P
USBP1N
USBP1P
USBP2N
USBP2P
USBP3N
USBP3P
USBP4N
USBP4P
USBP5N
USBP5P
USBP6N
USBP6P
USBP7N
USBP7P
USB_PN1 [27]
USB_PP1 [27]
USB_PN2 [39]
USB_PP2 [39]
USB_PN4 [39]
USB_PP4 [39]
USB_PN5 [23]
USB_PP5 [23]
USB_PN6 [23]
USB_PP6 [23]
USB_PN7 [39]
USB_PP7 [39]
USBRBIAS#
USBRBIAS
R252 22.6Ohm 1% /SB
USB 0 USB Conn
USB 1 USB Conn
USB 2 NA
USB 3 NA
USB 4 Card Reader
USB 5 WIFI
USB 6 Bluetooth
USB 7 Camera
SR41 10KOhm /SB
+3VSUS
USB_OC#0
USB_OC#1
USB_OC#23
USB_OC#4
USB_OC#5
USB_OC#6
USB_OC#7
RN2A
RN2C
RN2D
RN2E
Do Not Stuff
CRB & Checklist
1008HA
Title : SB-ICH7M(2)
ASUSTeK COMPUTER INC.
Engineer: Aaron Tsao
Size Custom
Project Name 1008HA
Rev 1.3G
Date: Friday, March 27, 2009
Sheet 16 of 49


+3VS
R138 10KOhm /SB
R137 10KOhm /SB
STP_PCI#
STP_CPU#
T252
[5] SYS_RESET#
T254
N/A
[8] PM_BMBUSY#
SMB_ALERT#
[4] STP_PCI#
[4] STP_CPU#
[39] CARD_READER_EN#
PM_CLKRUN#
PCB_ID1
PCB_ID2
[35] THRM_ALERT#
R304
Do Not Stuff
/SB/X
r0201
[8,32,43] VRM_PWRGD
R253
0Ohm
/SB
[37] Wireless_LED
[32] EXTSMI#
05/12/30, refer Z96J R1.01 to delete and change net name from VRMPWRGD to VRM_PWRGD.
Isolate alert# signal from thermal IC
U2C
S_SMB_CLK
S_SMB_DATA
LINKALERT#
SMB_LINK0
SMB_LINK1
RING#
SB_SPKR
SUS_STAT#
SMBCLK
SMBDATA
LINKALERT#
SMLINK0
SMLINK1
RI#
SPKR
SUS_STAT#
SYS_RST#
GPIO0/BM_BUSY#
SMBALERT#/GPIO11
GPIO18/STPPCI#
GPIO20/STPCPU#
GPIO26
GPIO27
GPIO28
GPIO32/CLKRUN#
GPIO33/AZ_DOCK_EN#
GPIO34/AZ_DOCK_RST#
WAKE#
SERIRQ
THRM#
VRMPWRGD
GPIO6
GPIO7
GPIO8
SMB
SYS GPIO
GPIO
ICH7M
/SB
SATA GPIO
Clocks
Power MGT
GPIO21/SATA0GP
GPIO19/SATA1GP
GPIO36/SATA2GP
GPIO37/SATA3GP
CLK14
CLK48
SUSCLK
SLP_S3#
SLP_S4#
SLP_S5#
PWROK
GPIO16/DPRSLPVR
TP0/BATLOW#
PWRBTN#
LAN_RST#
RSMRST#
GPIO9
GPIO10
GPIO12
GPIO13
GPIO14
GPIO15
GPIO24
GPIO25
GPIO35
GPIO38
GPIO39
SIMCARD_IN# [36]
CLK_REF_ICH [4]
CLK_48M_USB [4]
T250
Do Not Stuff
N/A
PM_SUSB# [32]
PM_SUSC# [32]
T253
PM_PWROK [33,41]
PM_DPRSLPVR [8]
PM_BATLOW# [32]
PM_PWRBTN# [32]
Int P.U
PLT_RST# [8,16,23,39]
PM_RSMRST# [33,41]
SATA_DET#0
WLAN_ON# [16]
KBC_SCI# [32]
3GLAN_OFF
CB_SD#
MINICARD_EN#
+1.5V_SEL# [44]
CAMERA_EN [39]
GPIO38
GPIO39
GPIO36
R254
0Ohm
/SB
BT_DIS# [23]
SC1
Do Not Stuff
/SB/X
GND
+3VS
SR45
10KOhm
/SB
GPIO19
Q51
Do Not Stuff
/SB/X
THRO_CPU [32]
GND
3GLAN_OFF
R5
Do Not Stuff
/SB/X
WLAN_ON#
R4
0Ohm
/WIFI or /3G/X
WLAN_ON#_3G_OFF [23]
GPIO25 Internal PU 20K
For +1.5V DIMM Power
S_SMB_CLK
S_SMB_DATA
S_SMB_CLK [4]
S_SMB_DATA [4]
WLAN_LED | WLAN | BT
High | v | v
High | v | x
High | x | v
Low | x | x
+3VSUS
R250 10KOhm /SB
R251 10KOhm /SB
SYS_RESET# [5]
RING#
3GLAN_OFF
MINICARD_EN#
WLAN_ON#
MODEM_EN
RN2B
RN2C
RN2A
RN2B
/SB
Q56A
UM6K1N
/SB
S_SMB_CLK
SMB_CLK [18,23]
+5VS
S_SMB_DATA
SMB_DATA [18,23]
Q56B
UM6K1N
/SB
SMB_CLK
SMB_DATA
4.7KOhm
RN95B
RN95A
RN95C
RN95D
S_SMB_CLK
S_SMB_DATA
CAMERA_EN
R871 10KOhm /SB
PCIE_WAKE#
R872 10KOhm /SB
Energy Star
CARD_READER_EN#
SMB_ALERT#
SMB_LINK0
SMB_LINK1
10KOhm
RN98C
RN3C
RN96C
RN96B
THRM_ALERT# S
INT_SERIRQ
PM_CLKRUN#
VRM_PWRGD
RN97D
RN97C
RN101C
RN97B
GPIO36
GPIO21
GPIO38
GPIO39
RN101A
RN101D
RN101B
RN97A
SATA_DET#0
PM_BATLOW#
CB_SD#
LINKALERT#
RN96A
RN98D
RN3D
RN96D
KBC_SCI#
EXTSMI#
GPIO13
RN98B
RN3B
RN3A
RN98A
PM_PWROK
R257 1MOhm /SB
PM_RSMRST#
R303 1MOhm /SB
R1.1G
PCB_ID[2:0]
000: R1.0
R259 10KOhm /SB
R260 10KOhm /SB
R261 10KOhm /SB
PCB_ID2
PCB_ID1
PCB_ID0
R1.02 for ESATA "111"
R262 Do Not Stuff /SB/X r0201
R263 Do Not Stuff /SB/X r0201
R264 Do Not Stuff /SB/X r0201
PCB_VID3 : PROJECT CODE
VRM_PWRGD
Do Not Stuff
T14
N/A
SR46
SB_SPKR
Do Not Stuff
/SB/X
[21] BL_EN
GPIO13
R255 Do Not Stuff /SB/X r0201
1008HA
Title : SB-ICH7M(3)
ASUSTeK COMPUTER INC
Engineer: Aaron Tsao
Size Custom
Project Name 1008HA
Rev 1.3G
Date: Friday, March 27, 2009
Sheet 17 of 49



1008HA

ASUSTek Computer INC.	Title : DDR2 SODIMM	
	Engineer: Aaron Tsao	
Size: A3	Project Name: 1008HA	Rev: 1.3G
Date: Friday, March 27, 2009	Sheet 18 of 49	



1008HA

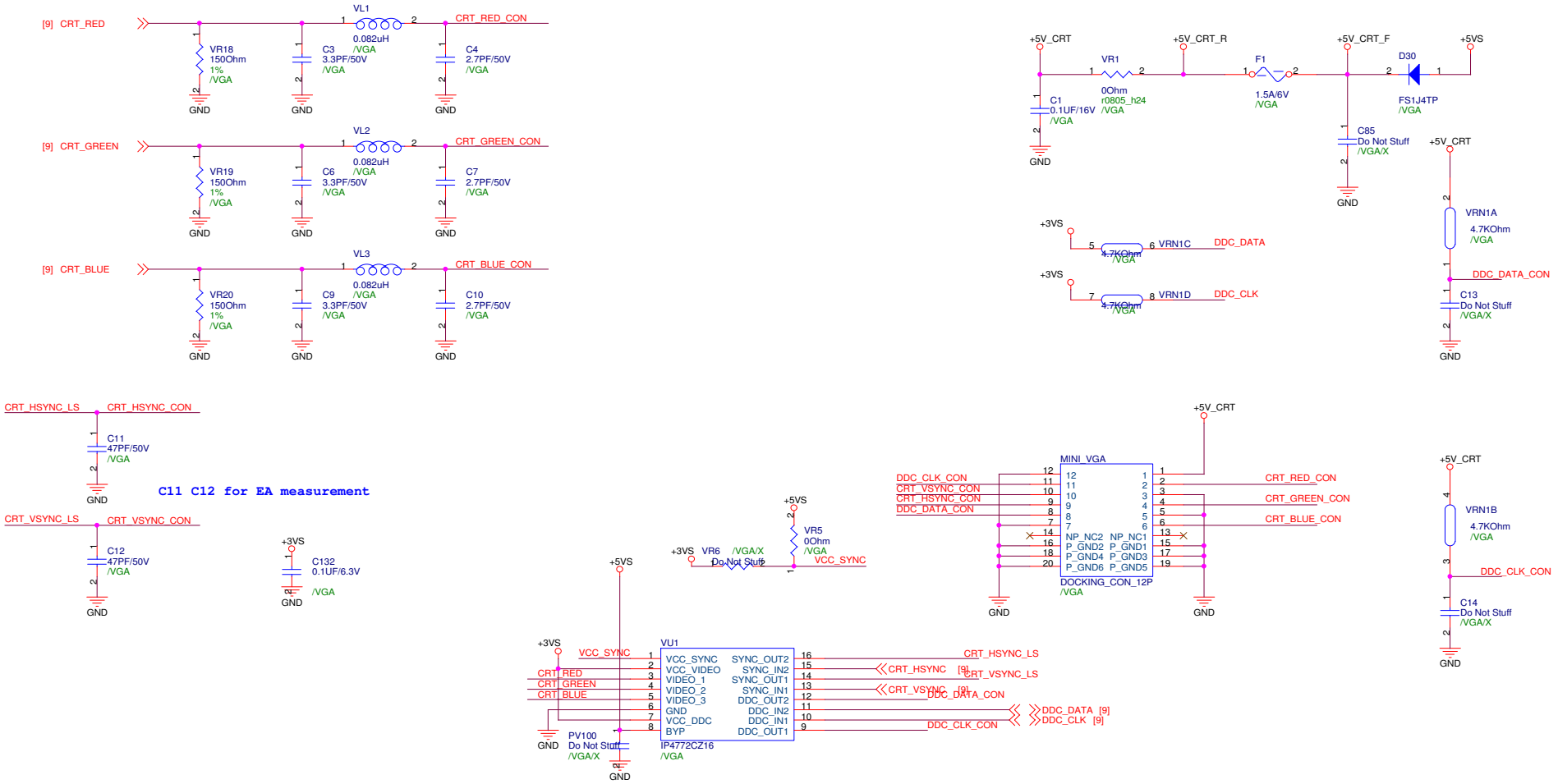
ASUSTek Computer INC.

Title : DDR2_Termination

Engineer: Aaron Tsao

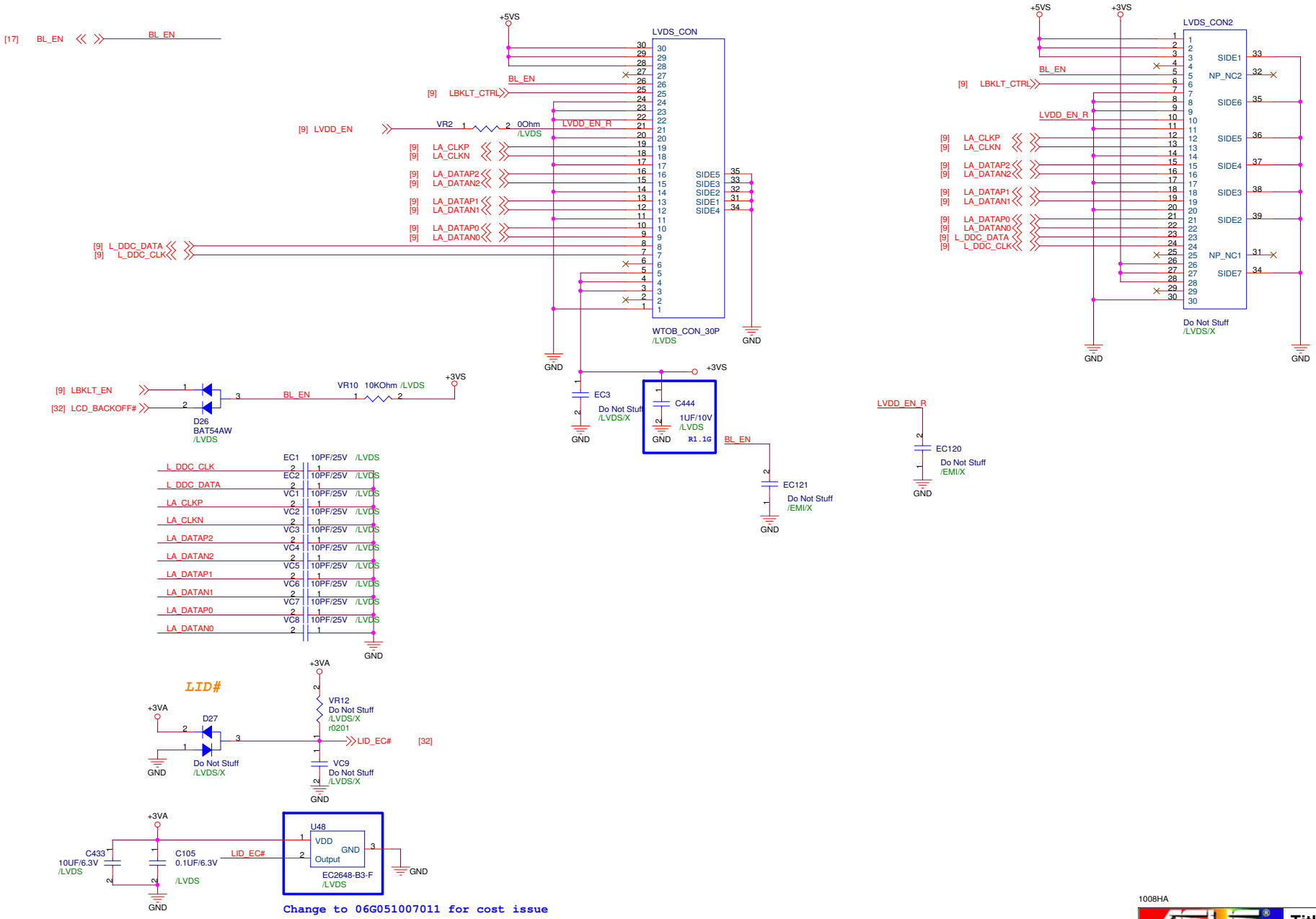
Size	Project Name	Rev
A4	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 19 of 49



1008HA

ASUSTek Computer INC.	Title : Onboard VGA	
	Engineer: Aaron Tsao	
Size: A3	Project Name: 1008HA	Rev: 1.3G
Date: Friday, March 27, 2009	Sheet 20 of 49	


[17] BL_EN
BL_EN
+5VS
LVDS_CON
[9] LBKLT_CTRL
[9] LVDD_EN
VR2 0Ohm /LVDS
LVDD_EN_R
[9] LA_CLKP
[9] LA_CLKN
[9] LA_DATAP2
[9] LA_DATAN2
[9] LA_DATAP1
[9] LA_DATAN1
[9] LA_DATAP0
[9] LA_DATAN0
[9] L_DDC_DATA
[9] L_DDC_CLK
SIDE5 SIDE3 SIDE2 SIDE1 SIDE4
WTOB_CON_30P /LVDS
GND
+3VS
EC3 Do Not Stuff /LVDS/X
C444 1UF/10V /LVDS
R1.1G
BL_EN
EC121 Do Not Stuff /EMI/X
LVDD_EN_R
EC120 Do Not Stuff /EMI/X
LVDS_CON2
SIDE1 NP_NC2 SIDE6 SIDE5 SIDE4 SIDE3 SIDE2 NP_NC1 SIDE7
Do Not Stuff /LVDS/X
[9] LBKLT_EN
[32] LCD_BACKOFF#
D26 BAT54AW /LVDS
VR10 10KOhm /LVDS
EC1 10PF/25V /LVDS
L_DDC_CLK
L_DDC_DATA
LA_CLKP
LA_CLKN
LA_DATAP2
LA_DATAN2
LA_DATAP1
LA_DATAN1
LA_DATAP0
LA_DATAN0
LID#
+3VA
D27 Do Not Stuff /LVDS/X
VR12 Do Not Stuff /LVDS/X r0201
LID_EC# [32]
VC9 Do Not Stuff /LVDS/X
C433 10UF/6.3V /LVDS
C105 0.1UF/6.3V /LVDS
U48 VDD GND Output EC2648-B3-F /LVDS
Change to 06G051007011 for cost issue
1008HA
Title : LVDS Conn_LID
ASUSTek Computer INC.
Engineer: Aaron Tsao
Size A3
Project Name 1008HA
Rev 1.3G
Date: Friday, March 27, 2009
Sheet 21 of 49
LID_EC#

1008HA

3.5G Module & External Antenna

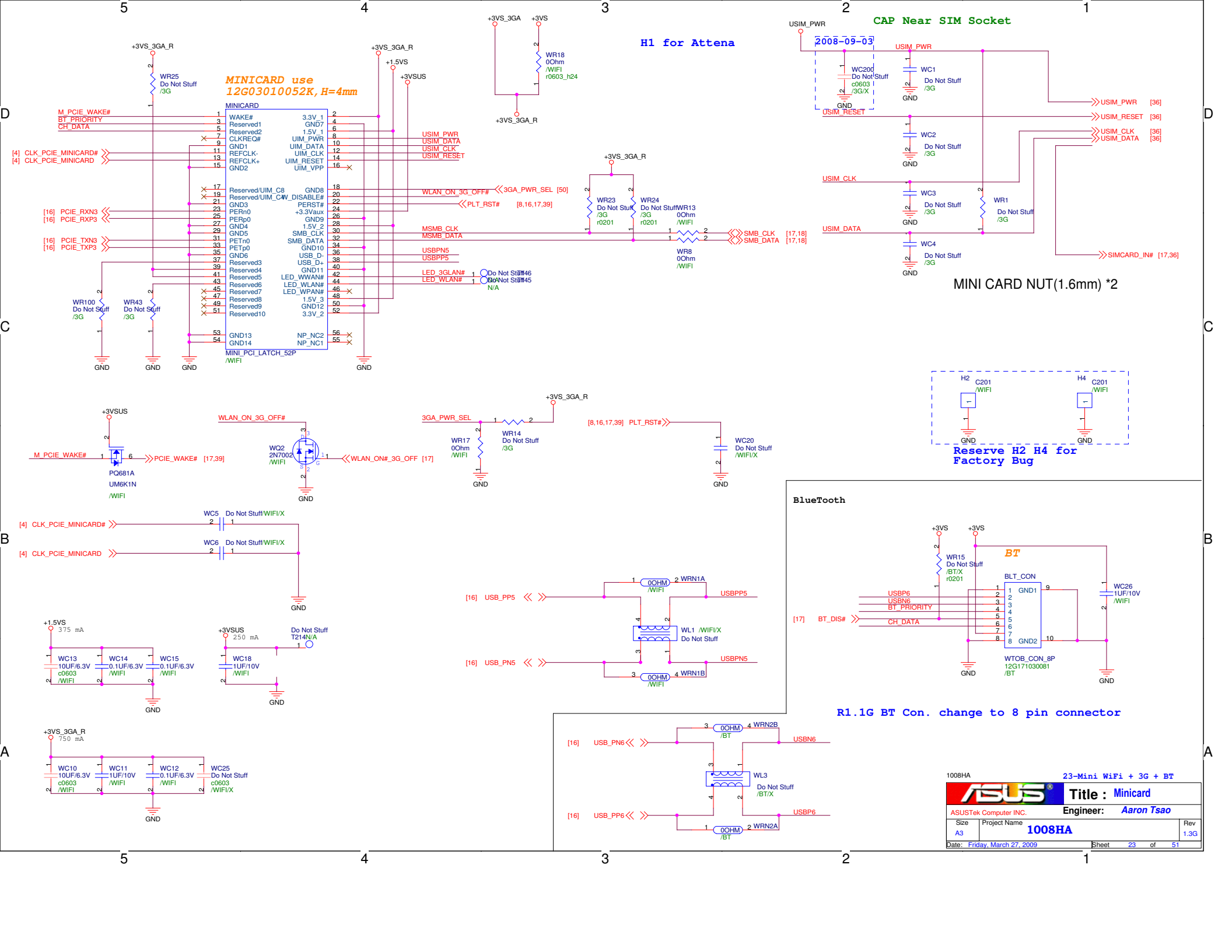
Title :

ASUSTek Computer INC.

Engineer: *Aaron Tsao*

Size	Project Name	Rev
A3	**1008HA**	1.3G

Date: Friday, March 27, 2009 Sheet 22 of 49


MINICARD use 12G03010052K, H=4mm
MINICARD
MINI_PCI_LATCH_52P
/WIFI
H1 for Attena
CAP Near SIM Socket
2008-09-03
MINI CARD NUT(1.6mm) *2
Reserve H2 H4 for Factory Bug
BlueTooth
BT
R1.1G BT Con. change to 8 pin connector
1008HA
23-Mini WiFi + 3G + BT
Title : Minicard
ASUSTek Computer INC.
Engineer: Aaron Tsao
Project Name 1008HA
Date: Friday, March 27, 2009
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1008HA

Title : AR8132

ASUSTek Computer INC

Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 24 of 49

1008HA

Title : RJ45

ASUSTek Computer INC.

Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 25 of 49

Naming Rule:
IC:IU?
R:IR?
C:IC?
L:IL?

1008HA

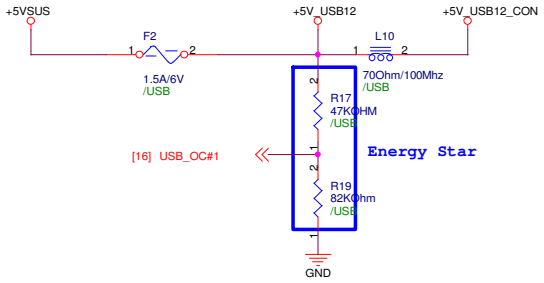
Title : G-Sensor

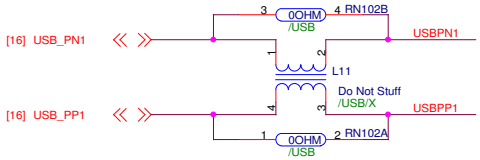
ASUSTek Computer INC.

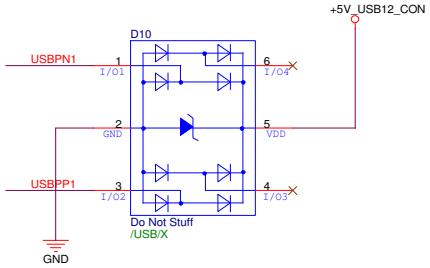
Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

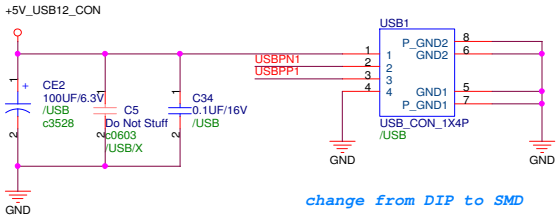
Date: Friday, March 27, 2009 Sheet 26 of 49





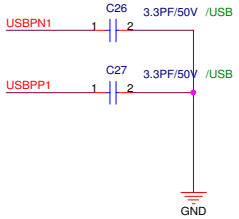


1.1G change USB con. to 12G131030042



change from DIP to SMD

1.1G change CE2 CE3 CE4 to POSCAP, 100uF/6.3V



1008HA

ASUSTek Computer INC.	Title : USB Port	
	Engineer: Aaron Tsao	
Size: A3	Project Name: 1008HA	Rev: 1.3G
Date: Friday, March 27, 2009	Sheet 27 of 49	

1008HA

Title : Camera Power

ASUSTek Computer INC.

Engineer: Aaron Tsao

Size	Project Name	Rev
A4	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 28 of 49

1008HA

ASUSTek Computer Inc.	Title : AU6336-C52	
	Engineer: Aaron Tsao	
Size: A3	Project Name: 1008HA	Rev: 1.3G
Date: Friday, March 27, 2009	Sheet 29 of 49	

1008HA

ASUSTek Computer Inc.	Title : ALC269-1	
	Engineer: Aaron Tsao	
Size: A3	Project Name: 1008HA	Rev: 1.3G
Date: Friday, March 27, 2009	Sheet 30 of 49	

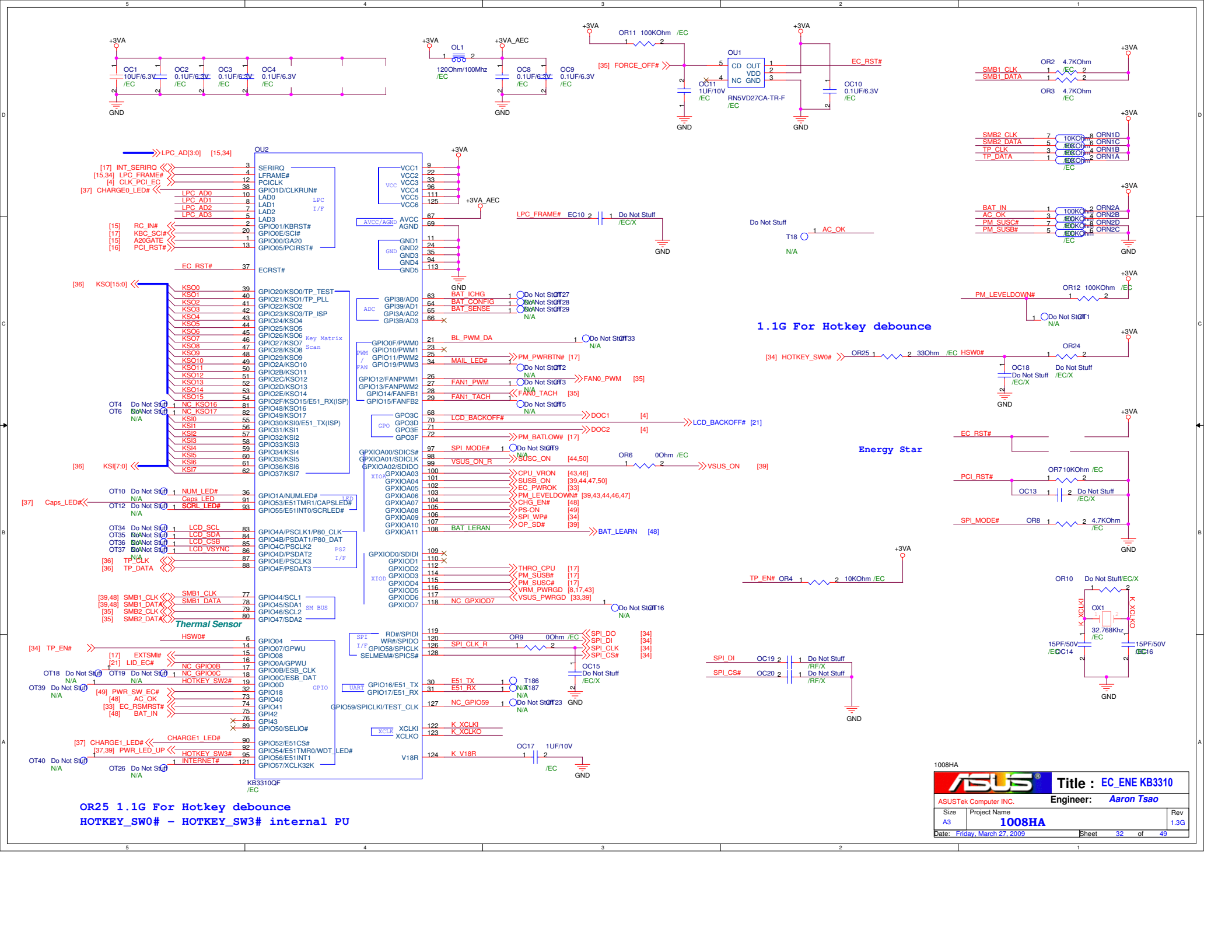
1.1G add PWR LED and Charge LED

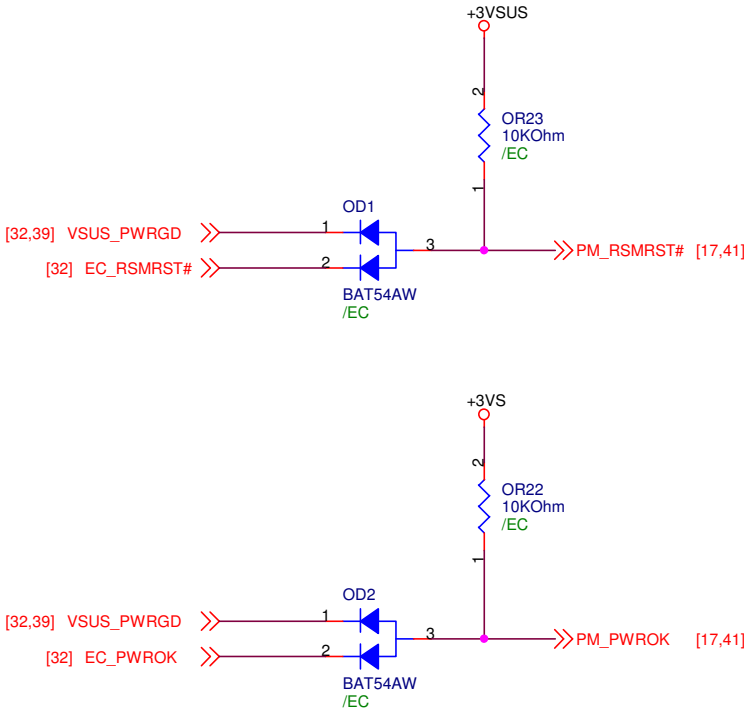
DMIC Cable length should be less 30cm

Change R291 R292 R293 to 510 Ohm

1008HA

ASUSTek Computer Inc.	Title : ALC269-2		
	Engineer: Aaron Tsao		
Size: A3	Project Name: 1008HA		Rev: 1.3G
Date: Friday, March 27, 2009	Sheet 31 of 49		


OU2
KB3310QF
/EC
Thermal Sensor
1.1G For Hotkey debounce
Energy Star
OR25 1.1G For Hotkey debounce
HOTKEY_SW0# - HOTKEY_SW3# internal PU
1008HA
Title : EC_ENE KB3310
ASUSTek Computer INC.
Engineer: Aaron Tsao
Size A3
Project Name 1008HA
Rev 1.3G
Date: Friday, March 27, 2009
Sheet 32 of 49



1008HA

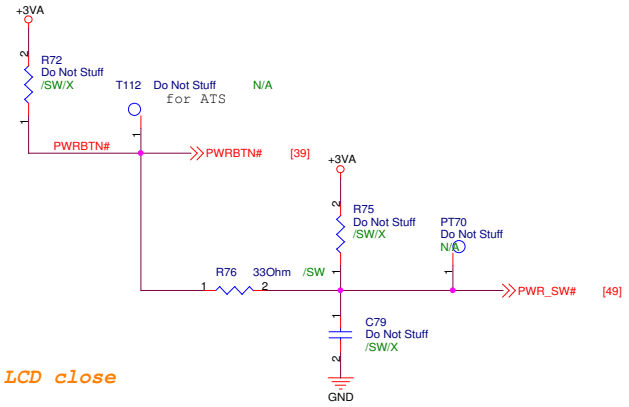
ASUSTek Computer INC.

Title : EC

Engineer: Aaron Tsao

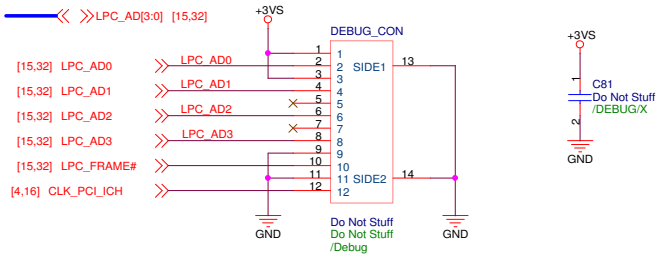
Size	Project Name	Rev
A4	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 33 of 49

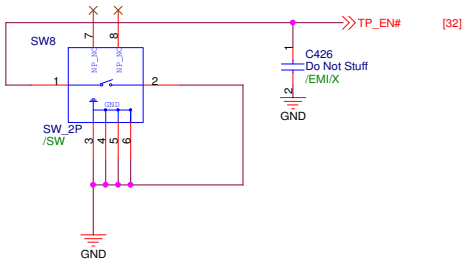


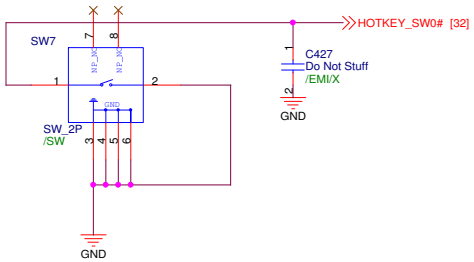
prevent system power on when LCD close

For Debug

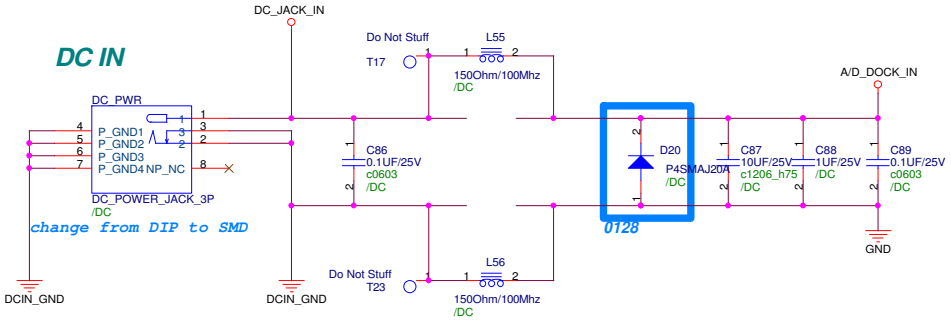


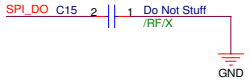
Debug Card cable use Z96 Touch Pad cable, P/N:
14G124110126, 14G124110120, 14G124110121
14G124110124, 14G124110125

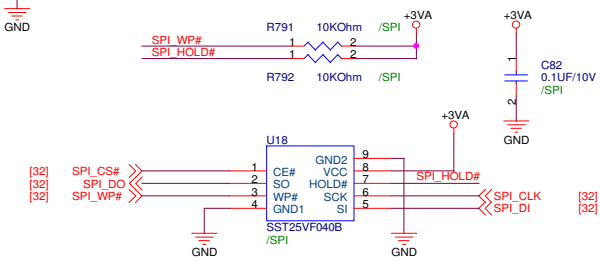




DC IN

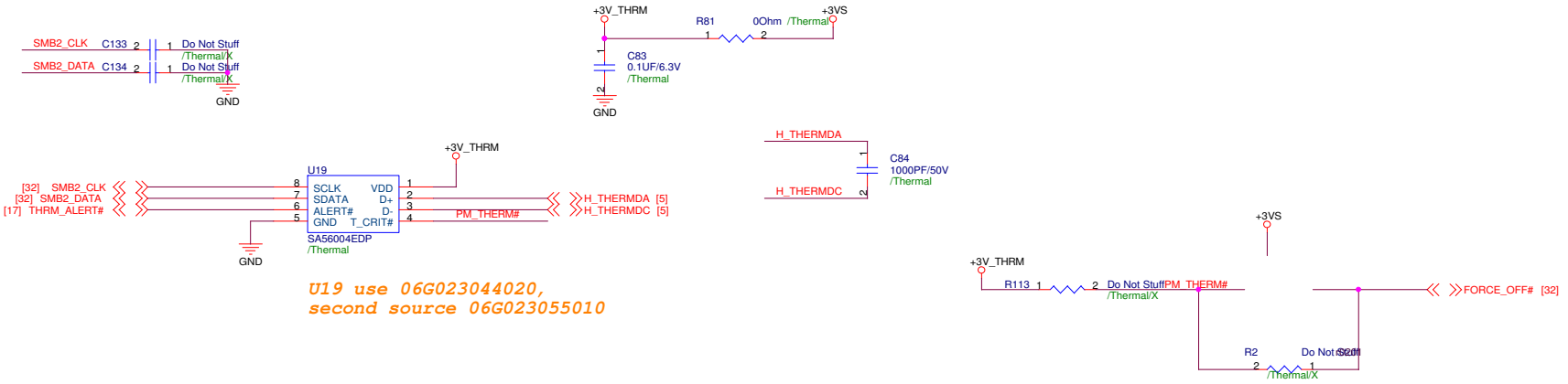


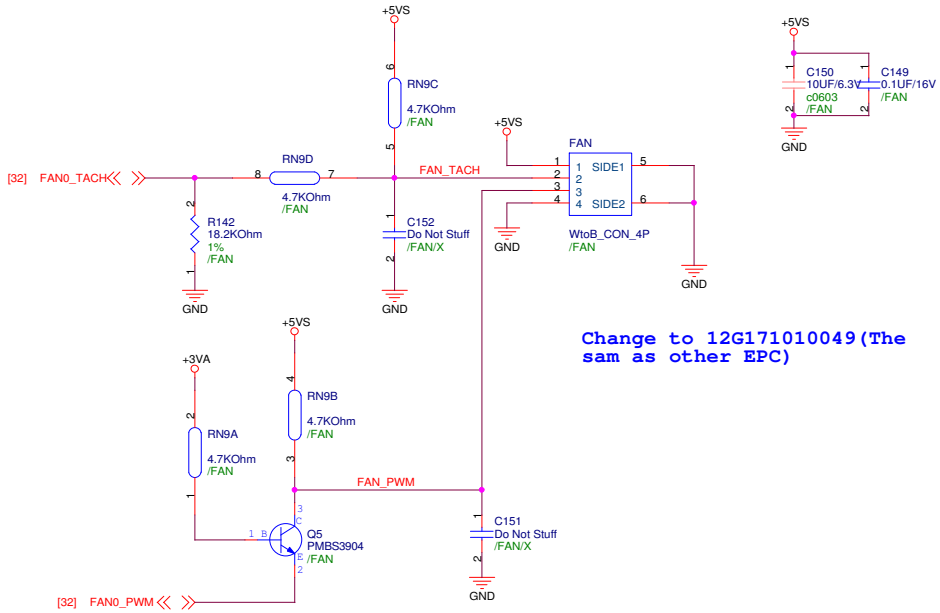




1008HA

ASUSTek Computer INC.	Title : 34_Switch_SPI _ITP_Debug	
	Engineer: Aaron Tsao	
Size: A3	Project Name: 1008HA	Rev: 1.3G
Date: Friday, March 27, 2009	Sheet 34 of 49	





1008HA

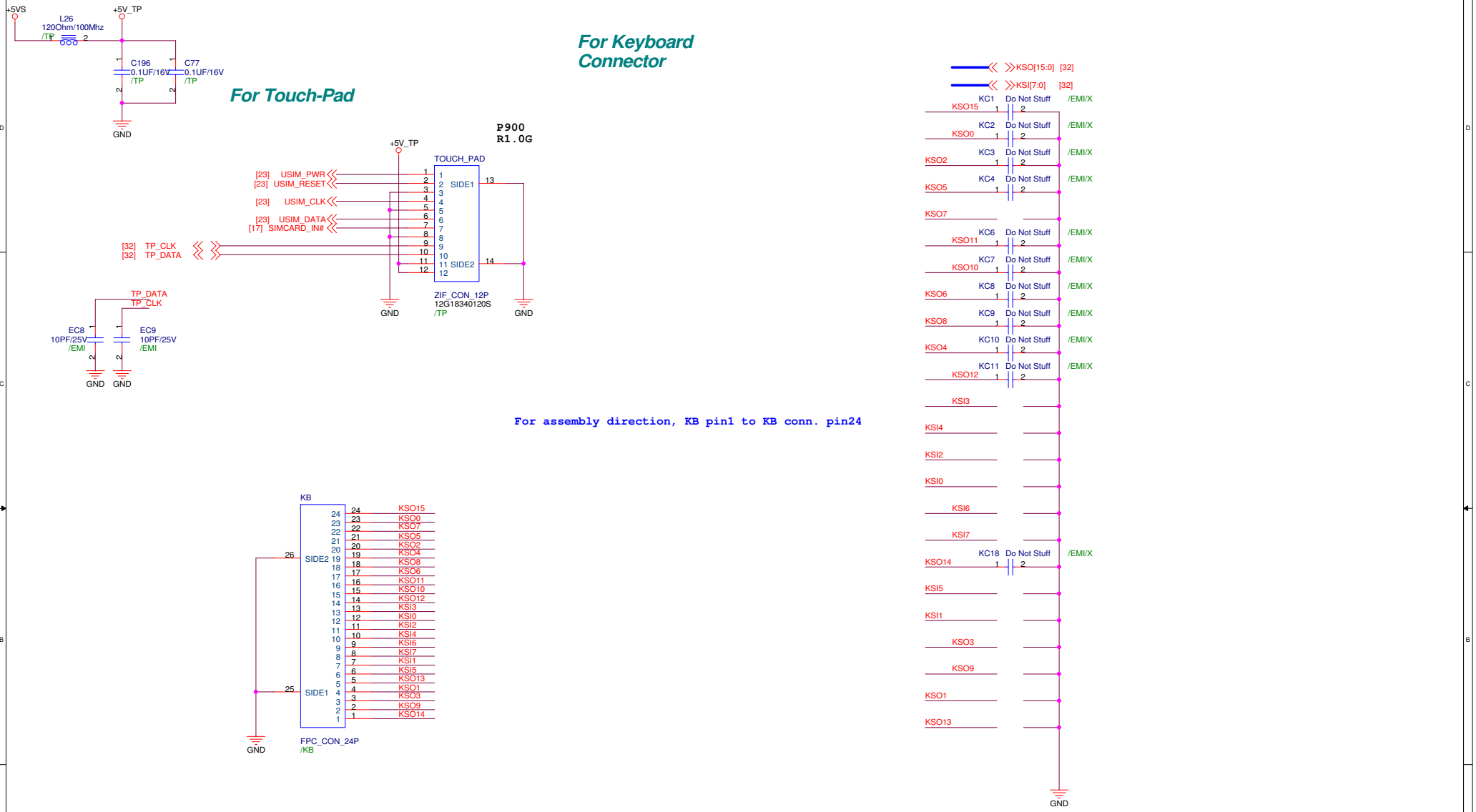
ASUSTek Computer INC.

Title : Thermal Sensor_FAN

Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 35 of 49

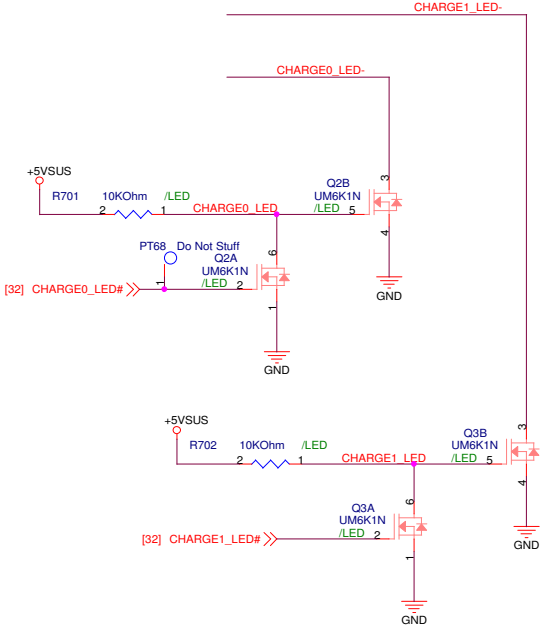


1008HA

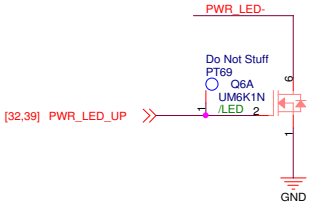
ASUSTek Computer INC.	Title : KB_Touch Pad	
	Engineer: Aaron Tsao	
Size: A3	Project Name: 1008HA	Rev: 1.3G
Date: Friday, March 27, 2009	Sheet 36 of 49	

1.1G change to EVERLIGHT

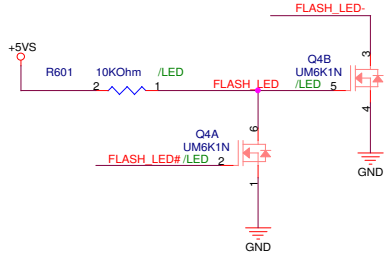
for CHARGE LED
Height : 0.55mm



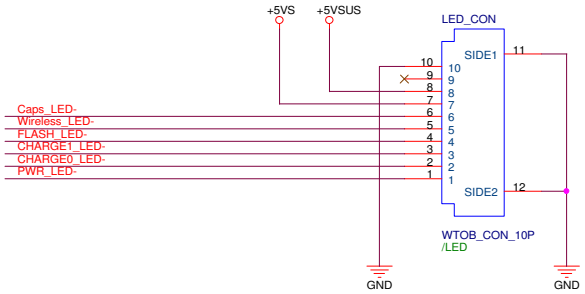
for POWER LED
White



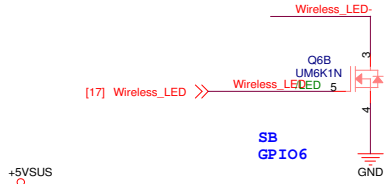
for FLASH LED
White



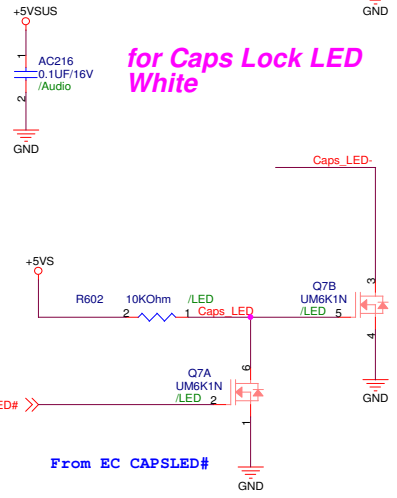
Change LED resistor to 510 Ohm, about 4mA

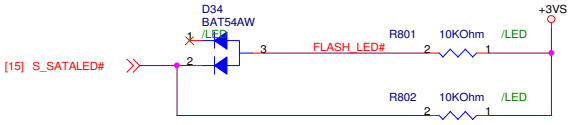


for WIFI/BlueTooth LED
White



for Caps Lock LED
White





1008HA

ASUSTek Computer INC.	Title : LED	
	Engineer: Kenneth_Hung	
Size: A3	Project Name: 1008HA	Rev: 1.3G
Date: Friday, March 27, 2009	Sheet 37 of 50	

1008HA

Title : Discharge

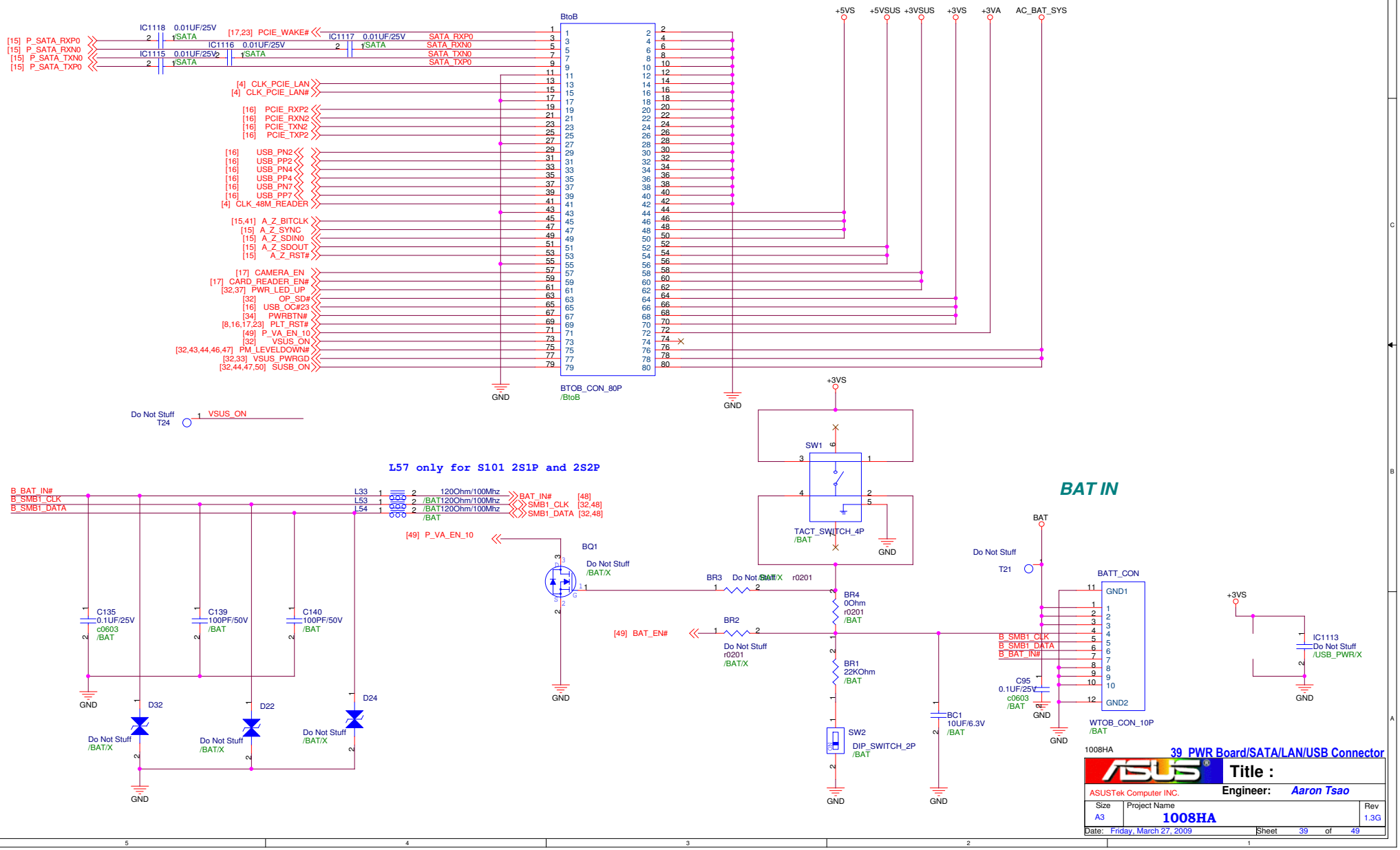
ASUSTek Computer INC.

Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 38 of 49

PWR Board/SATA/LAN/USB Connector



1008HA

39 PWR Board/SATA/LAN/USB Connector

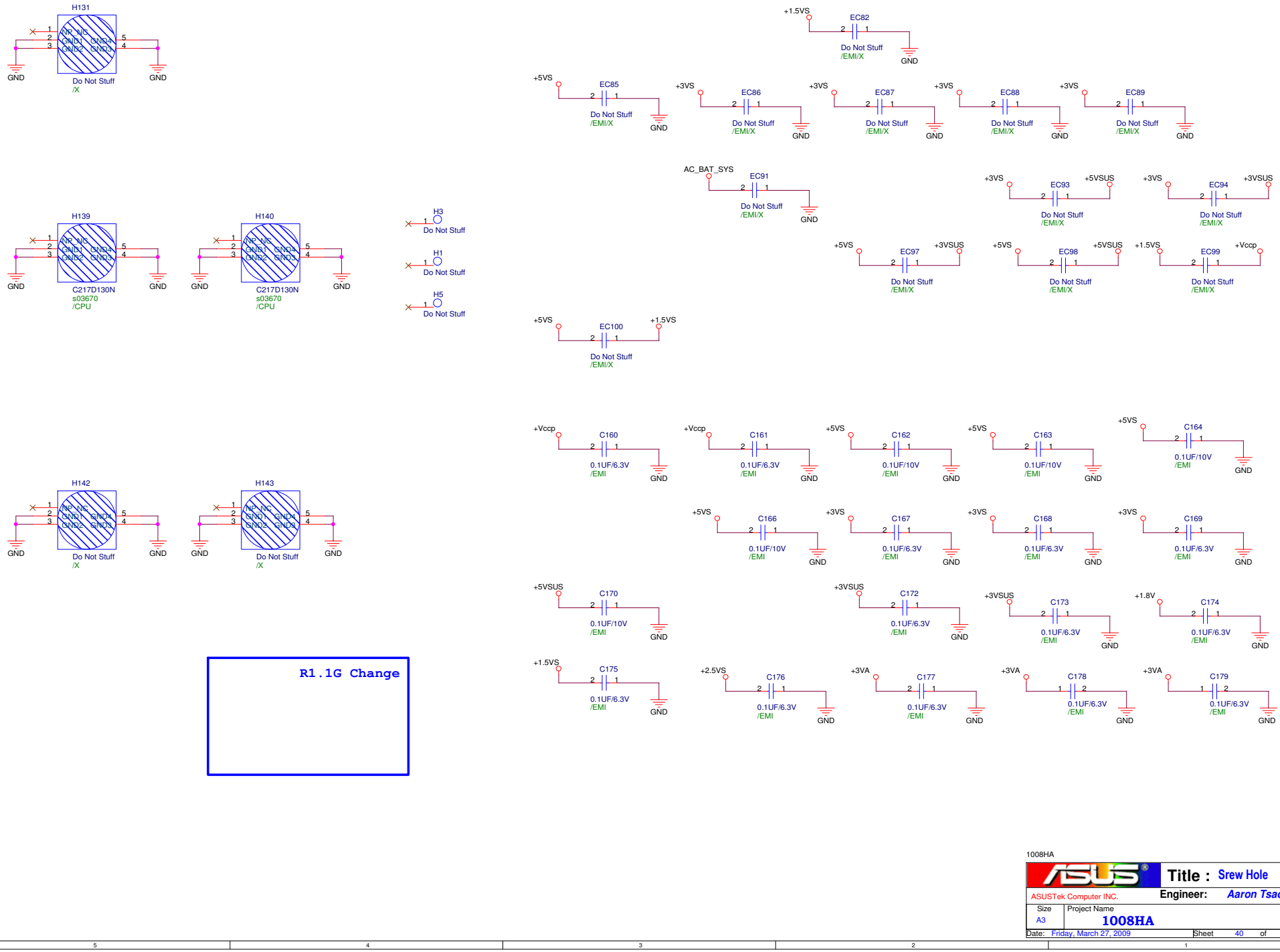
ASUSTek Computer INC.

Title :

Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 39 of 49



1008HA

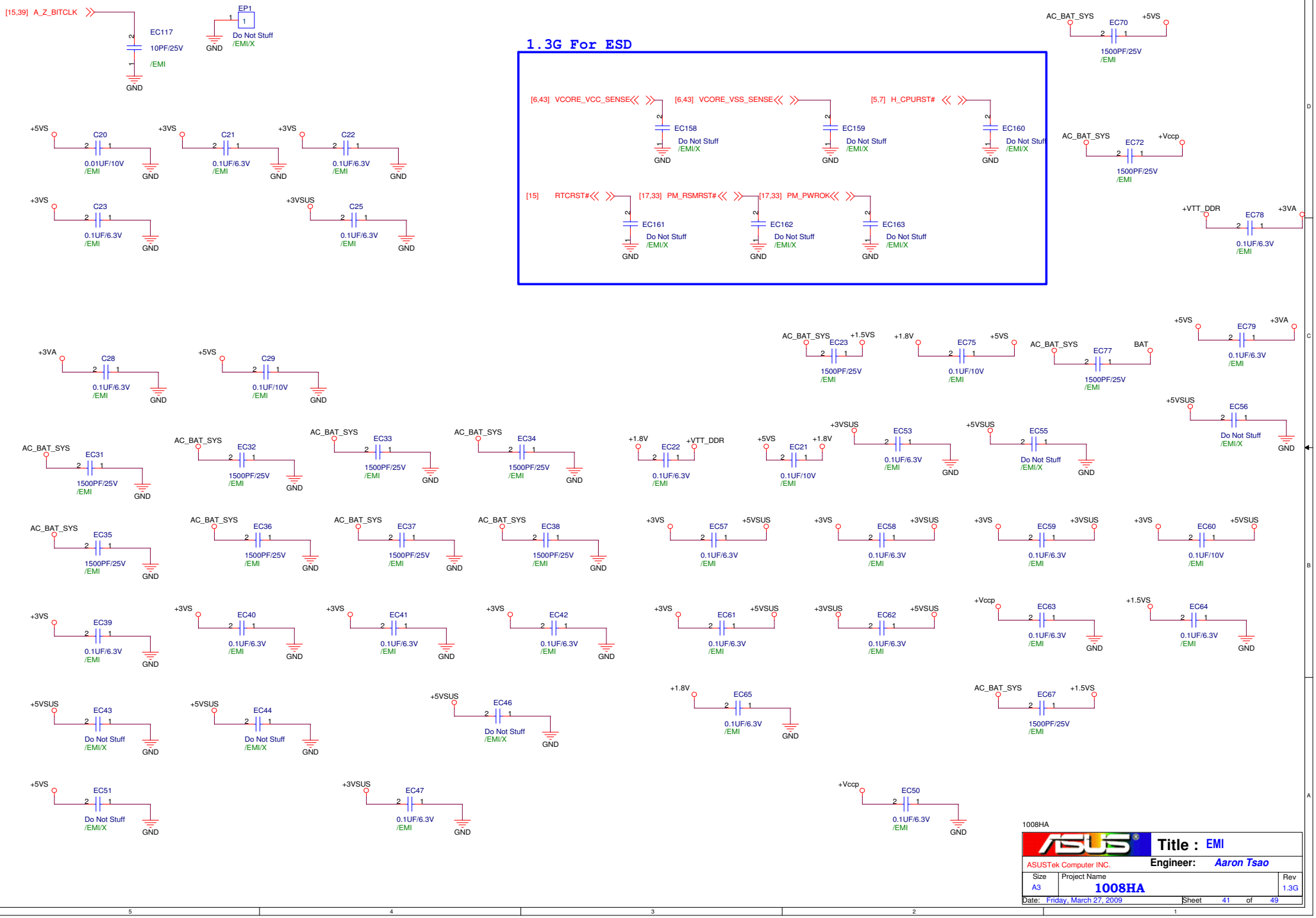
ASUSTek Computer INC.

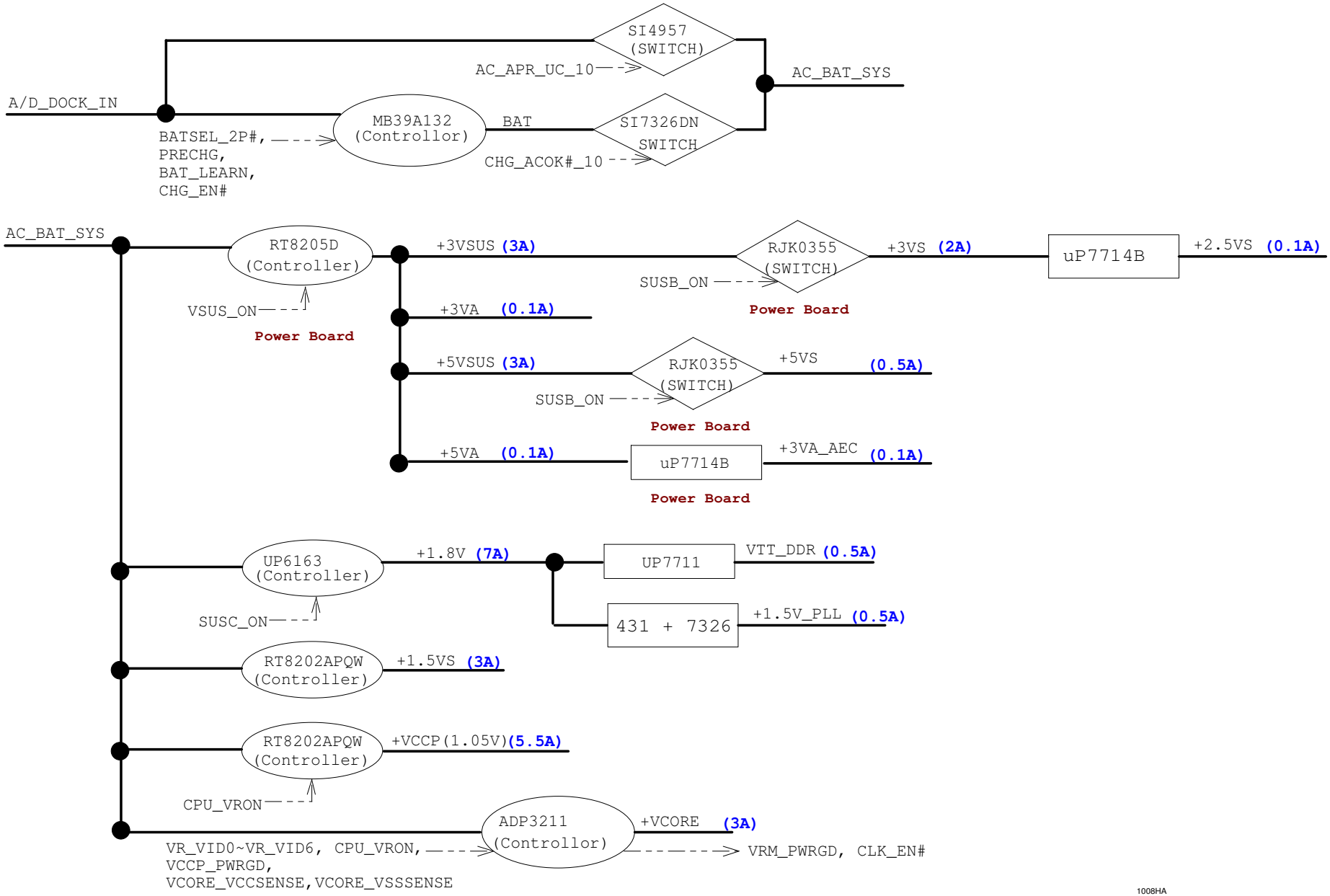
Title : Srew Hole

Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

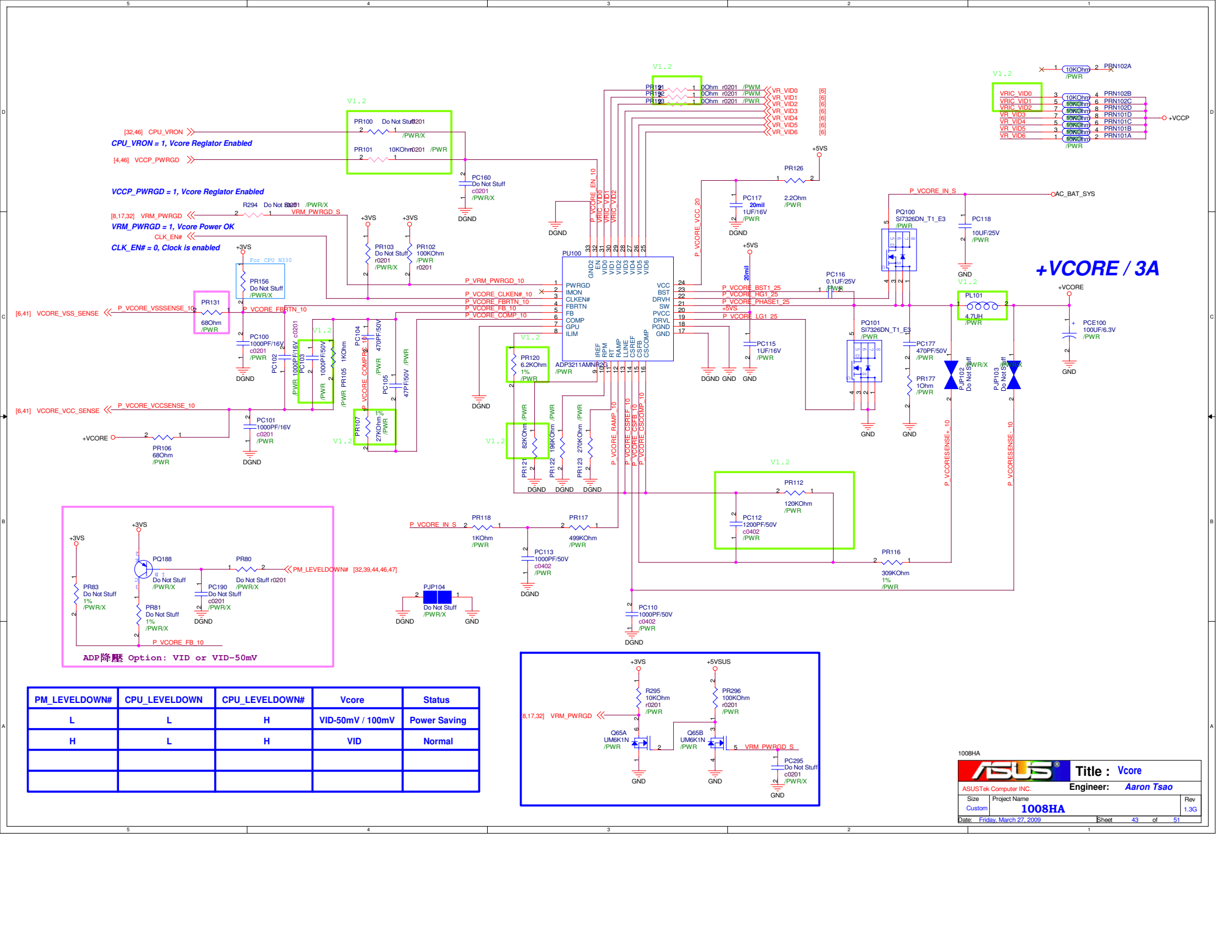
Date: Friday, March 27, 2009 Sheet 40 of 49


[15,39] A_Z_BITCLK
EC117
10PF/25V
/EMI
GND
EP1
Do Not Stuff
/EMI/X
1.3G For ESD
[6,43] VCORE_VCC_SENSE
[6,43] VCORE_VSS_SENSE
[5,7] H_CPURST#
EC158
EC159
EC160
Do Not Stuff
/EMI/X
[15] RTCRST#
[17,33] PM_RSMRST#
[17,33] PM_PWROK
EC161
EC162
EC163
AC_BAT_SYS
EC70
+5VS
1500PF/25V
/EMI
EC72
+Vccp
EC78
+VTT_DDR
+3VA
0.1UF/6.3V
/EMI
+5VS
C20
0.01UF/10V
/EMI
+3VS
C21
0.1UF/6.3V
/EMI
C22
C23
+3VSUS
C25
+3VA
C28
C29
0.1UF/10V
/EMI
EC23
+1.5VS
EC75
EC77
BAT
EC79
EC56
+5VSUS
EC31
EC32
EC33
EC34
+1.8V
EC22
EC21
+3VSUS
EC53
EC55
EC35
EC36
EC37
EC38
EC57
EC58
EC59
EC60
EC39
EC40
EC41
EC42
EC61
EC62
EC63
EC64
EC43
EC44
EC46
EC65
EC67
EC51
EC47
EC50
1008HA
ASUSTek Computer INC.
Title : EMI
Engineer: Aaron Tsao
Size A3
Project Name 1008HA
Rev 1.3G
Date: Friday, March 27, 2009
Sheet 41 of 49



1008HA

ASUSTek Computer INC.	Title : Power Flow	
	Engineer: Aaron Tsao	
Size: A3	Project Name: 1008HA	Rev: 1.3G
Date: Friday, March 27, 2009	Sheet 42 of 49	



PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Vcore	Status
L	L	H	VID-50mV / 100mV	Power Saving
H	L	H	VID	Normal

1008HA

ASUSTek Computer INC.

Title : Vcore

Engineer: Aaron Tsao

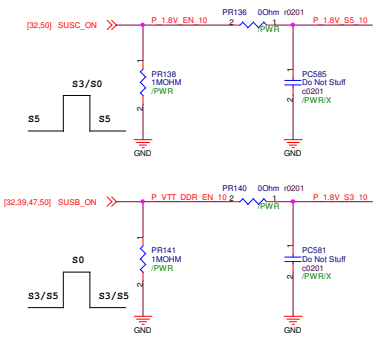
Size	Project Name	Rev
Custom	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 43 of 51

S3 And S5 Truth Table

State	S3	S5	VDDQ
S0	Hi	Hi	On
S3	Low	Hi	On
S4/S5	Low	Low	Off (Discharge)

State	VTTREF	VTT
S0	On	On
S3	On	Off(Hi-Z)
S4/S5	Off (Discharge)	Off (Discharge)



Controller

1. Voltage & Current:
   +1.8V@3A
2. Frequency:
   PR127=1Mohm Fosc=250KHz
3. OCP:
   PR181=22Kohm ->10A
4. POR:
   POR Hysteresis =20mV
   V on =3.9-4.5V
5. UVP:
   Vout*70%
6. OVP:
   Vout*115%
7. Enable Voltage:
   V rising = 2 V
   V falling = 0.8V
8. Soft start time:
   Tss=512us
9. Phase selection:
   /X
10. Inrush Current:
    C total = 110 uF
    I inrush= 0.39 A

Power stage

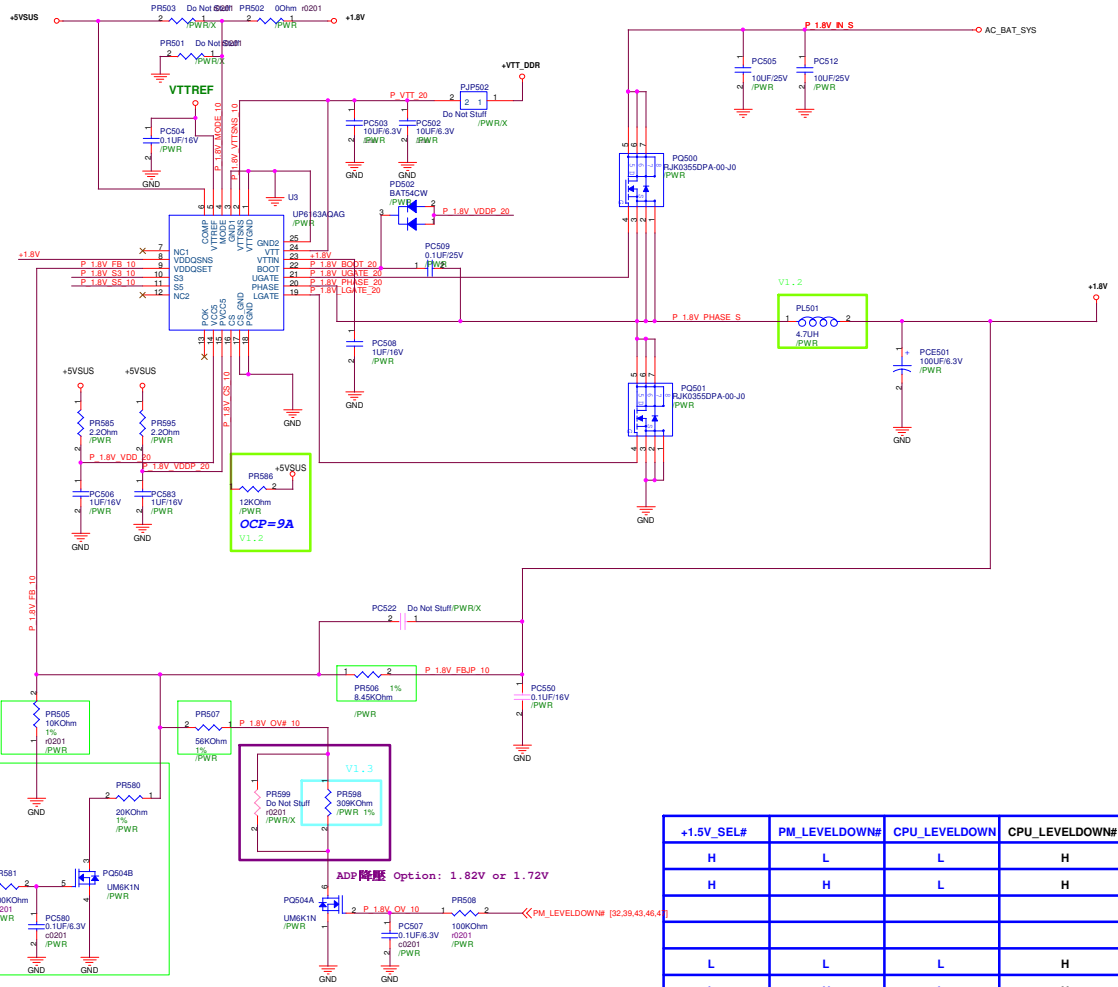
1. I/P Current:
   I in = Vo*Io/(0.8 * Vin) =0.94A
2. Ripple Current:
   I rip= 1.78A
   I spec=2A ×1 pcs
3. Dynamic:
   I peak=5 A
   ESR =18 mohm
   △V =90mV
4. Inductor Spec:
   I sat=10 A
   I dc =5.5 A
   DCR=37 mohm
5. MOSFET Spec:

   H-side MOSFET: AP4800AGM

   Rds(ON)= 21 mohm (Vgs=4.5 V)
   I cont = 9.6 A (T =25 ℃)
   I peak = 40 A

   L-side MOSFET: RAP4800AGM

   Rds(ON)= 21 mohm (Vgs=4.5 V)
   I cont = 9.6 A (T =25 ℃)
   I peak = 40 A



+1.5V_SEL#	PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Voltage	Status
H	L	L	H	1.72V	Power Saving
H	H	L	H	1.82V	Normal
L	L	L	H	1.4V	Power Saving
L	H	L	H	1.5V	Normal

1008HA

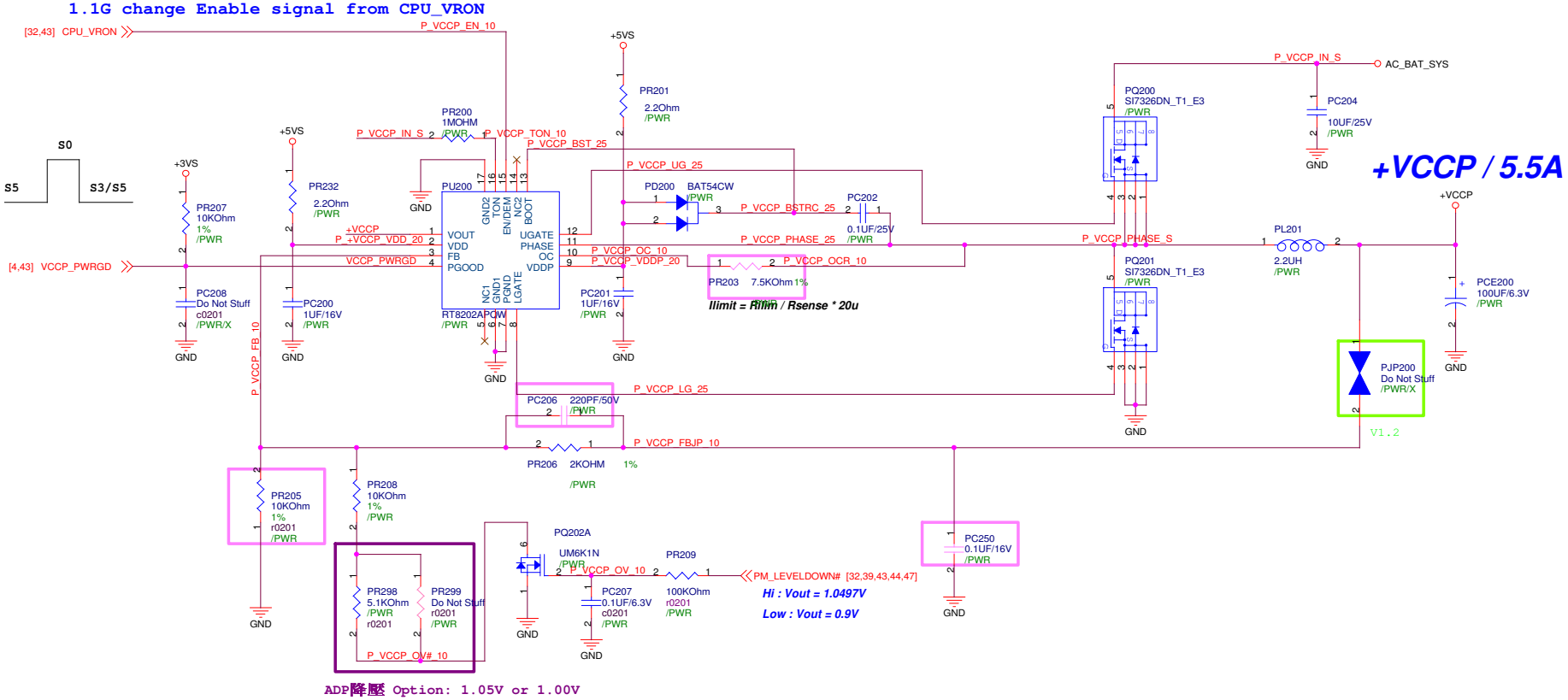
ASUSTek Computer INC.

Title : +1.8V & VTTDDR

Engineer: Aaron Tsao

Size	Project Name	Rev
A2	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 44 of 47



PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Voltage	Status
L	L	H	0.9V	Power Saving
H	L	H	1.05V	Normal

1008HA

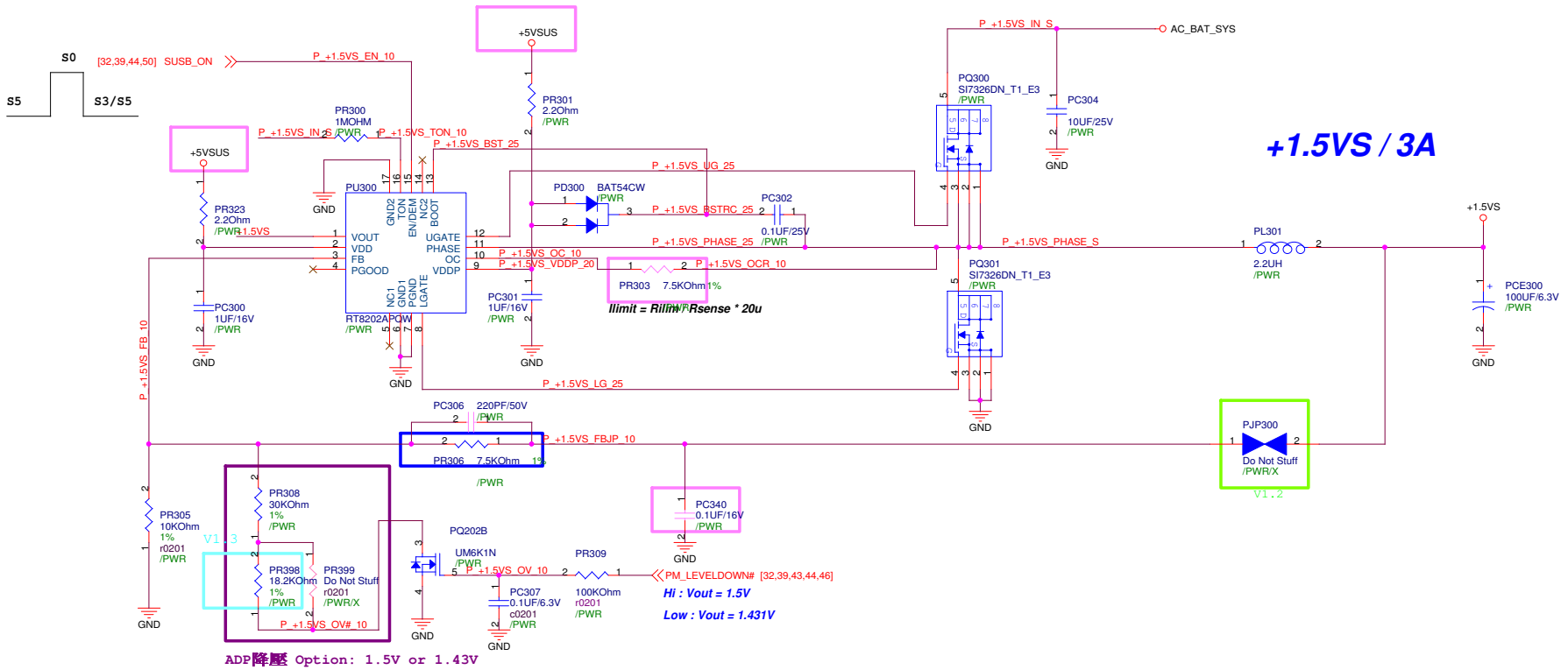
ASUSTek Computer INC.

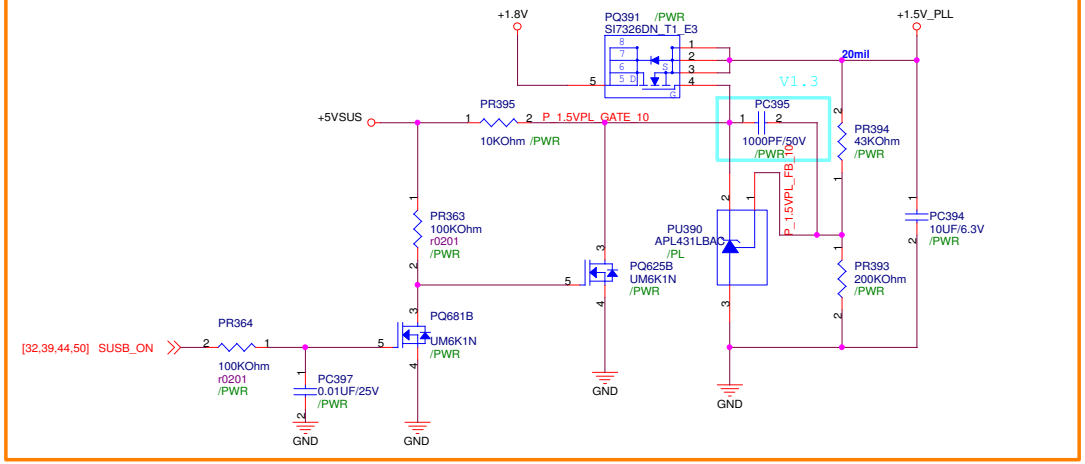
Title : VCCP

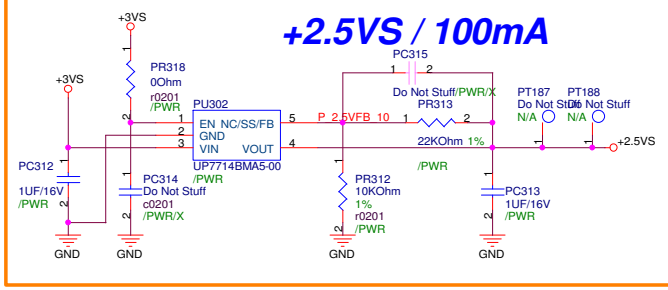
Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 46 of 51







1008HA

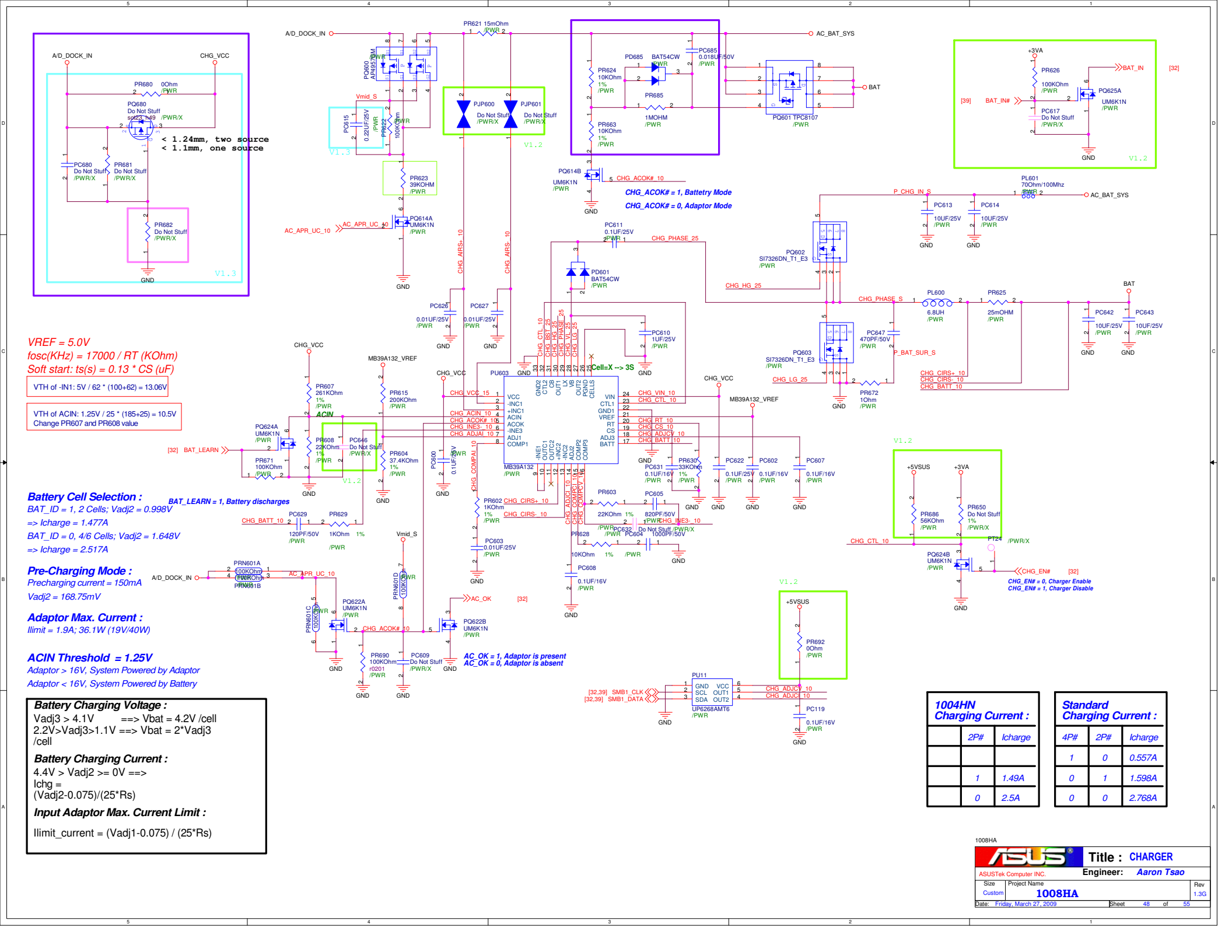
ASUSTek Computer INC.

Title : +1.5VS & +2.5VS

Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 47 of 51



VREF = 5.0V
fosc(KHz) = 17000 / RT (KOhm)
Soft start: ts(s) = 0.13 * CS (uF)

VTH of -IN1: 5V / 62 * (100+62) = 13.06V

VTH of ACIN: 1.25V / 25 * (185+25) = 10.5V
Change PR607 and PR608 value

Battery Cell Selection :
BAT_LEARN = 1, Battery discharges
BAT_ID = 1, 2 Cells; Vadj2 = 0.998V
=> Icharge = 1.477A
BAT_ID = 0, 4/6 Cells; Vadj2 = 1.648V
=> Icharge = 2.517A

Pre-Charging Mode :
Precharging current = 150mA
Vadj2 = 168.75mV

Adaptor Max. Current :
Ilimit = 1.9A; 36.1W (19V/40W)

ACIN Threshold = 1.25V
Adaptor > 16V, System Powered by Adaptor
Adaptor < 16V, System Powered by Battery

Battery Charging Voltage :
Vadj3 > 4.1V ==> Vbat = 4.2V /cell
2.2V>Vadj3>1.1V ==> Vbat = 2*Vadj3 /cell

Battery Charging Current :
4.4V > Vadj2 >= 0V ==>
Ichg = (Vadj2-0.075)/(25*Rs)

Input Adaptor Max. Current Limit :
Ilimit_current = (Vadj1-0.075) / (25*Rs)

1004HN Charging Current :

	2P#	Icharge
	1	1.49A
	0	2.5A

Standard Charging Current :

4P#	2P#	Icharge
1	0	0.557A
0	1	1.598A
0	0	2.768A

1008HA

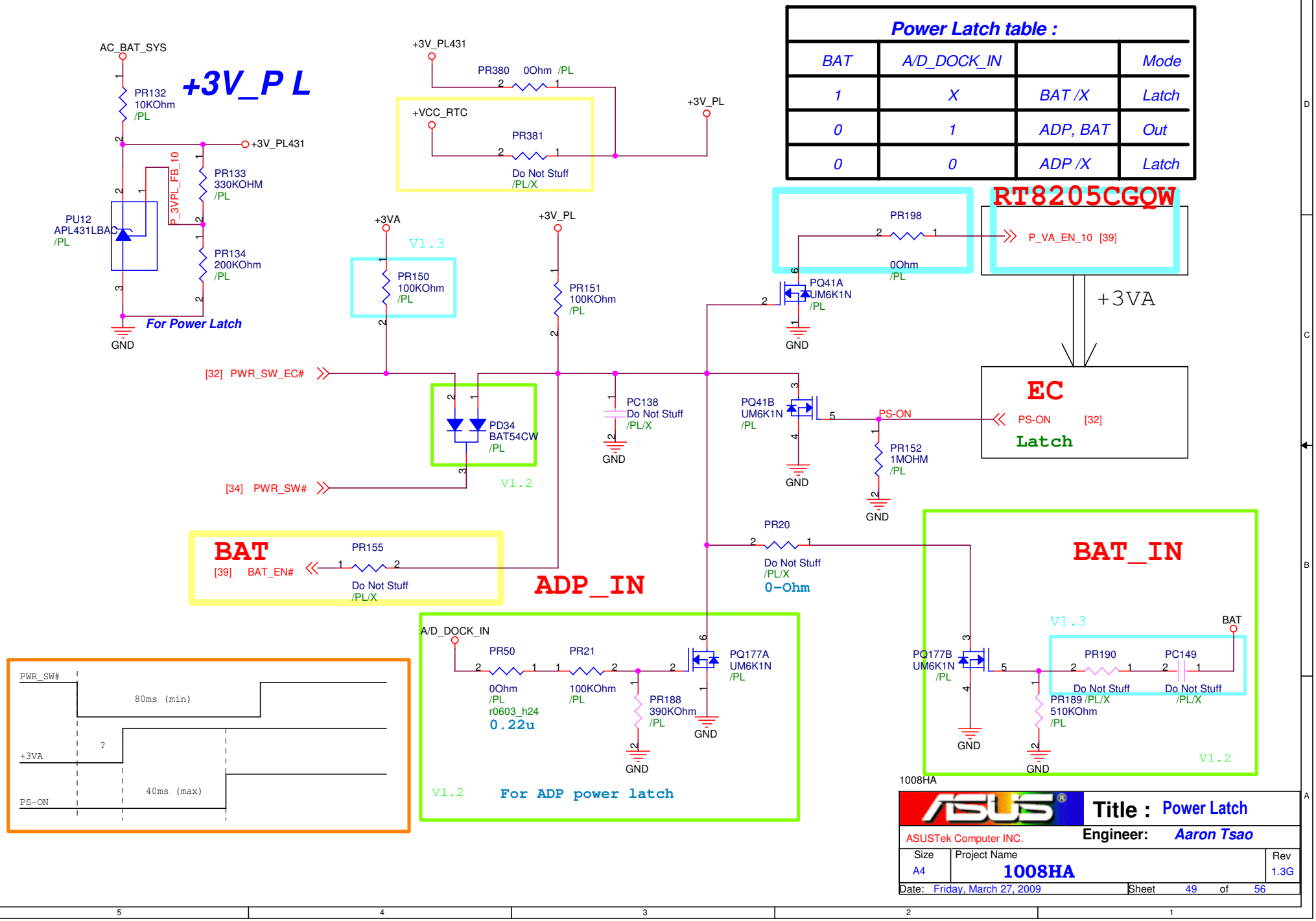
ASUSTek Computer INC.

Title : CHARGER

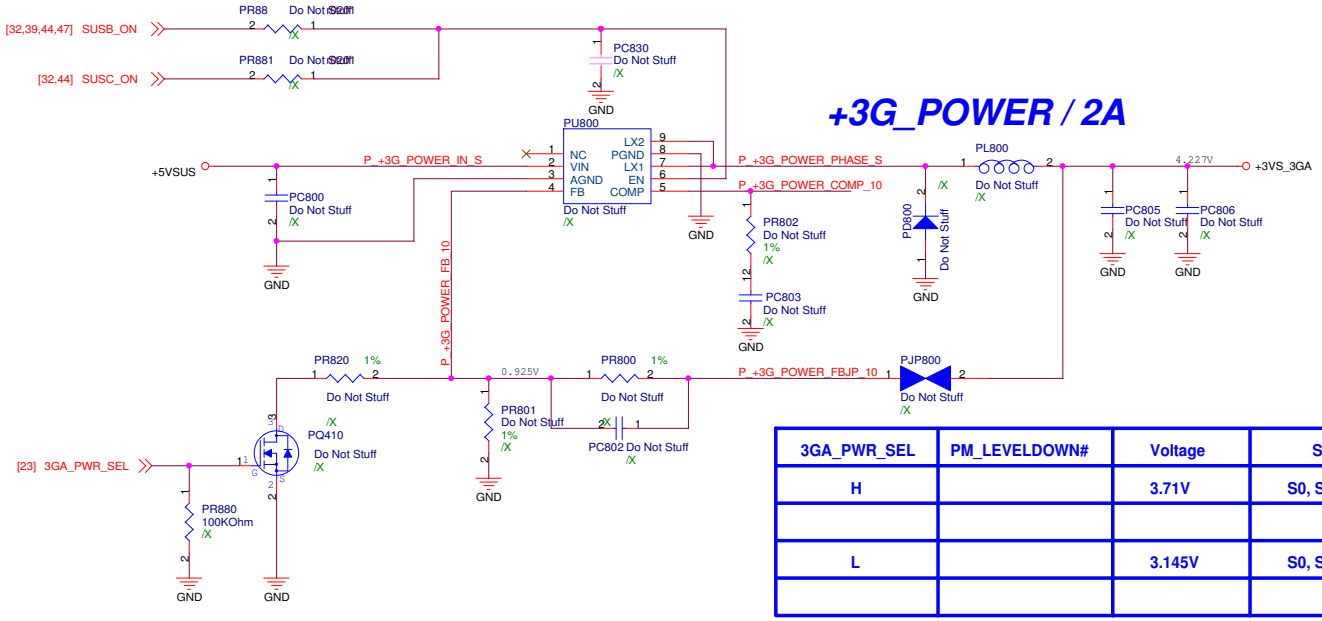
Engineer: Aaron Tsao

Size	Project Name	Rev
Custom	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 48 of 55


Power Latch table :
BAT | A/D_DOCK_IN | | Mode
1 | X | BAT /X | Latch
0 | 1 | ADP, BAT | Out
0 | 0 | ADP /X | Latch
+3V_P L
AC_BAT_SYS
PR132 10KOhm /PL
+3V_PL431
PR133 330KOHM /PL
P_3VPL_FB_10
PU12 APL431LBAC /PL
PR134 200KOhm /PL
For Power Latch
GND
+3V_PL431
PR380 0Ohm /PL
+VCC_RTC
PR381
Do Not Stuff /PL/X
+3V_PL
RT8205CGQW
PR198
0Ohm /PL
P_VA_EN_10 [39]
PQ41A UM6K1N /PL
+3VA
+3VA
V1.3
PR150 100KOhm /PL
+3V_PL
PR151 100KOhm /PL
[32] PWR_SW_EC#
PD34 BAT54CW /PL
V1.2
[34] PWR_SW#
PC138 Do Not Stuff /PL/X
PQ41B UM6K1N /PL
PS-ON
PR152 1MOHM /PL
EC
PS-ON [32]
Latch
BAT
[39] BAT_EN#
PR155
Do Not Stuff /PL/X
ADP_IN
PR20
Do Not Stuff /PL/X
0-Ohm
BAT_IN
A/D_DOCK_IN
PR50 0Ohm /PL r0603_h24 0.22u
PR21 100KOhm /PL
PR188 390KOhm /PL
PQ177A UM6K1N /PL
V1.3
BAT
PQ177B UM6K1N /PL
PR190
PC149
Do Not Stuff
Do Not Stuff /PL/X
PR189 /PL/X 510KOhm /PL
V1.2
V1.2
For ADP power latch
PWR_SW#
80ms (min)
+3VA
?
40ms (max)
PS-ON
1008HA
Title : Power Latch
ASUSTek Computer INC.
Engineer: Aaron Tsao
Size A4
Project Name 1008HA
Rev 1.3G
Date: Friday, March 27, 2009
Sheet 49 of 56

+3G_POWER / 2A



3GA_PWR_SEL	PM_LEVELDOWN#	Voltage	Status
H		3.71V	S0, S3
L		3.145V	S0, S3

1008HA

ASUSTek Computer INC.

Title : History

Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 50 of 51

Rev0.7	Initial release	2008/10/22
Rev0.71	Remove hotkey switch(R2, C427, SW6, OR25)	2008/10/22
	Remove White LED (LED4, R86, Q17, SB_GPIO7)	
	Add 40 pin FPC connector	
	Change netname from BT_LED to Wireless_LED	
	Change NMOS from 2N7002 to UM6K1N(Q25, 23, Q18, Q15, Q58, Q19, Q16, Q19)	
	Change 3G power name to +3VS_3GA	
	Add dual-core support (not ready)	
Rev0.72	Change the part for LAN connector	2008/10/23
	Change the SPEAKER connector (same as FAN)	
Rev0.73	Remove CC36-40, CC45, CC24-25, CC16-17 for layout space	2008/10/27
	Remove HC32, 33 for Layout space	
	Remove HC14-16, HC125 (stuff HC126) for Layout space	
	Remove C422, 212, 232 (stuff C223) for Layout space	
	Remove C436, 292, 305 for Layout space	
	Change U47 to /X and sutff R265	
	Remove MC5, 6, 7, 9, 15, 17, 22, 24, 26, 27, 33 for Layout space	
	Remove WIFI & 3G RSV component	

1008HA

ASUSTek Computer INC.

Title : History

Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

Date: Friday, March 27, 2009 Sheet 51 of 49