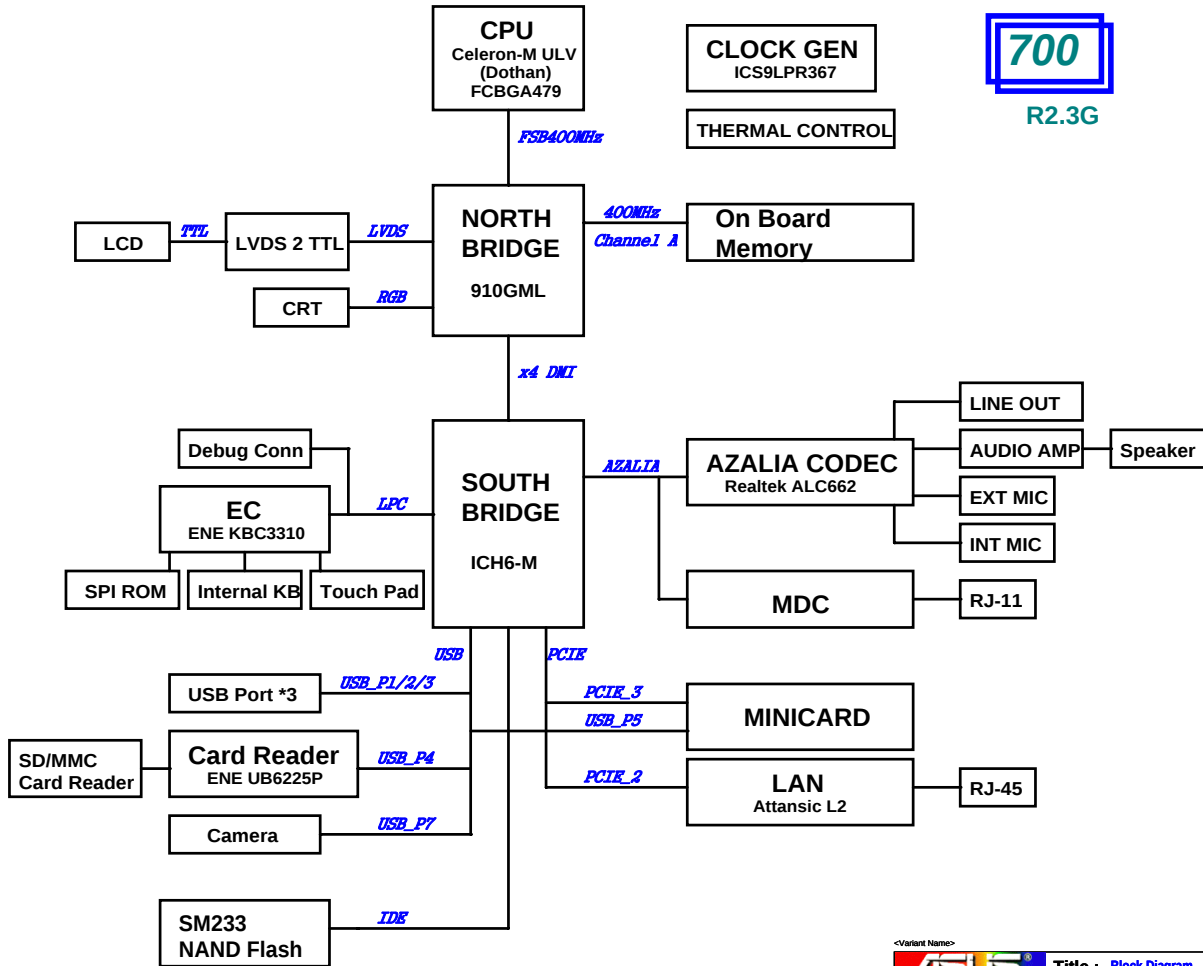


- 01_Block Diagram
- 02_System Setting
- 03_Power Sequence
- 04_EC Pin Define
- 05_History
- 06_*
- 07_Clock Gen_CY28442-2
- 08_Dothan_HOST
- 09_Dothan_PWR_GND
- 10_910GML_HOST_DMI
- 11_910GML_DRAM
- 12_910GML_VGA_LVDS_TV
- 13_910GML_PWR
- 14_910GML_GND
- 15_ICH6-M_Azalia_GPIO_PCI_LAN
- 16_ICH6-M_USB_PCIE_DMI_IDE_SATA
- 17_ICH6-M_PWR_GND
- 18_Onboard DRAM_1
- 19_Onboard DRAM_2
- 20_DDR2_Termination
- 21_Onboard VGA
- 22_LCD Conn
- 23_Minicard
- 24_LAN_Attansic L2
- 25_MDC_RJ11_RJ45
- 26_Flash Conn
- 27_USB Port
- 28_Card Reader_ENE UB6225P
- 29_Camera Conn
- 30_Codec_ALC662
- 31_Audio_AMP_Jack
- 32_EC_ENE KB3310
- 33_Switch_SPI ROM_Debug Conn
- 34_KB_Touch Pad
- 35_Thermal Sensor
- 36_LED
- 37_Discharge
- 38_PWR Jack
- 39_Srew Hole
- 40_EMI
- 41_POWER FLOW
- 42_CHARGER
- 43_VCORE(7A)
- 44_POWER_3V_5V_VTT_DDR
- 45_POWER_3VA_3VSB
- 46_POWER_1.05V_1.5V_2.5V
- 47_POWER_1.8V_DUAL_5VSB

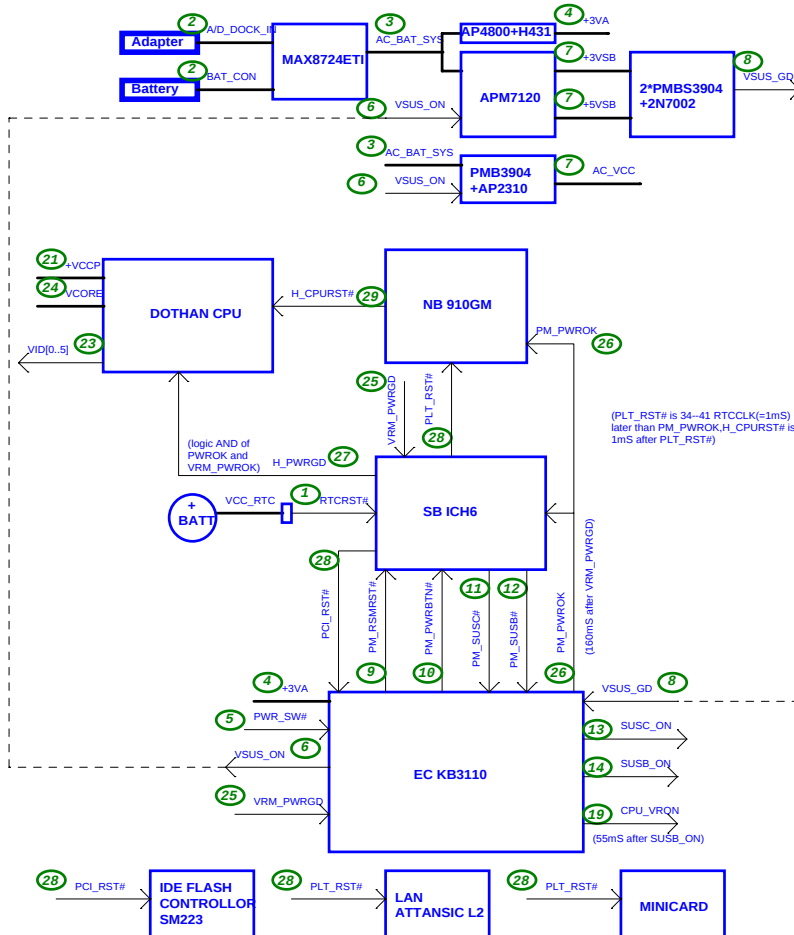


ICH6 GPIO SETTING

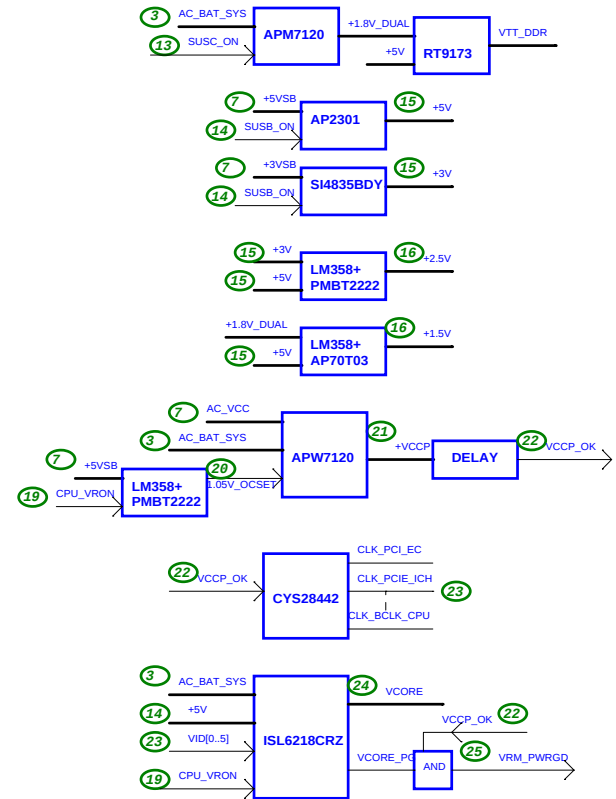
Pin	Pin Name	Connect to	Type	Input/Output Set
B7	GPIO/REQ6#	10K Pull +3V	I	fixed as Input only
E8	GPI1 / REQ5#	10K Pull +3V	I	fixed as Input only
D9	GPI2 / PIRQE#	10K Pull +3V	I	fixed as Input only
C7	GPI3 / PIRQF#	10K Pull +3V	I	fixed as Input only
C6	GPI4 / PIRQG#	10K Pull +3V	I	fixed as Input only
M3	GPI5 / PIRQH#	10K Pull +3V	I	fixed as Input only
AD19	GPI6 / BMBUSY#	NB BMBUSY#	I	Input
AE19	GPI7	NC	GPI	fixed as Input only
R1	GPI8	EC KBC_SCI#	GPI	fixed as Input only
C23	GPI9/OC4#	10K Pull +3V	I	Input
D23	GPI10/OC5#	10K Pull +3V	I	Input
W6	GPI11 / SMBALERT#	10K Pull +3V	I	Input
M2	GPI12	NC	GPI	fixed as Input only
R6	GPI13	EC EXTSM#	GPI	fixed as Input only
C25	GPI14/OC6#	10K Pull +3V	I	Input
C24	GPI15 /OC7#	10K Pull +3V	I	Input
D8	GPO16/GTN6#	NC	O	Output
F6	GPO17 / GNT5#	NC	O	Output
AC21	GPO18 / STP_PCI#	Clock GEN STP_PCI#	O	Output
AB21	GPO19	WLAN_LED#	GPO	fixed as Output only
AD22	GPO20 / STP_CPU#	STP_CPU#	O	Output
AD20	GPO21	NC	GPO	fixed as Output only
NA	GPIO22	NA	NA	NA
AD21	GPO23	NC	GPO	fixed as Output only
V3	GPIO24	WLAN	I/O	Output
P5	GPIO25	NC	I/O	Output

Pin	Pin Name	Connect to	Type	Input/Output Set
AF17	GPI26/SATA0GP	NC	GPI	(GPI)Input
R3	GPIO27	NC	I/O	Output
T3	GPIO28	NC	I/O	Output
AE18	GPI29 / SATA1GP	PCBVER0	GPI	(GPI)Input
AF18	GPI30 / SATA2GP	NC	GPI	(GPI)Input
AG18	GPI31 / SATA3GP	PCBVER1	GPI	(GPI)Input
AF19	GPIO32 / CLKRUN#	10K Pull +3V	I/O	Input
AF20	GPIO33	NC	I/O	Output
AC18	GPIO34	NC	I/O	Output
NA	GPIO35	NA	NA	NA
NA	GPIO36	NA	NA	NA
NA	GPIO37	NA	NA	NA
NA	GPIO38	NA	NA	NA
NA	GPIO39	NA	NA	NA
F7	GPI40 / REQ4#	10K Pull +3V	I	Input
P4	GPI41 / LDRQ1#	NC	I	Input
NA	GPIO42	NA	NA	NA
NA	GPIO43	NA	NA	NA
NA	GPIO44	NA	NA	NA
NA	GPIO45	NA	NA	NA
NA	GPIO46	NA	NA	NA
NA	GPIO47	NA	NA	NA
E7	GPO48 /GNT4#	NC	O	Output
AC25	GPO49 / CPUPWRGD	CPU Power Ok	O	Output

*This sequence is for Battery Plug-in and no Adapter,
if Adapter Plug-in,the sequence change to:
A/D_DOCK_IN-->AC_BAT_SYS-->+3VA-->VSUS_ON-->+3VSB & +5VSB
-->VSUS_GD-->PM_REMRST#-->PWR_SW#-->PM_PWRBTN-->PM_SUSC#-->PM_SUSB#



	Signal	S0/S1	S3	S4/S5	Power
Only Battery	VSUS_ON	H	H	L	VS
Adapter In	VSUS_ON	H	H	H	VS
	SUSB_ON	H	L	L	Main
	SUSC_ON	H	H	L	DUAL



EC KB3310 GPIO SETTING

Pin No.	Pin Name	Signal Name	Type	NOTE
1	GA20	A20GATE	O	A20GATE
2	KBRST#	RC_IN#	O	KBRST#
6	GPIOD4	CTRL_CAMER_PWR	I	Default : High
13	PCIRST#	PCI_RST#	I	PCI Reset
14	GPIOD7	N.C	O	Reserved
15	GPIOD8	EXTSMIR#	O	EXTSMIR, 10K Pull +3VSUS
16	GPIOD4	LID_EC#	I	LID_EC#, *
17	GPIOD8	LCD_CSB	O	LCD chip select
18	GPIODC	LCD_SDA	I/O	LCD Data
19	GPIODD	DISTP_SW#	I	Touch Pad Disabled, *
20	SCI#	KBC_SCI#	O	KBC_SCI#, 10K Pull +3VSUS
21	PWM1	BL_PWM_DA	O	LCD Light Switch
23	PWM2	LCD_SCL	O	LCD clock
25	GPIOD11	PM_PWRBTN#	OD	Power Button to SB, *
26	FANPWM1	FANO_PWM	O	CPU Fan(Unused)
27	FANPWM2	FANL_PWM	O	VGA Fan(Unused)
28	FANFB1	FANO_TACH	I	CPU FanTach(Unused)
29	FANFB2	FANL_TACH	I	VGA FanTach(Unused)
30	GPIOD16	ES1_TX	O	RS232 debug port
31	GPIOD17	N.C	O	Reserved
32	GPIOD18	PWR_SW#	I	power button, *
34	GPIOD19	MAIL_LED#	O	Mail LED(Unused)
36	GPIOD1A	CTRL_Mincard_PWR	O	Default : High
38	CLKRUN#	N.C	O	Reserved
39	KSO0	KSO0	O	For Keyboard interface
40	KSO1	KSO1	O	For Keyboard interface
41	KSO2	KSO2	O	For Keyboard interface
42	KSO3	KSO3	O	For Keyboard interface
43	KSO4	KSO4	O	For Keyboard interface
44	KSO5	KSO5	O	For Keyboard interface
45	KSO6	KSO6	O	For Keyboard interface
46	KSO7	KSO7	O	For Keyboard interface
47	KSO8	KSO8	O	For Keyboard interface
48	KSO9	KSO9	O	For Keyboard interface
49	KSO10	KSO10	O	For Keyboard interface
50	KSO11	KSO11	O	For Keyboard interface
51	KSO12	KSO12	O	For Keyboard interface
52	KSO13	KSO13	O	For Keyboard interface
53	KSO14	KSO14	O	For Keyboard interface
54	KSO15	KSO15	O	For Keyboard interface
55	KSI0	KSI0	I	For Keyboard interface
56	KSI1	KSI1	I	For Keyboard interface
57	KSI2	KSI2	I	For Keyboard interface
58	KSI3	KSI3	I	For Keyboard interface
59	KSI4	KSI4	I	For Keyboard interface
60	KSI5	KSI5	I	For Keyboard interface
61	KSI6	KSI6	I	For Keyboard interface
62	KSI7	KSI7	I	For Keyboard interface
63	AD0	P_PMON_10	I	Sense Power Loading
64	AD1	BAT_IN	I	sense Battery
65	AD2	N.C	I	Reserved
66	AD3	N.C	I	Reserved
78	GPO3C	DOC	O	Trigger Clock Gen

Pin No.	Pin Name	Signal Name	Type	NOTE
70	GPO3D	LCD_BACKOFF#	O	LCD_BACKOFF#
71	GPO3E	CLK_PWRSERVE#	O	Active when BAT_IN#1 and AC_10V30(Unused)
72	GPO3F	BAT_LL#	O	Battery Low Low
73	GPIOD40	AC_OK	I	AC Adaptor Plug in
74	GPIOD41	PM_RSMRST#	O	Reserved
75	GPIOD42	N.C	O	Reserved
76	GPIOD43	N.C	O	Reserved
77	SCL1	SMB0_CLK	I/O	4.7K Pull +3VA_EC
78	SDA1	SMB0_DAT	I/O	4.7K Pull +3VA_EC
79	SCL2	SMB1_CLK	I/O	10K Pull +3VS
80	SDA2	SMB1_DAT	I/O	10K Pull +3VS
81	KSO16	N.C	O	Reserved
82	KSO17	N.C	O	Reserved
83	PSCCLK1	N.C	O	Reserved
84	PSDAT1	N.C	O	Reserved
85	PSCCLK2	N.C	O	Reserved
86	PSDAT2	N.C	O	Reserved
87	PSCCLK3	TP_CLK	I/O	10K Pull +3VS
88	PSDAT3	TP_DAT	I/O	10K Pull +3VS
89	GPIOD50	BATSEL_3S	O	Battery select, W335, Lo25(Unused)
90	GPIOD52	CHG_LED_UP#	O	charger LED
91	GPIOD53	CTRL_L2_PWR	O	Default : High
92	GPIOD54	PWR_LED_UP	O	EC HW blinking
93	GPIOD55	SCRL_LED#	O	EC HW controls
95	GPIOD56	PWR4G_SW#	I	*
97	GPXOA00	SPI_MODE#	O	HW Strap for SPI Flash de External
98	GPXOA01	SUSC_ON	O	Pull Down 100K ohm to GND*
99	GPXOA02	VSUS_ON	O	
100	GPXOA03	CPU_VRON	O	
101	GPXOA04	SUSB_ON	O	
102	GPXOA05	ICH8_PWROK	O	
103	GPXOA06	N.C	O	Reserved
104	GPXOA07	CHG_EN#	O	Battery charging enabled
105	GPXOA08	PRECHG	O	
106	GPXOA09	SPI_WP#	O	
107	GPXOA10	OP_SD#	O	Audio OP
108	GPXOA11	BAT_LEARN	O	
109	GPXID0	BATSEL_2P#	O	Battery parallel, W12P, Lo2P-3P
110	GPXID1	N.C	O	Reserved
112	GPXID2	THRO_CPU	O	Active if Battery Temperature is Pull Down 100K ohm to GND
114	GPXID3	SUSB#	I	Pull Down 100K ohm to GND
115	GPXID4	SUSC#	I	Pull Down 100K ohm to GND
116	GPXID5	CPU_PWR_GD	I	10K Pull +3VS
117	GPXID6	VSUS_GD	I	Disabled **
118	GPXID7	N.C	O	Reserved
121	GPIOD57	INTERNET#	I	*
126	SPICLK	SPI_CLK	O	SPI Clock
127	GPIOD59	N.C	O	Reserved

EC KB3310 Other Pin SETTING

Pin No.	Pin Name	Signal Name	Type	NOTE
3	SERIRQ	INT_SERIRQ	I/O	8.2K Pull +3VS
4	LFRAME#	LPC_FRAME#	I	
5	LAD3	LPC_AD3	I/O	
7	LAD2	LPC_AD2	I/O	
8	LAD1	LPC_AD1	I/O	
9	VCC	+3VA_EC	P	
10	LAD0	LPC_AD0	I/O	
11	GND	GND	P	
12	PCICLK	CLK_PCI_EC	I	
22	VCC	+3VA_EC	P	
24	GND	GND	P	
33	VCC	+3VA_EC	P	
35	GND	GND	P	
37	ECRST#	EC_RST#	I	Add 100K ohm to GND
67	AVCC	+3VACC	P	
69	AGND	AGND	P	
94	GND	GND	P	
96	VCC	+3VA_EC	P	
111	VCC	+3VA_EC	P	
113	GND	GND	P	
119	RD#	SPI_SO	I	
120	WR#	SPI_SI	O	
112	XCLKI	32KXCLKI	I	
123	XCLKO	32KXCLKO	O	
124	V18R	K_V18R		Reserved 1uF to GND
125	VCC	+3VA_EC	P	
128	SPIC#	SPI_CE#	O	

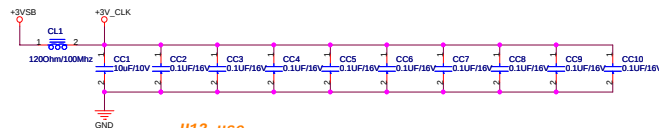
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ASUS		Title : EC Pin Define	
ASUSTek Computer INC.		Engineer: Keli Huang	
Size A3	Project Name	P700	Rev R2.36
Date: 2007.08.13	Sheet 4	of 48	

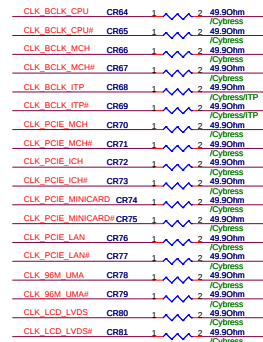
CIRCUIT UPDATED HISTORY

Rev	Date	Description
1.0G	2007/02/26	S701L Schematic 1.0G Beginning
	2007/03/16	S701L 1.0G Gerber Out
1.1G	2007/03/24	S701L Schematic 1.1G Beginning
	2007/04/19	S701L 1.1G Gerber Out
1.2G	2007/04/24	P701(S701L renamed) Schematic 1.0G Beginning 1. PC8054, PR6075 /X to N/A 2. Attansic L2 change to Atheros L2(pin to pin) 3. LC1, LC33 /CAP/X to N/A 4. C87 change to X5R to cost down 5. L1, L2, L3 change to 56 NH, R5, R6 change to 75 Ohm to pass CRT EA measure 6. PR48 change to 22K Ohm, PC35 change to 4700PF to fix no VCORE issue 7. PR6074 change to 4.7K Ohm to fix +3VSB OCP issue 8. Clock Gen CY28442-2 change to ICS9LPR367 9. Phase in Power Level Reduce solution, mark "Taipei0508" 10. Card Reader Socket change to SD Socket 12G25100091E 11. Add System FAN circuit 12. Camera change to USB port 7, Minicard change to USB port 5 13. Use SB GPIO27 to Enable/Disable Card Reader UB6225P 14. Use SB GPIO28 to Enable/Disable Modem 15. Stuff CC33, CC34, CC35, CC36, CC37, CC38 for EMI 16. Card Reader UB6225P share 48M clock from CLock Gen with SB USB part 17. Add D29 to fix LCD_CSB leakage current issue 18. LC29, LC30 change to 27PF to pass EA crystal measure 19. Change vaule of PR73, PR74, PC56 and add PC60 to adjust the power sequence timing between Stand By power and RSMRST# 20. Remove USB port 1 21. Add +5V generate +3V_LCD circuit 22. Remove +5V_CHG generate circuit 23. Use SB GPIO33, GPIO34 to controll the level of VCORE 24. U31 use APL5315BI-TRL to replace MAX8863TEUK(pin to pin, but reference voltage level different) 25. PR59 change to 130K Ohm for both 12V Adapter and 9.8V Adapter

Rev	Date	Description



S_SMB_DATA CC13 2 1 10PF/50V
S_SMB_CLK CC14 2 1 10PF/50V
/X
/X

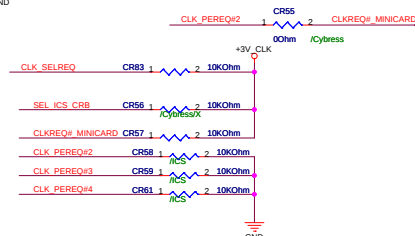


R2.00G

CLK_BCLK
CLK_BCLK
CLK_BCLK
CLK_BCLK
SYS_RESET

CLK_BCLK
CLK_BCLK
CLKREQ0

CLK_PCIE
CLK_PCIE

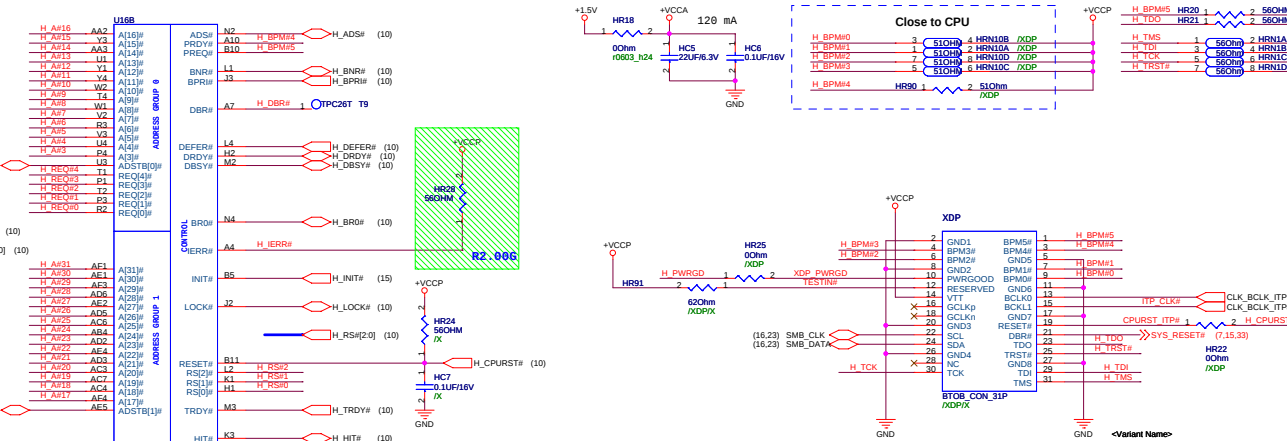


CR89 needs to stuff for
non-test mode

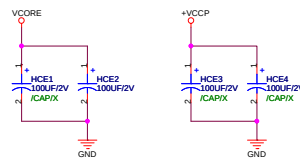
FS_C	FS_B	FS_A	CPU
1	0	1	100MHz
0	0	1	66.6MHz
0	1	1	83.3MHz
0	1	0	71.4MHz


Title : Clock Gen_IC9LP3

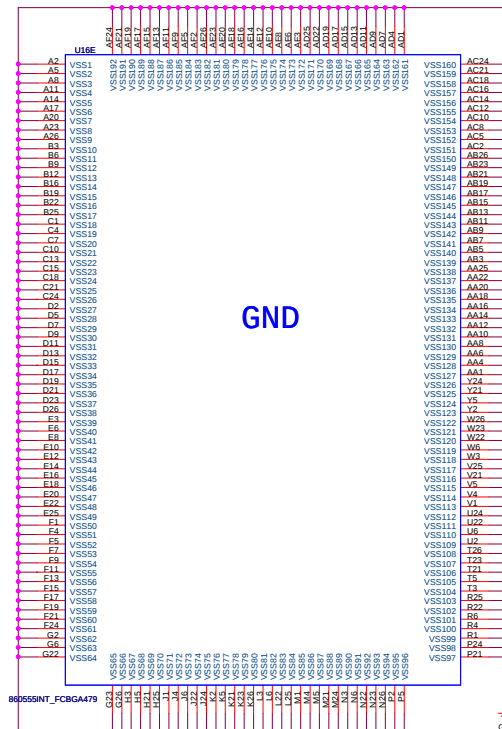
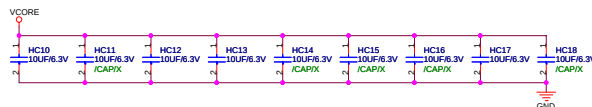
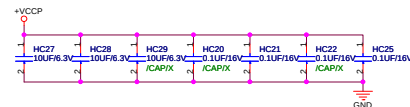
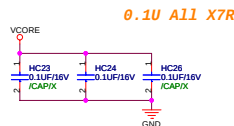
ASUSTek Computer INC.		Engineer:	Kell_Huang
Size	Project Name		



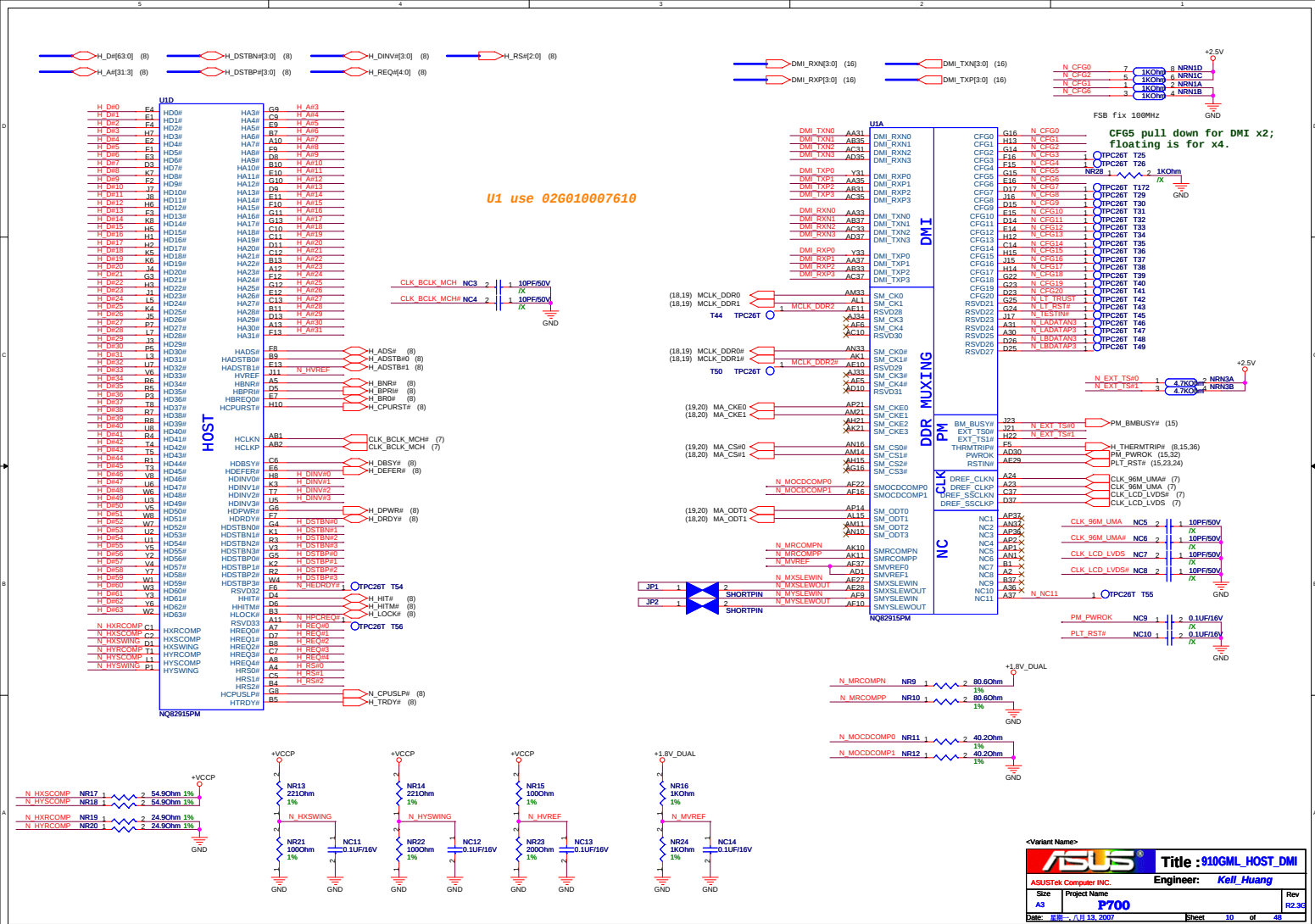
VCORE
Celeron-M(Dothan) ULV max 7 A



U16 use 01G011920000



<Variant Names>



MA_DQ[63:0] (18,19) MA_DM[7:0] (18,19)
MA_DQS[7:0] (18,19) MA_MA[13:0] (18,19,20)
MA_DQS[7:0] (18,19) MA_BA[2:0] (18,19,20)

U1 use 026010007610

U1B			
MA_DQ0	AG35	SA_D00	AK15
MA_DQ1	AK35	SA_D01	AK16
MA_DQ2	AL35	SA_D02	SA_B51
MA_DQ3	AL37	SA_D03	SA_B52
MA_DQ4	AK36	SA_D04	SA_DM0
MA_DQ5	AK35	SA_D05	AK37
MA_DQ6	AK37	SA_D06	AK38
MA_DQ7	AL34	SA_D07	AK39
MA_DQ8	AK36	SA_D08	AK40
MA_DQ9	AK35	SA_D09	AK41
MA_DQ10	AK32	SA_DQ10	AK42
MA_DQ11	AK31	SA_DQ11	AK43
MA_DQ12	AK34	SA_DQ12	AK44
MA_DQ13	AK33	SA_DQ13	AK45
MA_DQ14	AL32	SA_DQ14	AK46
MA_DQ15	AK32	SA_DQ15	AK47
MA_DQ16	AK31	SA_DQ16	AK48
MA_DQ17	AK31	SA_DQ17	AK49
MA_DQ18	AK30	SA_DQ18	AK50
MA_DQ19	AK29	SA_DQ19	AK51
MA_DQ20	AK28	SA_DQ20	AK52
MA_DQ21	AK27	SA_DQ21	AK53
MA_DQ22	AK26	SA_DQ22	AK54
MA_DQ23	AK25	SA_DQ23	AK55
MA_DQ24	AK24	SA_DQ24	AK56
MA_DQ25	AK23	SA_DQ25	AK57
MA_DQ26	AK22	SA_DQ26	AK58
MA_DQ27	AK21	SA_DQ27	AK59
MA_DQ28	AK20	SA_DQ28	AK60
MA_DQ29	AK19	SA_DQ29	AK61
MA_DQ30	AK18	SA_DQ30	AK62
MA_DQ31	AK17	SA_DQ31	AK63
MA_DQ32	AK16	SA_DQ32	AK64
MA_DQ33	AK15	SA_DQ33	AK65
MA_DQ34	AK14	SA_DQ34	AK66
MA_DQ35	AK13	SA_DQ35	AK67
MA_DQ36	AK12	SA_DQ36	AK68
MA_DQ37	AK11	SA_DQ37	AK69
MA_DQ38	AK10	SA_DQ38	AK70
MA_DQ39	AK09	SA_DQ39	AK71
MA_DQ40	AK08	SA_DQ40	AK72
MA_DQ41	AK07	SA_DQ41	AK73
MA_DQ42	AK06	SA_DQ42	AK74
MA_DQ43	AK05	SA_DQ43	AK75
MA_DQ44	AK04	SA_DQ44	AK76
MA_DQ45	AK03	SA_DQ45	AK77
MA_DQ46	AK02	SA_DQ46	AK78
MA_DQ47	AK01	SA_DQ47	AK79
MA_DQ48	AK00	SA_DQ48	AK80
MA_DQ49	AK00	SA_DQ49	AK81
MA_DQ50	AK00	SA_DQ50	AK82
MA_DQ51	AK00	SA_DQ51	AK83
MA_DQ52	AK00	SA_DQ52	AK84
MA_DQ53	AK00	SA_DQ53	AK85
MA_DQ54	AK00	SA_DQ54	AK86
MA_DQ55	AK00	SA_DQ55	AK87
MA_DQ56	AK00	SA_DQ56	AK88
MA_DQ57	AK00	SA_DQ57	AK89
MA_DQ58	AK00	SA_DQ58	AK90
MA_DQ59	AK00	SA_DQ59	AK91
MA_DQ60	AK00	SA_DQ60	AK92
MA_DQ61	AK00	SA_DQ61	AK93
MA_DQ62	AK00	SA_DQ62	AK94
MA_DQ63	AK00	SA_DQ63	AK95

DDR SYSTEM MEMORY A

SA_CAS# (18,19,20) MA_CAS# (18,19,20)
SA_RAS# (18,19,20) MA_RAS# (18,19,20)
SA_WE# (18,19,20) MA_WE# (18,19,20)

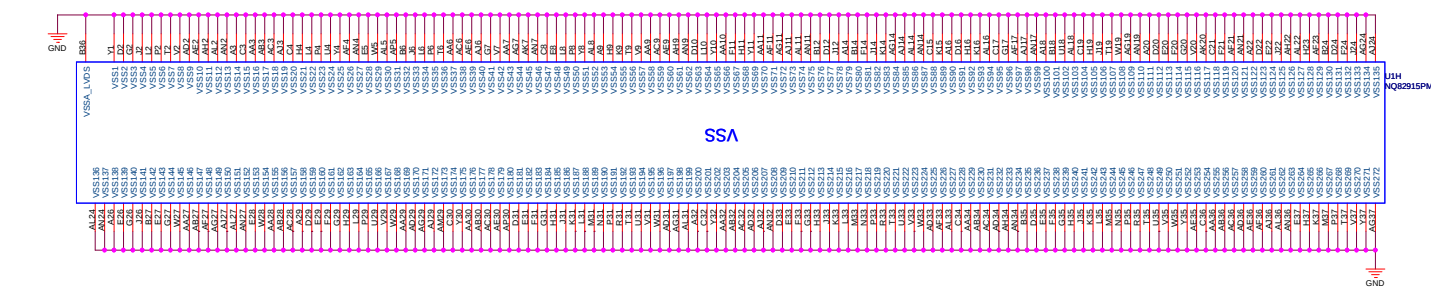
U1C			
MA_DQ0	AG35	SA_D00	AK15
MA_DQ1	AK35	SA_D01	AK16
MA_DQ2	AL35	SA_D02	SA_B51
MA_DQ3	AL37	SA_D03	SA_B52
MA_DQ4	AK36	SA_D04	SA_DM0
MA_DQ5	AK37	SA_D05	AK37
MA_DQ6	AK38	SA_D06	AK38
MA_DQ7	AL34	SA_D07	AK39
MA_DQ8	AK36	SA_D08	AK40
MA_DQ9	AK35	SA_D09	AK41
MA_DQ10	AK32	SA_DQ10	AK42
MA_DQ11	AK31	SA_DQ11	AK43
MA_DQ12	AK34	SA_DQ12	AK44
MA_DQ13	AK33	SA_DQ13	AK45
MA_DQ14	AL32	SA_DQ14	AK46
MA_DQ15	AK32	SA_DQ15	AK47
MA_DQ16	AK31	SA_DQ16	AK48
MA_DQ17	AK31	SA_DQ17	AK49
MA_DQ18	AK30	SA_DQ18	AK50
MA_DQ19	AK29	SA_DQ19	AK51
MA_DQ20	AK28	SA_DQ20	AK52
MA_DQ21	AK27	SA_DQ21	AK53
MA_DQ22	AK26	SA_DQ22	AK54
MA_DQ23	AK25	SA_DQ23	AK55
MA_DQ24	AK24	SA_DQ24	AK56
MA_DQ25	AK23	SA_DQ25	AK57
MA_DQ26	AK22	SA_DQ26	AK58
MA_DQ27	AK21	SA_DQ27	AK59
MA_DQ28	AK20	SA_DQ28	AK60
MA_DQ29	AK19	SA_DQ29	AK61
MA_DQ30	AK18	SA_DQ30	AK62
MA_DQ31	AK17	SA_DQ31	AK63
MA_DQ32	AK16	SA_DQ32	AK64
MA_DQ33	AK15	SA_DQ33	AK65
MA_DQ34	AK14	SA_DQ34	AK66
MA_DQ35	AK13	SA_DQ35	AK67
MA_DQ36	AK12	SA_DQ36	AK68
MA_DQ37	AK11	SA_DQ37	AK69
MA_DQ38	AK10	SA_DQ38	AK70
MA_DQ39	AK09	SA_DQ39	AK71
MA_DQ40	AK08	SA_DQ40	AK72
MA_DQ41	AK07	SA_DQ41	AK73
MA_DQ42	AK06	SA_DQ42	AK74
MA_DQ43	AK05	SA_DQ43	AK75
MA_DQ44	AK04	SA_DQ44	AK76
MA_DQ45	AK03	SA_DQ45	AK77
MA_DQ46	AK02	SA_DQ46	AK78
MA_DQ47	AK01	SA_DQ47	AK79
MA_DQ48	AK00	SA_DQ48	AK80
MA_DQ49	AK00	SA_DQ49	AK81
MA_DQ50	AK00	SA_DQ50	AK82
MA_DQ51	AK00	SA_DQ51	AK83
MA_DQ52	AK00	SA_DQ52	AK84
MA_DQ53	AK00	SA_DQ53	AK85
MA_DQ54	AK00	SA_DQ54	AK86
MA_DQ55	AK00	SA_DQ55	AK87
MA_DQ56	AK00	SA_DQ56	AK88
MA_DQ57	AK00	SA_DQ57	AK89
MA_DQ58	AK00	SA_DQ58	AK90
MA_DQ59	AK00	SA_DQ59	AK91
MA_DQ60	AK00	SA_DQ60	AK92
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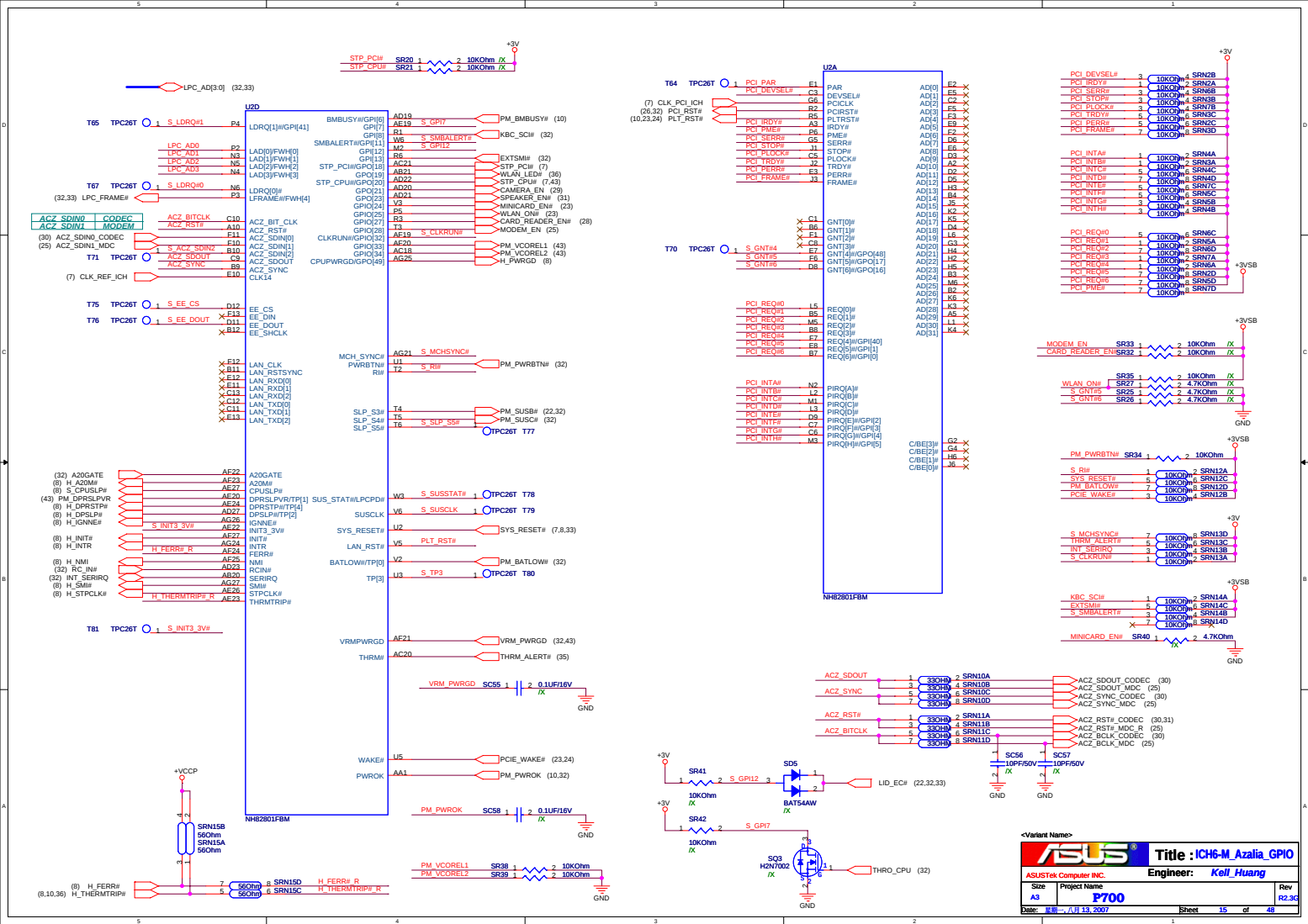
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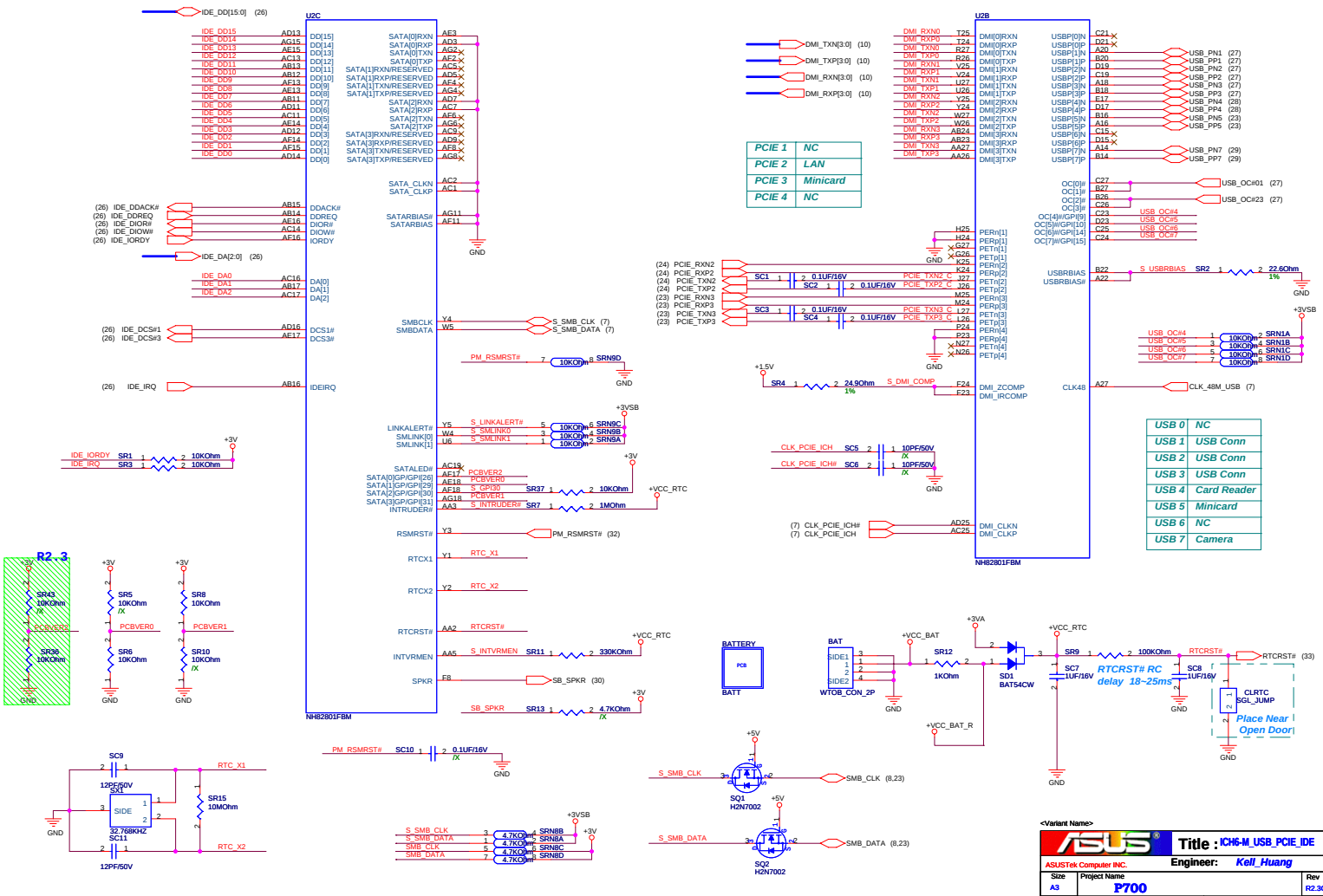
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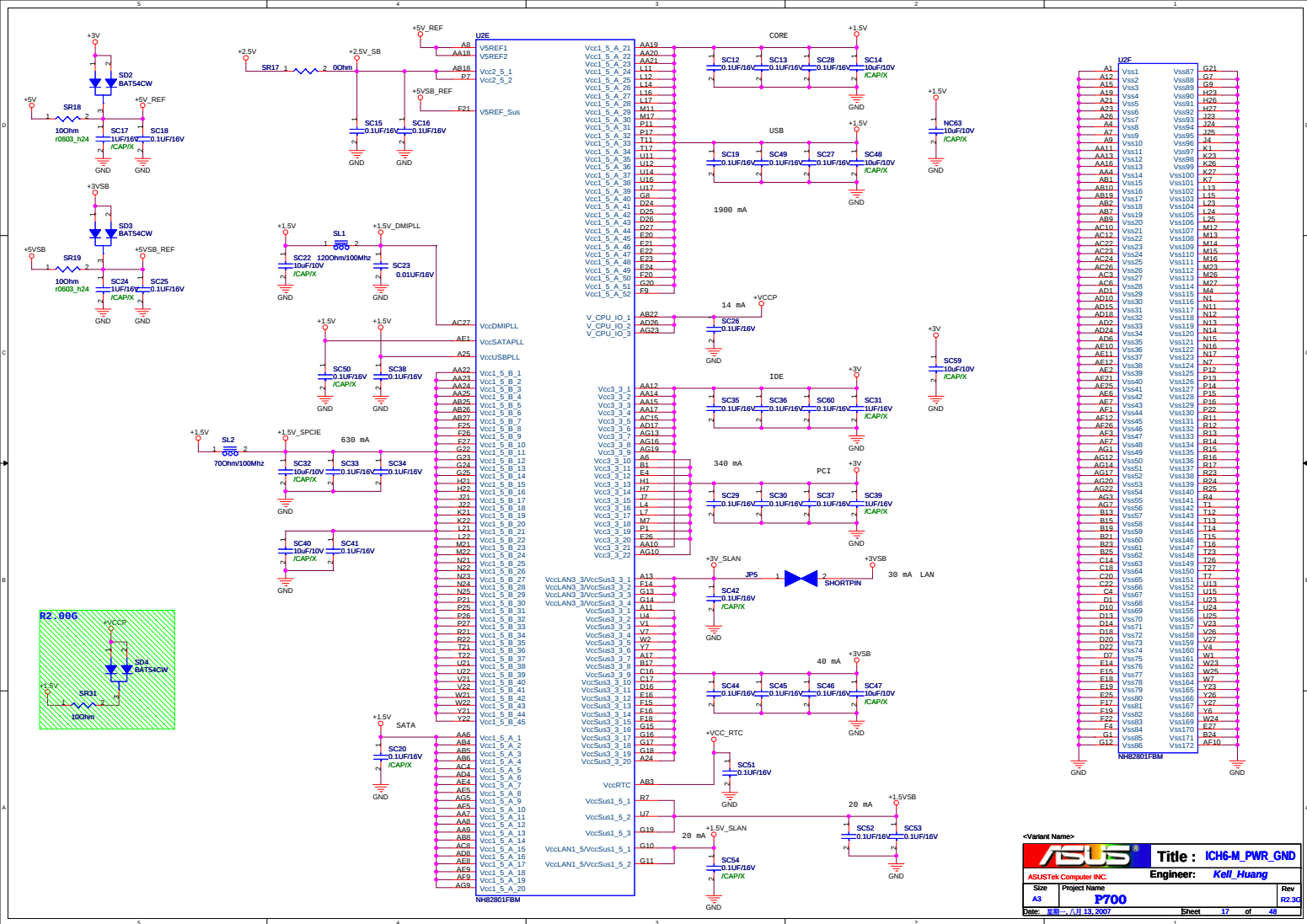
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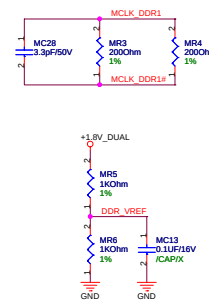
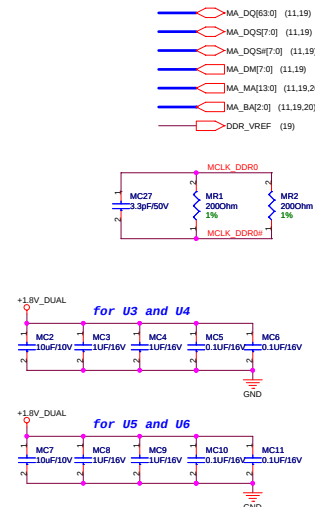
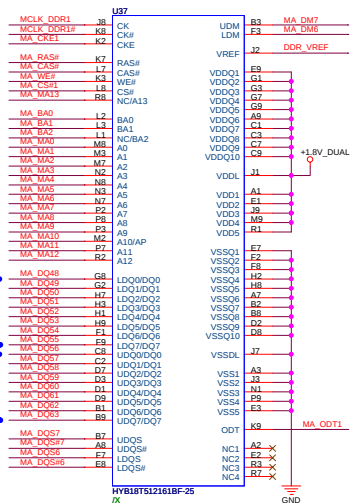
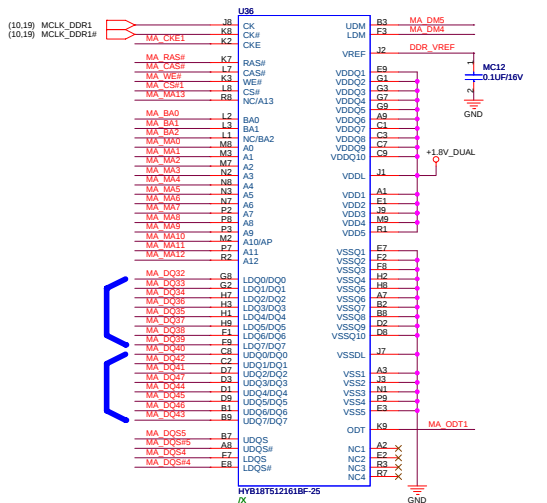
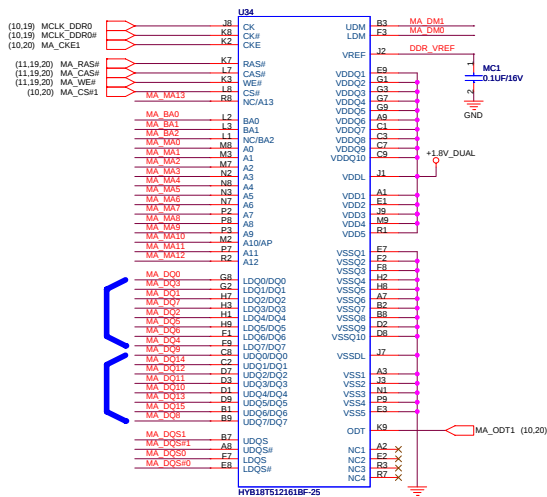


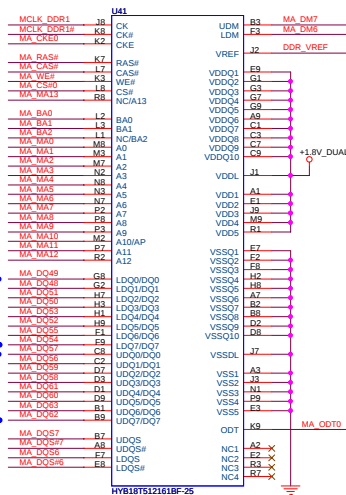
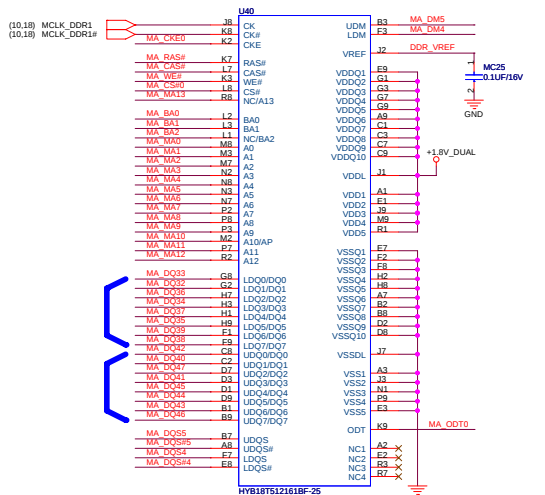
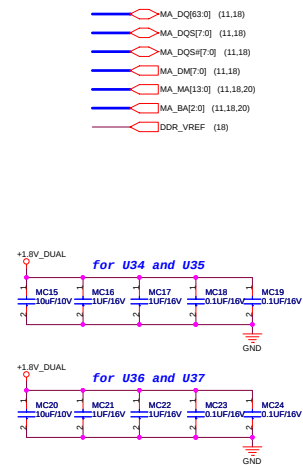
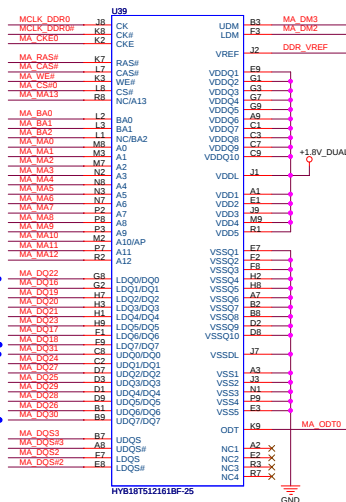
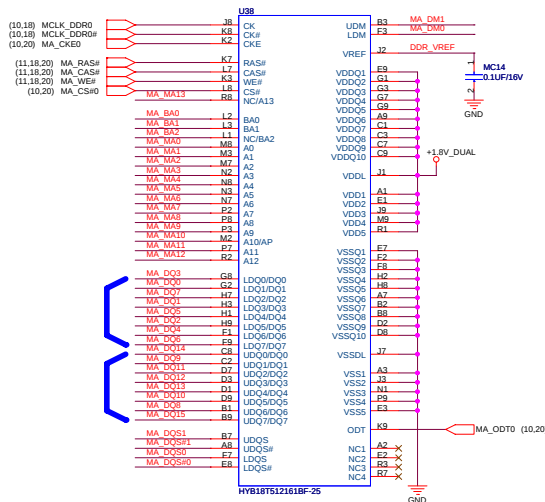


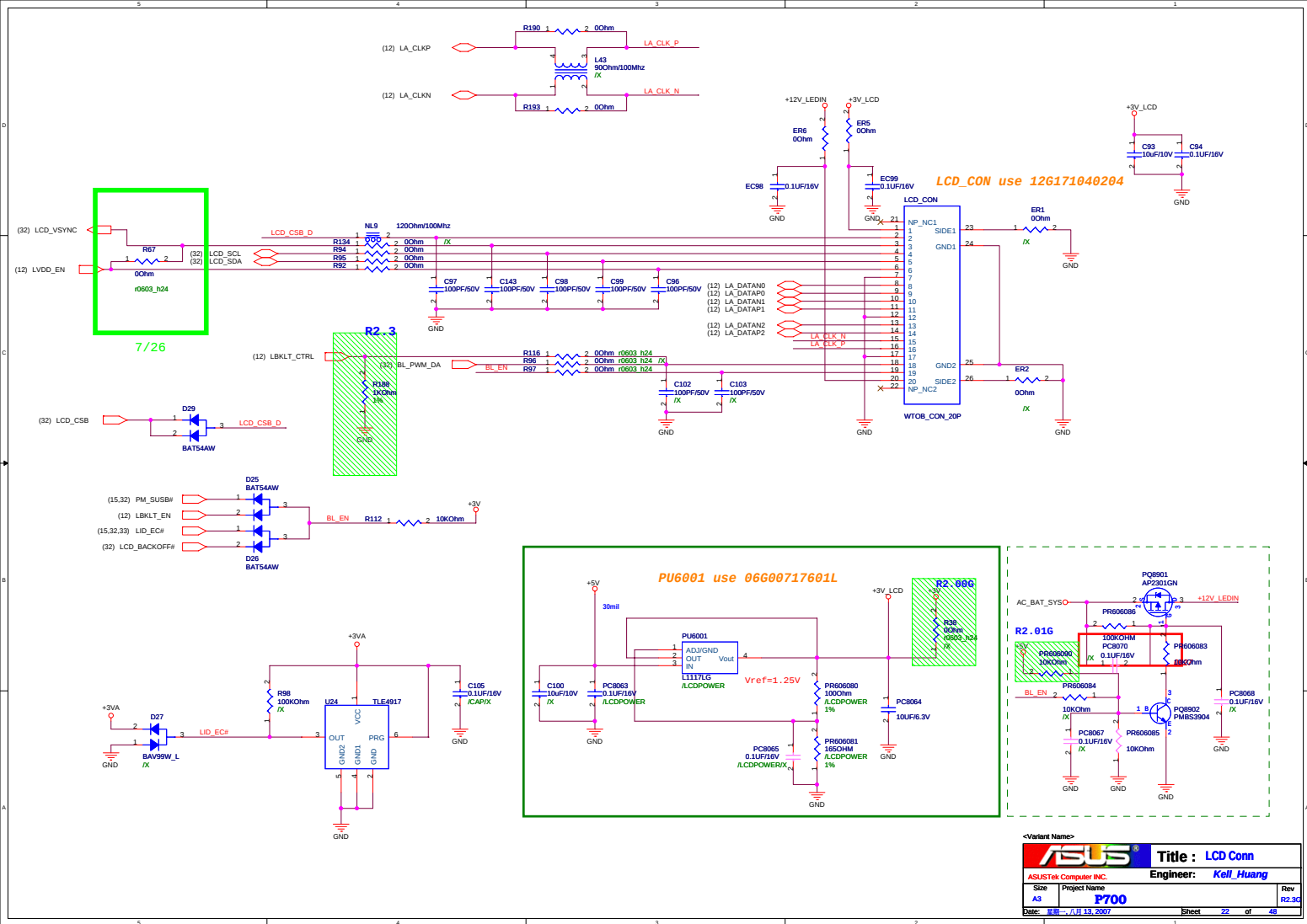




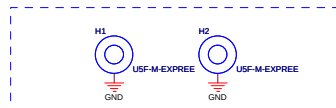
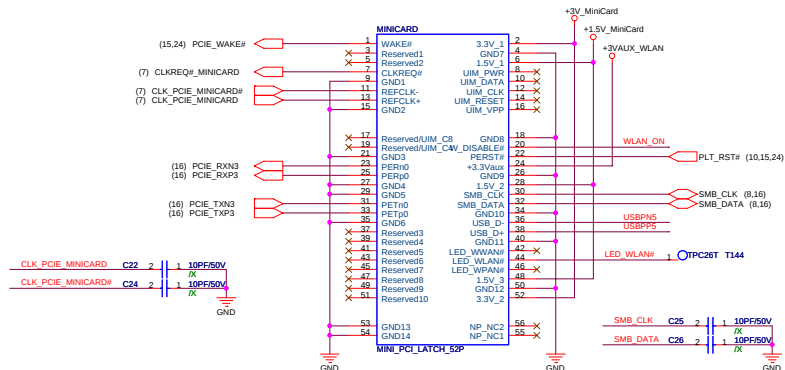




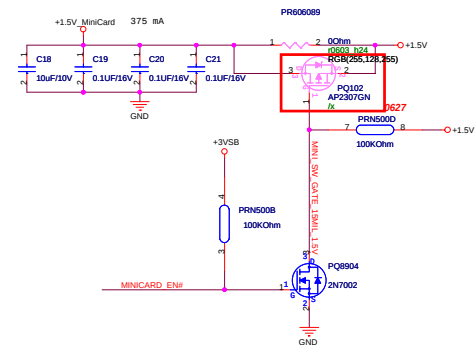
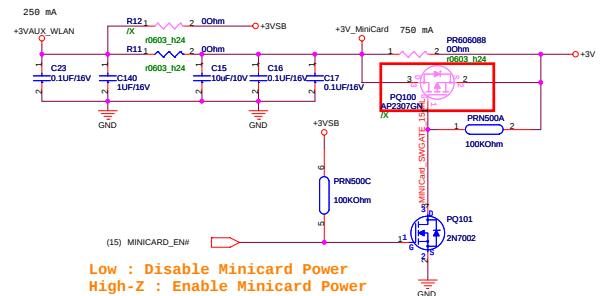




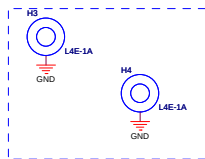
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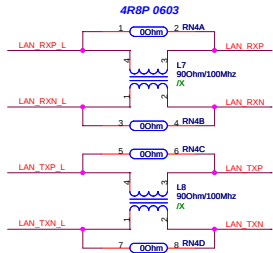
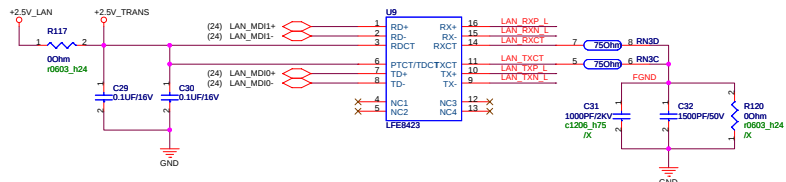
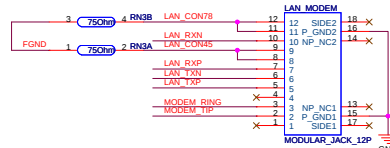
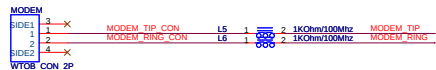
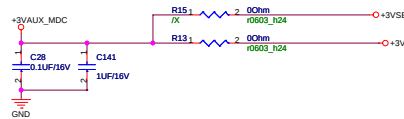
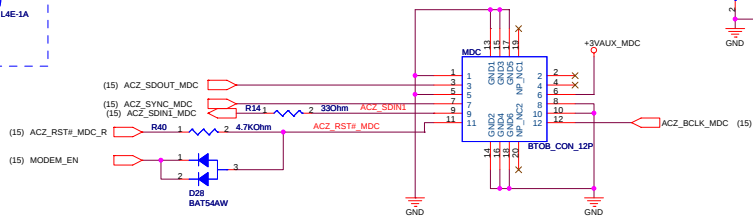
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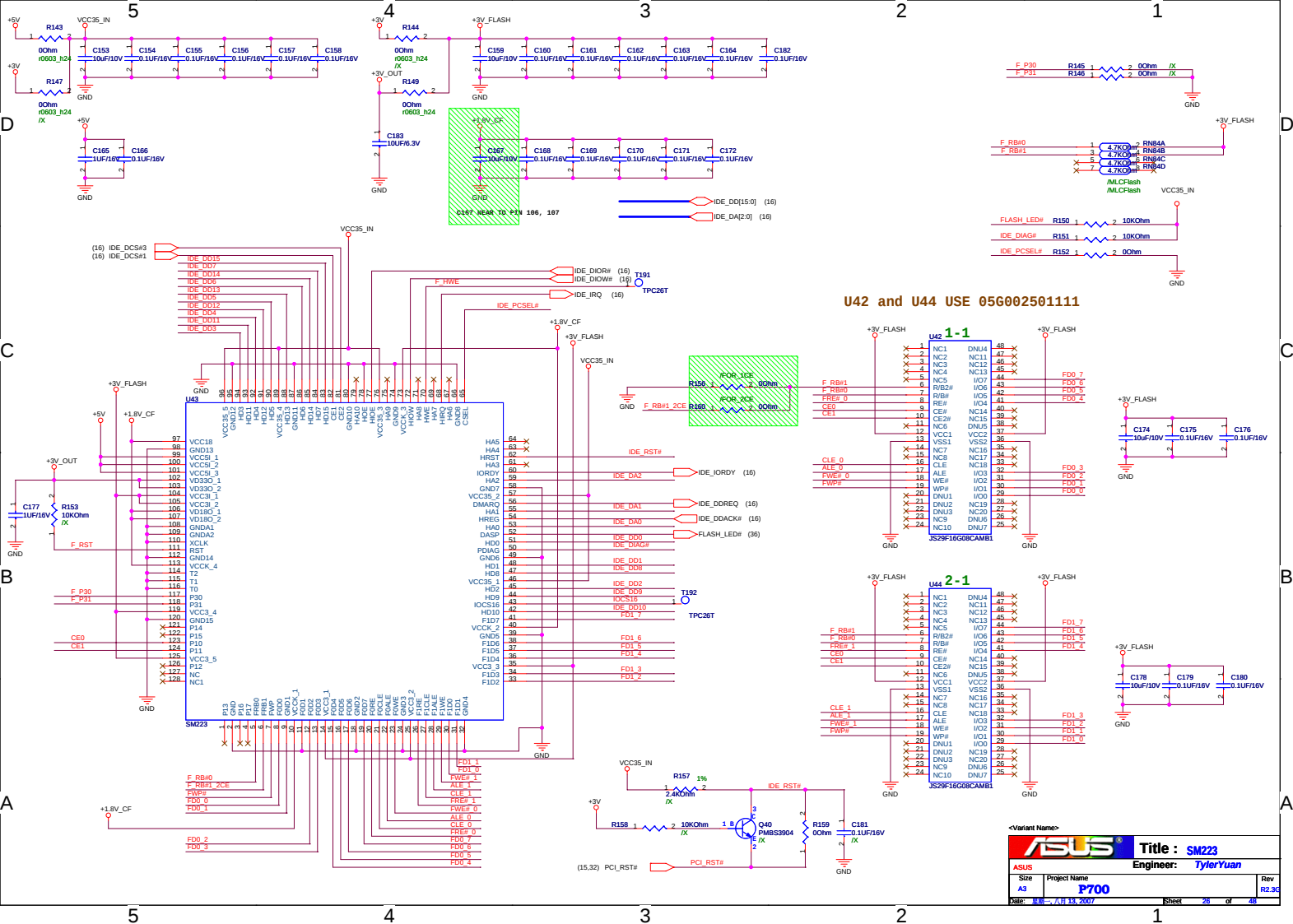


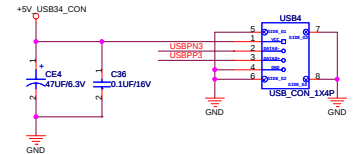
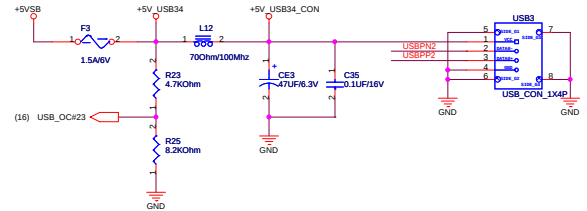
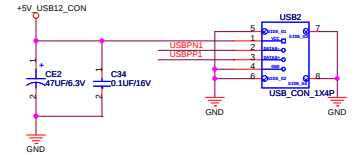
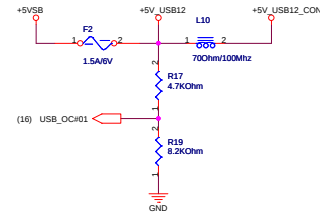
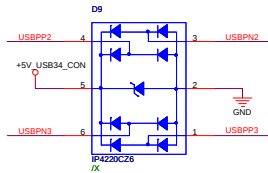
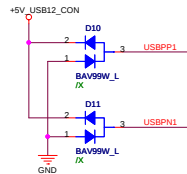
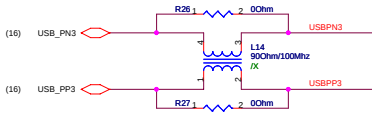
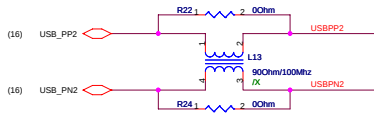
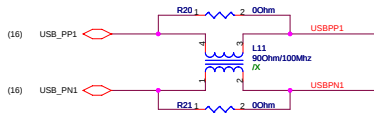
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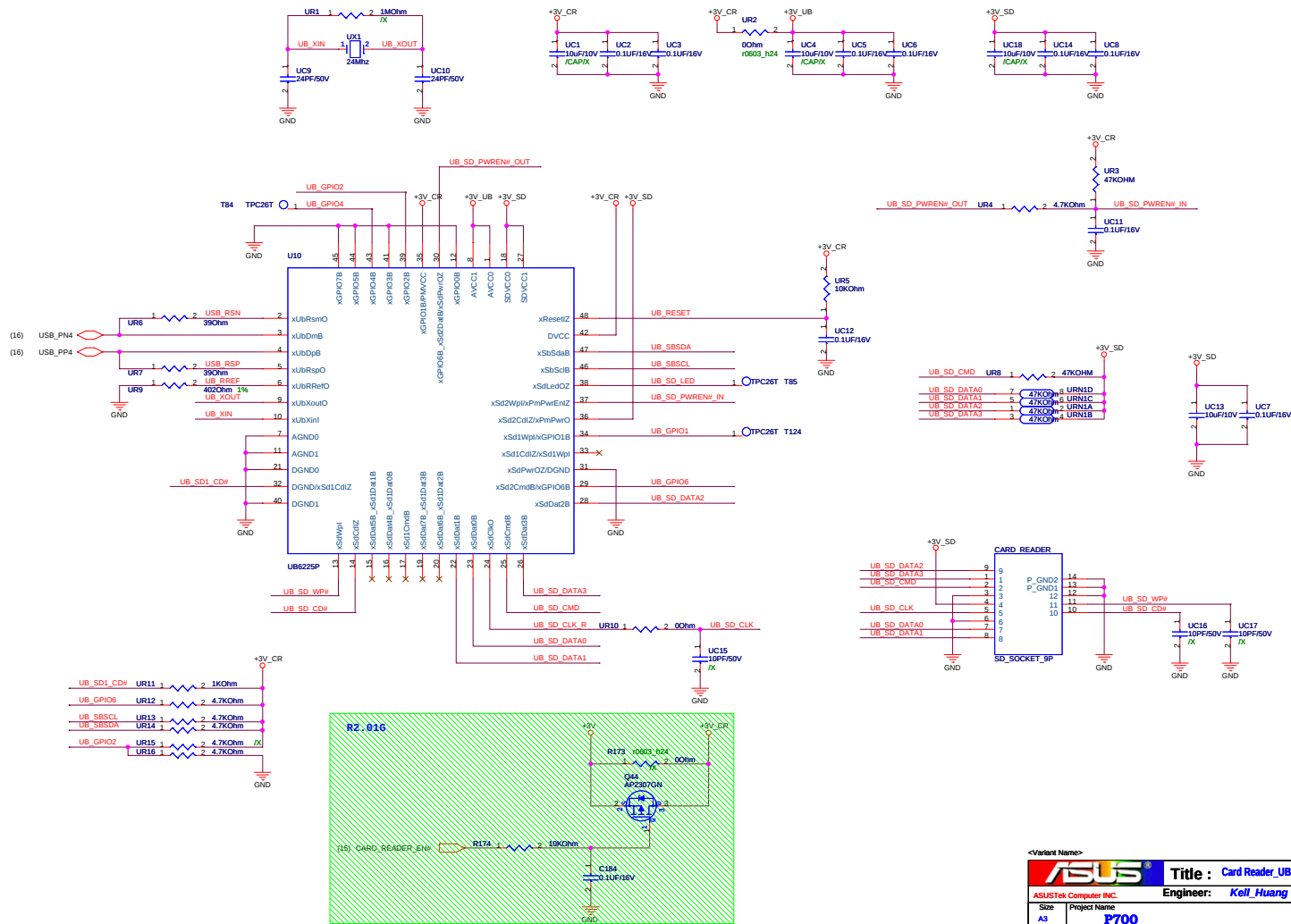


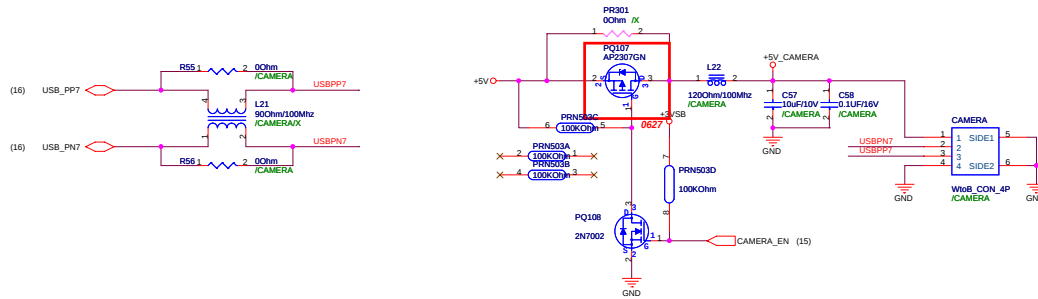
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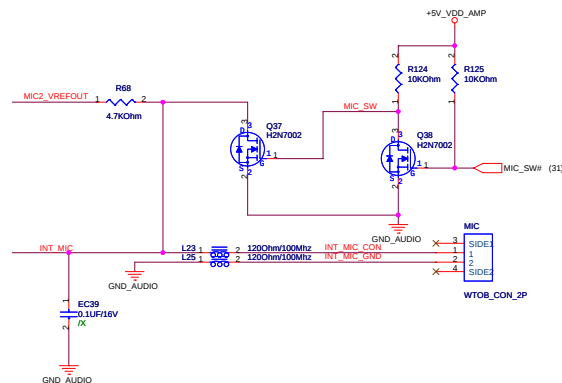
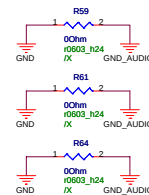






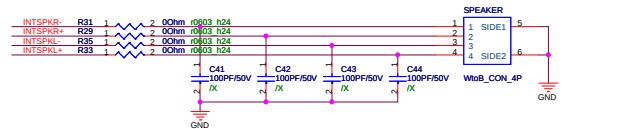
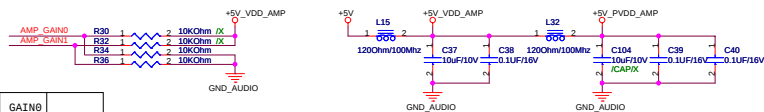




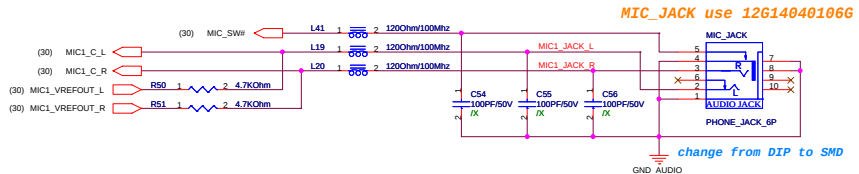
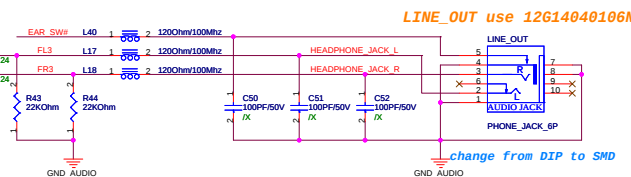
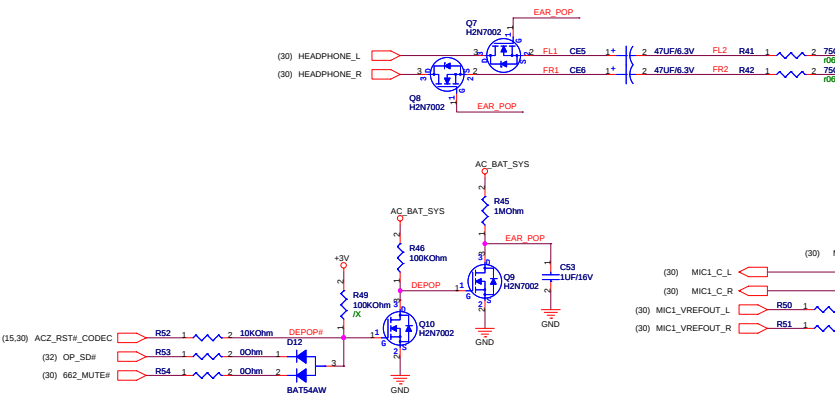
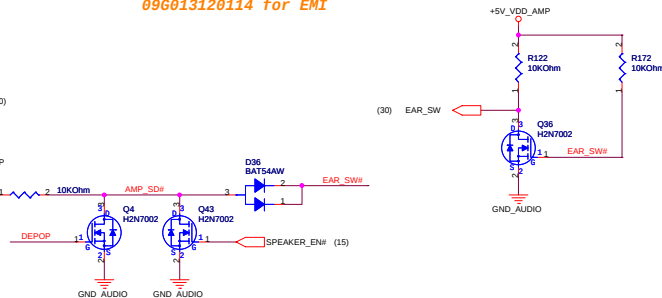
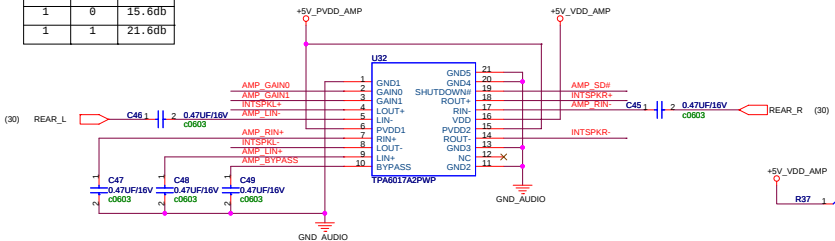


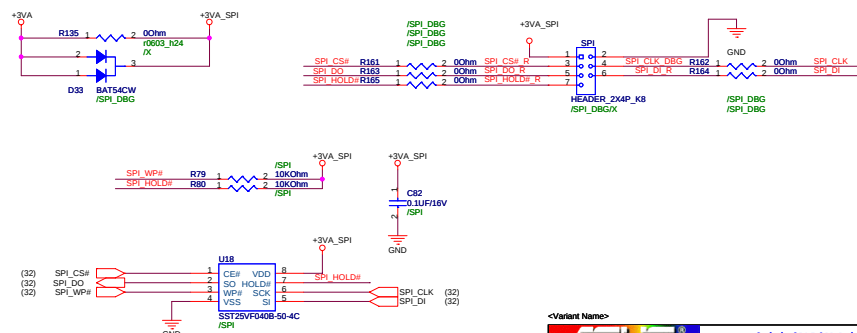
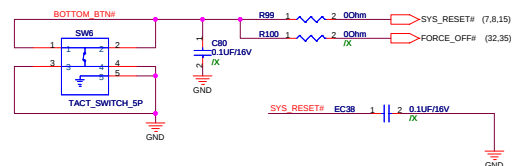
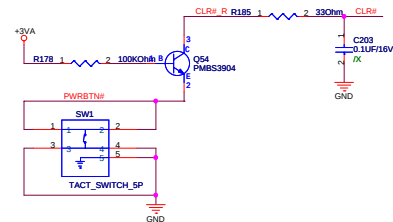
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0	1	10db
1	0	15.6db
1	1	21.6db

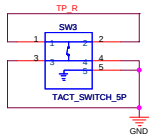
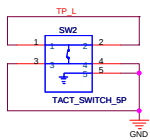


R29, R31, R33, R35 use Bead
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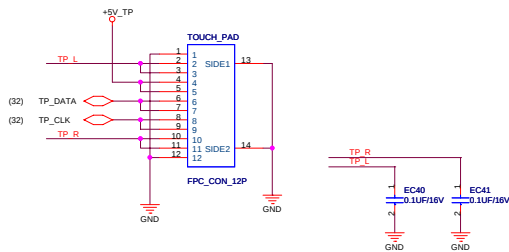




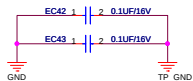
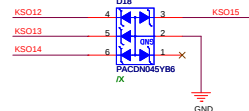
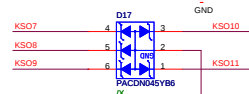
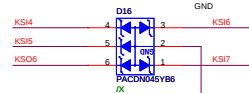
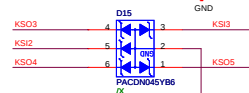
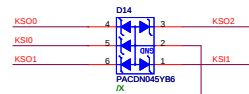
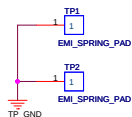
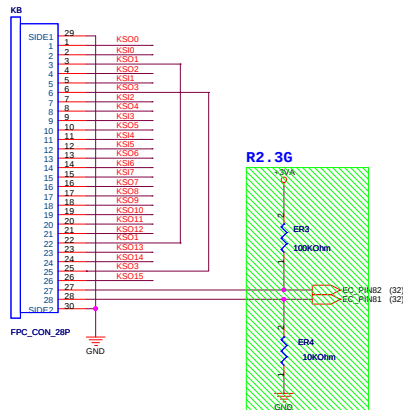
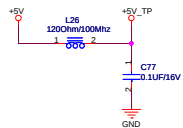
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ASUSTek Computer INC.		Engineer: <u>Kell Huang</u>	
Size A3	Project Name P700	Rev R2.3	
Date: <u>星期二, 八月 13, 2007</u>	Sheet	<u>33</u>	of <u>48</u>



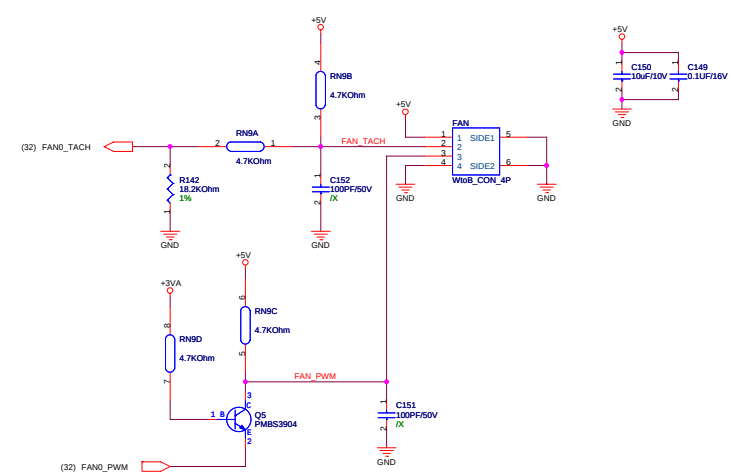
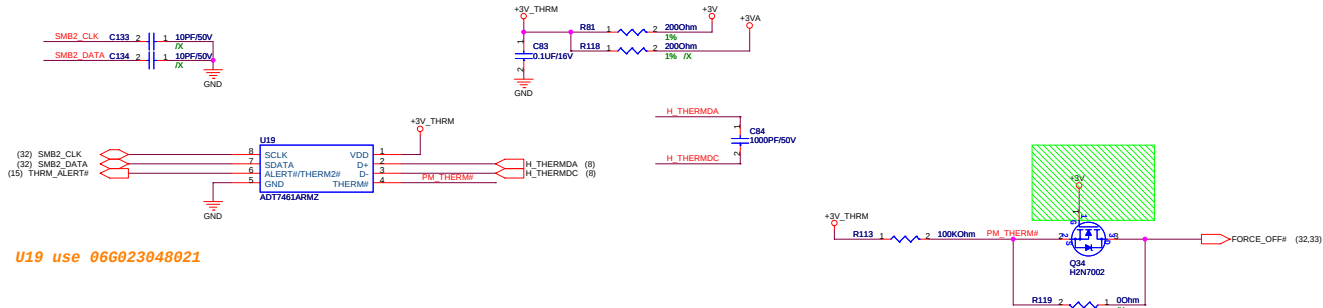
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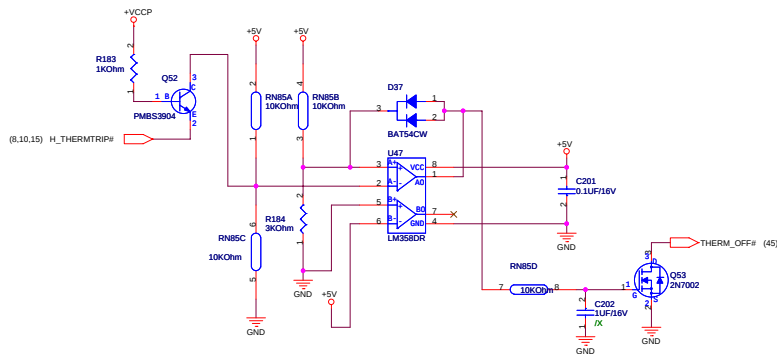
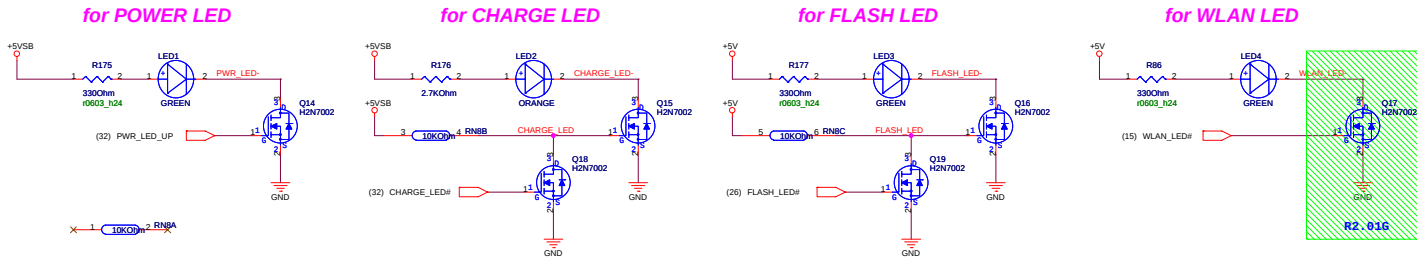


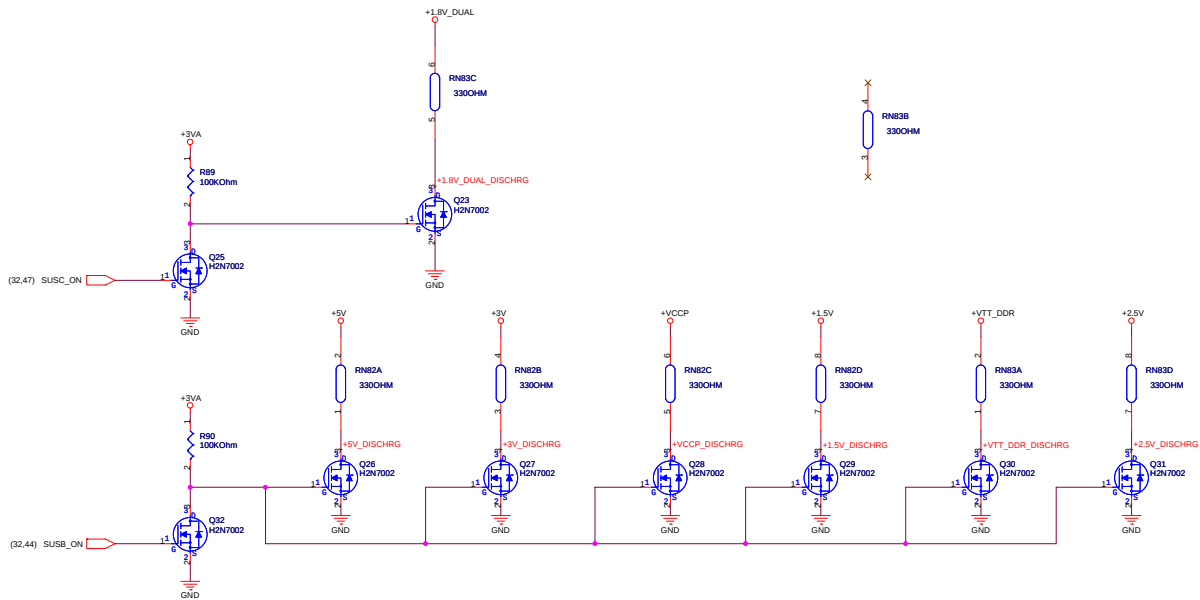
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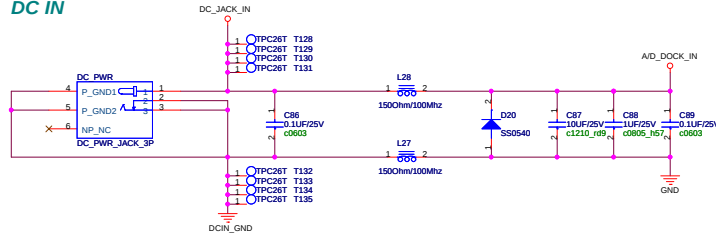




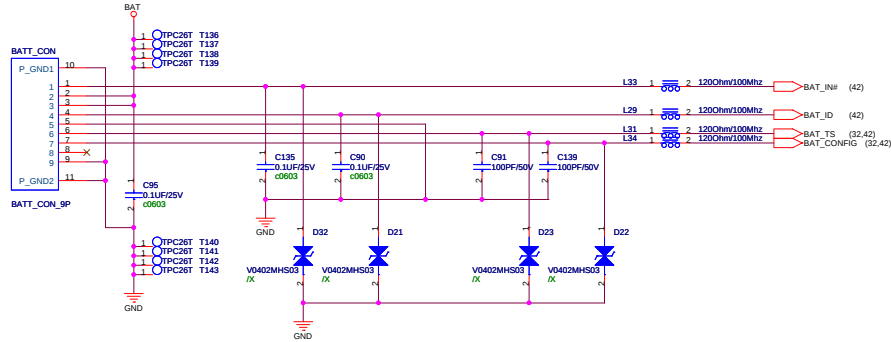
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ASUS		Title : Discharge	
ASUSTek Computer INC.		Engineer: <i>Kell Huang</i>	
Size	Project Name	Rev	
A3	P700	R2.36	
Date: 2022/11/13/2007	Sheet	97	of 48

DC IN

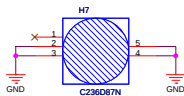
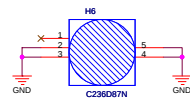
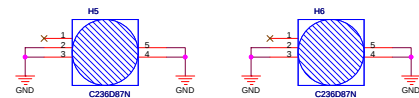


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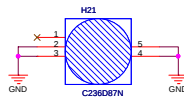
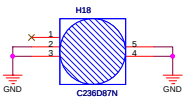
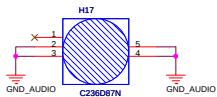
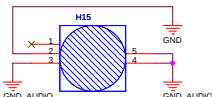
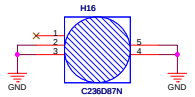
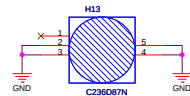
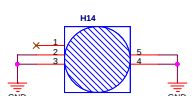
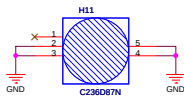
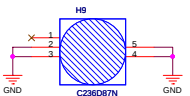
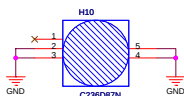


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		Title : PWR Jack	
ASUSTek Computer INC.		Engineer: Kell_Huang	
Size A3	Project Name P700		Rev R2.3C
Date: 星期五, 八月 13, 2007		Sheet 38 of 48	

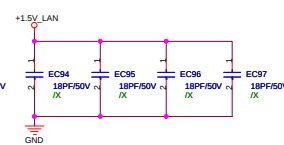
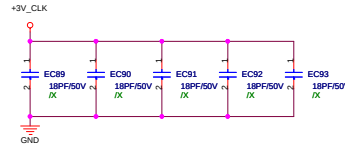
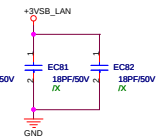
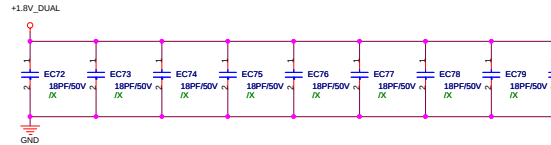
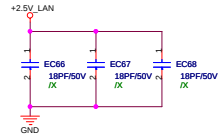
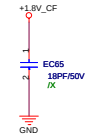
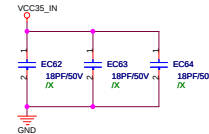
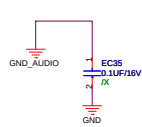
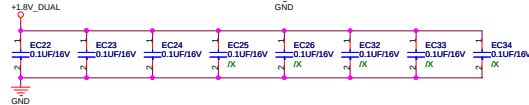
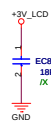
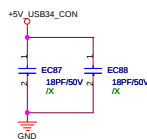
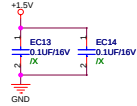
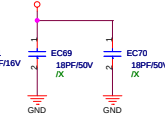
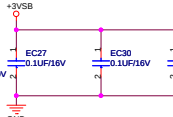
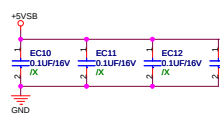
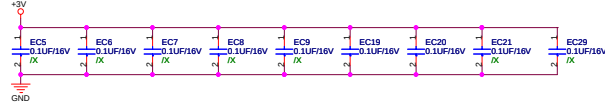
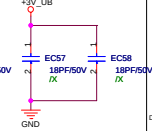
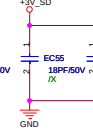
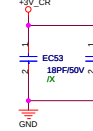
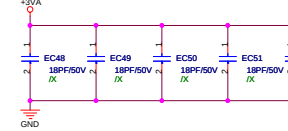
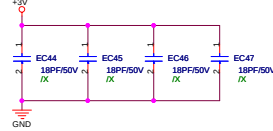
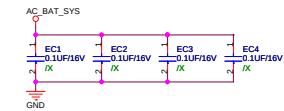


H20
SCREW_HOLE



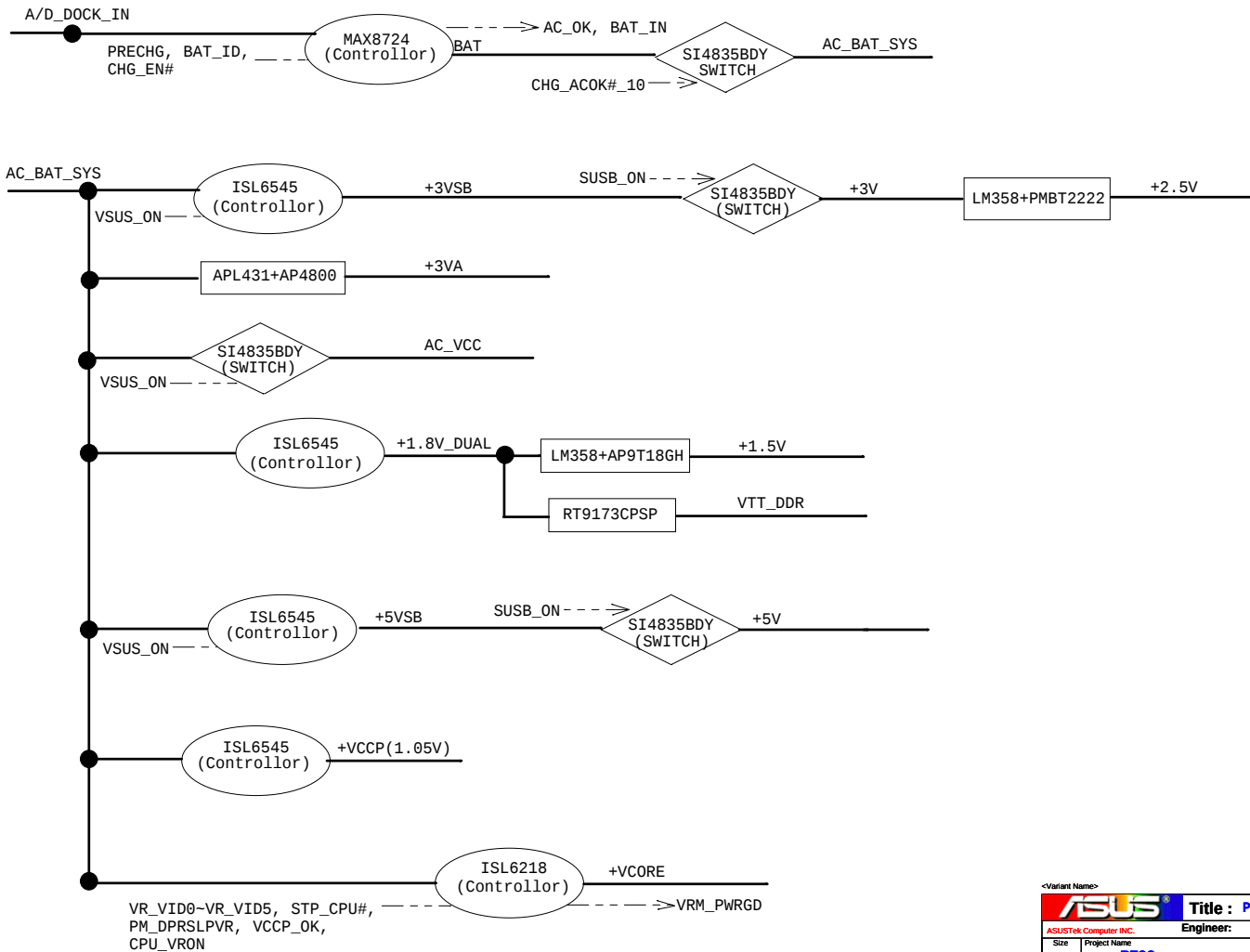
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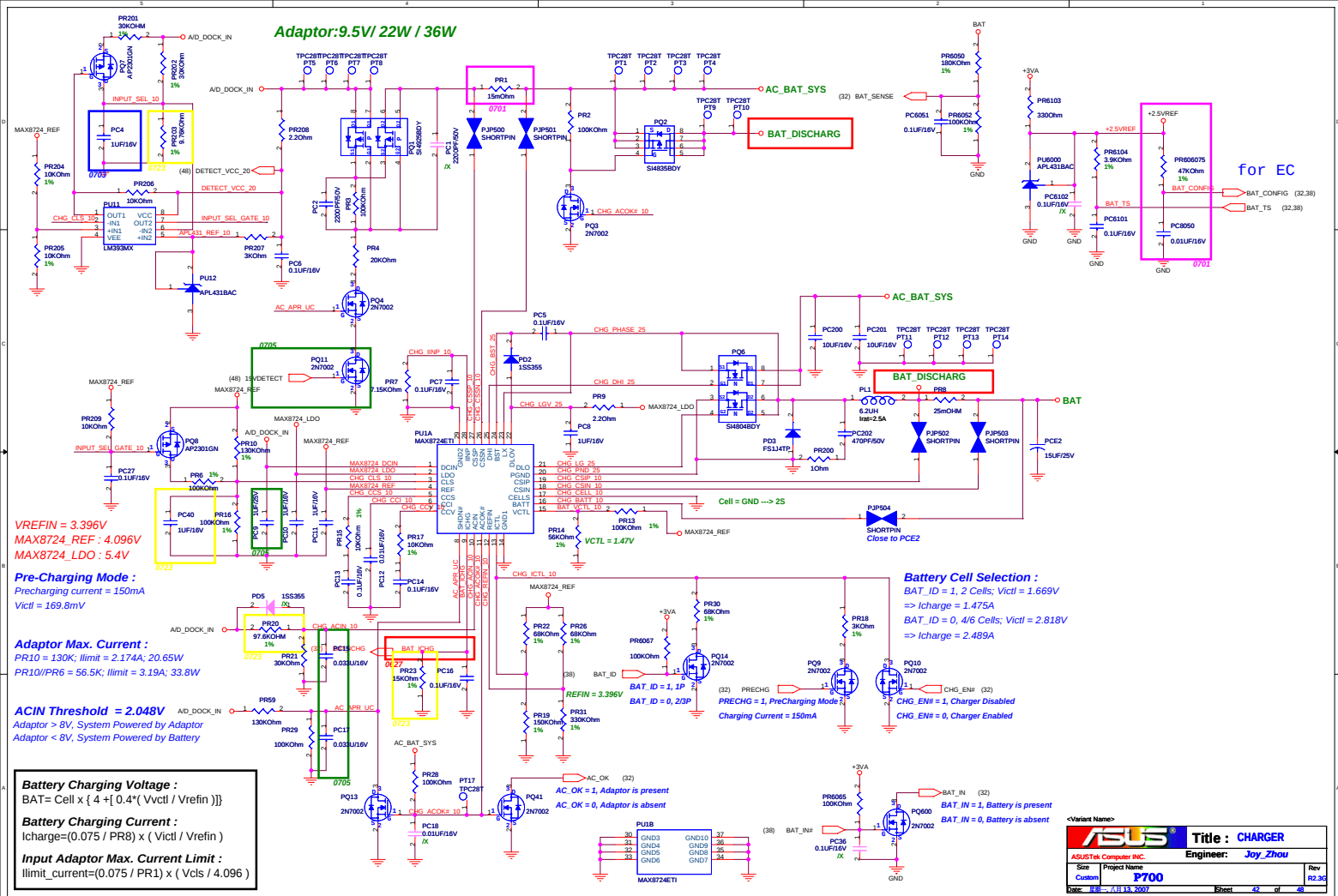
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ASUSTek Computer INC.		Engineer: Kelt_Huang	
Size	Project Name	Rev	
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Date:	2017.11.13	Sheet	39 of 48

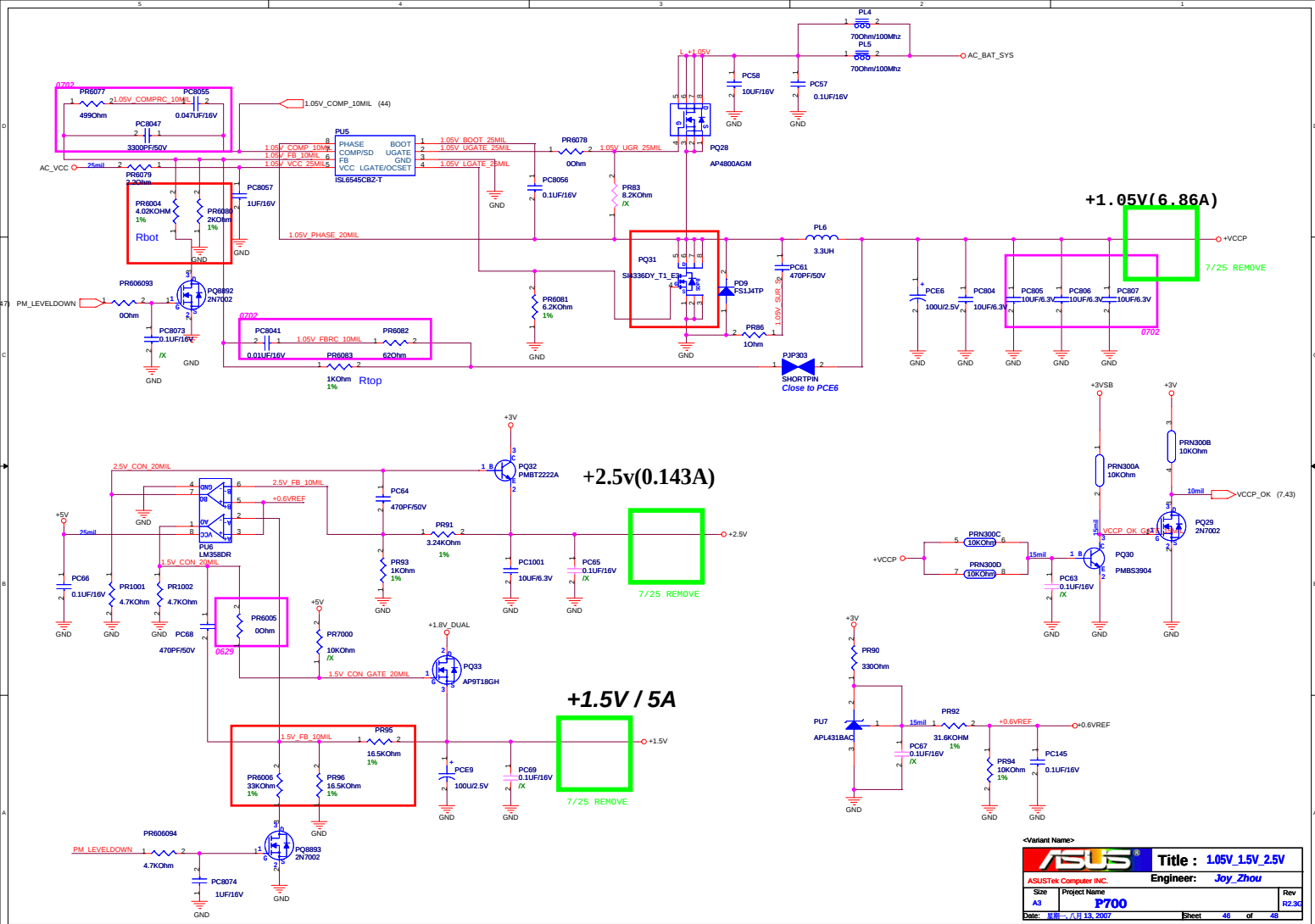


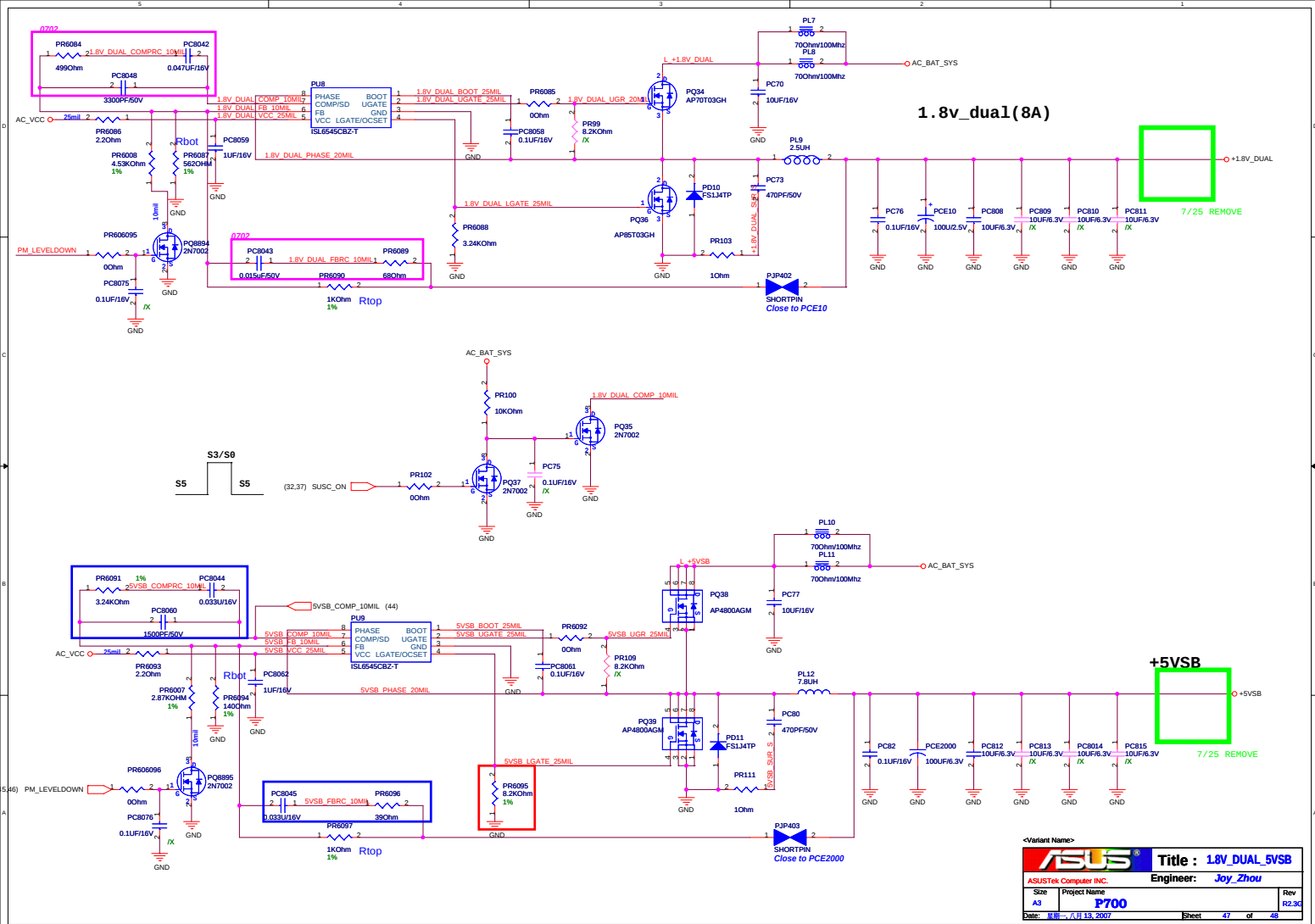
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Date: 2022.08.13.2022	Sheet: 40	of 40	

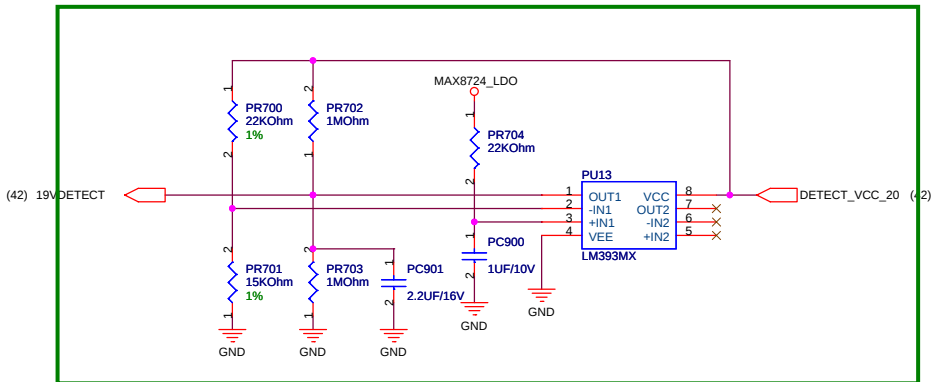








19V Detection Circuit



0705