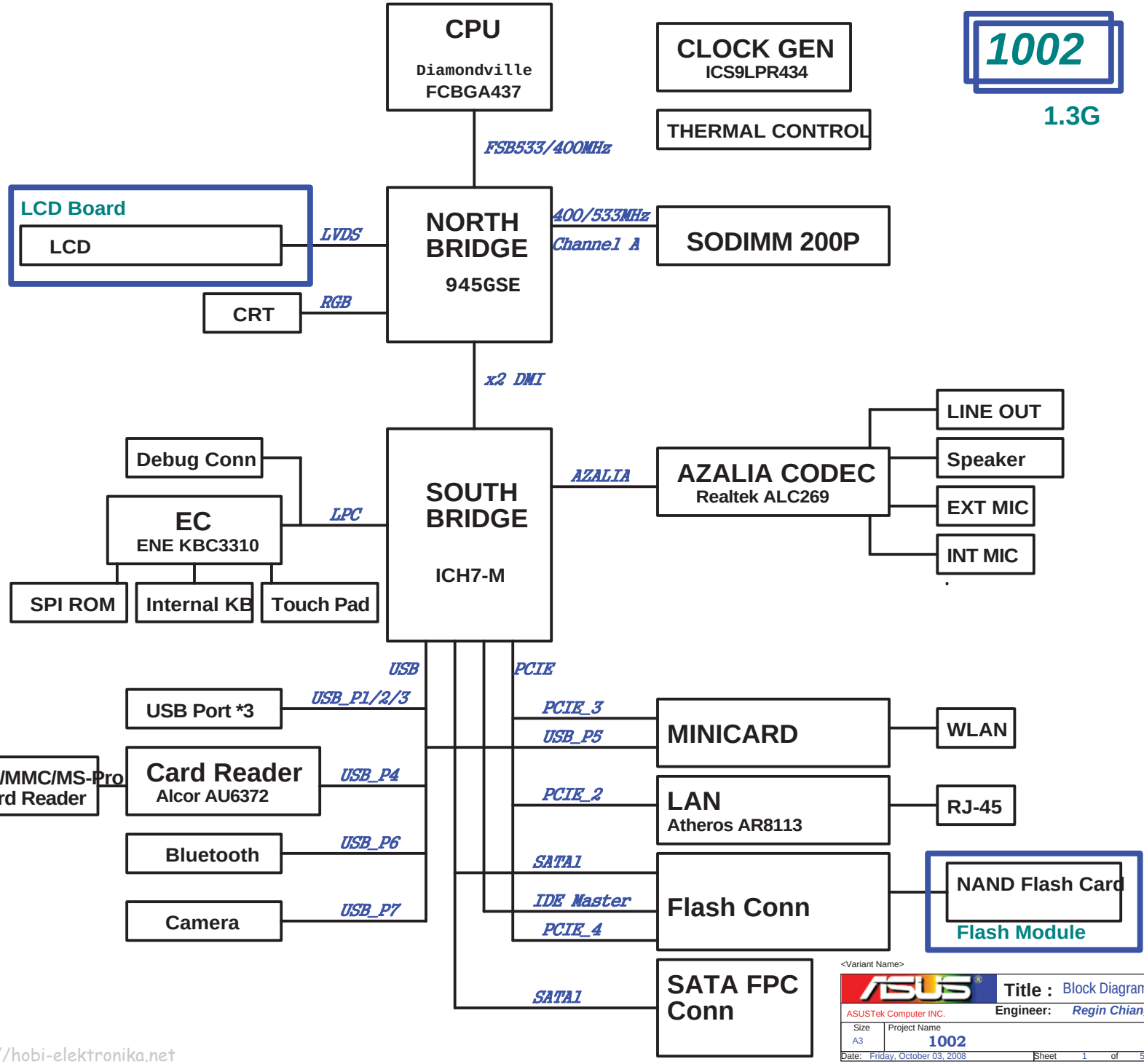


<Variant Name>

ASUSTek Computer INC.

Title : Block Diagram

Engineer: Regin Chiang

Size	Project Name	Rev
A3	1002	1.3G

Date: Friday, October 03, 2008 Sheet 1 of 50

EEE PC 701 PCB version

GPI37	GPI38	GPI39	PCB version
0	0	0	
0	0	0	
0	0	1	
0	0	1	
0	1	0	
0	1	0	
0	1	1	
0	1	1	
1	0	0	
1	0	0	
1	0	1	
1	0	1	
1	1	0	
1	1	0	
1	1	1	
1	1	1	

USB

USB 0	NC
USB 1	USB Conn
USB 2	USB Conn
USB 3	USB Conn
USB 4	Card Reader
USB 5	Minicard
USB 6	Bluetooth
USB 7	Camera

PCIE

PCIE 1	NC
PCIE 2	LAN
PCIE 3	Minicard
PCIE 4	SSD

Azalia

ACZ_SDIN0	CODEC
ACZ_SDIN1	NC
ACZ_SDIN2	NC

<Variant Name>

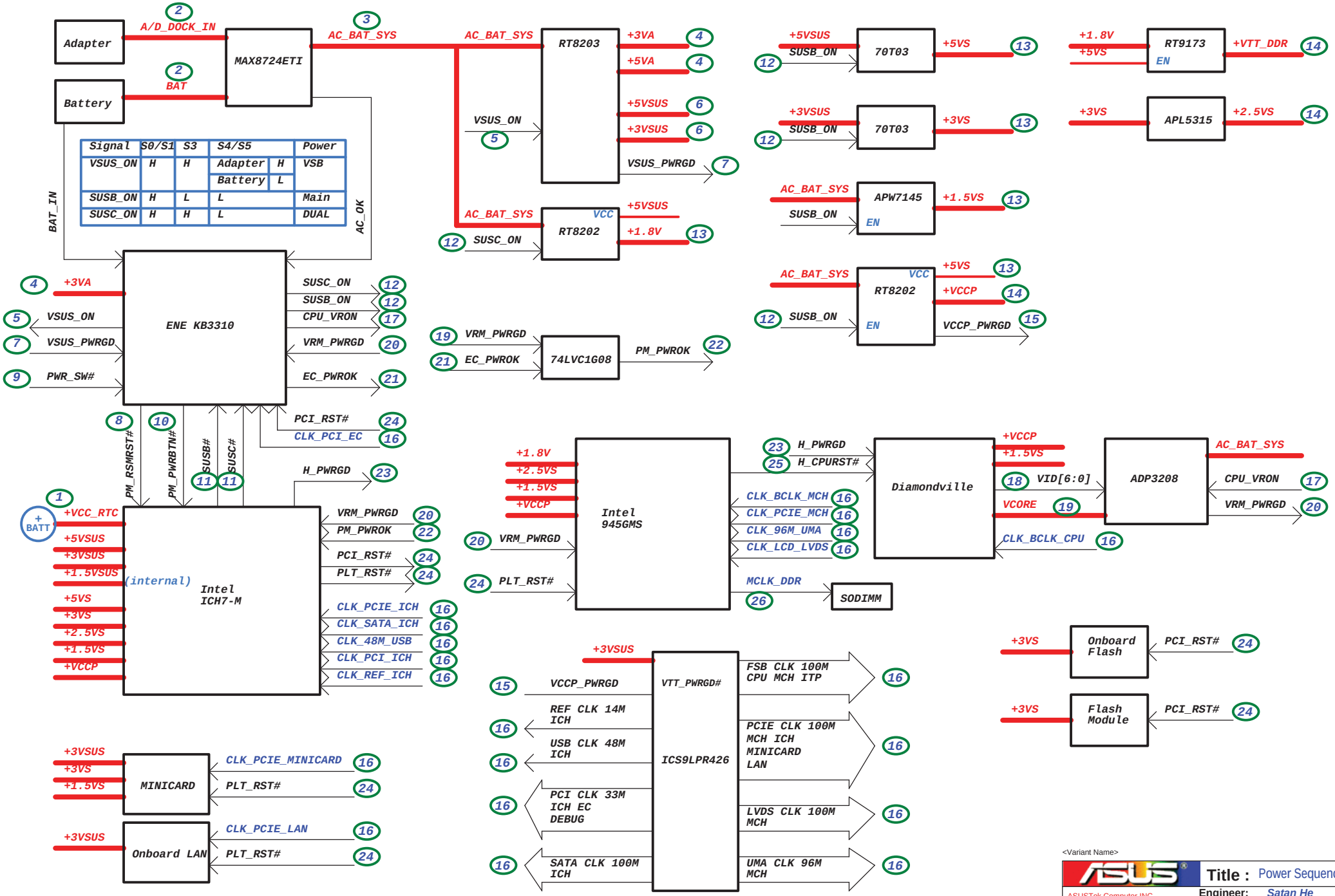
ASUSTek Computer INC.

Title : System Setting

Engineer: Satan_He

Size	Project Name	Rev
A3	S101	1.3G

Date: Friday, October 03, 2008 Sheet 2 of 50



<Variant Name>

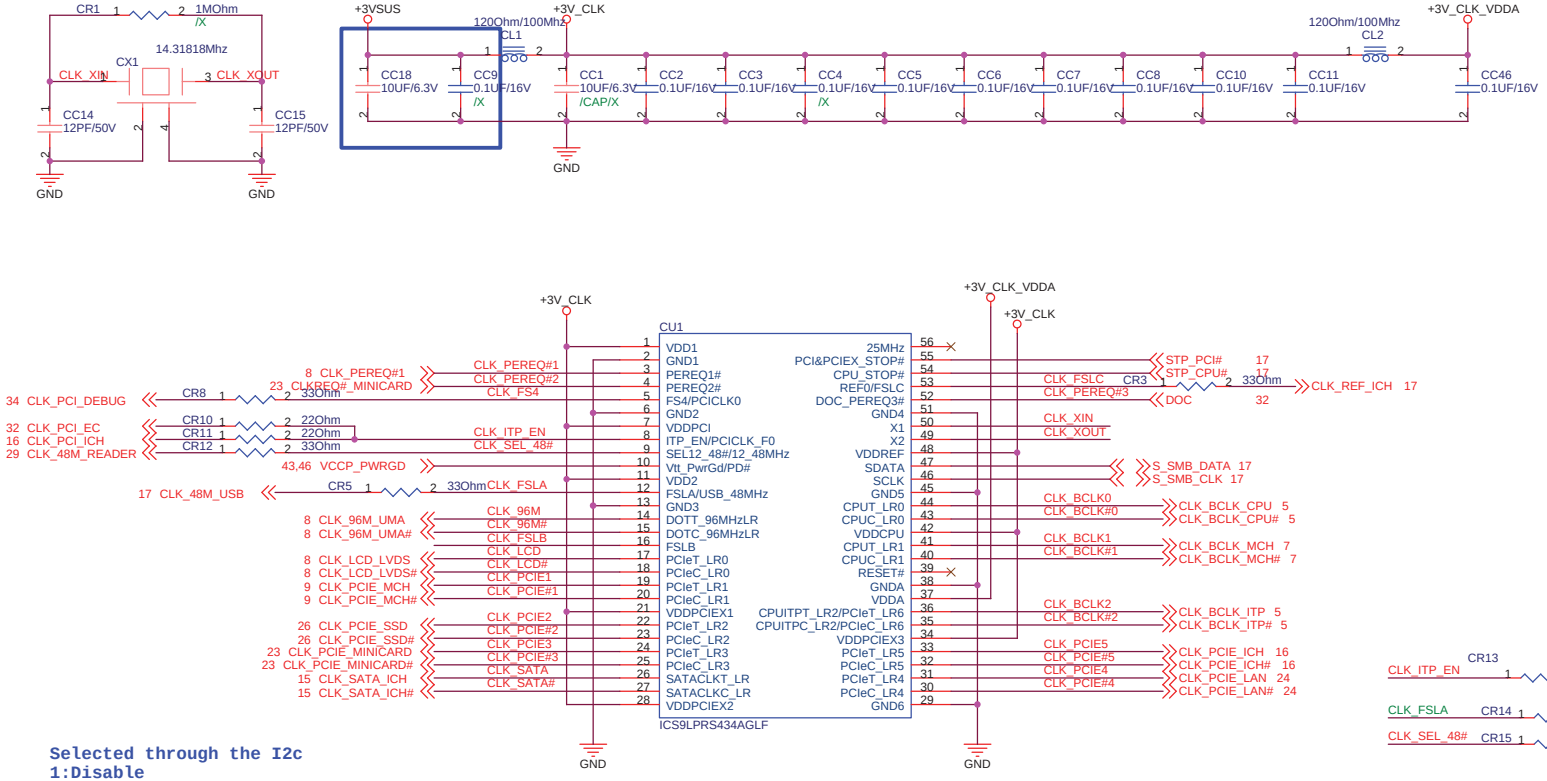
ASUSTek Computer INC.

Title : Power Sequence

Engineer: Satan He

Size	Project Name	Rev
A3	1002	1.3G

Date: Friday, October 03, 2008 Sheet 3 of 50

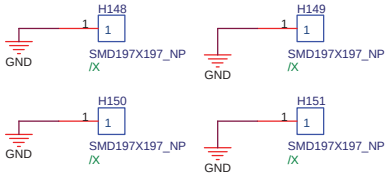


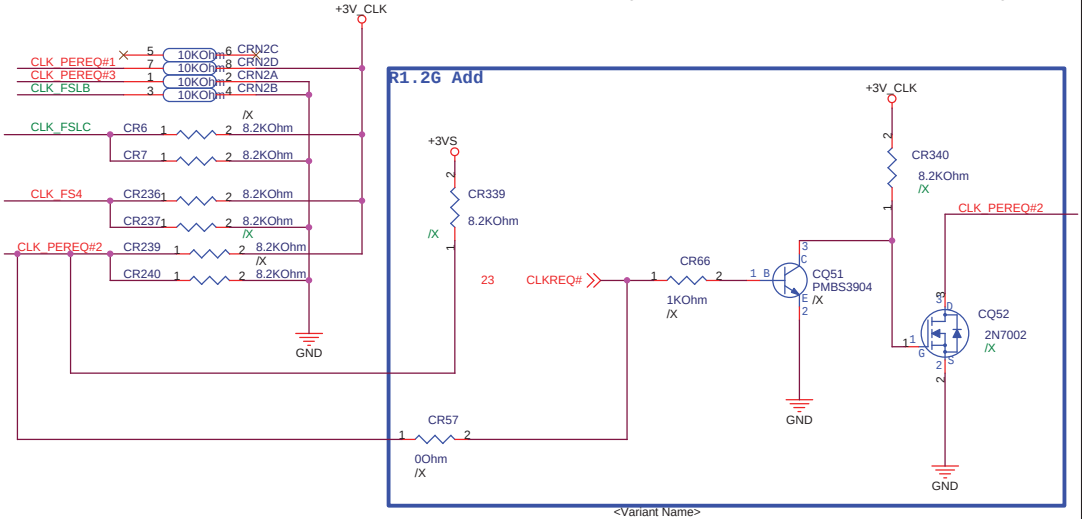
Selected through the I2c
1:Disable
0:Enable

PEREQ1:PCIEx0 & PCIEx1
PEREQ2:PCIEx2 & PCIEx3 & SATA
PEREQ3:PCIEx4 & PCIEx5 & PCIEx6

FSC	FSB	FSA	CPU	PCIE	SATA
0	0	1	133	100	100
1	0	1	100	100	100

H148-H151 reserve to place GASKET for EMI





<Variant Name>

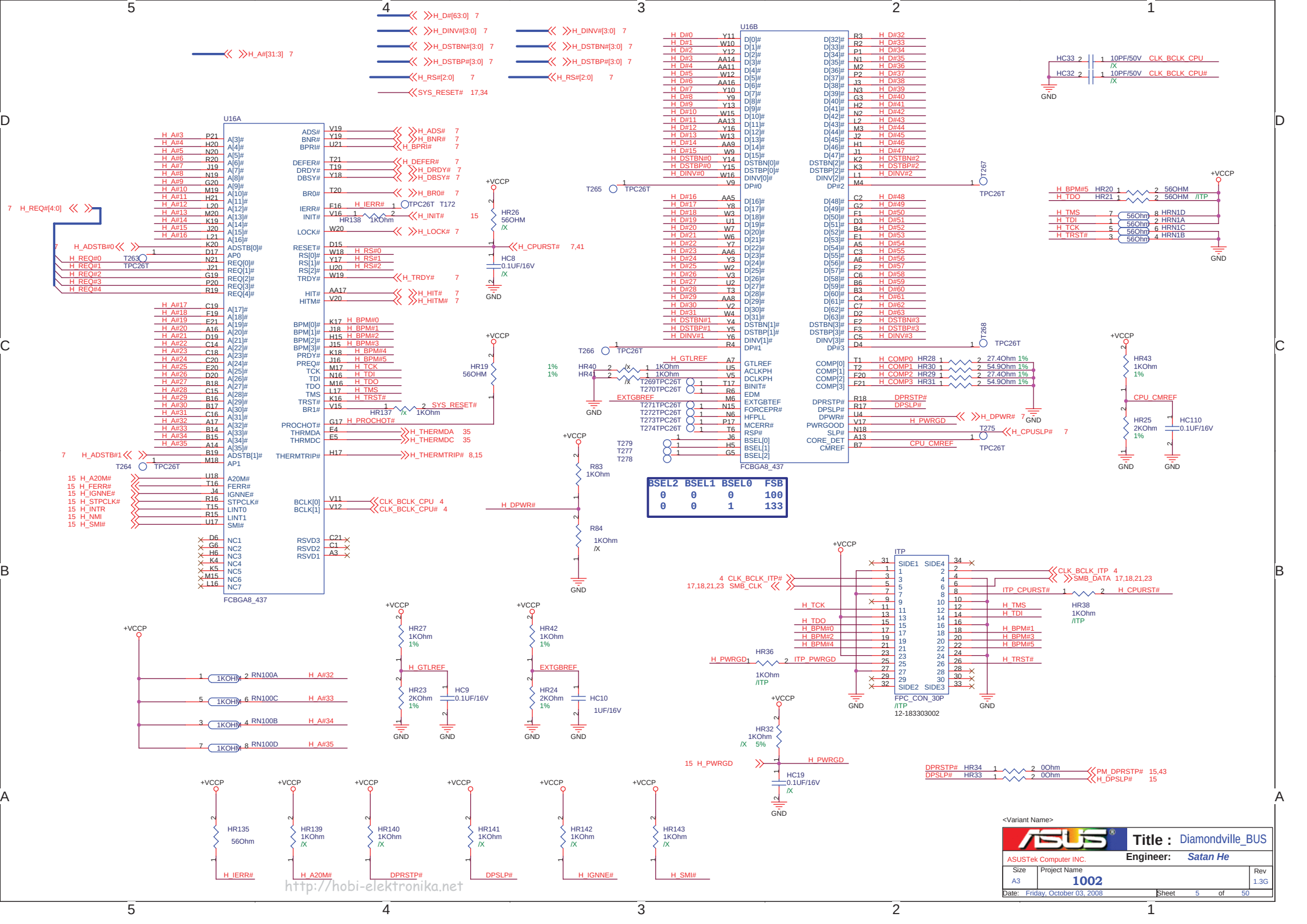
ASUSTek Computer INC.

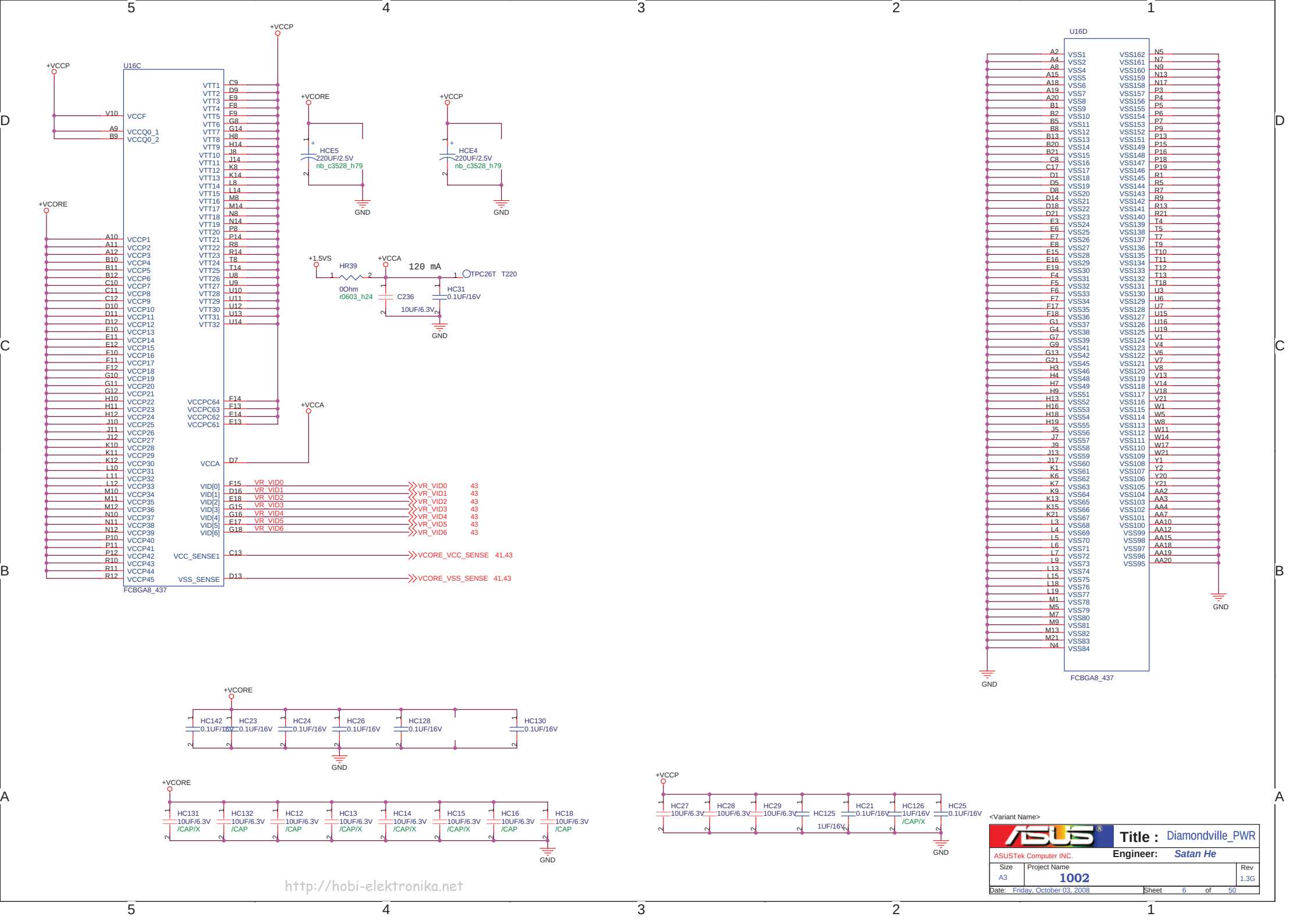
Title : Clock Gen_ICS9LPRS434

Engineer: Satan He

Size	Project Name	Rev
A3	1002	1.3G

Date: Friday, October 03, 2008 Sheet 4 of 50




U16C
FCBGA8_437
+VCCP
+VCORE
+VCCA
+1.5VS
GND
HCE5
220UF/2.5V
nb_c3528_h79
HCE4
220UF/2.5V
nb_c3528_h79
HR39
0Ohm
r0603_h24
C236
10UF/6.3V
HC31
0.1UF/16V
120 mA
TPC26T T220
VCC_SENSE1
VSS_SENSE
VR_VID0
VR_VID1
VR_VID2
VR_VID3
VR_VID4
VR_VID5
VR_VID6
VCORE_VCC_SENSE 41,43
VCORE_VSS_SENSE 41,43
HC142 0.1UF/16V
HC23 0.1UF/16V
HC24 0.1UF/16V
HC26 0.1UF/16V
HC128 0.1UF/16V
HC130 0.1UF/16V
HC131 10UF/6.3V /CAP/X
HC132 10UF/6.3V /CAP
HC12 10UF/6.3V /CAP
HC13 10UF/6.3V /CAP/X
HC14 10UF/6.3V /CAP/X
HC15 10UF/6.3V /CAP/X
HC16 10UF/6.3V /CAP
HC18 10UF/6.3V /CAP
U16D
FCBGA8_437
HC27 10UF/6.3V
HC28 10UF/6.3V
HC29 10UF/6.3V
HC125 1UF/16V
HC21 0.1UF/16V
HC126 1UF/16V /CAP/X
HC25 0.1UF/16V
<Variant Name>
Title : Diamondville_PWR
ASUSTek Computer INC.
Engineer: Satan He
Size A3
Project Name 1002
Rev 1.3G
Date: Friday, October 03, 2008
Sheet 6 of 50
http://hobi-elektronika.net

Power : +VCCP

RCOMP

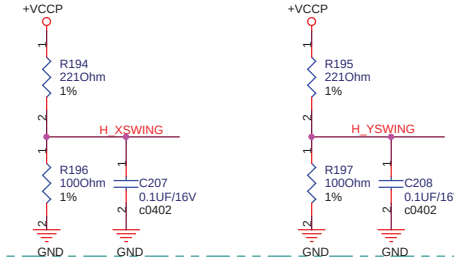
For Calibrating the FSB I/O Buffer

SCOMP

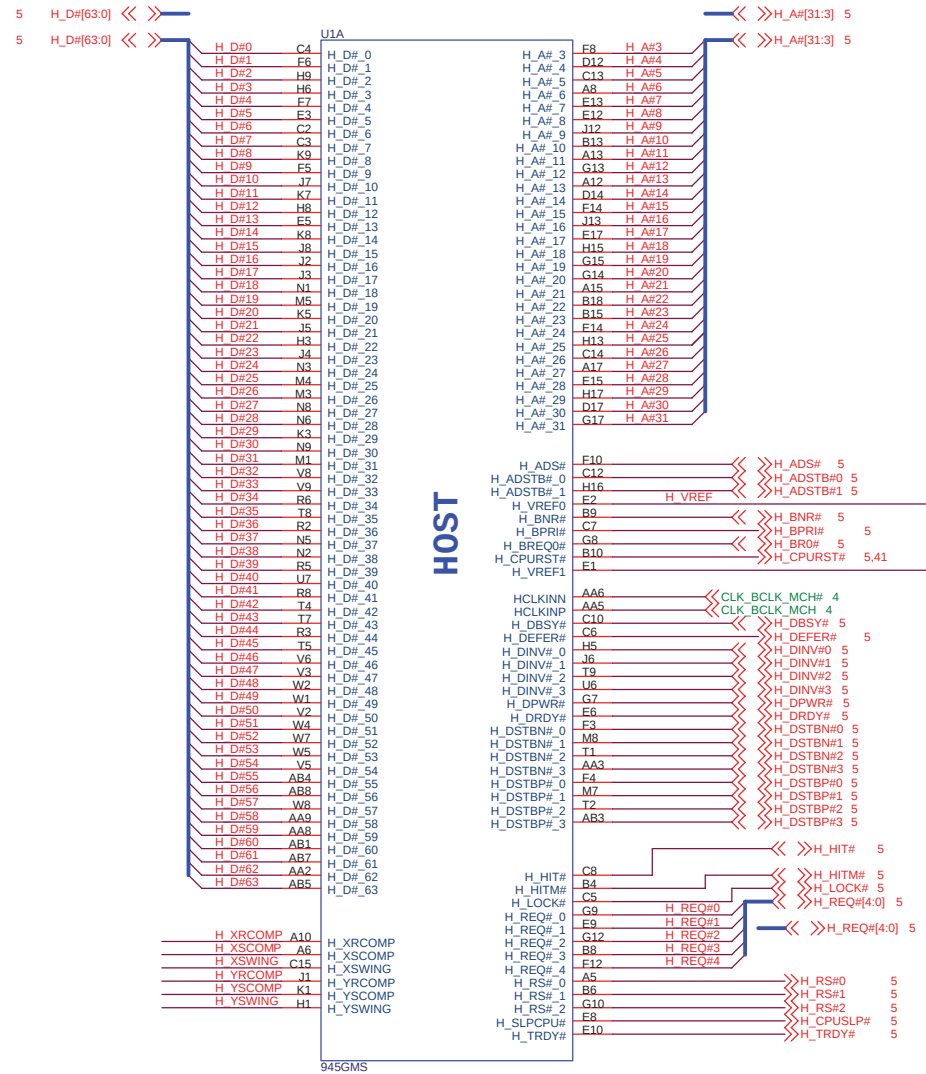
For Slew Rate Compenssation on the FSB

Voltage Swing

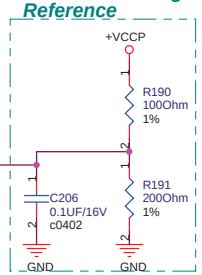
For Providing a Reference Voltage to The FSB RCOMP circuits



Signal voltage level = 0.3125*VCCP
Trace should be 10 mil wide with 20 mil spacing



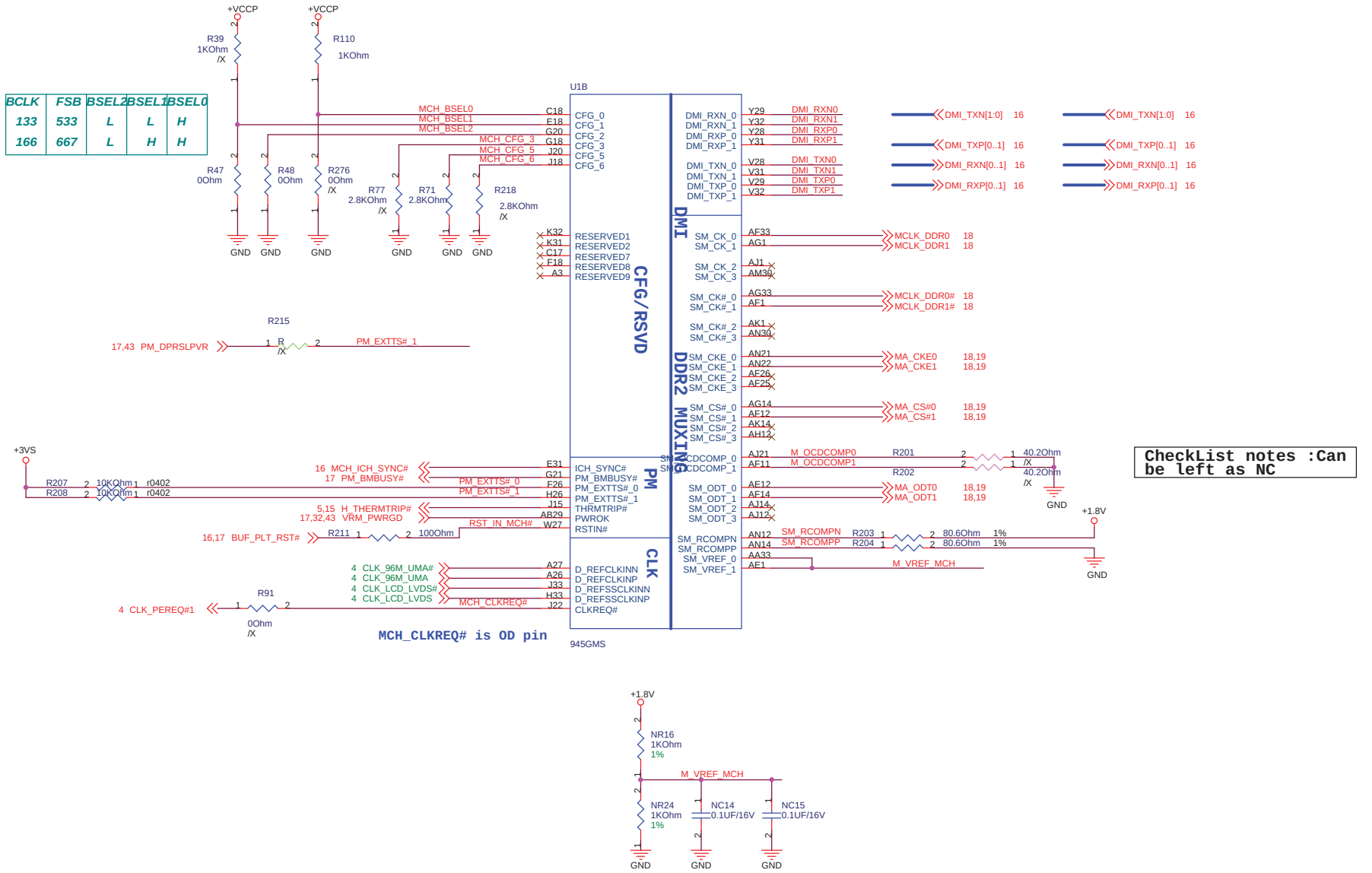
AGTL+ I/O Voltage Reference



Layout Note:
0.1uF should be placed 100mils or less from GMCH pin.

<Variant Name>

ASUSTeK COMPUTER INC.	Title : NB-945GMS(HOST)	
	Engineer: Satan He	
Size: A3	Project Name: 1002	Rev: 1.3G
Date: Friday, October 03, 2008	Sheet 7 of 50	



<Variant Name>

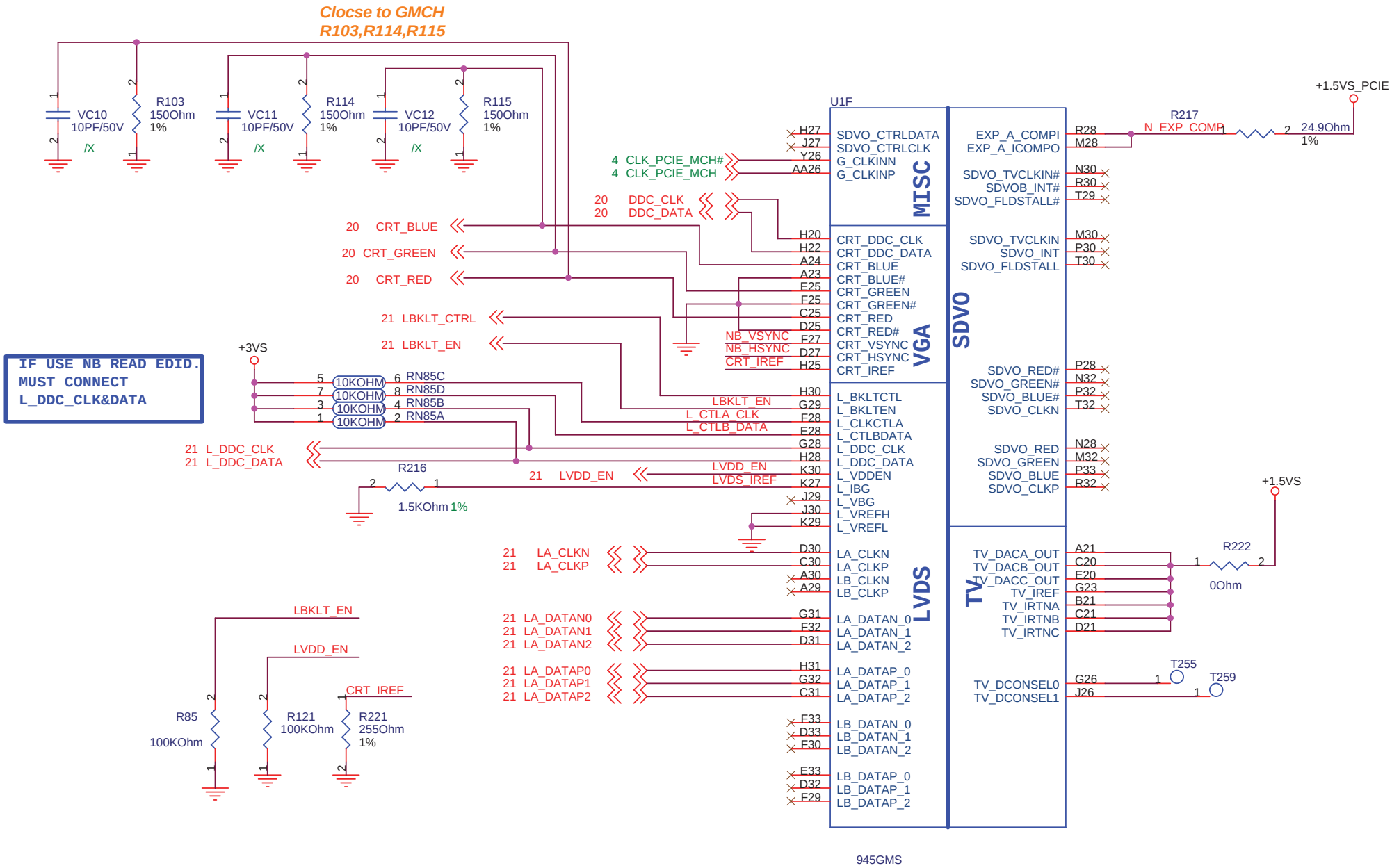
ASUSTeK COMPUTER INC.

Title : NB-945GMS(DMI & CFG)

Engineer: Satan He

Size	Project Name	Rev
A3	1002	1.3G

Date: Friday, October 03, 2008 Sheet 8 of 50


Clocse to GMCH
R103,R114,R115
VC10
10PF/50V
/X
R103
150Ohm
1%
VC11
10PF/50V
/X
R114
150Ohm
1%
VC12
10PF/50V
/X
R115
150Ohm
1%
U1F
SDVO_CTRLDATA
SDVO_CTRLCLK
G_CLKINN
G_CLKINP
MISC
4 CLK_PCIE_MCH#
4 CLK_PCIE_MCH
20 DDC_CLK
20 DDC_DATA
20 CRT_BLUE
20 CRT_GREEN
20 CRT_RED
21 LBKLT_CTRL
21 LBKLT_EN
CRT_DDC_CLK
CRT_DDC_DATA
CRT_BLUE
CRT_BLUE#
CRT_GREEN
CRT_GREEN#
CRT_RED
CRT_RED#
CRT_VSYNC
CRT_HSYNC
CRT_IREF
VGA
NB_VSYNC
NB_HSYNC
CRT_IREF
IF USE NB READ EDID.
MUST CONNECT
L_DDC_CLK&DATA
+3VS
RN85C
RN85D
RN85B
RN85A
10KOHM
21 L_DDC_CLK
21 L_DDC_DATA
R216
1.5KOhm 1%
LBKLT_EN
L_CTLA_CLK
L_CTLB_DATA
L_BKLTCTL
L_BKLTEN
L_CLKCTLA
L_CTLBDATA
L_DDC_CLK
L_DDC_DATA
L_VDDEN
L_IBG
L_VBG
L_VREFH
L_VREFL
21 LVDD_EN
LVDD_EN
LVDS_IREF
21 LA_CLKN
21 LA_CLKP
LA_CLKN
LA_CLKP
LB_CLKN
LB_CLKP
21 LA_DATAN0
21 LA_DATAN1
21 LA_DATAN2
LA_DATAN_0
LA_DATAN_1
LA_DATAN_2
21 LA_DATAP0
21 LA_DATAP1
21 LA_DATAP2
LA_DATAP_0
LA_DATAP_1
LA_DATAP_2
LB_DATAN_0
LB_DATAN_1
LB_DATAN_2
LB_DATAP_0
LB_DATAP_1
LB_DATAP_2
LVDS
SDVO
EXP_A_COMPI
EXP_A_ICOMPO
SDVO_TVCLKIN#
SDVOB_INT#
SDVO_FLDSTALL#
SDVO_TVCLKIN
SDVO_INT
SDVO_FLDSTALL
SDVO_RED#
SDVO_GREEN#
SDVO_BLUE#
SDVO_CLKN
SDVO_RED
SDVO_GREEN
SDVO_BLUE
SDVO_CLKP
TV_DACA_OUT
TV_DACB_OUT
TV_DACC_OUT
TV_IREF
TV_IRTNA
TV_IRTNB
TV_IRTNC
TV_DCONSEL0
TV_DCONSEL1
TV
+1.5VS_PCIE
R217
N_EXP_COMP
24.9Ohm
1%
+1.5VS
R222
0Ohm
T255
T259
LBKLT_EN
LVDD_EN
CRT_IREF
R85
100KOhm
R121
100KOhm
R221
255Ohm
1%
20 CRT_HSYNC
20 CRT_VSYNC
CRT_HSYNC
CRT_VSYNC
R220
R219
22Ohm
22Ohm
NB_HSYNC
NB_VSYNC
Clocse to GMCH
945GMS
<Variant Name>
Title : NB-945GMS(GRAPHIC)
ASUSTeK COMPUTER INC.
Engineer: Satan_He
Size A4
Project Name 1002
Rev 1.3G
Date: Friday, October 03, 2008
Sheet 9 of 50
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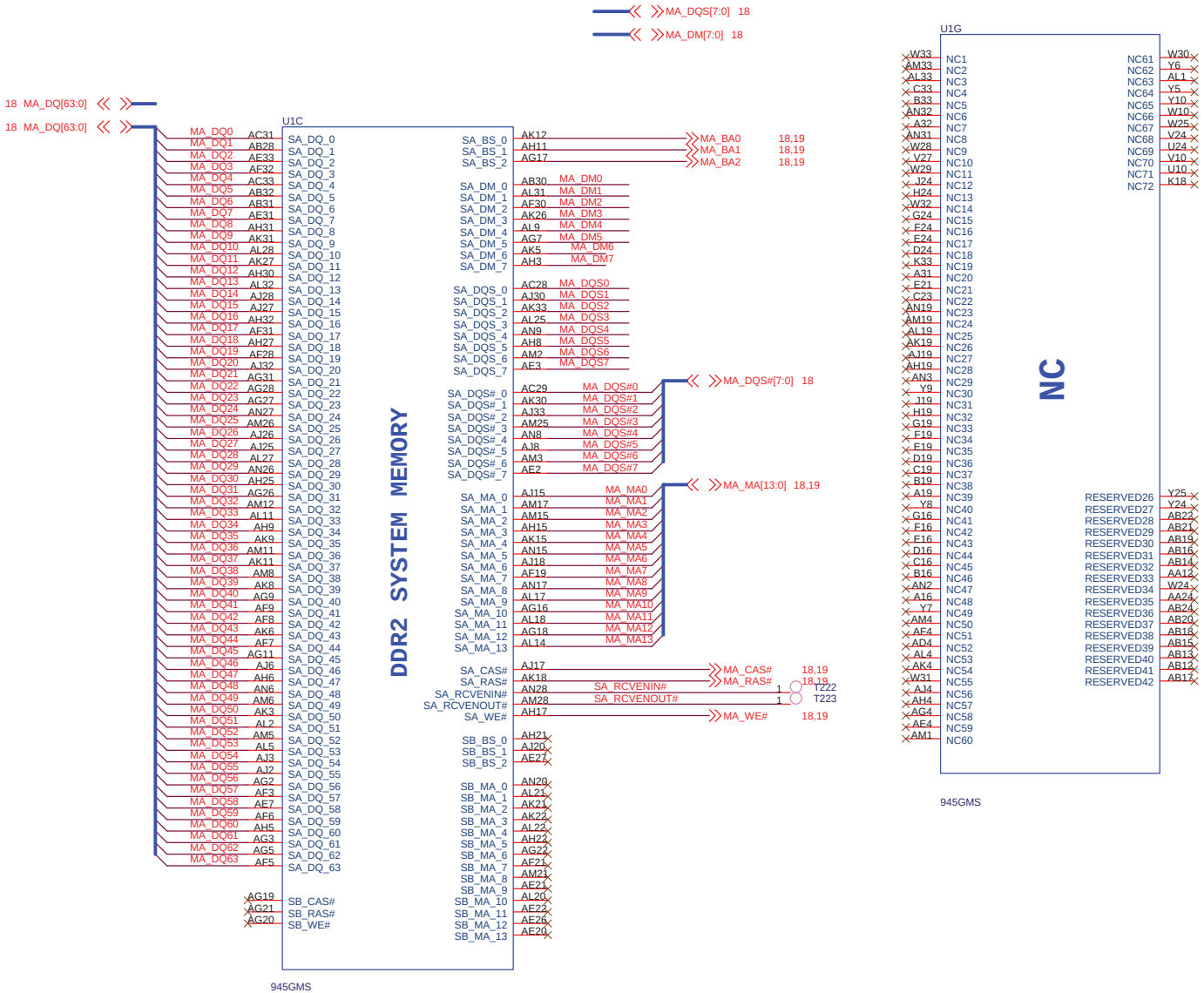
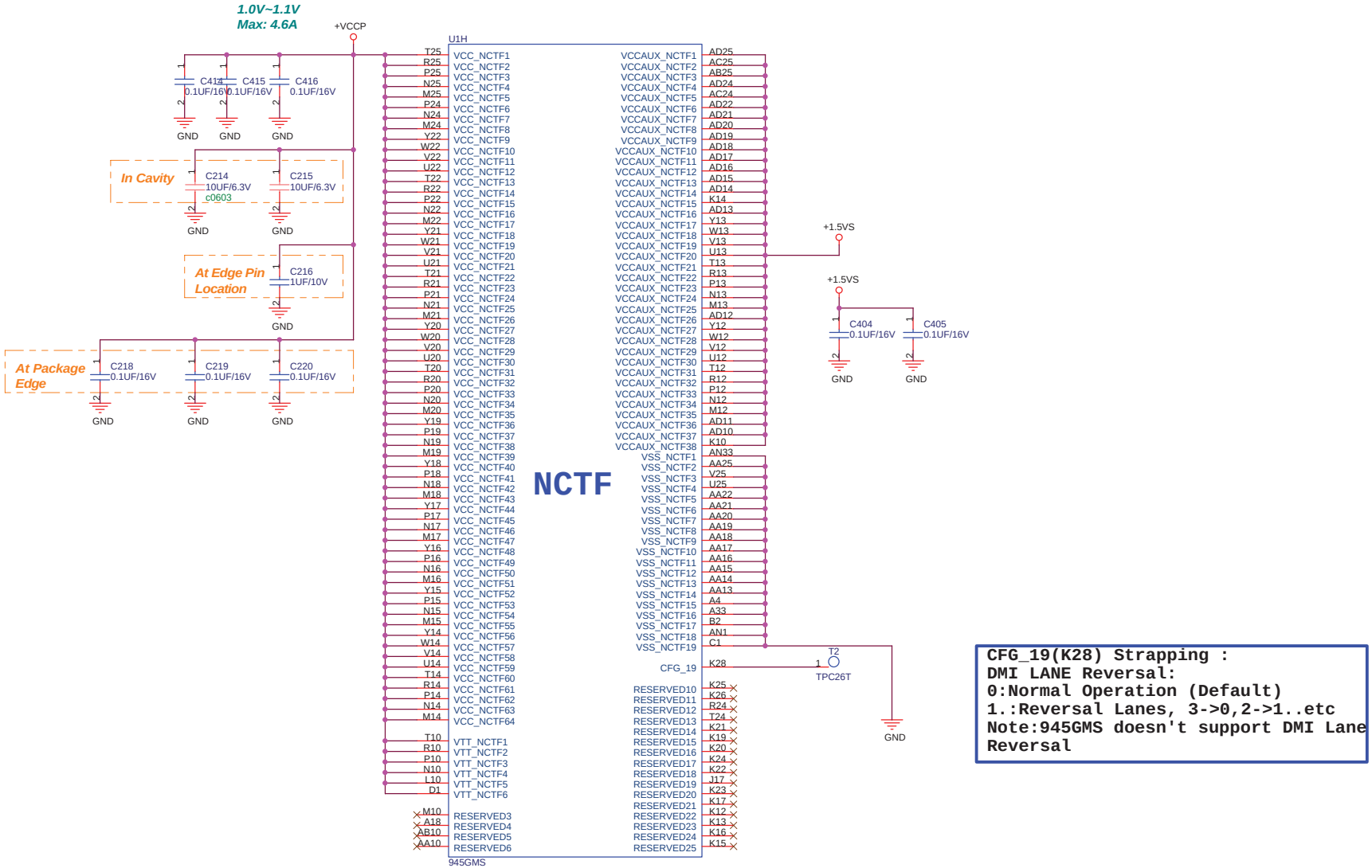


<Variant Name>

ASUSTeK COMPUTER INC.	Title : NB-945GMS(DDR2)	
	Engineer: Satan He	
Size: A3	Project Name: 1002	Rev: 1.3G
Date: Friday, October 03, 2008	Sheet 10 of 50	



<Variant Name>

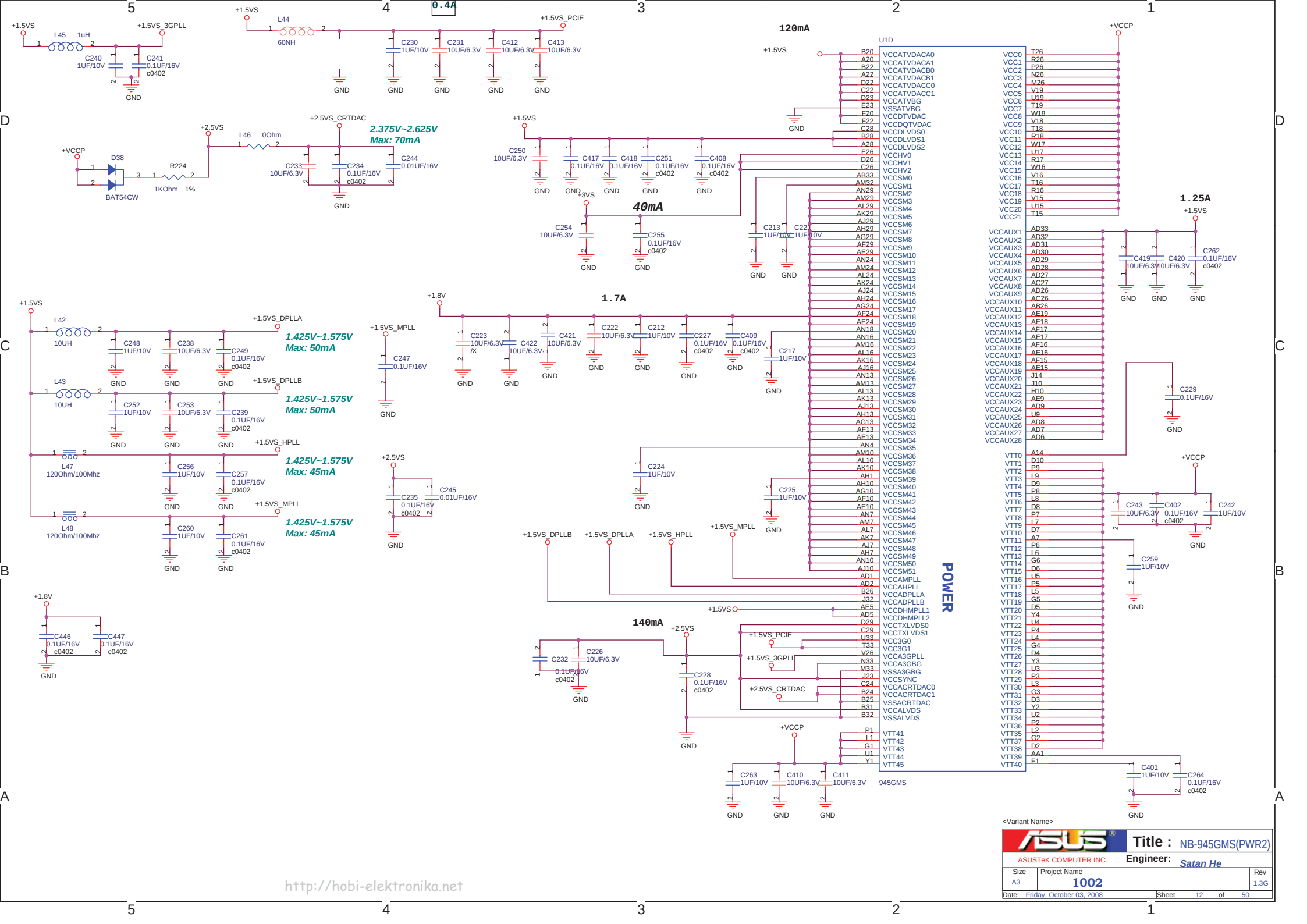
ASUSTeK COMPUTER INC.

Title : NB-945GMs(PWR)

Engineer: Satan_He

Size	Project Name	Rev
A3	1002	1.3G

Date: Friday, October 03, 2008 Sheet 11 of 50



<Variant Name>

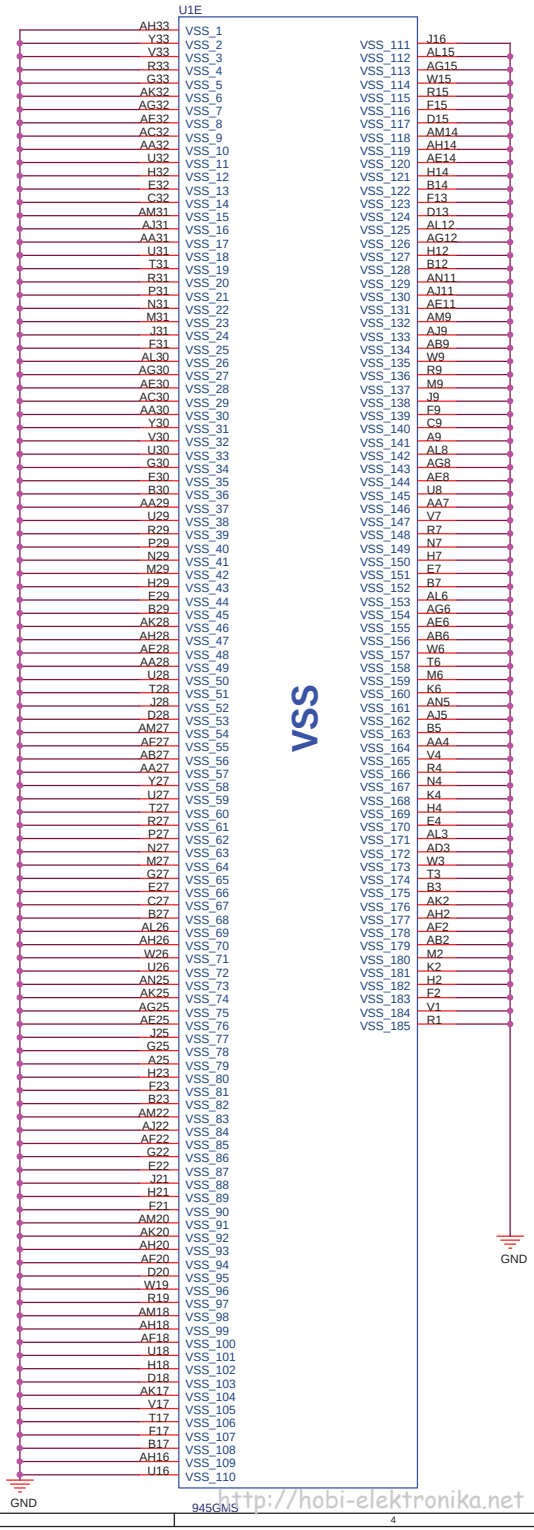
ASUSTeK COMPUTER INC.

Title : NB-945GMS(PWR2)

Engineer: Satan He

Size	Project Name	Rev
A3	1002	1.3G

Date: Friday, October 03, 2008 Sheet 12 of 50



<Variant Name>

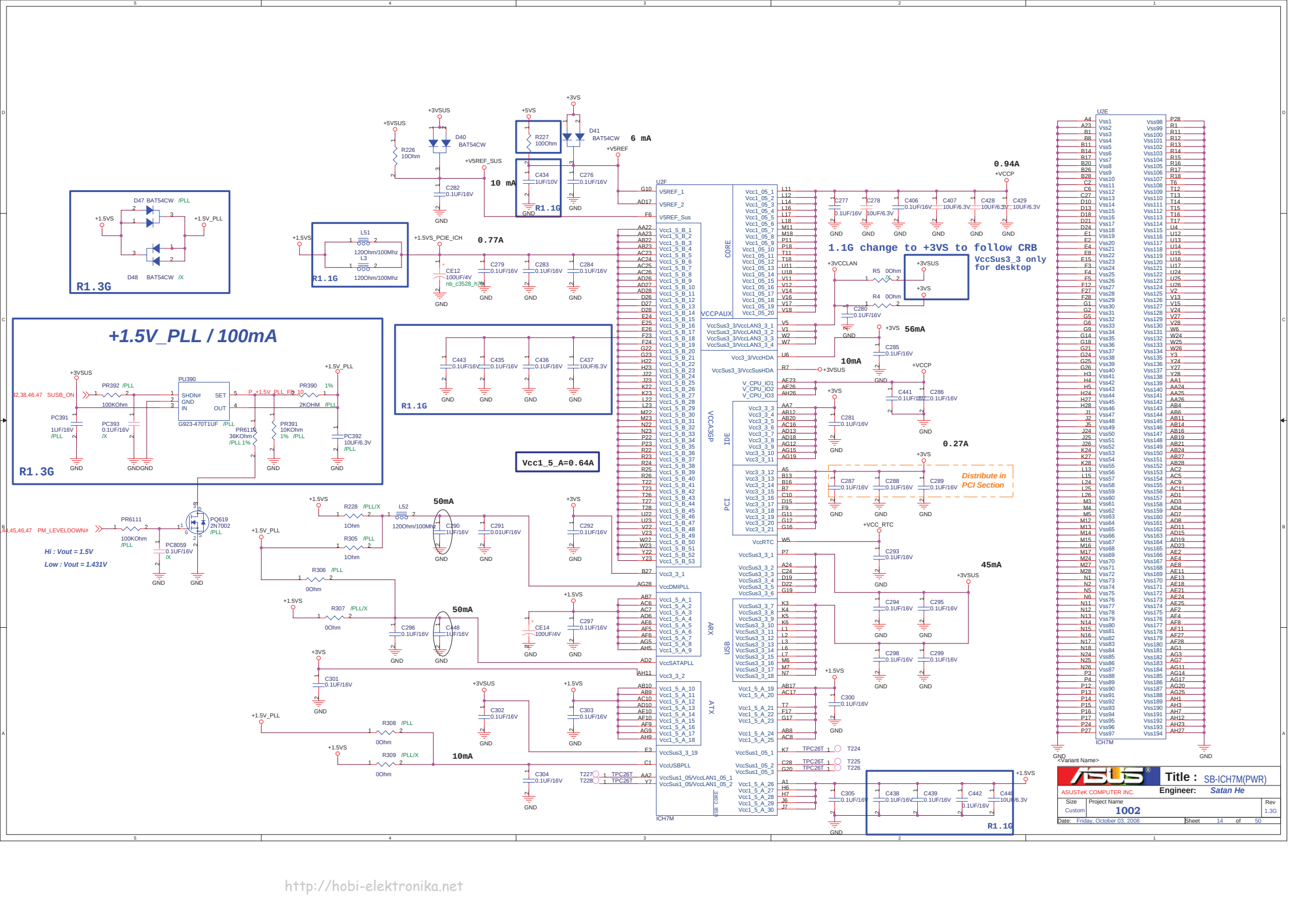
ASUSTeK COMPUTER INC.

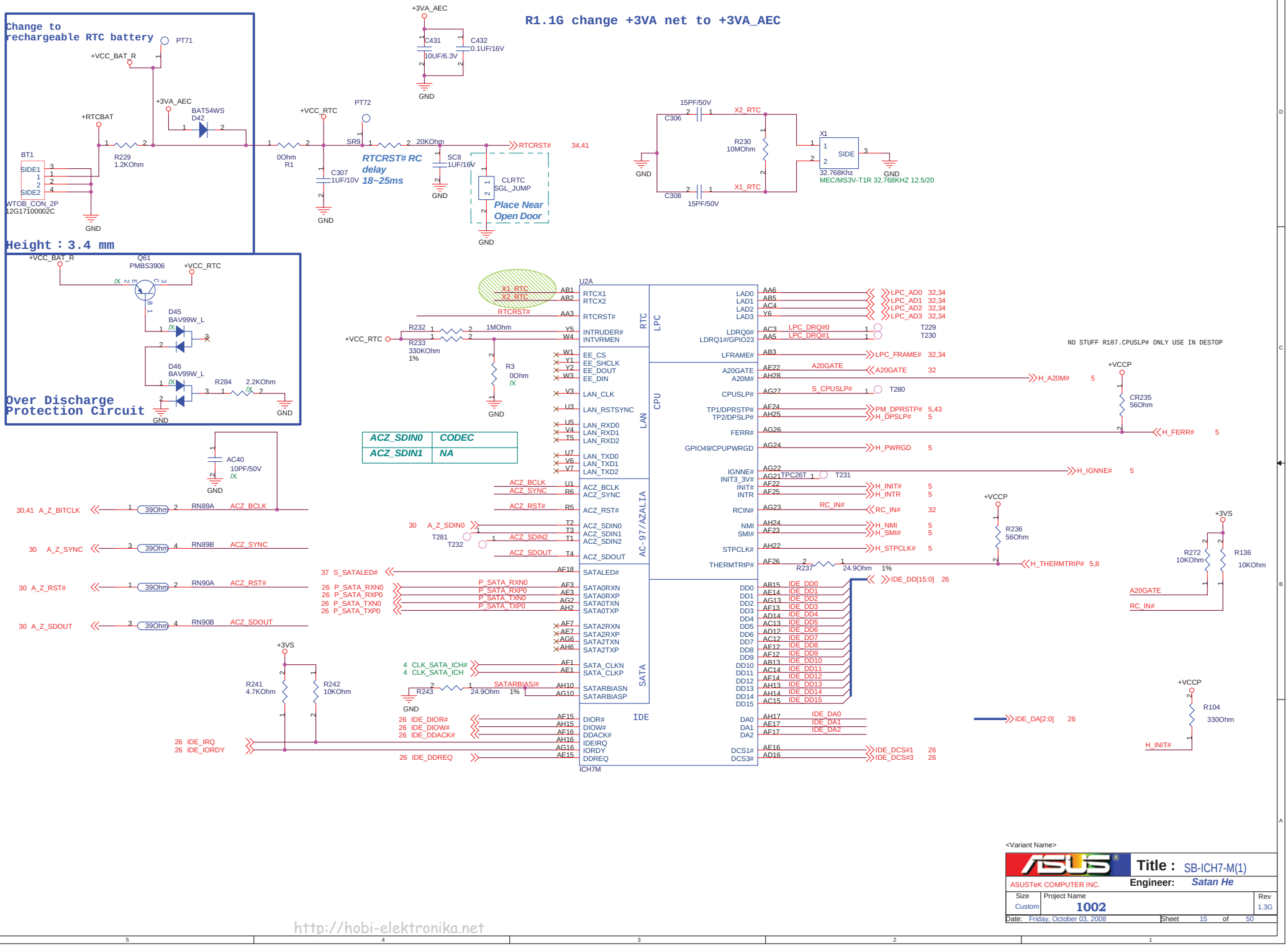
Title : NB-945PMS(GND)

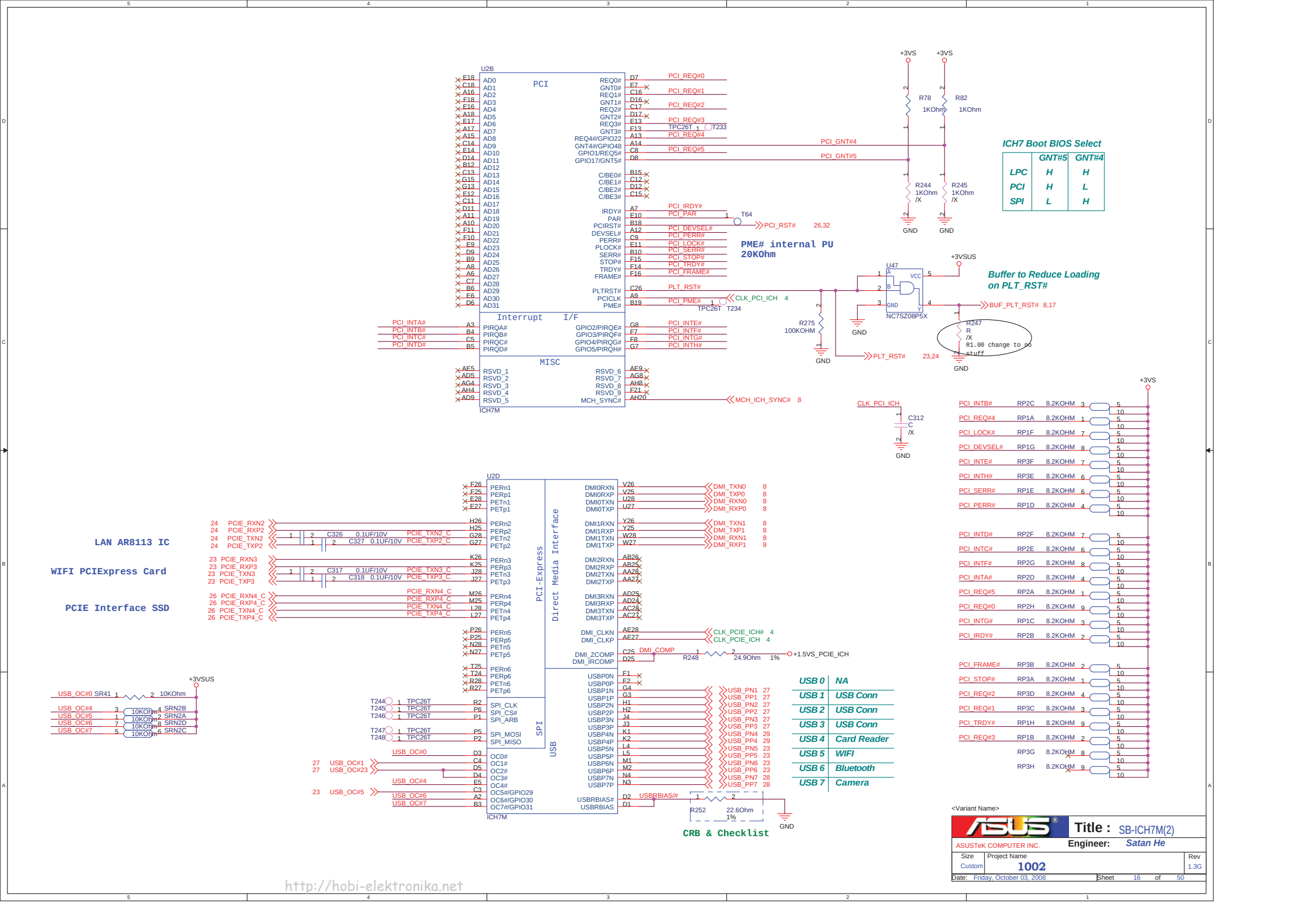
Engineer: Satan_He

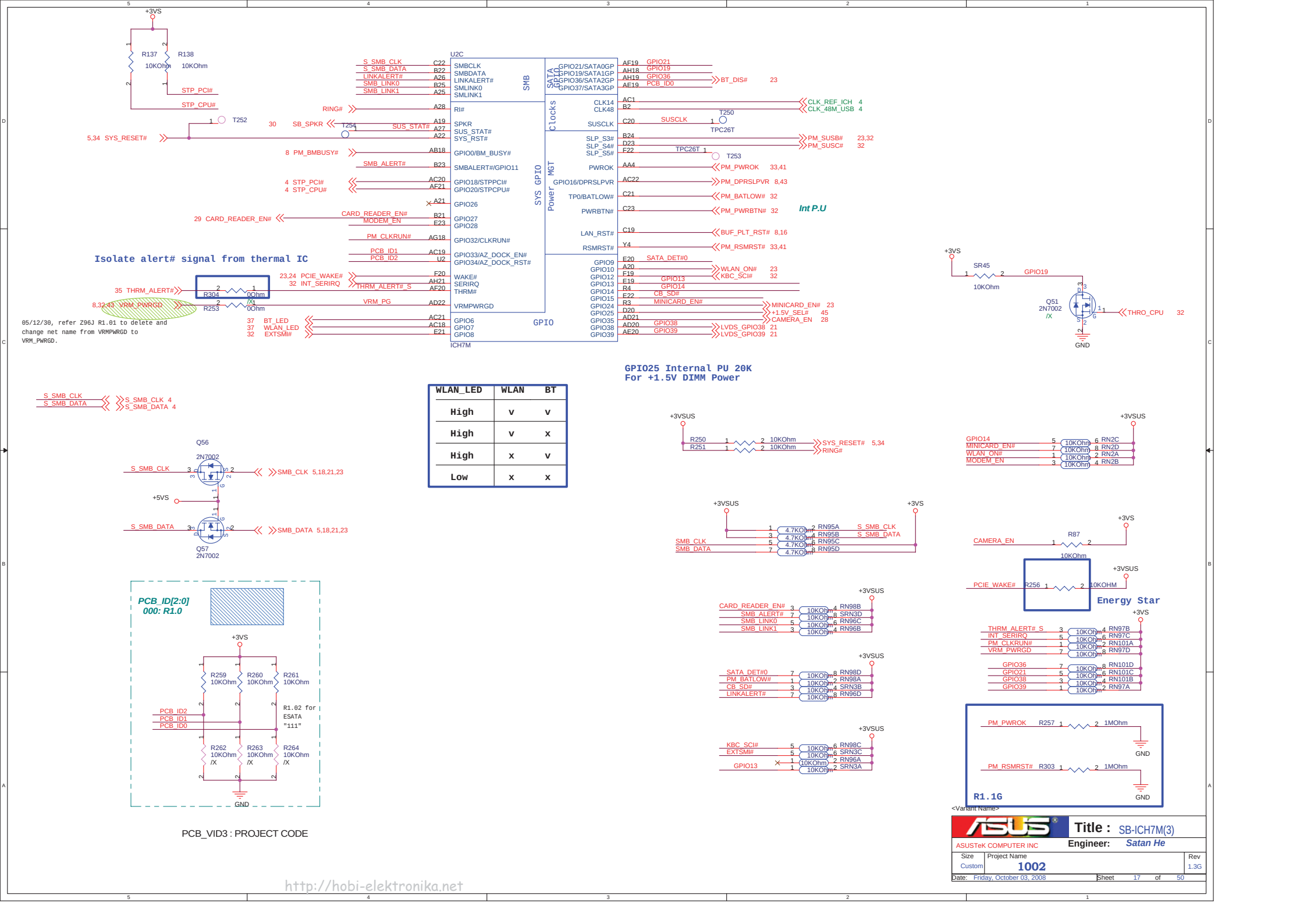
Size	Project Name	Rev
A3	1002	1.3G

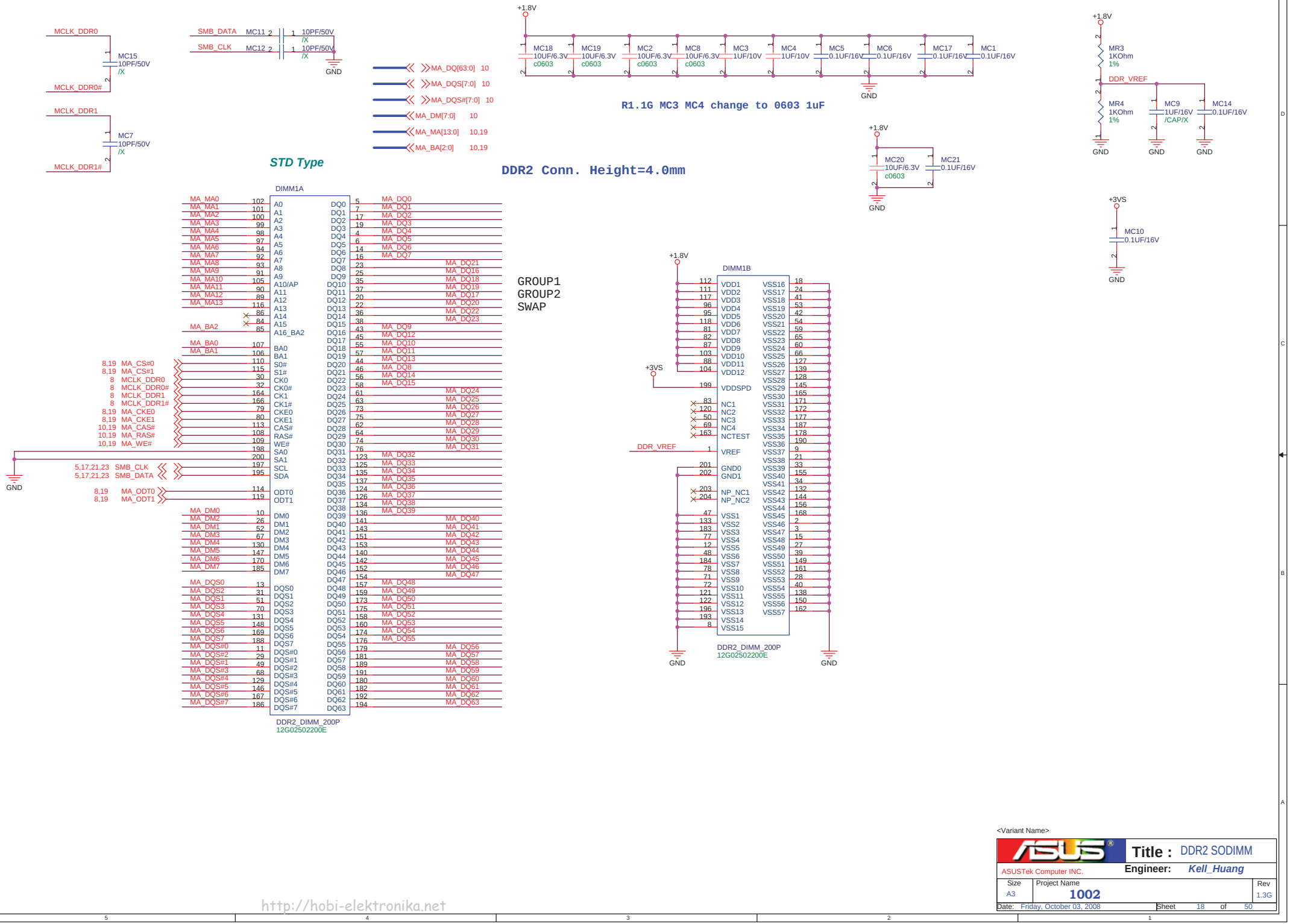
Date: Friday, October 03, 2008 Sheet 13 of 50


+1.5V_PLL / 100mA
R1.3G
R1.1G
D47 BAT54CW /PLL
D48 BAT54CW /X
D40 BAT54CW
D41 BAT54CW
6 mA
10 mA
0.77A
0.94A
1.1G change to +3VS to follow CRB
VccSus3_3 only for desktop
56mA
10mA
0.27A
45mA
50mA
50mA
10mA
Vcc1_5_A=0.64A
Distribute in PCI Section
Hi : Vout = 1.5V
Low : Vout = 1.431V
U2F
ICH7M
U2E
Title : SB-ICH7M(PWR)
ASUSTeK COMPUTER INC.
Engineer: Satan He
Project Name 1002
Rev 1.3G
Date: Friday, October 03, 2008
Sheet 14 of 50
<Variant Name>
http://hobi-elektronika.net


R1.1G change +3VA net to +3VA_AEC
Change to rechargeable RTC battery
PT71
+VCC_BAT_R
+3VA_AEC
BAT54WS D42
+RTCBAT
BT1
SIDE1
SIDE2
WTOB_CON_2P
12G17100002C
R229 1.2KOhm
GND
Height : 3.4 mm
+VCC_RTC
PT72
0Ohm R1
C307 10UF/10V
SR9
20KOhm
RTCRST# RC delay 18~25ms
SC8 1UF/16V
CLRTC
SGL_JUMP
Place Near Open Door
RTCRST#
34,41
+3VA_AEC
C431 10UF/6.3V
C432 0.1UF/16V
15PF/50V
C306
X2_RTC
R230 10MOhm
X1
SIDE
32.768Khz
MEC/MS3V-T1R 32.768KHZ 12.5/20
C308
X1_RTC
Over Discharge Protection Circuit
Q61 PMBS3906
D45 BAV99W_L
D46 BAV99W_L
R284 2.2KOhm
U2A
RTCX1
RTCX2
RTCRST#
INTRUDER#
INTVRMEN
R232 1MOhm
R233 330KOhm 1%
R3 0Ohm
LAN_CLK
LAN_RSTSYNC
LAN_RXD0
LAN_RXD1
LAN_RXD2
LAN_TXD0
LAN_TXD1
LAN_TXD2
ACZ_SDIN0 CODEC
ACZ_SDIN1 NA
ACZ_BCLK
ACZ_SYNC
ACZ_RST#
ACZ_SDOUT
AC40 10PF/50V
30,41 A_Z_BITCLK
30 A_Z_SYNC
30 A_Z_RST#
30 A_Z_SDOUT
RN89A 39Ohm ACZ_BCLK
RN89B 39Ohm ACZ_SYNC
RN90A 39Ohm ACZ_RST#
RN90B 39Ohm ACZ_SDOUT
30 A_Z_SDIN0
T281
T232
ACZ_SDIN2
37 S_SATALED#
SATALED#
26 P_SATA_RXN0
26 P_SATA_RXP0
26 P_SATA_TXN0
26 P_SATA_TXP0
SATA0RXN
SATA0RXP
SATA0TXN
SATA0TXP
SATA2RXN
SATA2RXP
SATA2TXN
SATA2TXP
4 CLK_SATA_ICH#
4 CLK_SATA_ICH
SATA_CLKN
SATA_CLKP
R243 24.9Ohm 1%
SATARBIAS#
SATARBIASN
SATARBIASP
R241 4.7KOhm
R242 10KOhm
+3VS
26 IDE_DIOR#
26 IDE_DIOW#
26 IDE_DDACK#
26 IDE_IRQ
26 IDE_IORDY
26 IDE_DDREQ
DIOR#
DIOW#
DDACK#
IDEIRQ
IORDY
DDREQ
ICH7M
RTC
LPC
CPU
LAN
AC-97/AZALIA
SATA
IDE
LAD0
LAD1
LAD2
LAD3
LDRQ0#
LDRQ1#/GPIO23
LFRAME#
A20GATE
A20M#
CPUSLP#
TP1/DPRSTP#
TP2/DPSLP#
FERR#
GPIO49/CPUPWRGD
IGNNE#
INIT3_3V#
INIT#
INTR
RCIN#
NMI
SMI#
STPCLK#
THERMTRIP#
LPC_AD0 32,34
LPC_AD1 32,34
LPC_AD2 32,34
LPC_AD3 32,34
LPC_DRQ#0
LPC_DRQ#1
T229
T230
LPC_FRAME# 32,34
A20GATE 32
H_A20M# 5
S_CPUSLP# 1 T280
PM_DPRSTP# 5,43
H_DPSLP# 5
H_FERR# 5
H_PWRGD 5
H_IGNNE# 5
T231
H_INIT# 5
H_INTR 5
RC_IN# 32
H_NMI 5
H_SMI# 5
H_STPCLK# 5
R237 24.9Ohm 1%
H_THERMTRIP# 5,8
IDE_DD[15:0] 26
IDE_DA[2:0] 26
IDE_DCS#1 26
IDE_DCS#3 26
NO STUFF R107.CPUSLP# ONLY USE IN DESTOP
+VCCP
CR235 56Ohm
R236 56Ohm
R272 10KOhm
R136 10KOhm
R104 330Ohm
A20GATE
RC_IN#
H_INIT#
<Variant Name>
Title : SB-ICH7-M(1)
ASUSTeK COMPUTER INC.
Engineer: Satan He
Size Custom
Project Name 1002
Rev 1.3G
Date: Friday, October 03, 2008
Sheet 15 of 50
http://hobi-elektronika.net

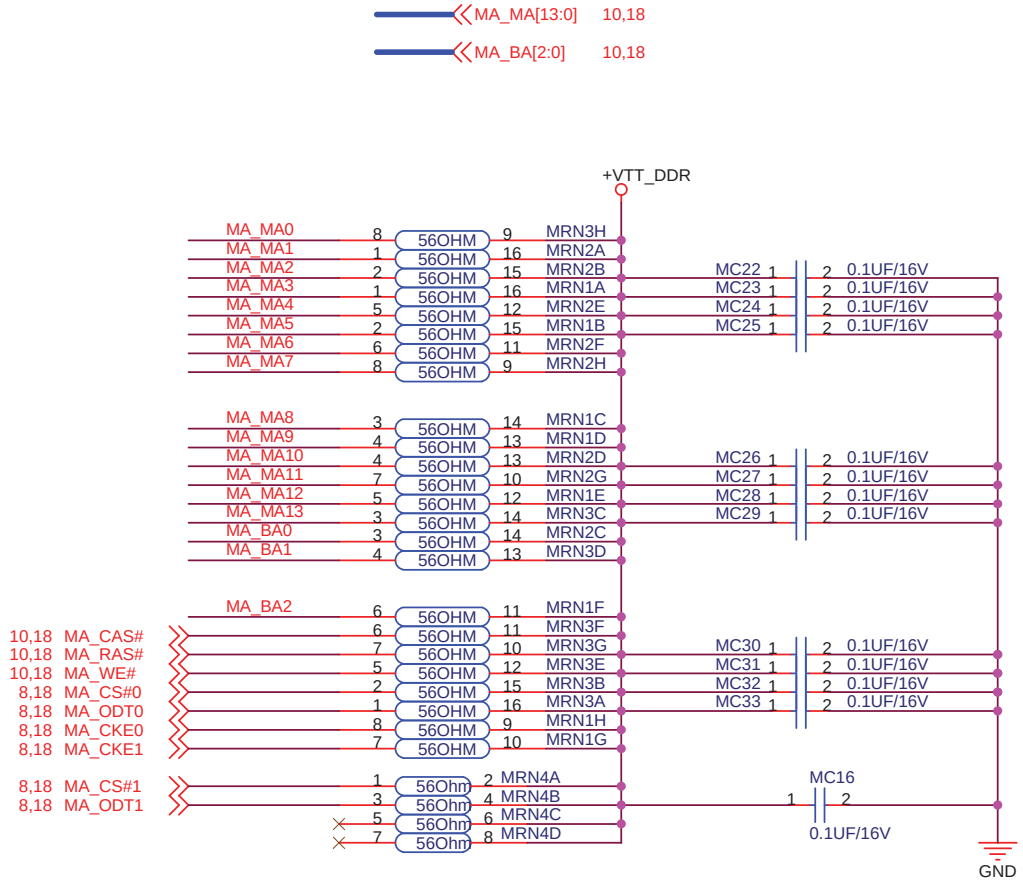

U2B
PCI
Interrupt I/F
MISC
ICH7M
PME# internal PU 20KOhm
ICH7 Boot BIOS Select
GNT#5 GNT#4
LPC H H
PCI H L
SPI L H
Buffer to Reduce Loading on PLT_RST#
NC7SZ08P5X
R247 R /X R1.00 change to no stuff
LAN AR8113 IC
WIFI PCIExpress Card
PCIE Interface SSD
U2D
PCI-Express
Direct Media Interface
USB
SPI
USB 0 NA
USB 1 USB Conn
USB 2 USB Conn
USB 3 USB Conn
USB 4 Card Reader
USB 5 WIFI
USB 6 Bluetooth
USB 7 Camera
CRB & Checklist
<Variant Name>
Title : SB-ICH7M(2)
ASUSTeK COMPUTER INC.
Engineer: Satan He
Size Custom
Project Name 1002
Rev 1.3G
Date: Friday, October 03, 2008
Sheet 16 of 50
http://hobi-elektronika.net


U2C
ICH7M
SMB
SYS GPIO
GPIO
Clocks
Power MGT
SATA GPIO
Isolate alert# signal from thermal IC
05/12/30, refer Z96J R1.01 to delete and change net name from VRMPWRGD to VRM_PWRGD.
GPIO25 Internal PU 20K
For +1.5V DIMM Power
Int P.U
Energy Star
R1.1G
PCB_ID[2:0]
000: R1.0
R1.02 for ESATA "111"
PCB_VID3 : PROJECT CODE
WLAN_LED | WLAN | BT
High | v | v
High | v | x
High | x | v
Low | x | x
Title : SB-ICH7M(3)
ASUSTeK COMPUTER INC
Engineer: Satan He
Project Name 1002
Rev 1.3G
Date: Friday, October 03, 2008
Sheet 17 of 50
http://hobi-elektronika.net



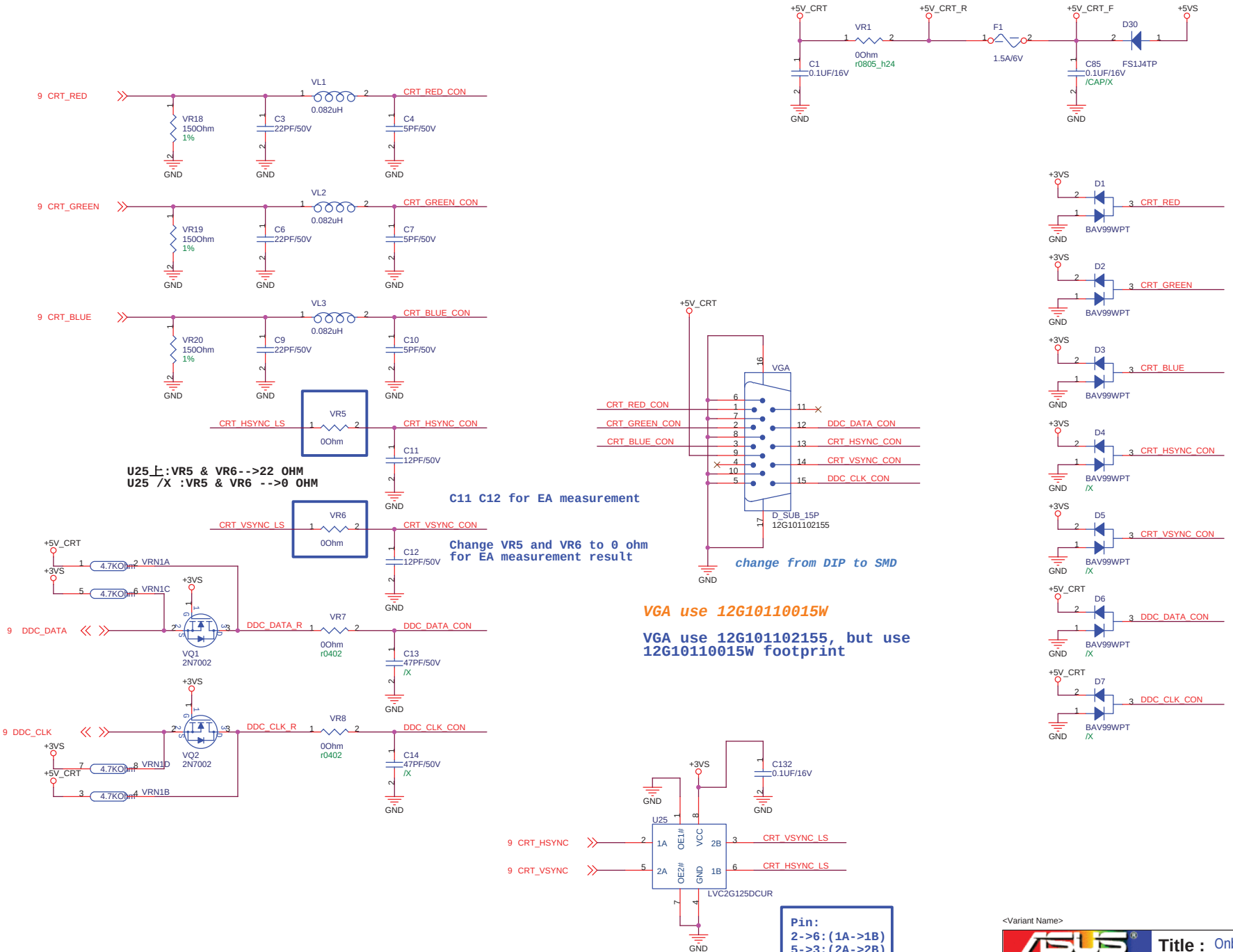
<Variant Name>

ASUSTek Computer INC.	Title : DDR2 SODIMM	
	Engineer: Kell_Huang	
Size: A3	Project Name: 1002	Rev: 1.3G
Date: Friday, October 03, 2008	Sheet 18 of 50	



<Variant Name>

ASUSTek Computer INC.	Title : DDR2_Termination	
	Engineer: Kell_Huang	
Size: A4	Project Name: 1002	Rev: 1.3G
Date: Friday, October 03, 2008	Sheet 19 of 50	



<Variant Name>

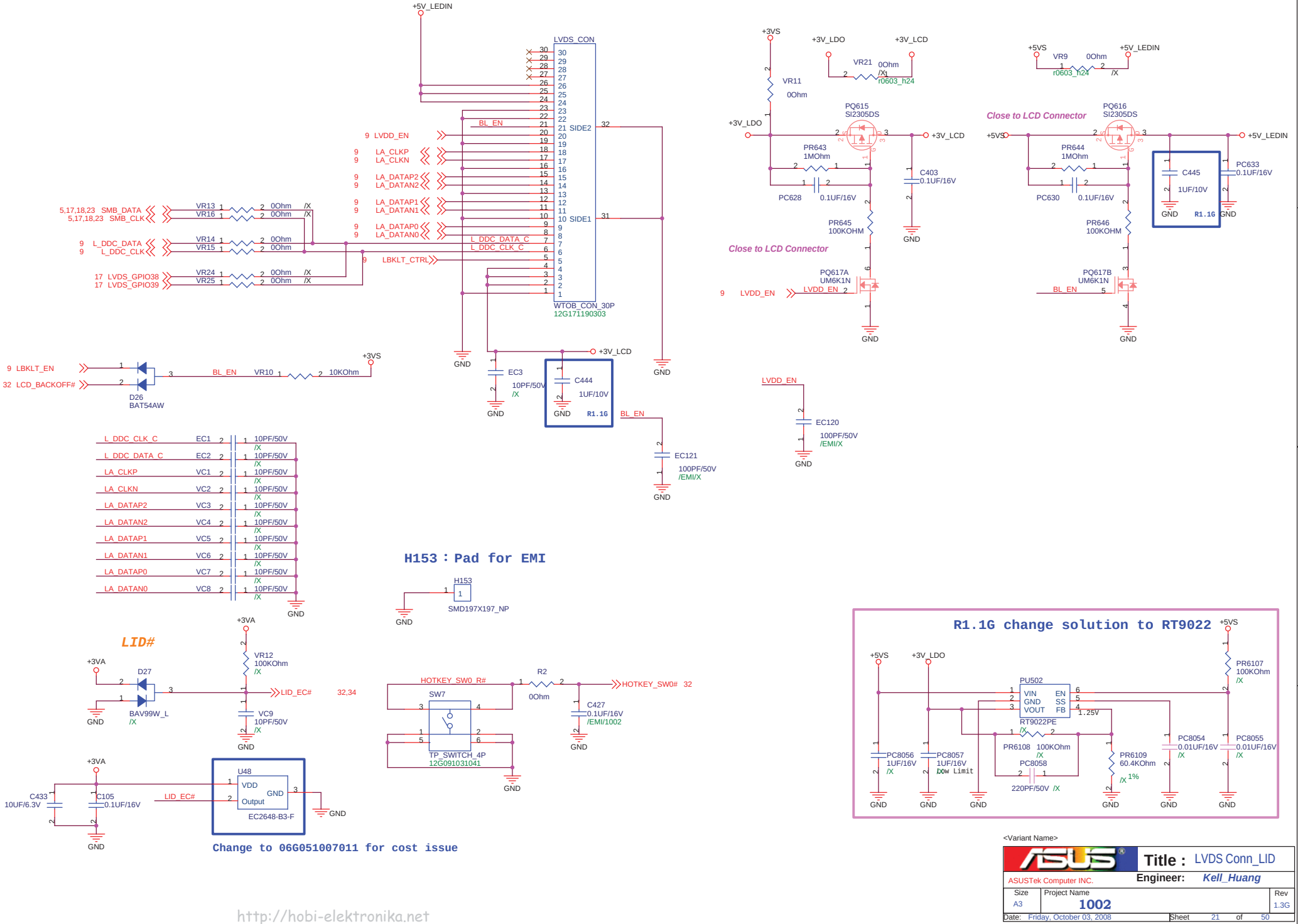
ASUSTek Computer INC.

Title : Onboard VGA

Engineer: Kell_Huang

Size	Project Name	Rev
A3	1002	1.3G

Date: Friday, October 03, 2008 Sheet 20 of 50

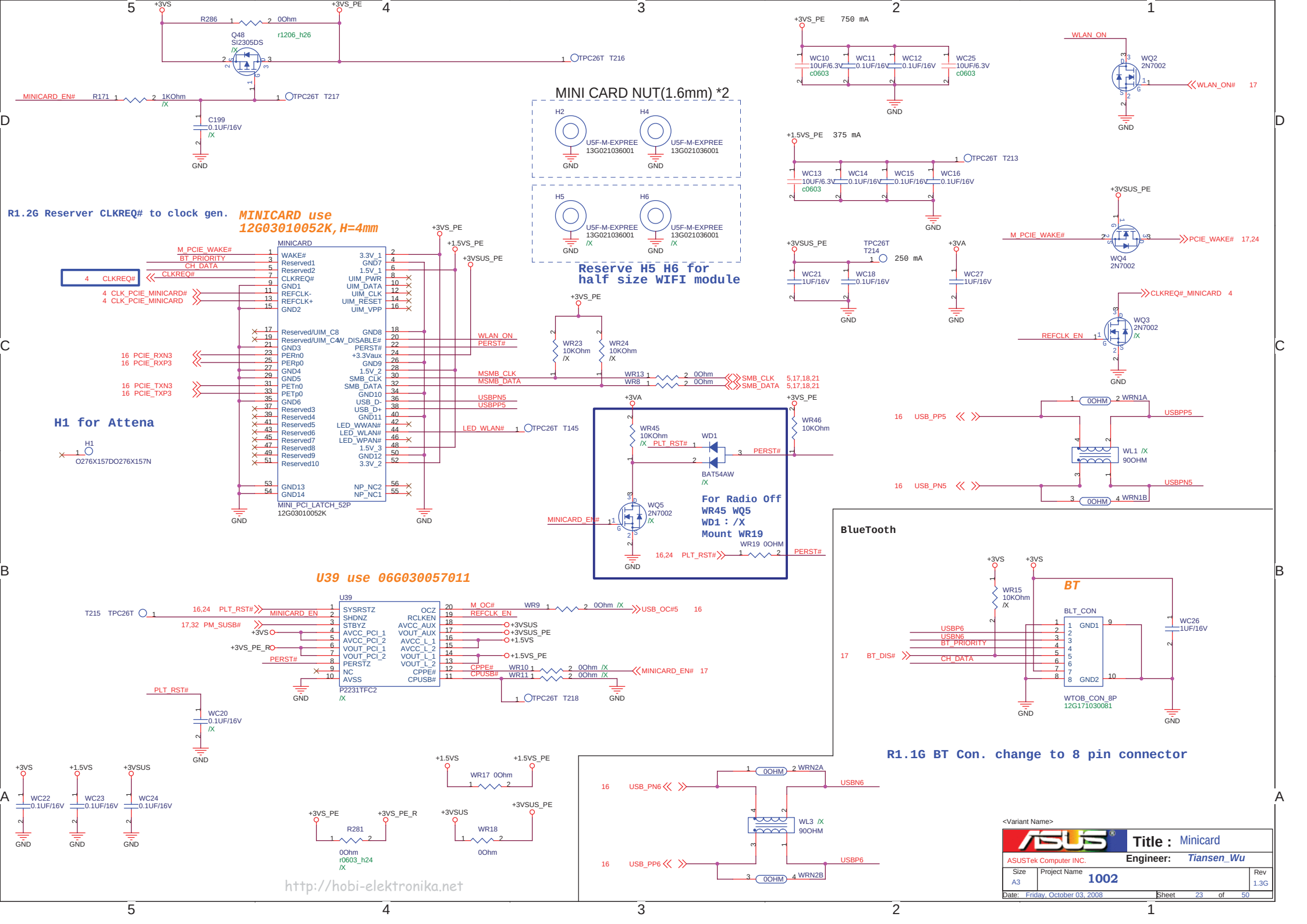


<Variant Name>

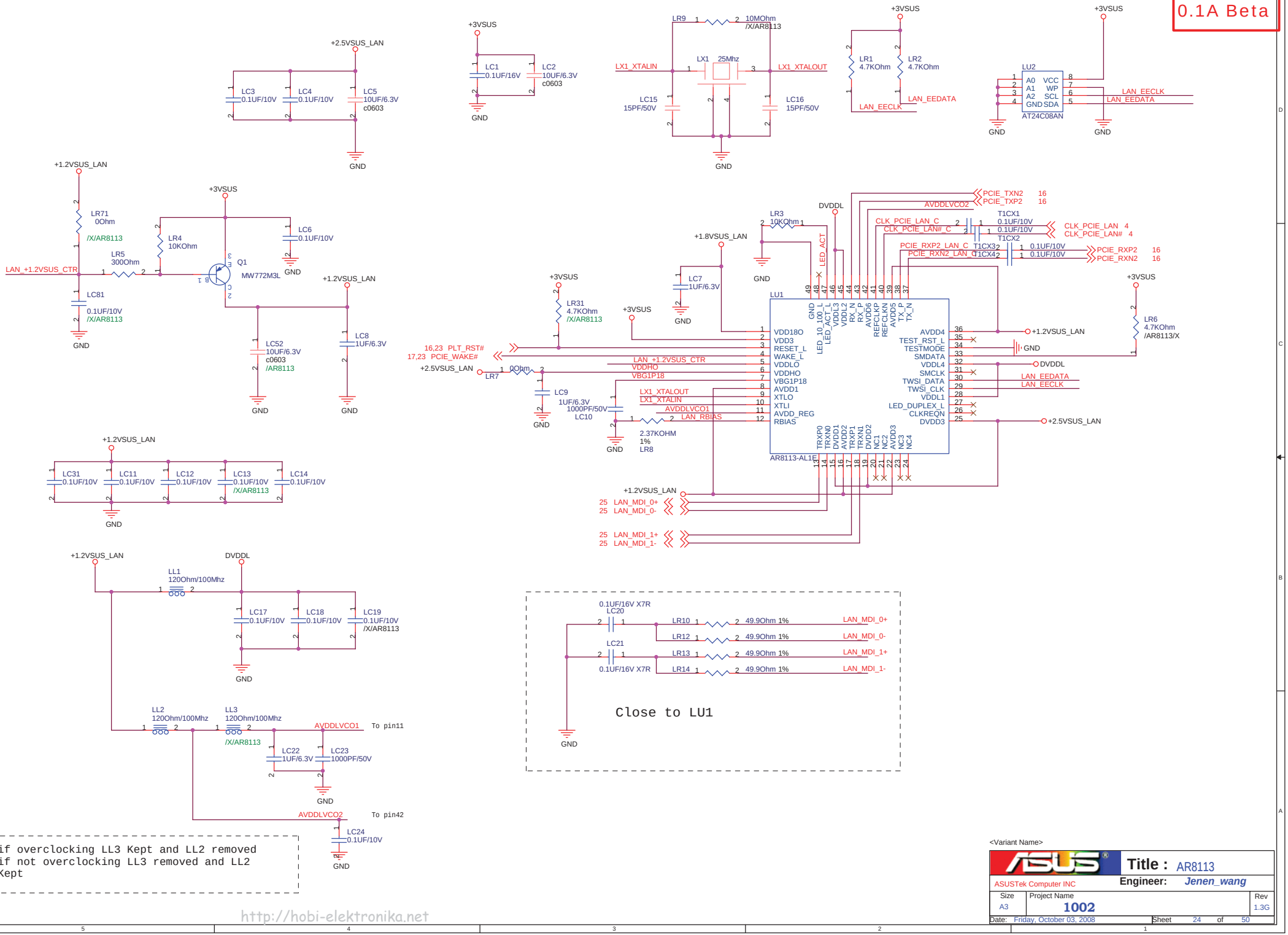
ASUSTek Computer INC.	Title : LVDS Conn_LID	
	Engineer: Kell_Huang	
Size: A3	Project Name: 1002	Rev: 1.3G
Date: Friday, October 03, 2008	Sheet 21 of 50	

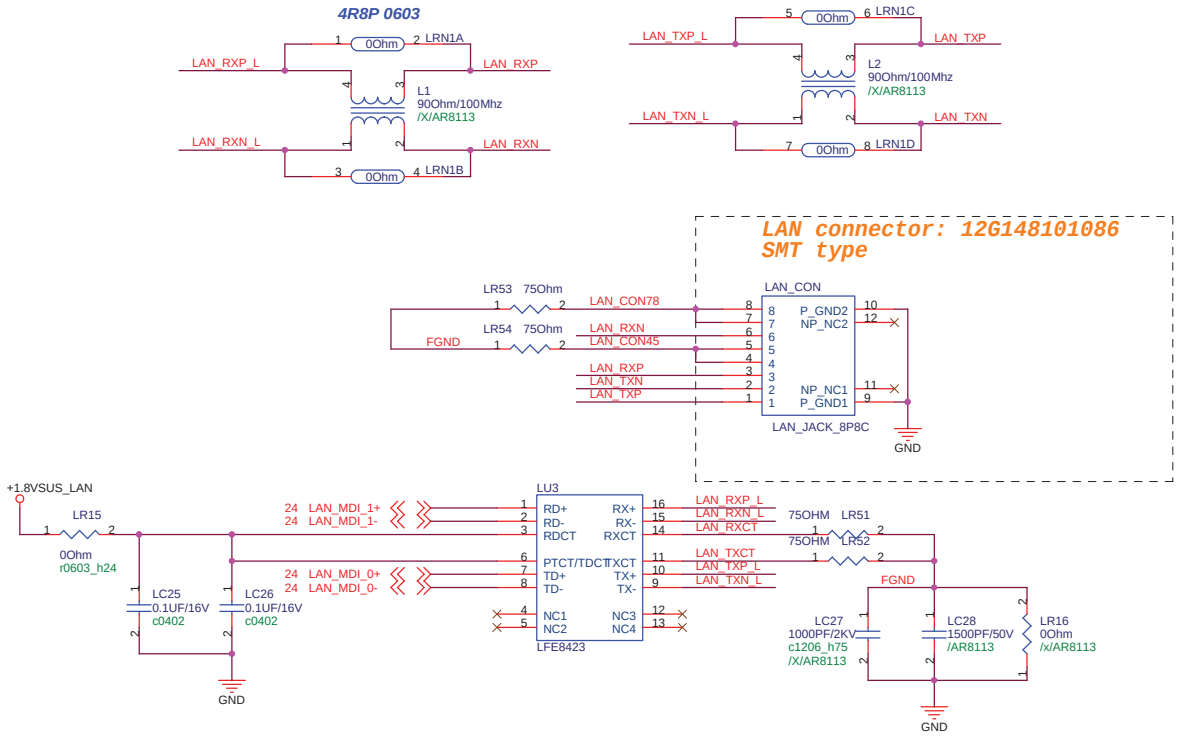
<Variant Name>

ASUSTek Computer INC.	Title : Blank	
	Engineer: *Kell_Huang*	
Size: A3	Project Name: 1002	Rev: 1.3G
Date: Friday, October 03, 2008	Sheet 22 of 50	


R1.2G Reserver CLKREQ# to clock gen.
MINICARD use 12G03010052K,H=4mm
MINI CARD NUT(1.6mm) *2
Reserve H5 H6 for half size WIFI module
H1 for Attena
For Radio Off
WR45 WQ5
WD1 : /X
Mount WR19
U39 use 06G030057011
BlueTooth
BT
R1.1G BT Con. change to 8 pin connector
MINICARD
MINI_PCI_LATCH_52P
12G03010052K
P2231TFC2
BLT_CON
WTOB_CON_8P
12G171030081
Title : Minicard
ASUSTek Computer INC.
Engineer: Tiansen_Wu
Project Name 1002
Size A3
Rev 1.3G
Date: Friday, October 03, 2008
Sheet 23 of 50
http://hobi-elektronika.net

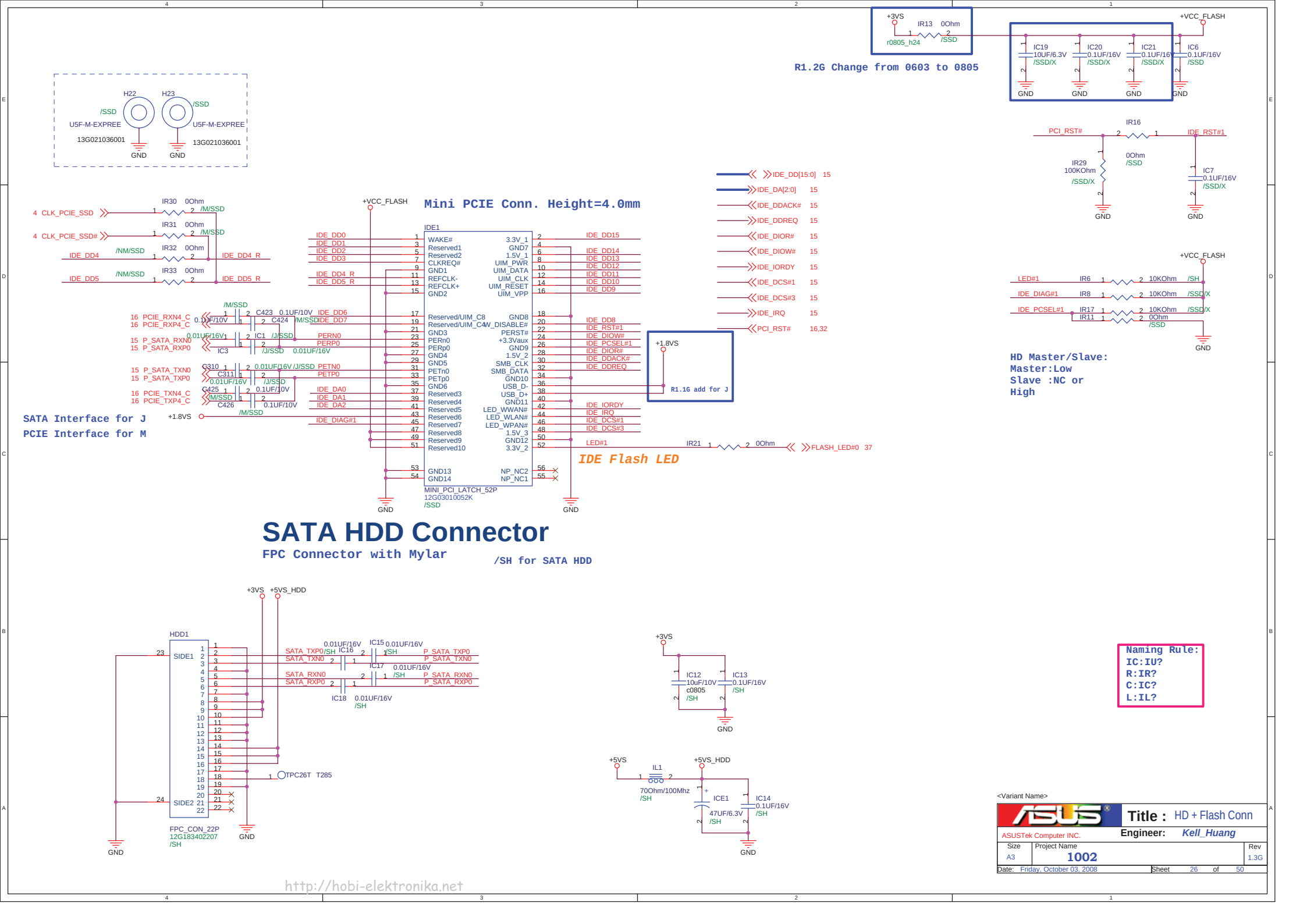
0.1A Beta

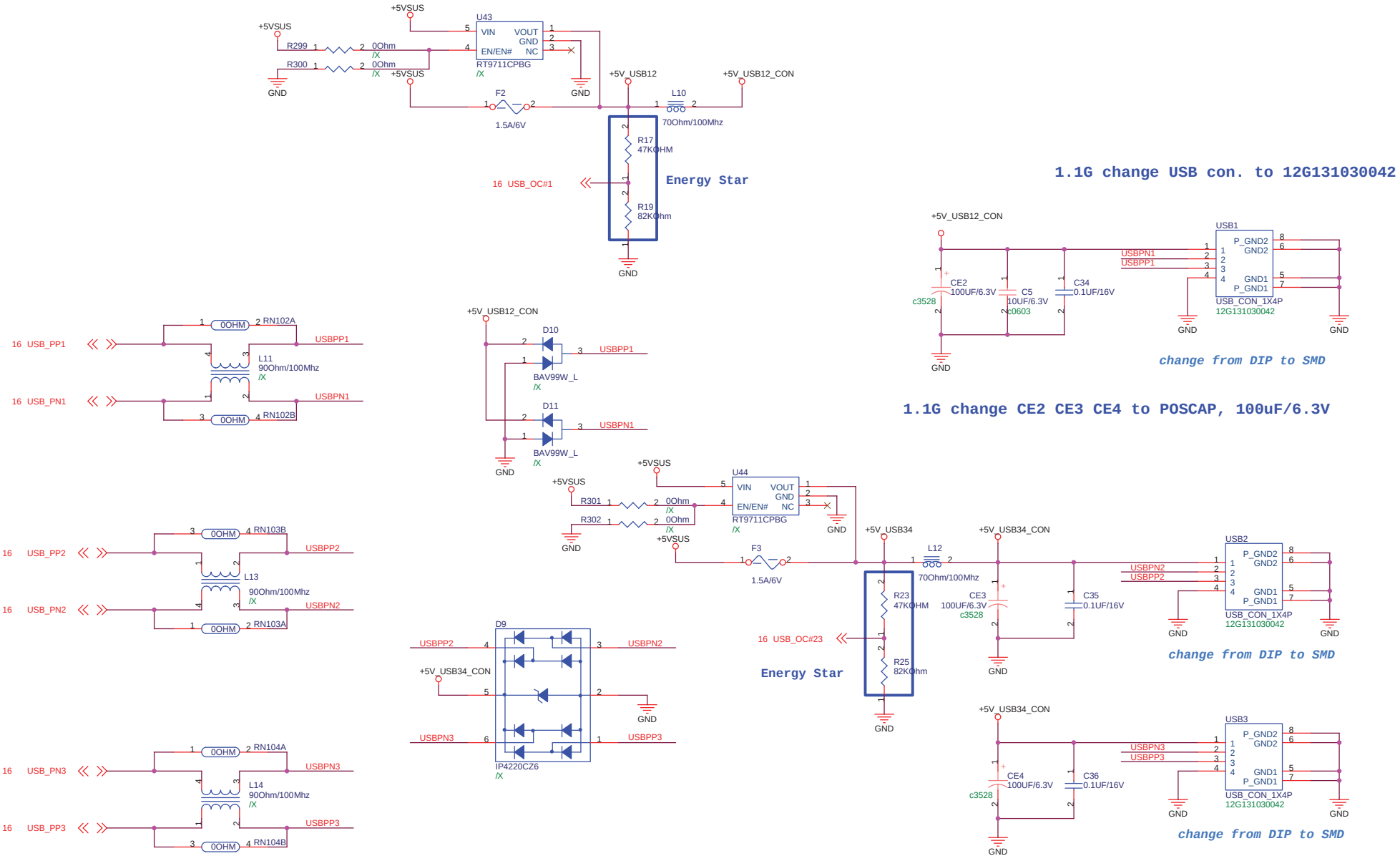

+2.5VSUS_LAN
LC3 0.1UF/10V
LC4 0.1UF/10V
LC5 10UF/6.3V c0603
GND
+3VSUS
LC1 0.1UF/16V
LC2 10UF/6.3V c0603
LR9 10MOhm /X/AR8113
LX1_XTALIN
LX1 25Mhz
LX1_XTALOUT
LC15 15PF/50V
LC16 15PF/50V
LR1 4.7KOhm
LR2 4.7KOhm
LAN_EECLK
LAN_EEDATA
LU2
A0 A1 A2 GND VCC WP SCL SDA
AT24C08AN
+1.2VSUS_LAN
LR71 0Ohm /X/AR8113
LR4 10KOhm
LR5 300Ohm
LAN_+1.2VSUS_CTR
Q1 MW772M3L
LC6 0.1UF/10V
LC81 0.1UF/10V /X/AR8113
LC52 10UF/6.3V c0603 /AR8113
LC8 1UF/6.3V
LR3 10KOhm
LC7 1UF/6.3V
LED_ACT
LR31 4.7KOhm /X/AR8113
16,23 PLT_RST#
17,23 PCIE_WAKE#
+2.5VSUS_LAN
LR7 0Ohm
LC9 1UF/6.3V
LC10 1000PF/50V
LX1_XTALOUT
LX1_XTALIN
AVDDLVCO1
LAN_RBIAS
2.37KOHM 1%
LR8
LU1
VDD18O VDD3 RESET_L WAKE_L VDDLO VDDHO VBG1P18 AVDD1 XTLO XTLI AVDD_REG RBIAS
GND LED_10_100_L LED_ACT_L VDDL3 VDDL2 RX_N RX_P AVDD6 RECLKN RECLKP AVDD5 TX_P TX_N
AVDD4 TEST_RST_L TESTMODE SMDATA VDDL4 SMCLK TWSI_DATA TWSI_CLK VDDL1 LED_DUPLEX_L CLKREQN DVDD3
TRXP0 TRXN0 DVDD1 AVDD2 TRXP1 TRXN1 DVDD2 NC1 NC2 AVDD3 NC3 NC4
AR8113-AL1E
DVDDL
AVDDLVCO2
PCIE_TXN2 16
PCIE_TXP2 16
T1CX1 0.1UF/10V
CLK_PCIE_LAN_C
CLK_PCIE_LAN# C
CLK_PCIE_LAN 4
CLK_PCIE_LAN# 4
T1CX2 0.1UF/10V
PCIE_RXP2_LAN_C
PCIE_RXN2_LAN_C
T1CX3 0.1UF/10V
T1CX4 0.1UF/10V
PCIE_RXP2 16
PCIE_RXN2 16
LR6 4.7KOhm /AR8113/X
LAN_EEDATA
LAN_EECLK
+1.2VSUS_LAN
LC31 0.1UF/10V
LC11 0.1UF/10V
LC12 0.1UF/10V
LC13 0.1UF/10V /X/AR8113
LC14 0.1UF/10V
LL1 120Ohm/100Mhz
LC17 0.1UF/10V
LC18 0.1UF/10V
LC19 0.1UF/10V /X/AR8113
LL2 120Ohm/100Mhz
LL3 120Ohm/100Mhz
AVDDLVCO1
To pin11
/X/AR8113
LC22 1UF/6.3V
LC23 1000PF/50V
AVDDLVCO2
To pin42
LC24 0.1UF/10V
25 LAN_MDI_0+
25 LAN_MDI_0-
25 LAN_MDI_1+
25 LAN_MDI_1-
0.1UF/16V X7R
LC20
LC21
LR10 49.9Ohm 1%
LR12 49.9Ohm 1%
LR13 49.9Ohm 1%
LR14 49.9Ohm 1%
Close to LU1
if overclocking LL3 Kept and LL2 removed
if not overclocking LL3 removed and LL2 Kept
http://hobi-elektronika.net
<Variant Name>
Title : AR8113
ASUSTek Computer INC
Engineer: Jenen_wang
Size A3
Project Name 1002
Rev 1.3G
Date: Friday, October 03, 2008
Sheet 24 of 50



<Variant Name>

ASUSTek Computer INC.	Title : RJ45	
	Engineer: Kell_Huang	
Size: A3	Project Name: 1002	Rev: 1.3G
Date: Friday, October 03, 2008	Sheet 25 of 50	

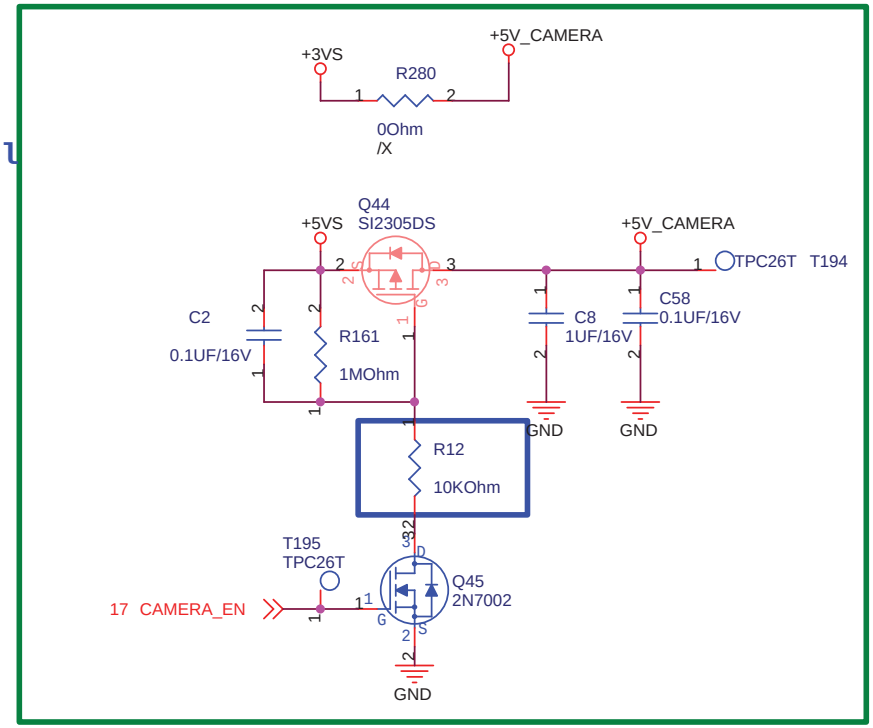

R1.2G Change from 0603 to 0805
Mini PCIE Conn. Height=4.0mm
SATA Interface for J
PCIE Interface for M
IDE Flash LED
HD Master/Slave:
Master:Low
Slave :NC or
High
SATA HDD Connector
FPC Connector with Mylar
/SH for SATA HDD
Naming Rule:
IC:IU?
R:IR?
C:IC?
L:IL?
R1.1G add for J
MINI_PCI_LATCH_52P
12G03010052K
/SSD
FPC_CON_22P
12G183402207
/SH
Title : HD + Flash Conn
ASUSTek Computer INC.
Engineer: Kell_Huang
Size A3
Project Name 1002
Rev 1.3G
Date: Friday, October 03, 2008
Sheet 26 of 50
http://hobi-elektronika.net

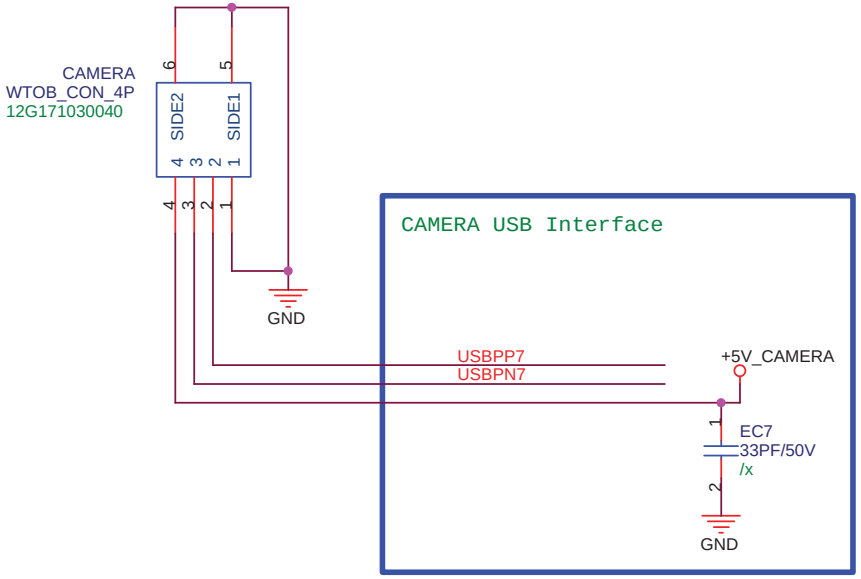


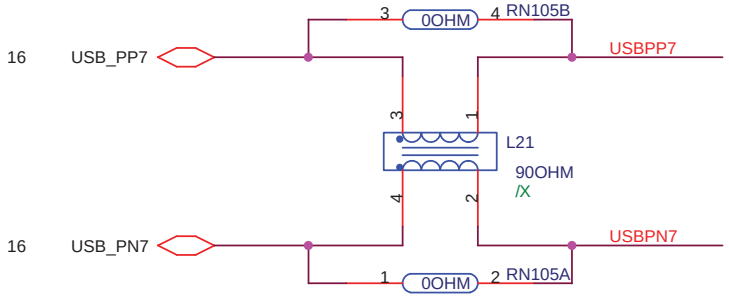
<Variant Name>

ASUSTek Computer INC.	Title : USB Port	
	Engineer: Kell_Huang	
Size: A3	Project Name: 1002	Rev: 1.3G
Date: Friday, October 03, 2008	Sheet 27 of 50	

Power Control







<Variant Name>

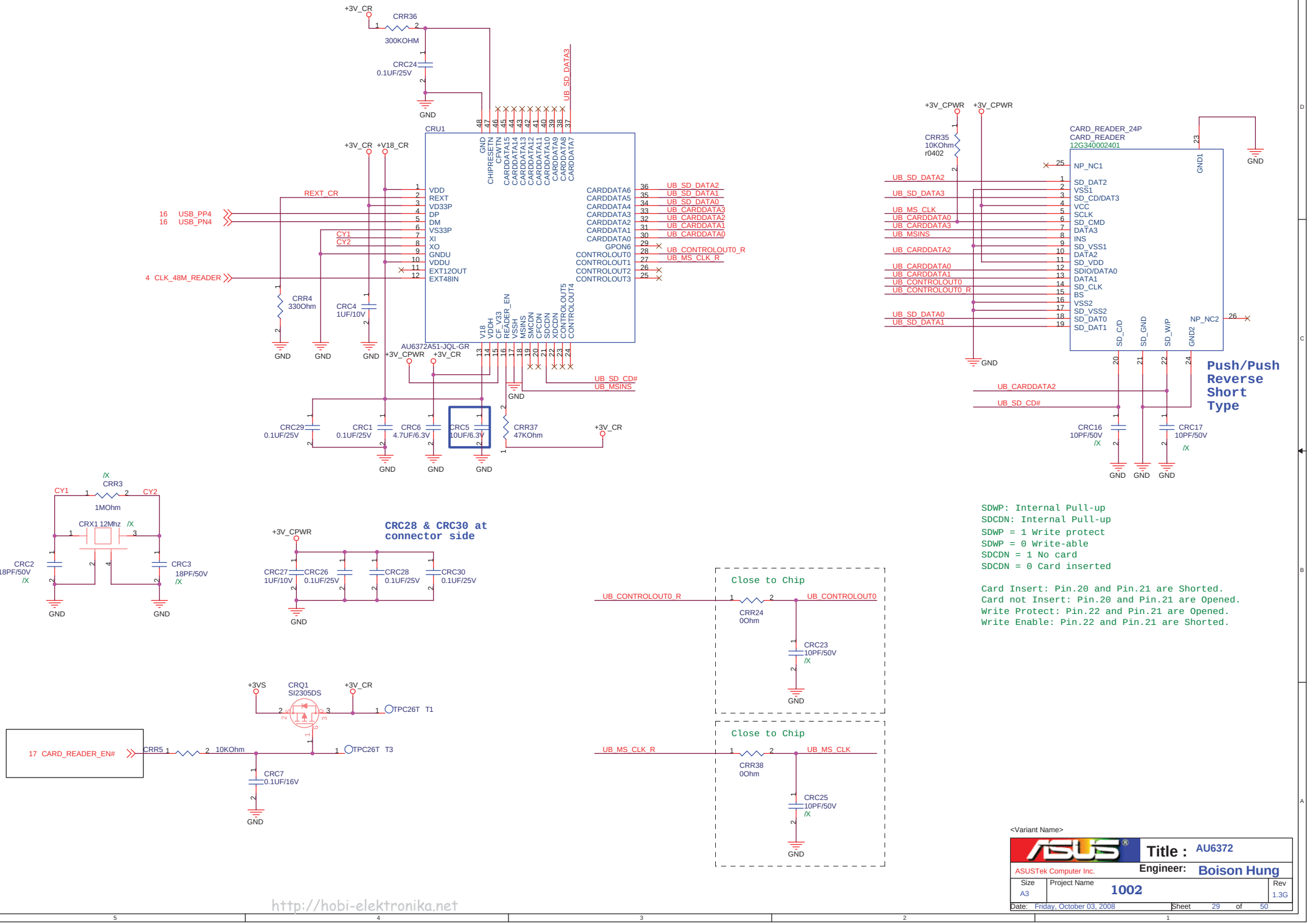
ASUSTek Computer INC.

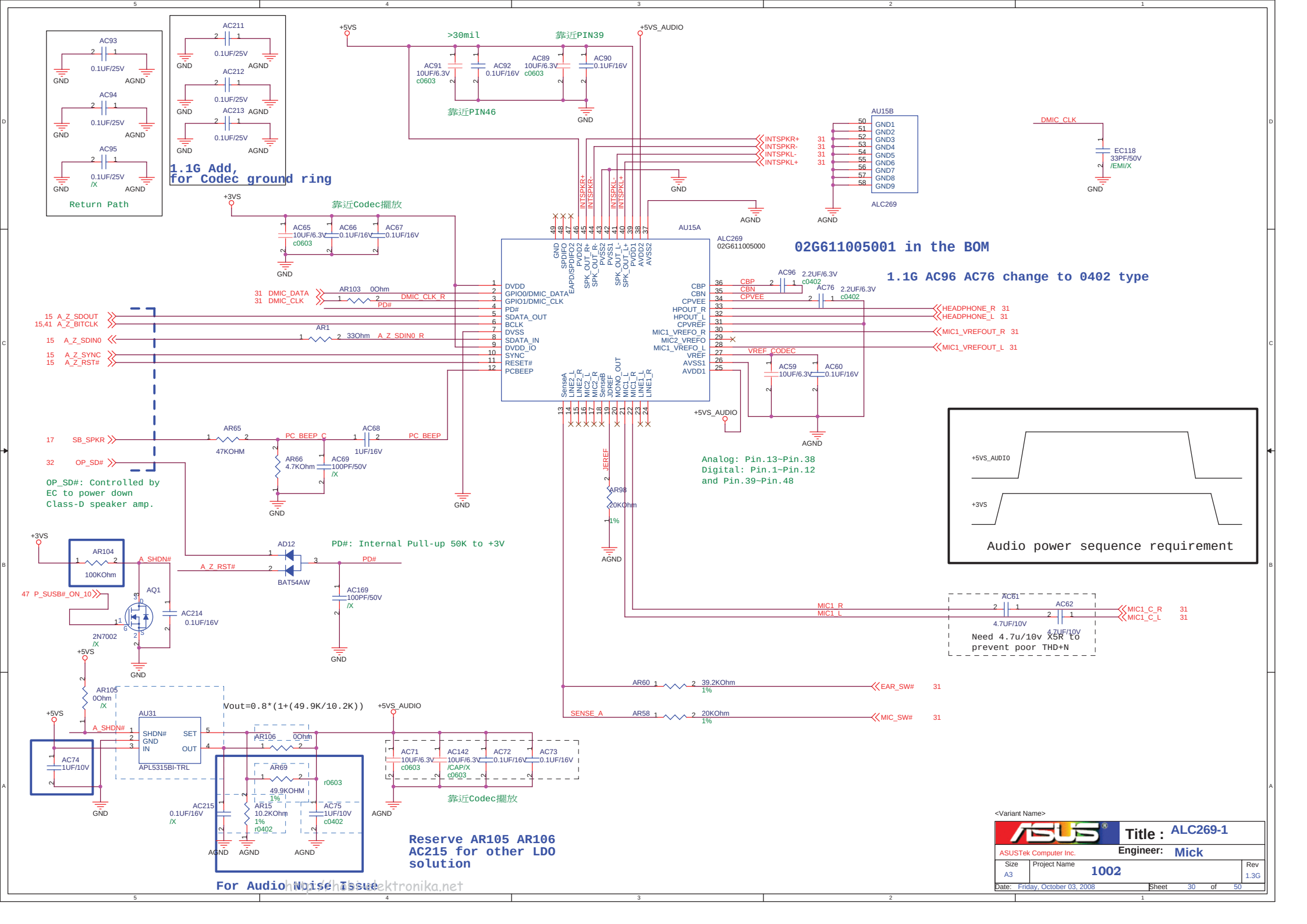
Title : Camera Power

Engineer: Kell_Huang

Size	Project Name	Rev
A4	1002	1.3G

Date: Friday, October 03, 2008 Sheet 28 of 50





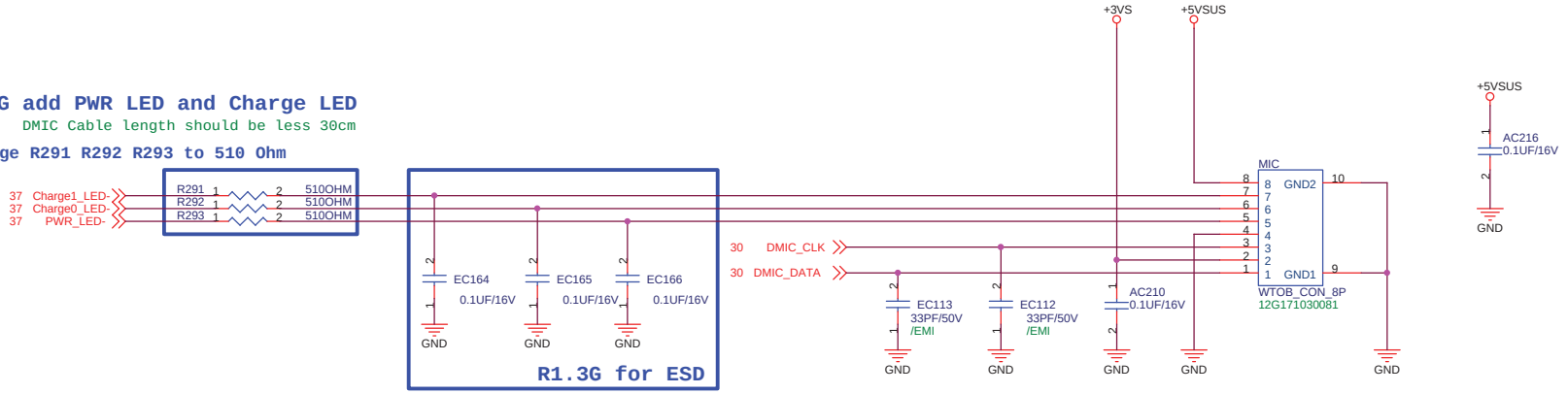
<Variant Name>

ASUSTek Computer Inc.	Title : ALC269-1	
	Engineer: Mick	
Size: A3	Project Name: 1002	Rev: 1.3G
Date: Friday, October 03, 2008	Sheet 30 of 50	

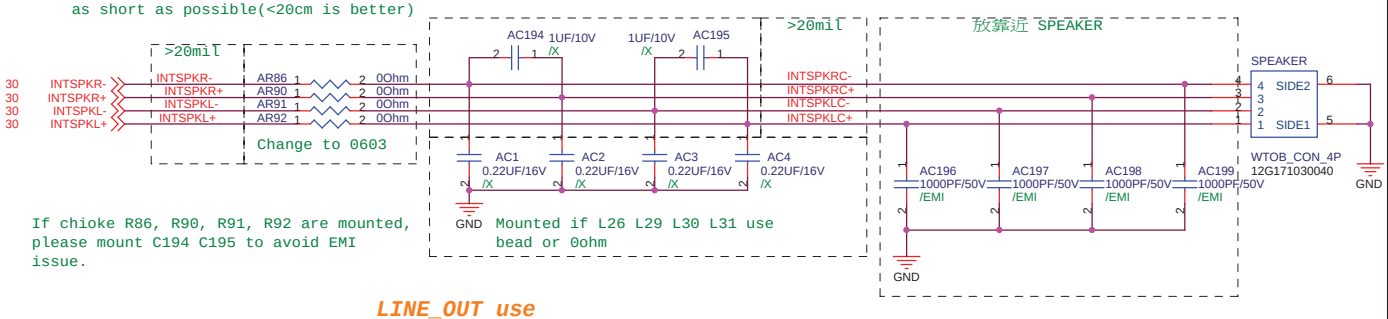
1.1G add PWR LED and Charge LED

DMIC Cable length should be less 30cm

Change R291 R292 R293 to 510 Ohm

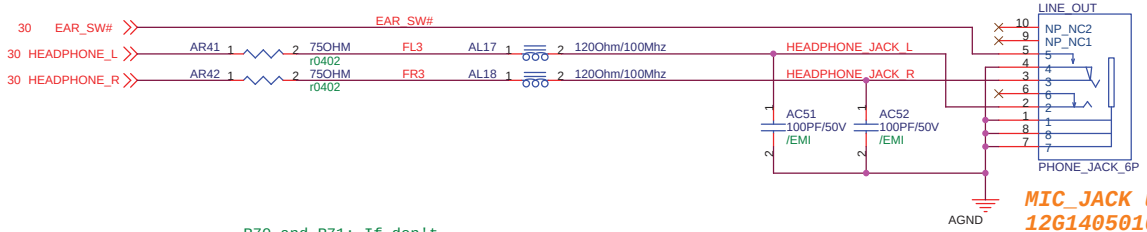


Total length from speakerR+- L+-(pin40 41 44 45) to internal speaker please as short as possible(<20cm is better)



If chioke R86, R90, R91, R92 are mounted, please mount C194 C195 to avoid EMI issue.

LINE_OUT use 12G14050106P(SINGATRON) Black



1.1G Change audio con. to black

change from DIP to SMD

MIC_JACK use 12G14050106P(SINGATRON) Black

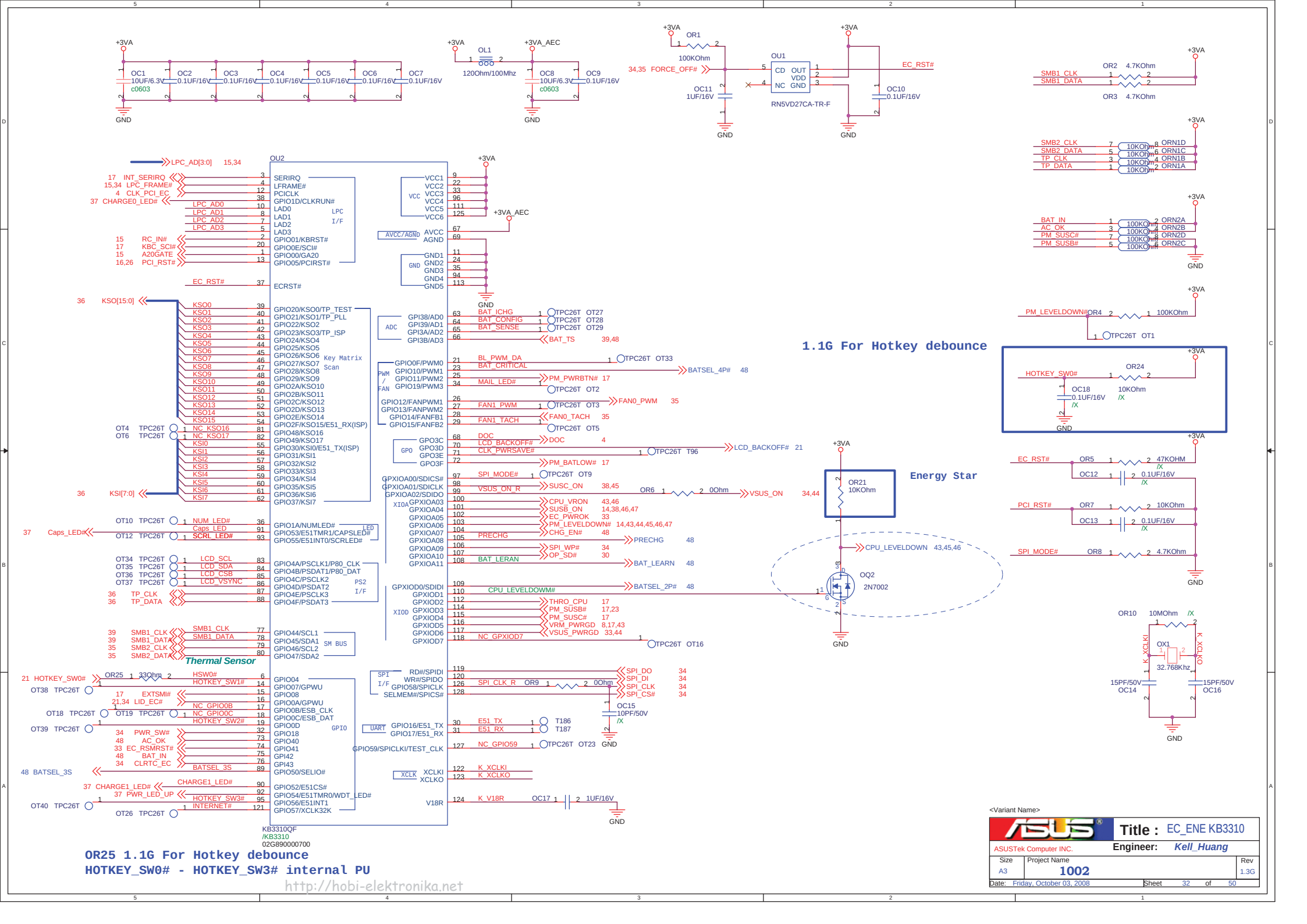
R70 and R71: If don't need retasking function, change to 1K.

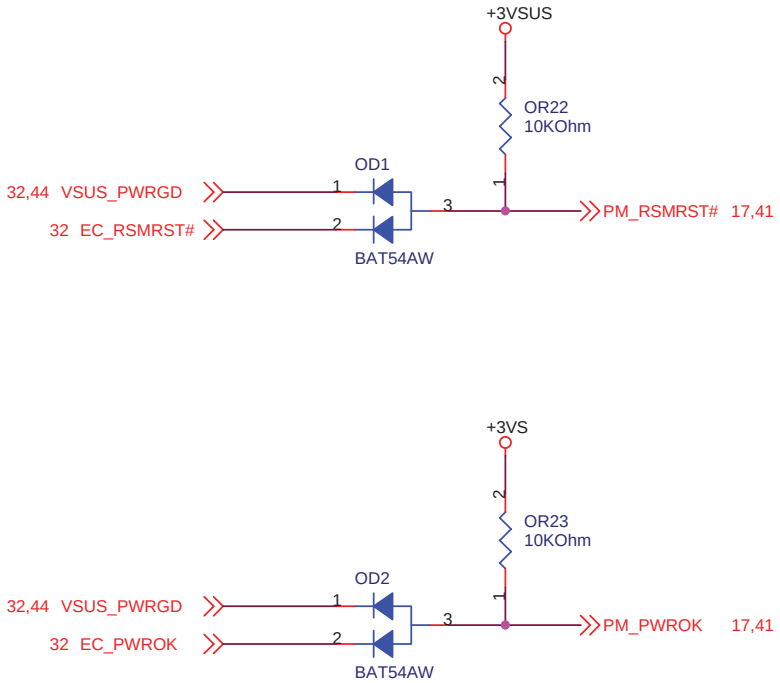
1.1G Change audio con. to black

change from DIP to SMD

<Variant Name>

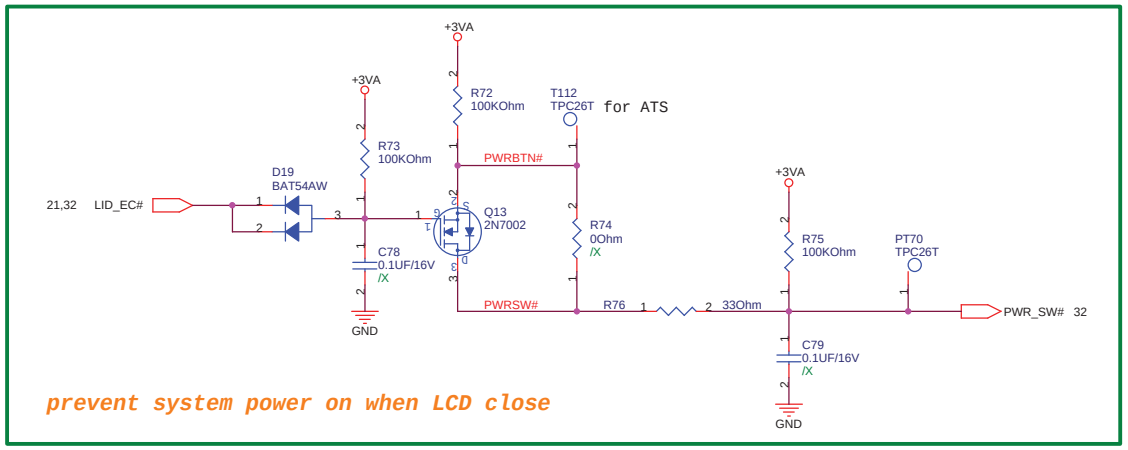
ASUSTek Computer Inc.	Title : ALC269-2	
	Engineer: MICK	
Size: A3	Project Name: 1002	Rev: 1.3G
Date: Friday, October 03, 2008	Sheet 31 of 50	

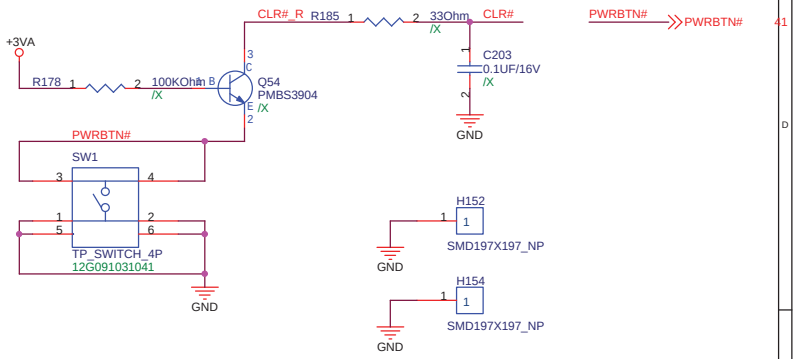

OU2
KB3310QF
/KB3310
02G890000700
1.1G For Hotkey debounce
Energy Star
Thermal Sensor
OR25 1.1G For Hotkey debounce
HOTKEY_SW0# - HOTKEY_SW3# internal PU
http://hobi-elektronika.net
Title : EC_ENE KB3310
ASUSTek Computer INC.
Engineer: Kell_Huang
Size A3
Project Name 1002
Rev 1.3G
Date: Friday, October 03, 2008
Sheet 32 of 50

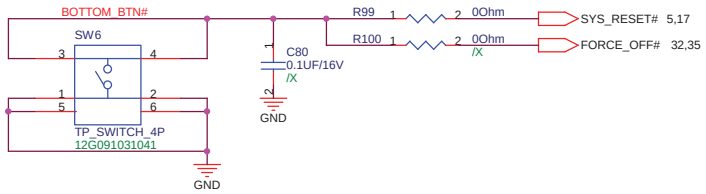


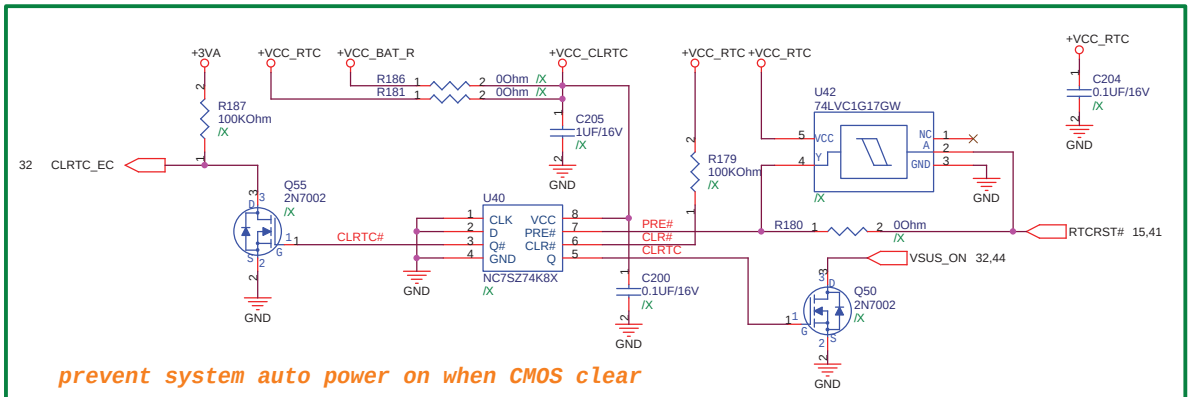
<Variant Name>

ASUSTek Computer INC.	Title : EC_UART_KC3820	
	Engineer: Kell_Huang	
Size: A4	Project Name: 1002	Rev: 1.3G
Date: Friday, October 03, 2008	Sheet 33 of 50	

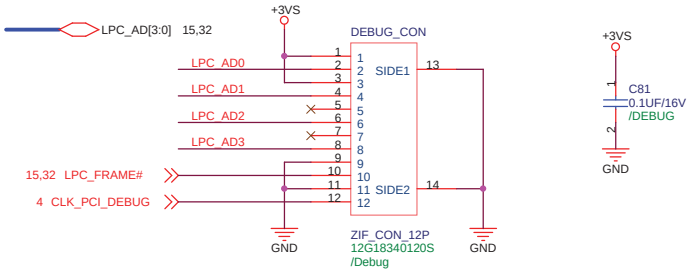




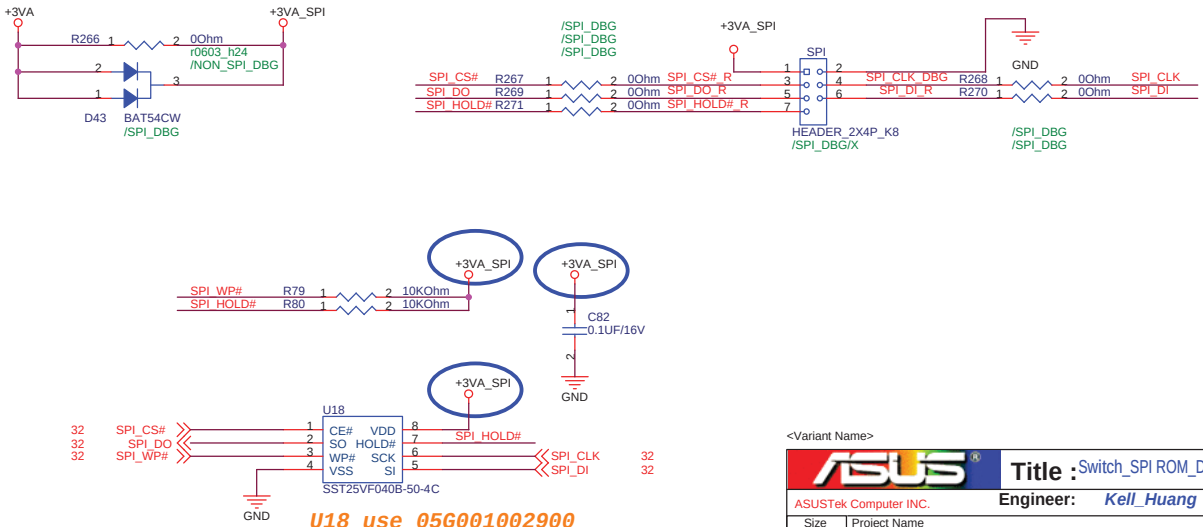




For Debug

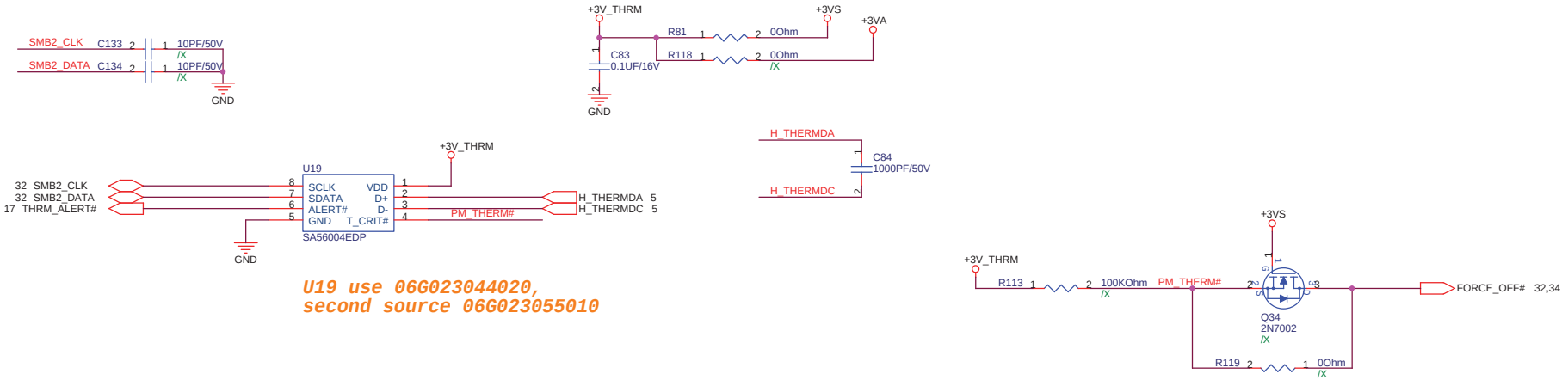


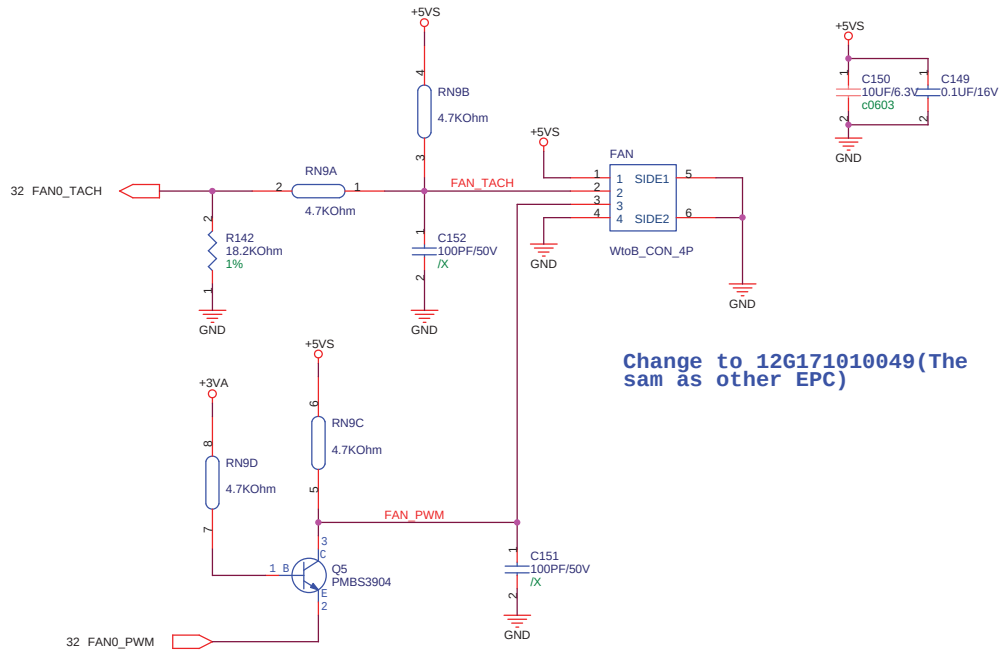
Debug Card cable use Z96 Touch Pad cable, P/N: 14G124110126, 14G124110120, 14G124110121 14G124110124, 14G124110125



<Variant Name>

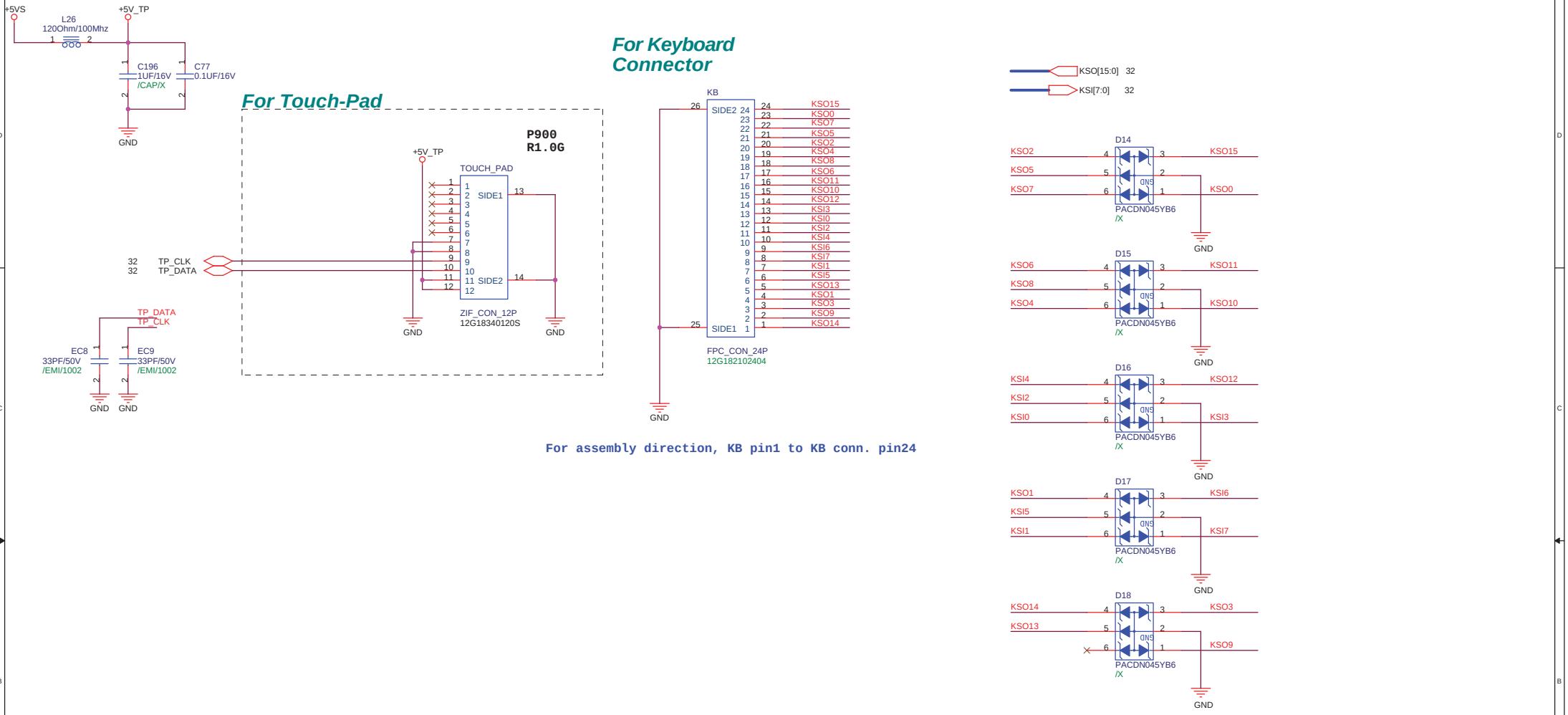
ASUSTek Computer INC.	Title : Switch_SPI ROM_Debug	
	Engineer: Kell_Huang	
Size: A3	Project Name: 1002	Rev: 1.3G
Date: Friday, October 03, 2008	Sheet 34 of 50	





<Variant Name>

ASUSTek Computer INC.	Title : Thermal Sensor_FAN	
	Engineer: Kell_Huang	
Size: A3	Project Name: 1002	Rev: 1.3G
Date: Friday, October 03, 2008	Sheet 35 of 50	

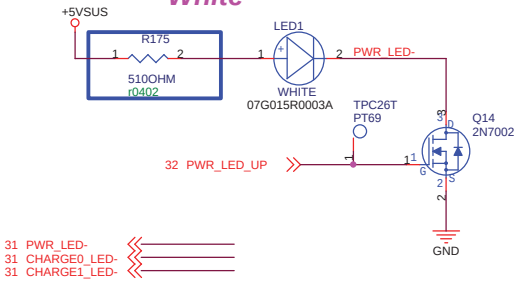


For assembly direction, KB pin1 to KB conn. pin24

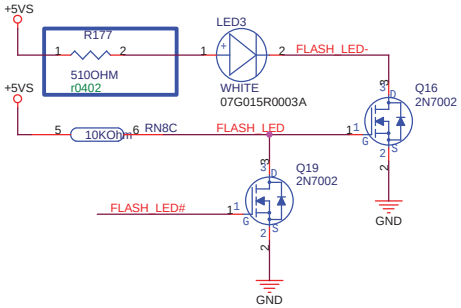
<Variant Name>

ASUSTek Computer INC.	Title : KB_Touch Pad	
	Engineer: Kell_Huang	
Size: A3	Project Name: 1002	Rev: 1.3G
Date: Friday, October 03, 2008	Sheet 36 of 50	

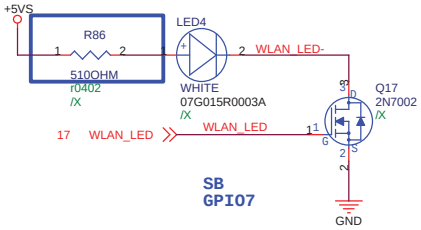
for POWER LED White



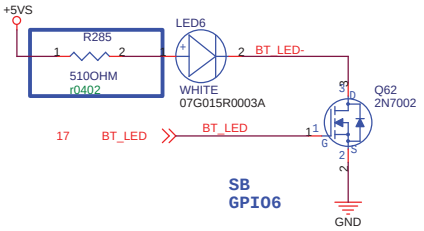
for FLASH LED White



White /X

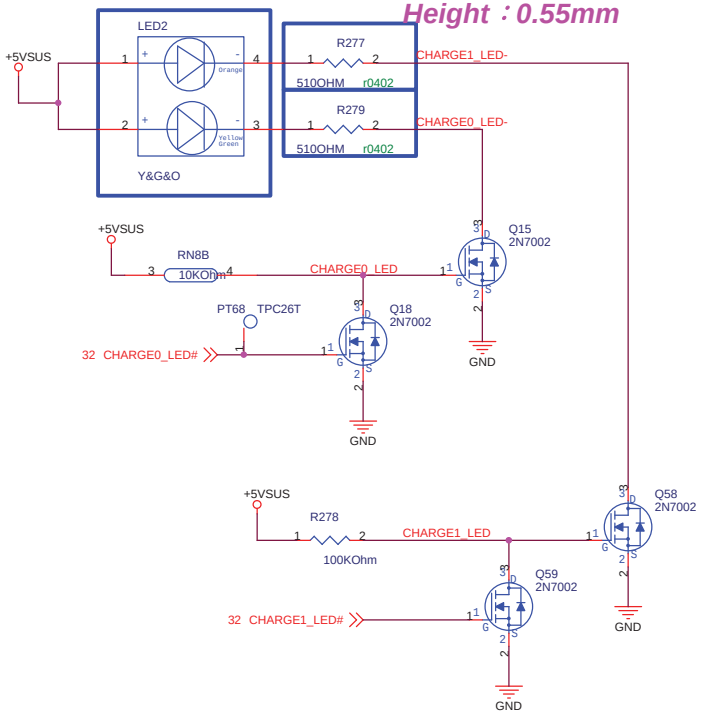


for WIFI/BlueTooth LED White

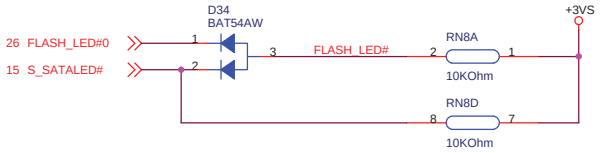


1.1G change to EVERLIGHT

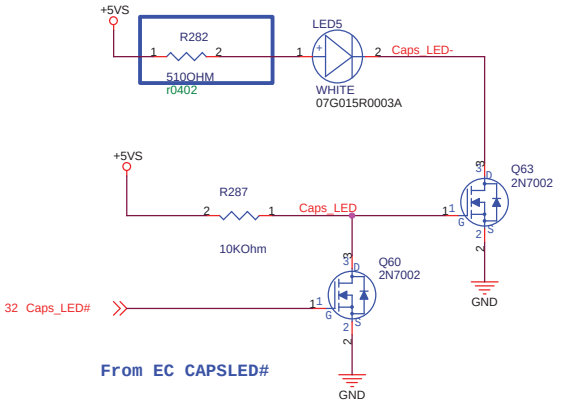
for CHARGE LED Height：0.55mm



Change LED resistor to 510 Ohm, about 4mA



for Caps Lock LED White



From EC CAPSLED#

The battery charge indicator (LED) shows the status of the battery' s power as follows:

cenario	Adapter mode	Battery mode
Battery power is between 100%~80%	Orange ON	Green ON
Battery power is between 80%~10%	Orange Blinking Slowly	Green Blinking Slowly
Battery power is less than 10%	Orange Blinking Quickly	Green Blinking Quickly
S3/S5 Mode	Scenario the same as above	Off

Note: The BATTERY LED should be off when the machine has no battery attached.

<Variant Name>

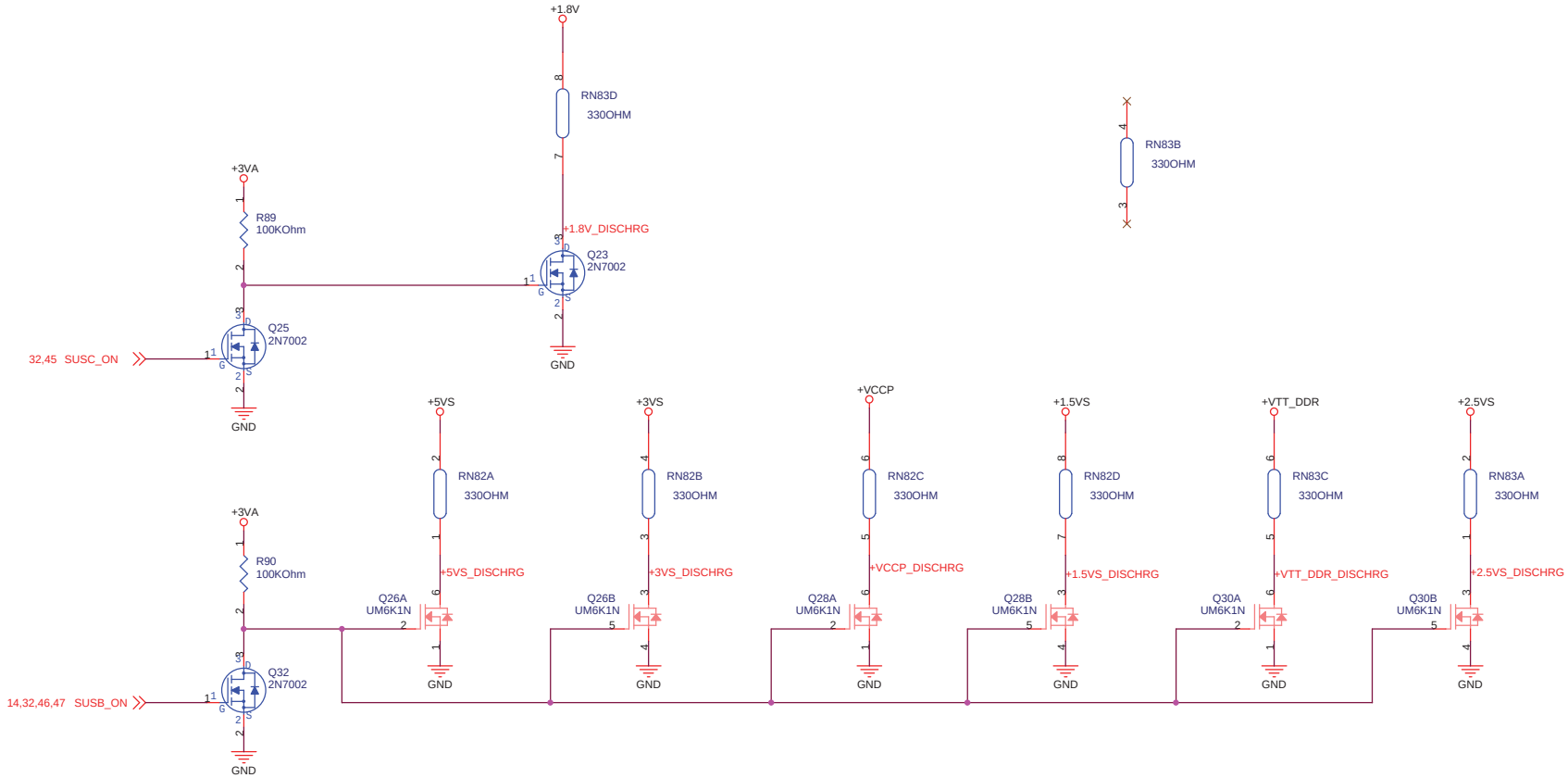
ASUSTek Computer INC.

Title : LED

Engineer: Kell_Huang

Size	Project Name	Rev
A3	1002	1.3G

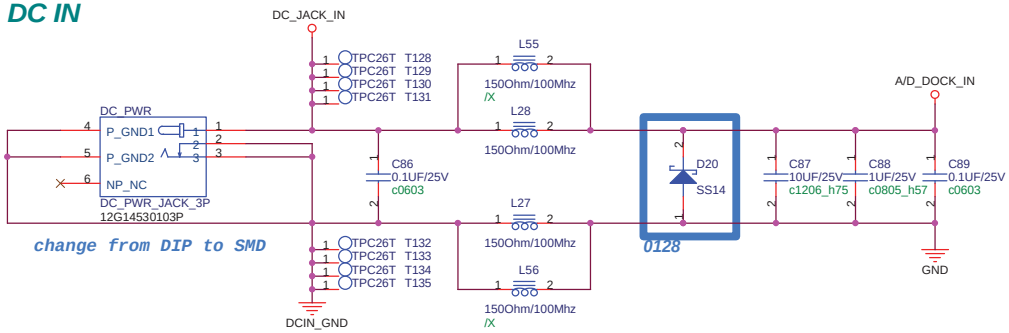
Date: Friday, October 03, 2008 Sheet 37 of 50



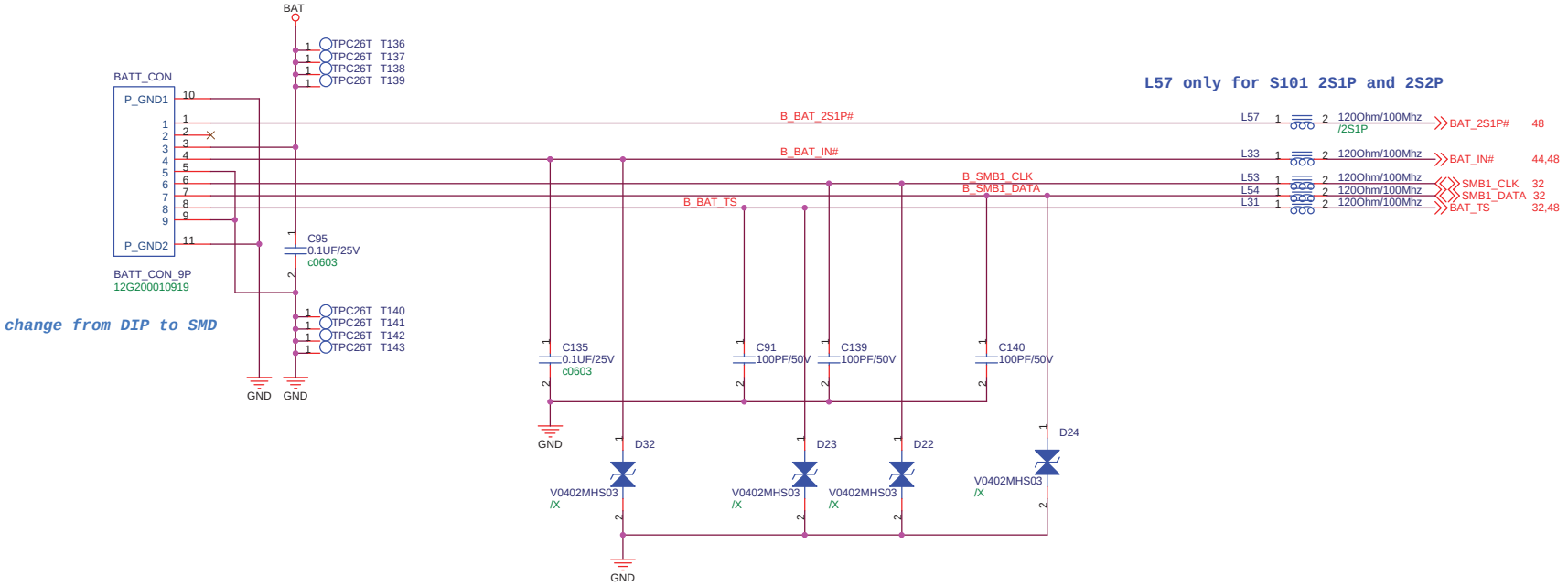
<Variant Name>

ASUSTek Computer INC.	Title : Discharge	
	Engineer: Kell_Huang	
Size: A3	Project Name: 1002	Rev: 1.3G
Date: Friday, October 03, 2008	Sheet 38 of 50	

DC IN

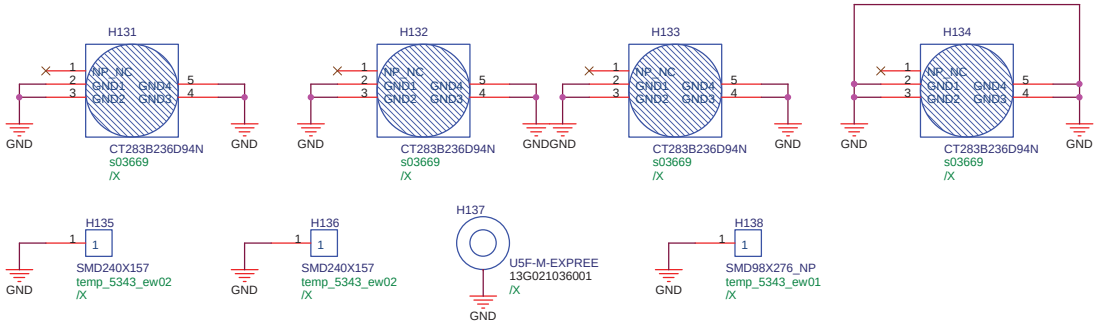


BAT IN



<Variant Name>

ASUSTek Computer INC.	Title : PWR Jack	
	Engineer: Kell_Huang	
Size: A3	Project Name: 1002	Rev: 1.3G
Date: Friday, October 03, 2008	Sheet 39 of 50	



<Variant Name>

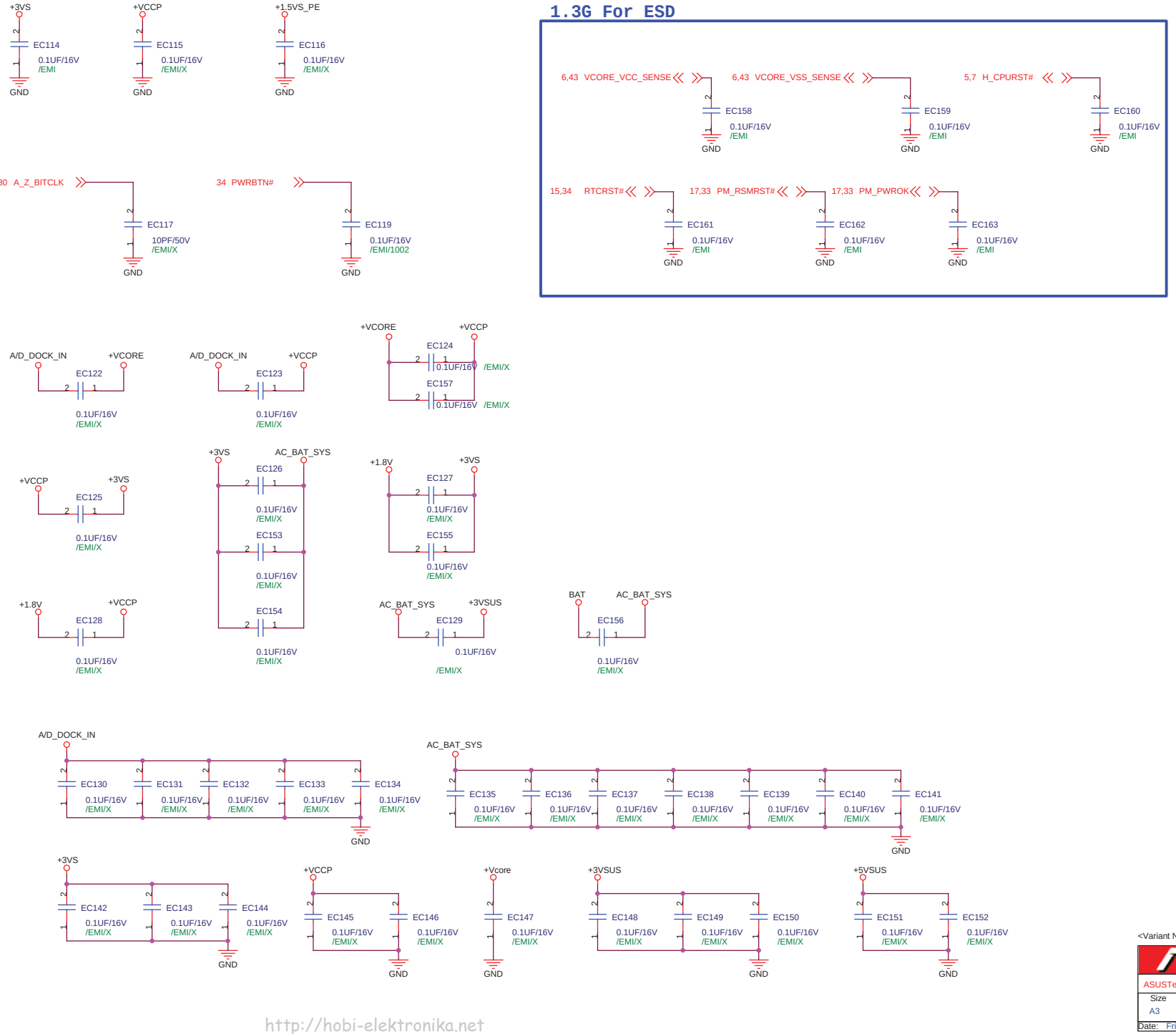
ASUSTek Computer INC.

Title : Srew Hole

Engineer: Kell_Huang

Size	Project Name	Rev
A3	1002	1.3G

Date: Friday, October 03, 2008 Sheet 40 of 50



<Variant Name>

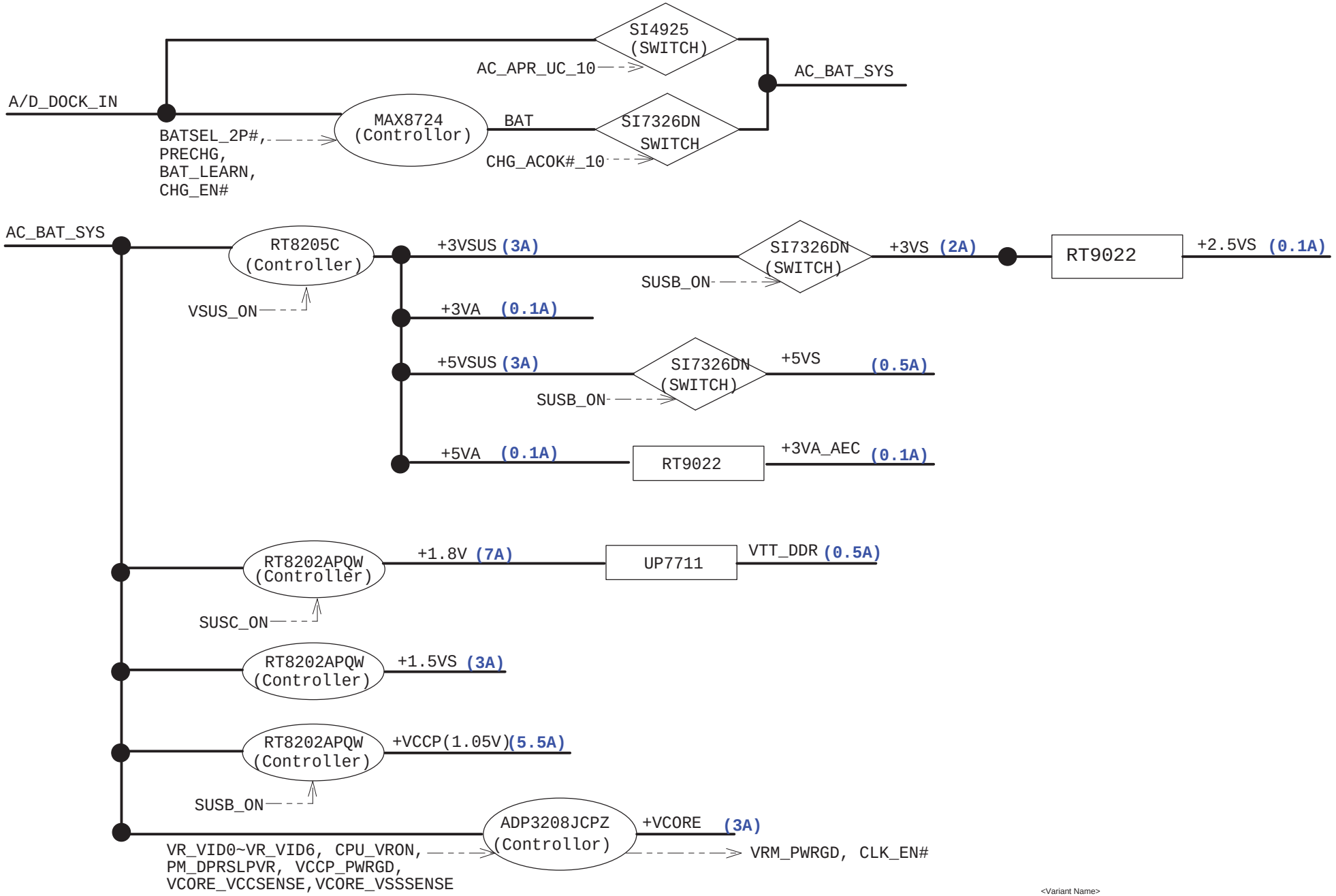
ASUSTek Computer INC.

Title : EMI

Engineer: Kell_Huang

Size	Project Name	Rev
A3	1002	1.3G

Date: Friday, October 03, 2008 Sheet 41 of 50



<Variant Name>

ASUSTek Computer INC.

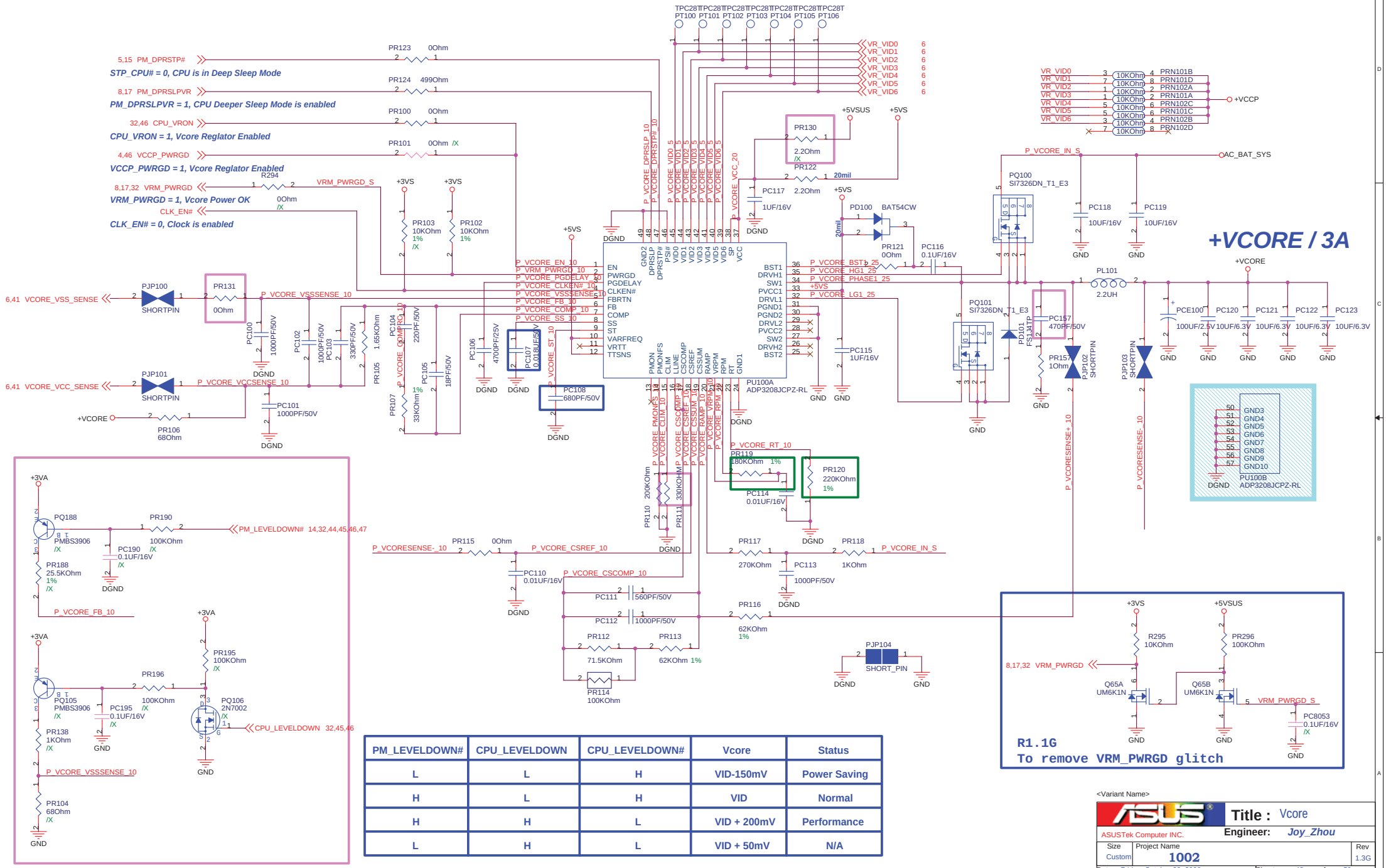
Title : Power Flow

Engineer: Joy_Zhou

Size	Project Name	Rev
A3	1002	1.3G

Date: Friday, October 03, 2008 Sheet 42 of 50

+VCORE / 3A



PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Vcore	Status
L	L	H	VID-150mV	Power Saving
H	L	H	VID	Normal
H	H	L	VID + 200mV	Performance
L	H	L	VID + 50mV	N/A

<Variant Name>

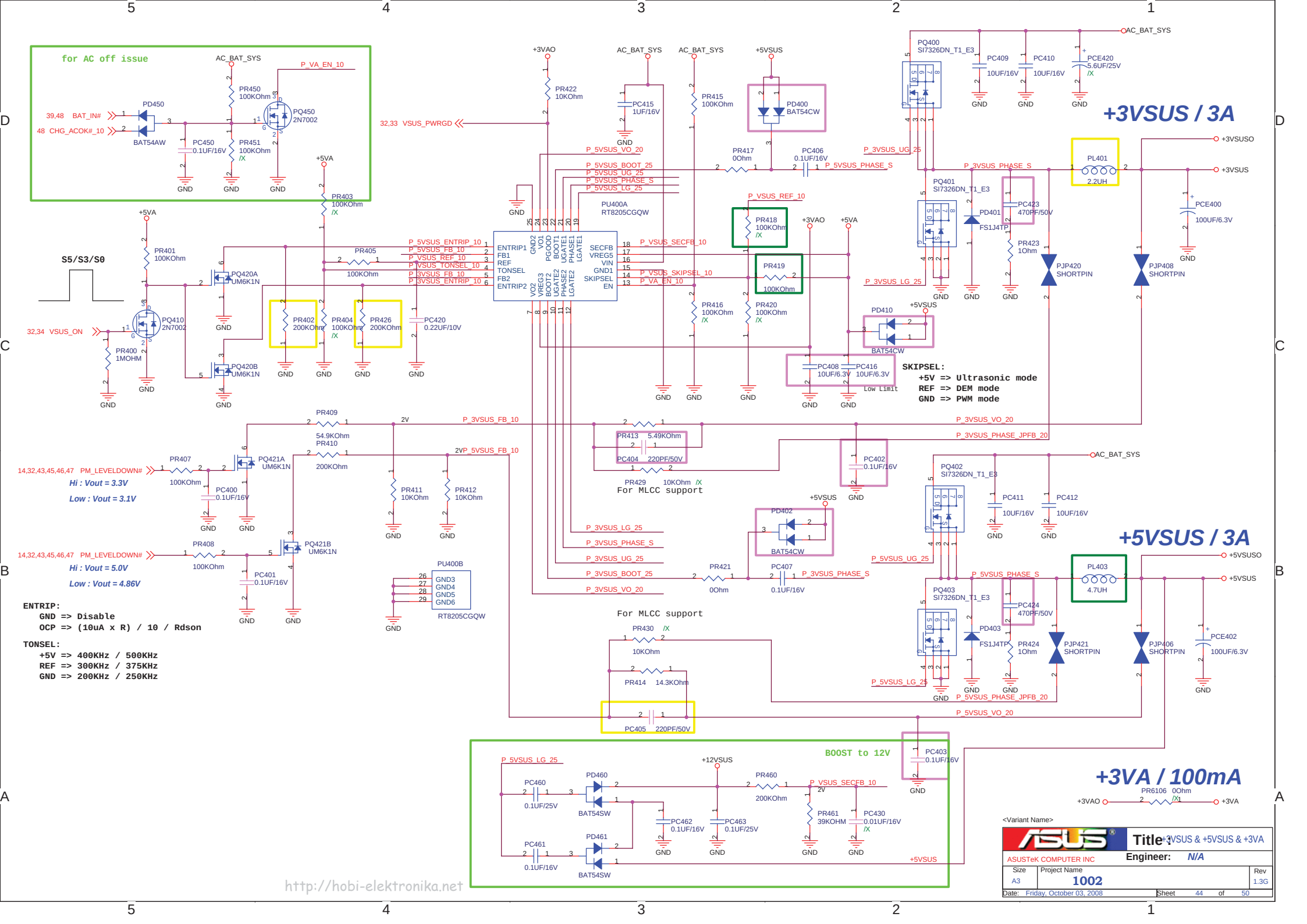
ASUSTek Computer INC.

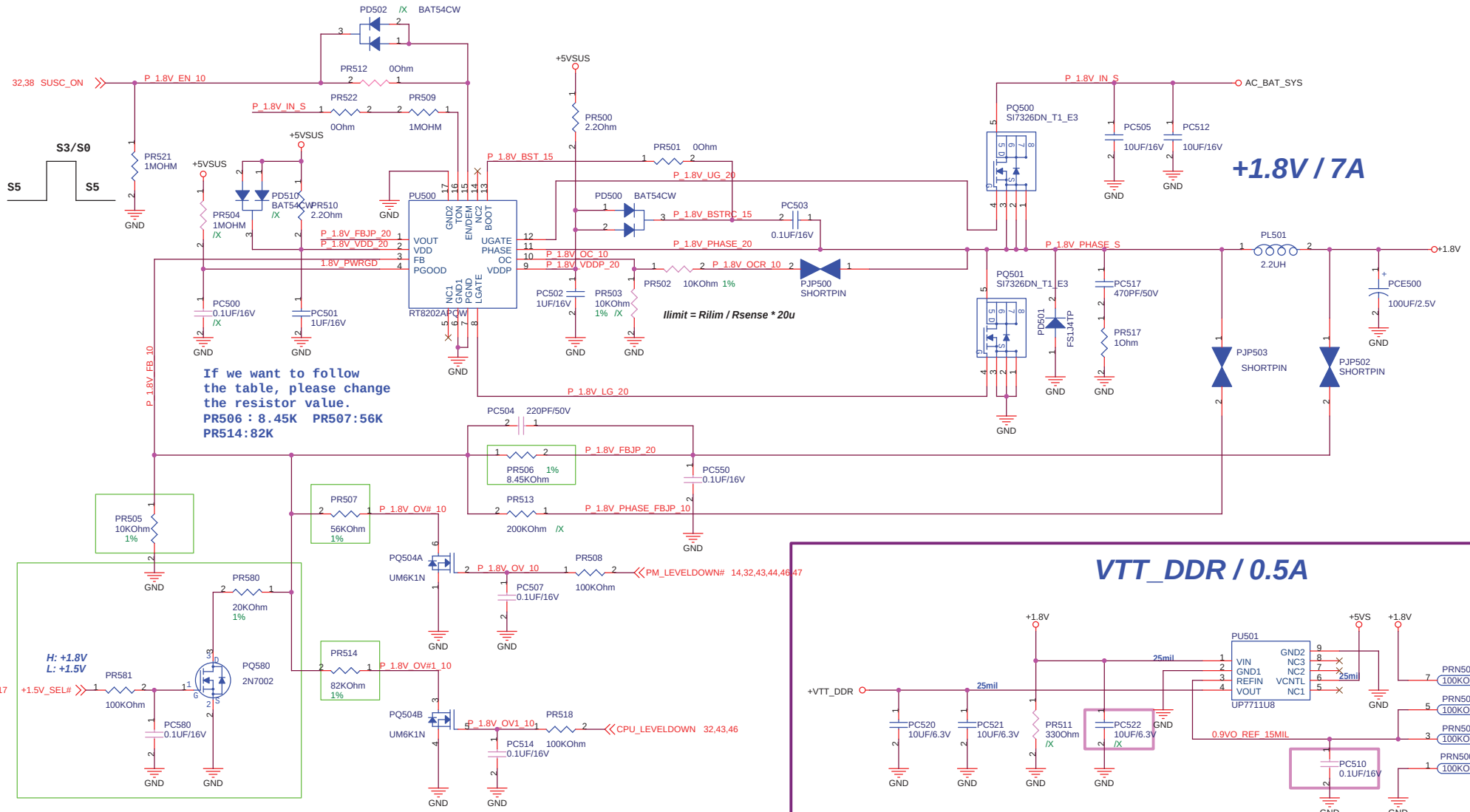
Title : Vcore

Engineer: Joy_Zhou

Size	Project Name	Rev
Custom	1002	1.3G

Date: Friday, October 03, 2008 Sheet 43 of 50


for AC off issue
AC_BAT_SYS
P_VA_EN_10
PD450
39,48 BAT_IN#
48 CHG_ACOK#_10
BAT54AW
PR450 100KOhm
PQ450 2N7002
PC450 0.1UF/16V
PR451 100KOhm /X
GND
+5VA
PR403 100KOhm /X
+3VAO
PR422 10KOhm
32,33 VSUS_PWRGD
PC415 1UF/16V
PR415 100KOhm
PD400 BAT54CW
+5VSUS
P_5VSUS_VO_20
P_5VSUS_BOOT_25
P_5VSUS_UG_25
P_5VSUS_PHASE_S
P_5VSUS_LG_25
PR417 0Ohm
PC406 0.1UF/16V
P_5VSUS_PHASE_S
PU400A RT8205CGQW
P_VSUS_REF_10
PR418 100KOhm /X
PR419 100KOhm
PR416 100KOhm /X
PR420 100KOhm /X
P_VSUS_SECFB_10
P_VSUS_SKIPSEL_10
P_VA_EN_10
P_5VSUS_ENTRIP_10
P_5VSUS_FB_10
P_VSUS_REF_10
P_VSUS_TONSEL_10
P_3VSUS_FB_10
P_3VSUS_ENTRIP_10
ENTRIP1
FB1
REF
TONSEL
FB2
ENTRIP2
GND2
VO1
PGOOD
BOOT1
UGATE1
PHASE1
LGATE1
SECFB
VREG5
VIN
GND1
SKIPSEL
EN
VO2
VREG3
BOOT2
UGATE2
PHASE2
LGATE2
S5/S3/S0
+5VA
PR401 100KOhm
PQ420A UM6K1N
PQ420B UM6K1N
PR405 100KOhm
PR402 200KOhm
PR404 100KOhm /X
PR426 200KOhm
PC420 0.22UF/10V
32,34 VSUS_ON
PQ410 2N7002
PR400 1MOHM
PD410
BAT54CW
PC408 10UF/6.3V
PC416 10UF/6.3V
Low Limit
SKIPSEL:
+5V => Ultrasonic mode
REF => DEM mode
GND => PWM mode
PQ400 SI7326DN_T1_E3
PC409 10UF/16V
PC410 10UF/16V
PCE420 5.6UF/25V /X
AC_BAT_SYS
+3VSUS / 3A
PL401 2.2UH
+3VSUSO
+3VSUS
PQ401 SI7326DN_T1_E3
PD401 FS1J4TP
PC423 470PF/50V
PR423 1Ohm
PJP420 SHORTPIN
PJP408 SHORTPIN
PCE400 100UF/6.3V
P_3VSUS_PHASE_S
P_3VSUS_UG_25
P_3VSUS_LG_25
PR409 54.9KOhm
PR410 200KOhm
2V
P_3VSUS_FB_10
2VP_5VSUS_FB_10
PR413 5.49KOhm
PC404 220PF/50V
PR429 10KOhm /X
For MLCC support
P_3VSUS_VO_20
P_3VSUS_PHASE_JPFB_20
PC402 0.1UF/16V
PR407 100KOhm
PQ421A UM6K1N
PC400 0.1UF/16V
14,32,43,45,46,47 PM_LEVELDOWN#
Hi : Vout = 3.3V
Low : Vout = 3.1V
PR411 10KOhm
PR412 10KOhm
PQ402 SI7326DN_T1_E3
PC411 10UF/16V
PC412 10UF/16V
+5VSUS / 3A
PD402
BAT54CW
PR421 0Ohm
PC407 0.1UF/16V
P_5VSUS_UG_25
P_5VSUS_PHASE_S
PR408 100KOhm
PQ421B UM6K1N
PC401 0.1UF/16V
Hi : Vout = 5.0V
Low : Vout = 4.86V
PU400B
GND3
GND4
GND5
GND6
RT8205CGQW
PQ403 SI7326DN_T1_E3
PD403 FS1J4TP
PC424 470PF/50V
PR424 1Ohm
PJP421 SHORTPIN
PJP406 SHORTPIN
PCE402 100UF/6.3V
PL403 4.7UH
+5VSUSO
+5VSUS
P_5VSUS_LG_25
P_5VSUS_PHASE_JPFB_20
P_5VSUS_VO_20
ENTRIP:
GND => Disable
OCP => (10uA x R) / 10 / Rdson
TONSEL:
+5V => 400KHz / 500KHz
REF => 300KHz / 375KHz
GND => 200KHz / 250KHz
For MLCC support
PR430 /X
10KOhm
PR414 14.3KOhm
PC405 220PF/50V
BOOST to 12V
P_5VSUS_LG_25
PC460 0.1UF/25V
PD460 BAT54SW
PD461 BAT54SW
PC461 0.1UF/16V
+12VSUS
PR460 200KOhm
P_VSUS_SECFB_10
PC462 0.1UF/16V
PC463 0.1UF/25V
PR461 39KOHM
PC430 0.01UF/16V /X
PC403 0.1UF/16V
+5VSUS
+3VA / 100mA
PR6106 0Ohm /X
+3VAO
+3VA
<Variant Name>
Title: +3VSUS & +5VSUS & +3VA
ASUSTeK COMPUTER INC
Engineer: N/A
Size A3
Project Name 1002
Rev 1.3G
Date: Friday, October 03, 2008
Sheet 44 of 50
http://hobi-elektronika.net



+1.5V_SEL#	PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Voltage	Status
H	L	L	H	1.72V	Power Saving
H	H	L	H	1.82V	Normal
H	H	H	L	1.9V	Performance
H	L	H	L	1.782V	N/A
L	L	L	H	1.4V	Power Saving
L	H	L	H	1.5V	Normal
L	H	H	L	1.58V	Performance

<Variant Name>

Title : +1.8V & VTTDDR

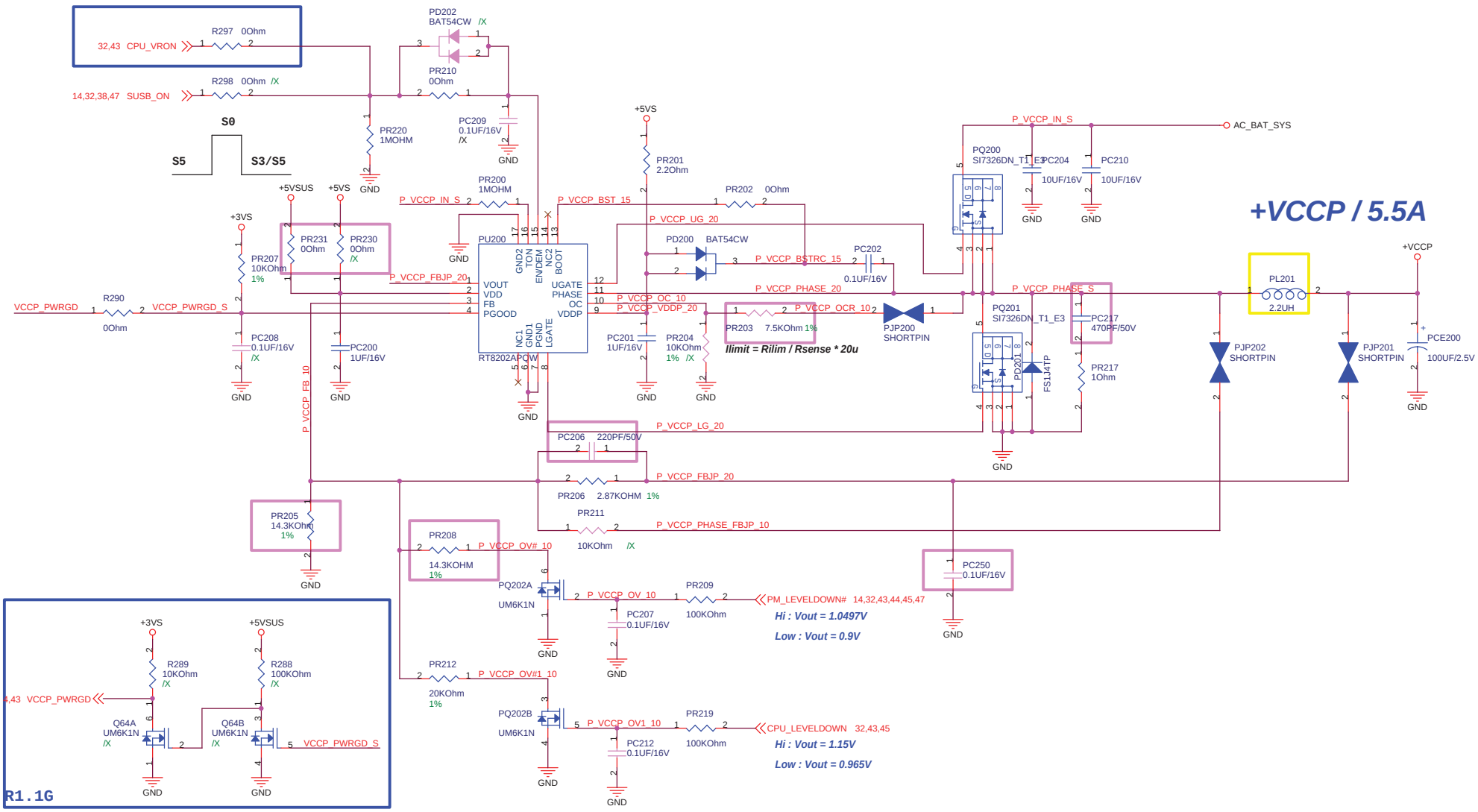
ASUSTek Computer INC.

Engineer: Joy_Zhou

Size	Project Name	Rev
A3	1002	1.3G

Date: Friday, October 03, 2008 Sheet 45 of 50

1.1G change Enable signal from CPU_VRON

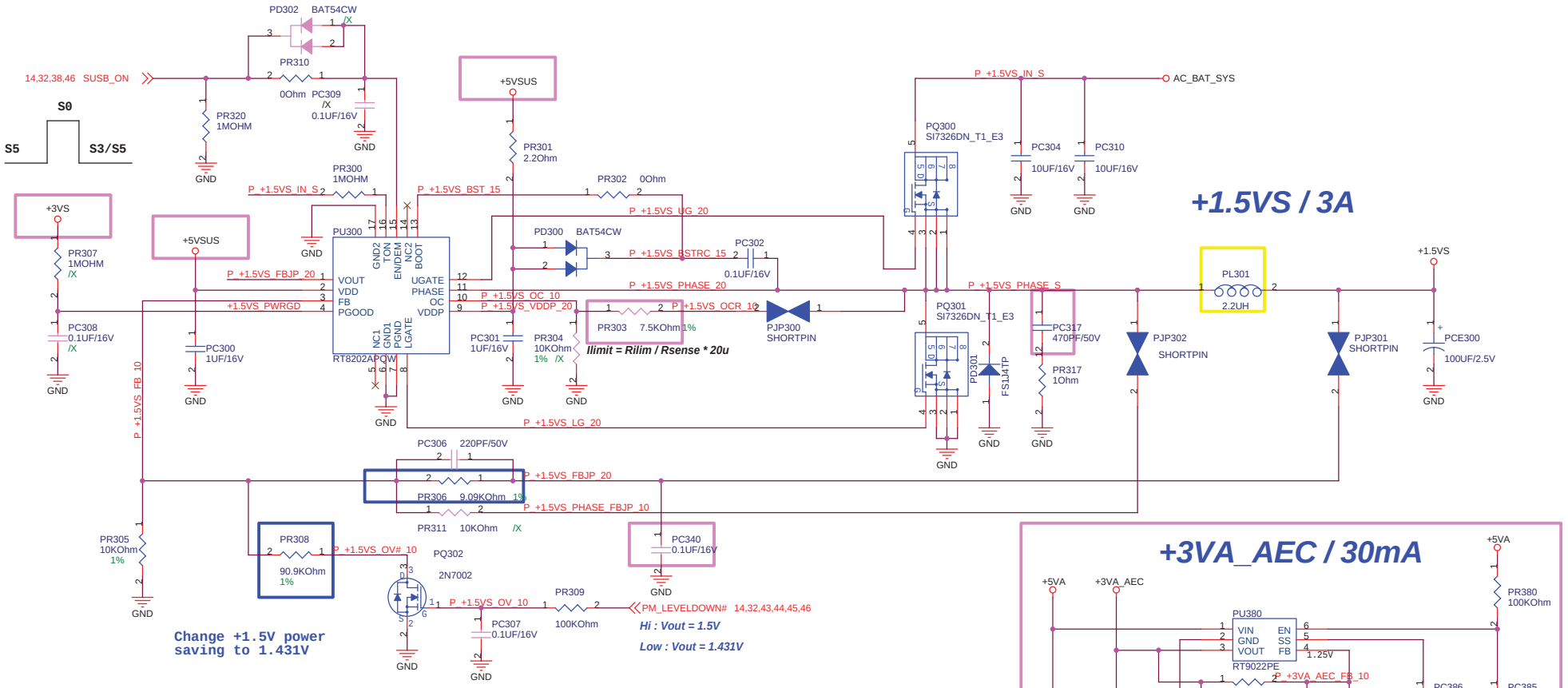


To remove VCCP_PWRGD glitch

PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Voltage	Status
L	L	H	0.9V	Power Saving
H	L	H	1.048V	Normal
H	H	L	1.157V	Performance
L	H	L	1.072V	N/A

<Variant Name>

ASUSTek Computer INC.	Title : VCCP	
	Engineer: Joy_Zhou	
Size: A3	Project Name: 1002	Rev: 1.3G
Date: Friday, October 03, 2008	Sheet 46 of 50	


+1.5VS / 3A
S0
S5
S3/S5
14,32,38,46 SUSB_ON
PD302 BAT54CW
PR310 0Ohm
PC309 /X 0.1UF/16V
PR320 1MOHM
PR300 1MOHM
P +1.5VS_IN_S
+5VSUS
PR301 2.2Ohm
PR302 0Ohm
P +1.5VS_BST_15
P +1.5VS_UG_20
PD300 BAT54CW
PC302 0.1UF/16V
P +1.5VS_BSTRC_15
+3VS
PR307 1MOHM /X
PC308 0.1UF/16V /X
PC300 1UF/16V
PU300
RT8202APQW
VOUT
VDD
FB
PGOOD
UGATE
PHASE
OC
VDDP
P +1.5VS_FBJP_20
+1.5VS_PWRGD
P +1.5VS_OC_10
P +1.5VS_VDDP_20
P +1.5VS_OCR_10
PR303 7.5KOhm 1%
PC301 1UF/16V
PR304 10KOhm 1% /X
Ilimit = Rilim / Rsense * 20u
PJP300 SHORTPIN
P +1.5VS_PHASE_20
P +1.5VS_LG_20
P +1.5VS_FB_10
PQ300 SI7326DN_T1_E3
PQ301 SI7326DN_T1_E3
PC304 10UF/16V
PC310 10UF/16V
AC_BAT_SYS
P +1.5VS_PHASE_S
PC317 470PF/50V
PR317 10Ohm
PD301 FS1L4TP
PJP302 SHORTPIN
PL301 2.2UH
PJP301 SHORTPIN
+1.5VS
PCE300 100UF/2.5V
PC306 220PF/50V
PR306 9.09KOhm 1%
P +1.5VS_FBJP_20
P +1.5VS_PHASE_FBJP_10
PR311 10KOhm /X
PR305 10KOhm 1%
PR308 90.9KOhm 1%
P +1.5VS_OV# 10
PQ302 2N7002
PC340 0.1UF/16V
PR309 100KOhm
P +1.5VS_OV_10
PM_LEVELDOWN# 14,32,43,44,45,46
PC307 0.1UF/16V
Hi : Vout = 1.5V
Low : Vout = 1.431V
Change +1.5V power saving to 1.431V
+3VA_AEC / 30mA
+5VA
+3VA_AEC
PU380
RT9022PE
VIN
GND
VOUT
EN
SS
FB
1.25V
P +3VA_AEC_FB_10
PR380 100KOhm
PR381 100KOhm
PR382 60.4KOhm 1%
PC380 1UF/16V
PC381 1UF/16V Low Limit
PC386 0.01UF/16V
PC385 0.01UF/16V
PC388 220PF/50V /X
+2.5VS / 100mA
+5VS
+2.5VS
PU350
RT9022PE
P +2.5VS_EN_10
P +2.5VS_FB_10
PR352 100KOhm
PR351 1% 60.4KOhm
PC390 220PF/50V /X
PR353 60.4KOhm
PC350 1UF/16V
PC351 1UF/16V
PC355 0.01UF/16V
PC356 0.01UF/16V /X
AC_BAT_SYS
+12VSUS
PQ408 SI7326DN_T1_E3
PQ409 SI7326DN_T1_E3
Shape
+3VSUS
P_3VS5VS_EN_10
PR481 62KOhm 1%
PC481 0.01UF/16V
PRN480B 100KOhm
PR483 100KOhm /X
PR484 100KOhm
30 P_SUSB#_ON_10
P_SUSB#_ON_10
PQ480B UM6K1N
PRN480C 100KOhm
PQ480A UM6K1N
10mil
PRN480D 100KOhm
PC483 0.01UF/16V
PR482 10KOhm
PC482 0.01UF/16V
P_3VS5VS_EN_10 45
http://hobi-elektronika.net
<Variant Name>
Title : +1.5VS & +2.5VS
ASUSTek Computer INC.
Engineer: Joy_Zhou
Size A3
Project Name 1002
Rev 1.3G
Date: Friday, October 03, 2008
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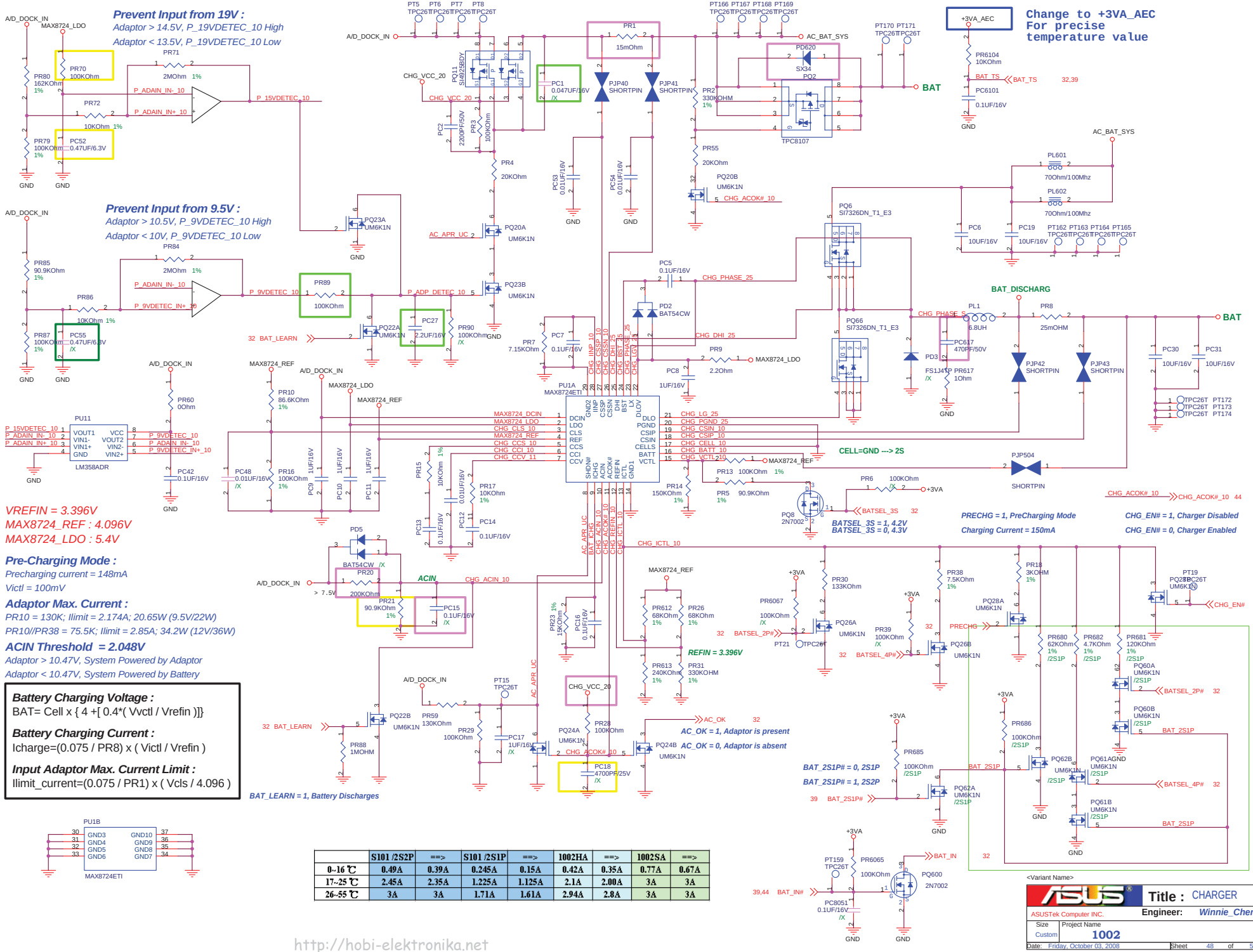

	S101 /2S2P	==>	S101 /2S1P	==>	1002HA	==>	1002SA	==>
0~16 ℃	0.49A	0.39A	0.245A	0.15A	0.42A	0.35A	0.77A	0.67A
17~25 ℃	2.45A	2.35A	1.225A	1.125A	2.1A	2.00A	3A	3A
26~55 ℃	3A	3A	1.71A	1.61A	2.94A	2.8A	3A	3A

EC KB3310 GPIO SETTING

Pin	Pin Name	Signal Name	Type	Note
1	GPIO00/GA20	A20GATE	O	
2	GPIO01/KBRST#	RC_IN#	O	
6	GPIO04	HOTKEY_SW0#	I	Internal pull high
13	GPIO05/PCIRST#	PCI_RST#	I	
14	GPIO07	HOTKEY_SW1#	I	Internal Pull Up
15	GPIO08	EXTSMI#	OD	10K ohm Pull Up to +3VSU
16	GPIO0A	LID_EC#	I	Internal pull high
17	GPIO0B/ESB_CLK	NC	O	
18	GPIO0C/ESB_DAT	NC	O	
19	GPIO0D	HOTKEY_SW2#	I	Internal pull high
20	GPIO0E/SCI#	KBC_SCI#	OD	10K ohm Pull Up to +3VSUS
21	GPIO0F/PWM0	BL_PWM_DA	O	
23	GPIO10/PWM1	BATSEL_4P#	O	Battery charging current setting
25	GPIO11/PWM2	PM_PWRBTN#	OD	Internal pull high in ICH
26	GPIO12/FANPWM1	FAN0_PWM	O	CPU Fan
27	GPIO13/FANPWM2	FAN1_PWM	O	VGA Fan
28	GPIO14/FANFB1	FAN0_TACH	I	CPU FanTach
29	GPIO15/FANFB2	FAN1_TACH	I	VGA FanTach
30	GPIO16/E51_TX	E51_TX	O	RS232 debug port
31	GPIO17/E51_RX	E51_RX	O	RS232 debug port
32	GPIO18	PWR_SW#	I	Internal pull high
34	GPIO19/PWM3	MAIL_LED#	O	
36	GPIO1A/NUMLED	NUM_LED#	O	
38	GPIO1D/CLKRUN#	NC	O	
39	GPIO20/KSO0/TP_TEST	KSO0	O	
40	GPIO21/KSO1/TP_PLL	KSO1	O	
41	GPIO22/KSO2	KSO2	O	
42	GPIO23/KSO3	KSO3	O	
43	GPIO24/KSO4	KSO4	O	
44	GPIO25/KSO5	KSO5	O	
45	GPIO26/KSO6	KSO6	O	
46	GPIO27/KSO7	KSO7	O	
47	GPIO28/KSO8	KSO8	O	
48	GPIO29/KSO9	KSO9	O	
49	GPIO2A/KSO10	KSO10	O	
50	GPIO2B/KSO11	KSO11	O	
51	GPIO2C/KSO12	KSO12	O	
52	GPIO2D/KSO13	KSO13	O	
53	GPIO2E/KSO14	KSO14	O	
54	GPIO2F/KSO15	KSO15	O	
55	GPIO30/KSI0	KSI0	I	Internal pull high
56	GPIO31/KSI1	KSI1	I	Internal pull high
57	GPIO32/KSI2	KSI2	I	Internal pull high
58	GPIO33/KSI3	KSI3	I	Internal pull high
59	GPIO34/KSI4	KSI4	I	Internal pull high
60	GPIO35/KSI5	KSI5	I	Internal pull high
61	GPIO36/KSI6	KSI6	I	Internal pull high
62	GPIO37/KSI7	KSI7	I	Internal pull high
63	GPI38/AD0	BAT_ICHG	I	
64	GPI39/AD1	BAT_CONFIG	I	Battery configureation
65	GPIO3A/AD2	BAT_SENSE	I	Battery Voltage Sensor
66	GPIO3B/AD3	BAT_TS	I	Battery Thermal Sensor
68	GPO3C/DA0	DOC	O	Trigger Clock Gen

Pin	Pin Name	Signal Name	Type	Note
70	GPO3D/DA1	LCD_BACKOFF#	O	
71	GPO3E/DA2	CLK_PWRSAVE#	O	
72	GPO3F/DA3	BAT_LL#	O	Battery Low Low
73	GPIO40	AC_OK	I	AC Adaptor Plug in
74	GPIO41	PM_RSMRST#	O	10K pull down to GND
75	GPI42	BAT_IN	I	
76	GPI43	CLRTC_EC	I	
77	GPIO44/SCL1	SMB0_CLK	I/OD	4.7K pull high to +3VA_EC
78	GPIO45/SDA1	SMB0_DAT	I/OD	4.7K pull high to +3VA_EC
79	GPIO46/SCL2	SMB1_CLK	I/OD	10K pull high to +3V
80	GPIO47/SDA2	SMB1_DAT	I/OD	10K pull high to +3V
81	GPIO48/KSO16	KB pin 28	I	for KB type detection
82	GPIO49/KSO17	KB pin 27	I	for KB type detection
83	GPIO4A/PSCLK1	AUO_SCL	O	for AUO, default H at S0
84	GPIO4B/PSDAT1	AUO_SDA	O	for AUO, default L at S0
85	GPIO4C/PSCLK2	AUO_CSB	O	for AUO, default H at S0
86	GPIO4D/PSDAT2	LVDD_EN	I	for AUO 7" Panel
87	GPIO4E/PSCLK3	TP_CLK	I/OD	10K pull high to +3V
88	GPIO4F/PSDAT3	TP_DAT	I/OD	10K pull high to +3V
89	GPIO50/SELIO#	BATSEL_3S	O	Battery series, H:3S, L:4S
90	GPIO52/E51_CS#	CHG_LED_UP#	O	
91	GPIO53/CAPLED	CAP_LED#	O	
92	GPIO54	PWR_LED_UP	O	
93	GPIO55/SCRLED	SCRL_LED#	O	
95	GPIO56	PWR4G_SW#	I	Internal pull high
97	GPXOA00/SDICS#	SPI_MODE#	O	4.7K pull down to GND
98	GPXOA01/SDICLK	SUSC_ON	O	
99	GPXOA02/SDIDO	VSUS_ON	O	
100	GPXOA03	CPU_VRON	O	
101	GPXOA04	SUSB_ON	O	
102	GPXOA05	ICH_PWROK	O	
103	GPXOA06	VOLT_CTRL	O	
104	GPXOA07	CHG_EN#	O	Battery charging enabled
105	GPXOA08	PRECHG	O	
106	GPXOA09	SPI_WP#	O	
107	GPX0A10	OP_SD#	O	Audio OP
108	GPXOA11	BAT_LEARN	O	
109	GPXID0/SDIDI	BATSEL_2P#	O	Battery parallel, H:1P, L:2P~3P
110	GPXID1	NC	O	
112	GPXID2	THRO_CPU	O	Active if CPU temperature over spec
114	GPXID3	SUSB#	I	100K pull down to GND
115	GPXID4	SUSC#	I	100K pull down to GND
116	GPXID5	CPUPWR_GD	I	Pull high to +3V
117	GPXID6	VSUS_GD	I	
118	GPXID7	NC	O	
121	GPIO57	INTERNET#	I	Internal pull high
126	GPIO57/SPICLK	SPI_CLK	O	
127	GPIO59/TEST_CLK	NC	O	

EC KB3310 Other Pin SETTING

Pin	Pin Name	Signal Name	Type	Note
3	SERIRQ	INT_SERIRQ	I/OD	10K pull high to +3V
4	LFRAME#	LPC_FRAME#	I	
5	LAD3	LPC_AD3	I/O	
7	LAD2	LPC_AD2	I/O	
8	LAD1	LPC_AD1	I/O	
9	VCC	+3VA_EC	P	
10	LAD0	LPC_AD0	I/O	
11	GND	GND	P	
12	PCICLK	CLK_PCI_EC	I	
22	VCC	+3VA_EC	P	
24	GND	GND	P	
33	VCC	+3VA_EC	P	
35	GND	GND	P	
37	ECRST#	EC_RST#	I	100K pull high to +3VA_EC
67	AVCC	+3VACC	P	
69	AGND	AGND	P	
94	GND	GND	P	
96	VCC	+3VA_EC	P	
111	VCC	+3VA_EC	P	
113	GND	GND	P	
119	RD#/SPIDI	SPI_SO	I	
120	WR#/SPIDO	SPI_SI	O	
112	XCLKI	32KXCLKI	I	
123	XCLKO	32KXCLKO	O	
124	V18R	V18R	P	Reserved 1uF to GND
125	VCC	+3VA_EC	P	
128	SPICS#/SELMEM#	SPI_CE#	O	

<Variant Name>

ASUSTek Computer INC.

Title : EC Pin Define

Engineer: Satan He

Size	Project Name	Rev
A3	1002	1.3G

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<Variant Name>

ASUSTek Computer INC.	Title : History	
	Engineer: *Satan He*	
Size: A3	Project Name: 1002	Rev: 1.3G
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