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8. Block Diagram and Schematic

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Bloomington

CPU : INTEL PINEVIEW-M
Chip Set : INTEL TIGERPOINT-M
Remarks :

Model Name : PINE TRAIL-M
PCB Part No :

Dev. Step : PV2
Revision : 0.9
T.R. Date : 2009.11.09

DRAW	CHECK	APPROVAL

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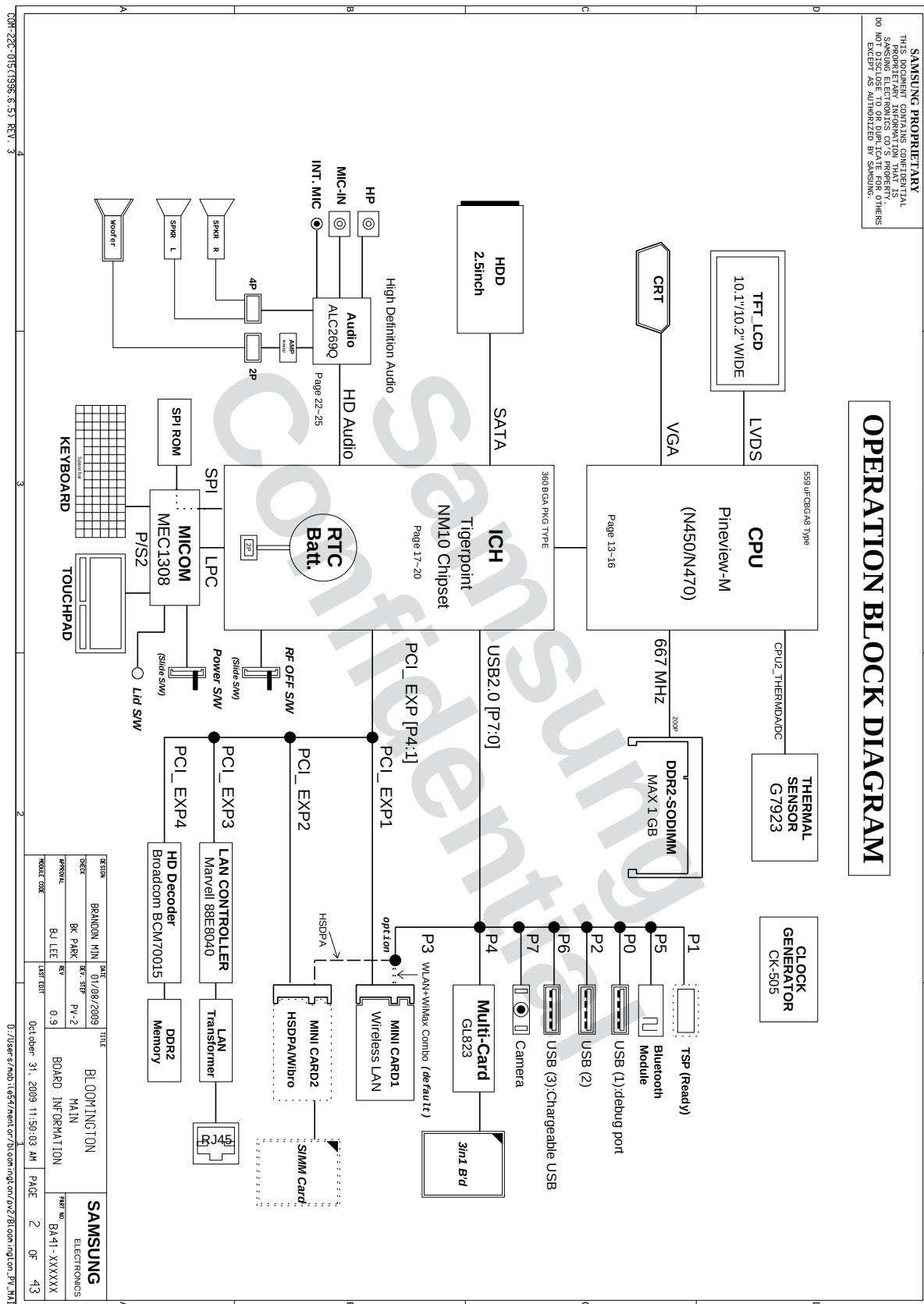
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DESIGN	BRD	TIME	SAMSUNG	
TOUCH	BRANDON MIN	REV: 01/08/2009	BLOOMINGTON	ELECTRONICS
JUNCTION	BK PARK	REV: PV-2	MAIN COVER	BA41-XXXXXX
MODEL CODE	BJ LEE	REV: 0.9	October 31, 2009 11:50:03 AM	PAGE 1 OF 43

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BOARD INFORMATION

SCHEMATIC ANNOTATIONS AND BOARD INFORMATION

PCI Devices			
Devices	IDSEL#	REQGATE#	Interrupts
USB PCI AD24(Internal) AD24(Internal) AD24(Internal) Internal MAC	AD24(Internal) AD24(Internal) AD24(Internal) AD24(Internal)	Programmable	

I/C / SMB Address			
Devices	Address	Hex	Bus
ICP/M (Clock Generator)	Master	700 000X	SMBUS Master
SCS/ADM	Slave	100 000X	Clock Unused
CPU Thermal Sensor	0111 101X	7Ah	Clock Output Disable Thermal Sensor

USB PORT Assign			
Port Number	ASSIGNED TO	Port Number	ASSIGNED TO
UHCI_0	USB PORT	UHCI_2	USB PORT
UHCI_1	Wireless LAN	UHCI_3	USB PORT
UHCI_3	BLUETOOTH	UHCI_7	USB PORT
			HSIOA CAMERA

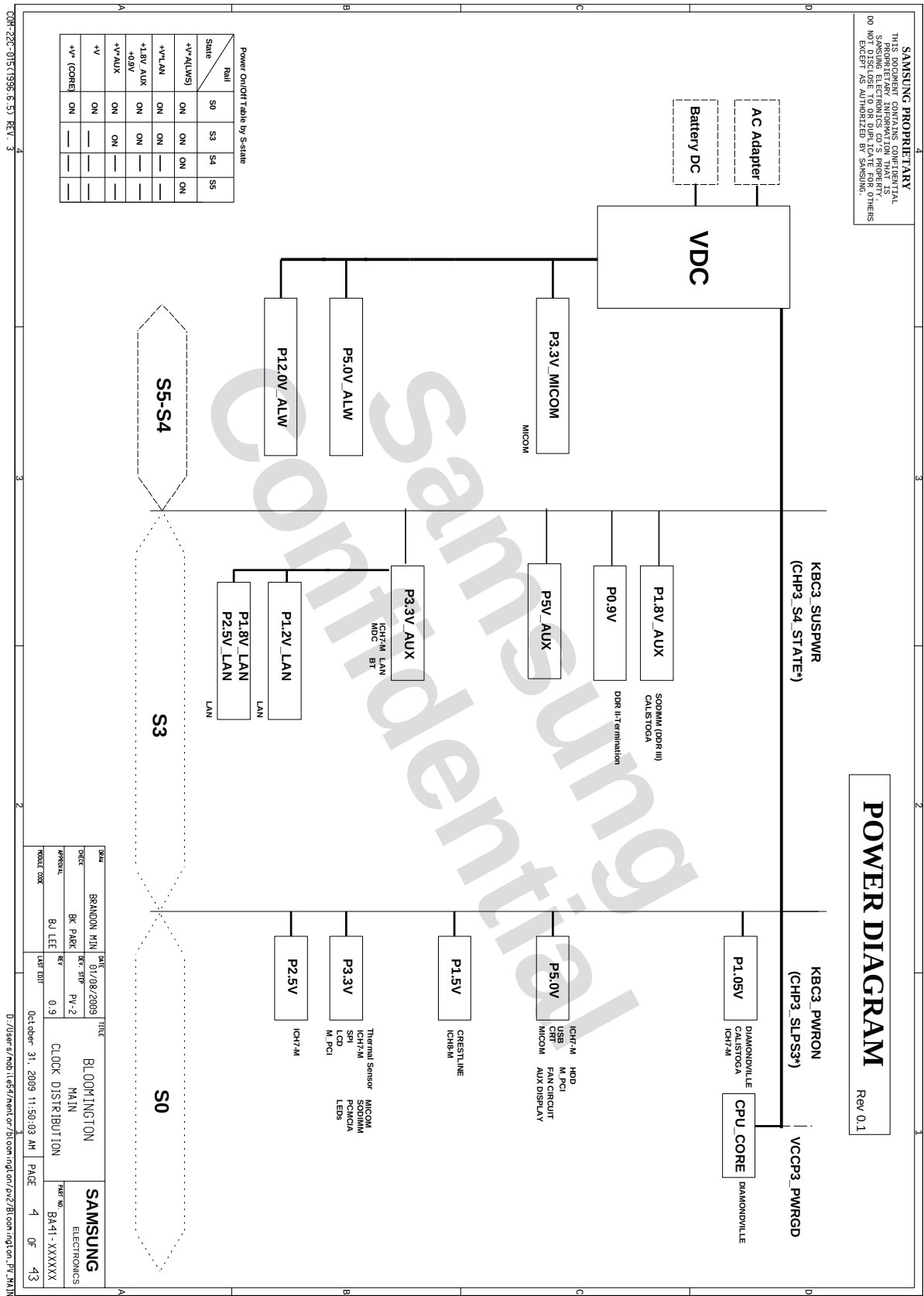
PCI Express Assign			
Port Number	ASSIGNED TO	Port Number	ASSIGNED TO
1	Mini Card 1(Wireless LAN)	2	Mini Card 2 (HSDPA/WiBro)
3	LOM	3	LOM

Voltage Rails	
Power Rail	Descriptions
PRTC_BAT	3.3V (can drop to 2.0V min. in G3 state) supply for the RTC wall.
VDC	Primary DC system power supply (0 to 19V)
P3_3V_MICROM	3.3V (always power rail for Microm)
P1_3V_AUX	1.5V switched power rail (off in S3-S5)
P0_3V	0.9V power rail for DDR (off in S4-S5)
P5V_AUX	5.0V power rail (off in S4-S5)
P3V_AUX	3.3V switched power rail (off in S3-S5)
P3V	3.3V switched power rail (off in S3-S5)
CPU_CORE	Core voltage for Adm CPU

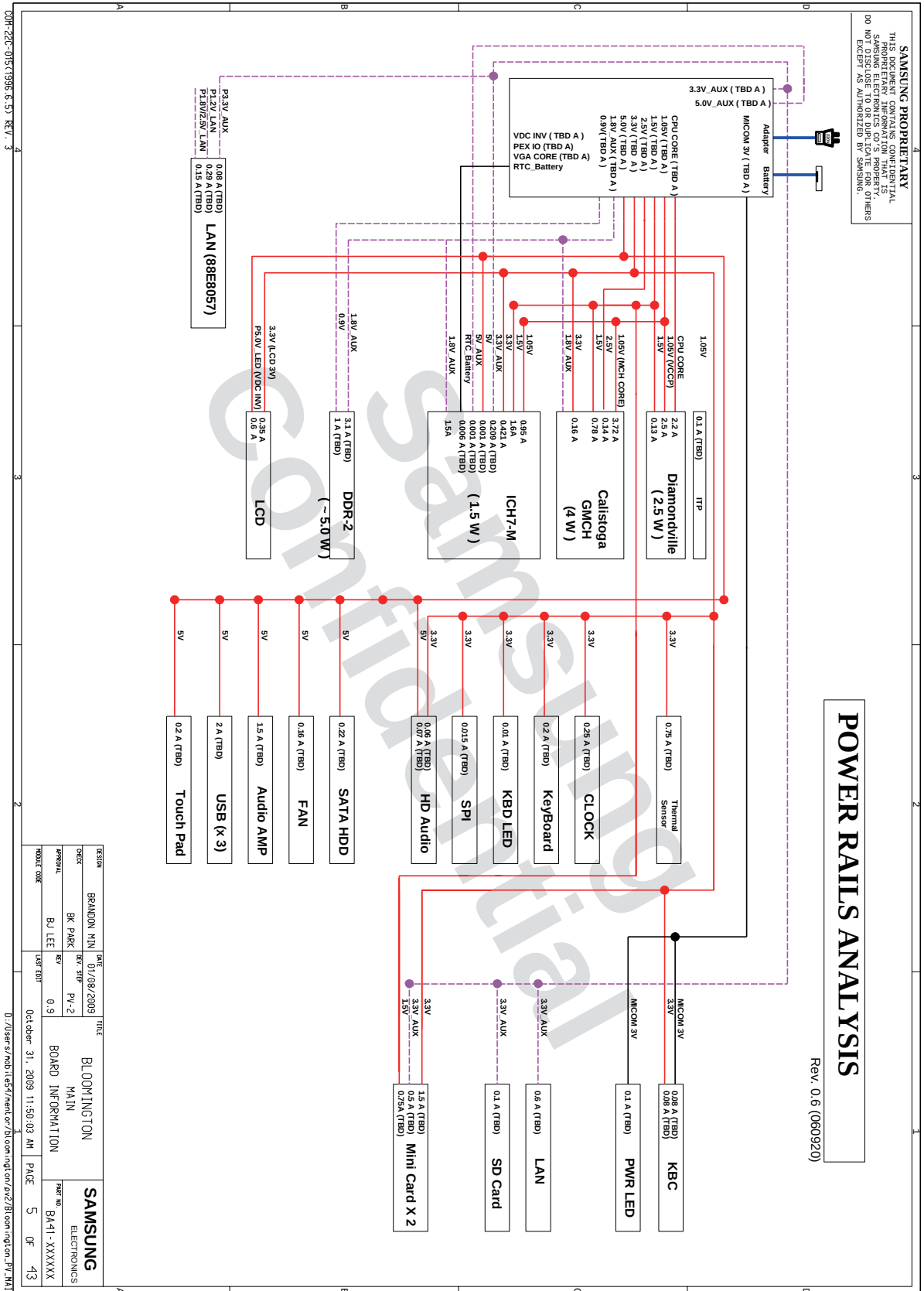
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Doc#	BU LEE	Rev	0.9	Doc#	POWER SEQUENCE	Doc#	
Doc#	MOORE TOOK	Rev	0.1	Doc#		Doc#	

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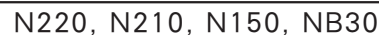
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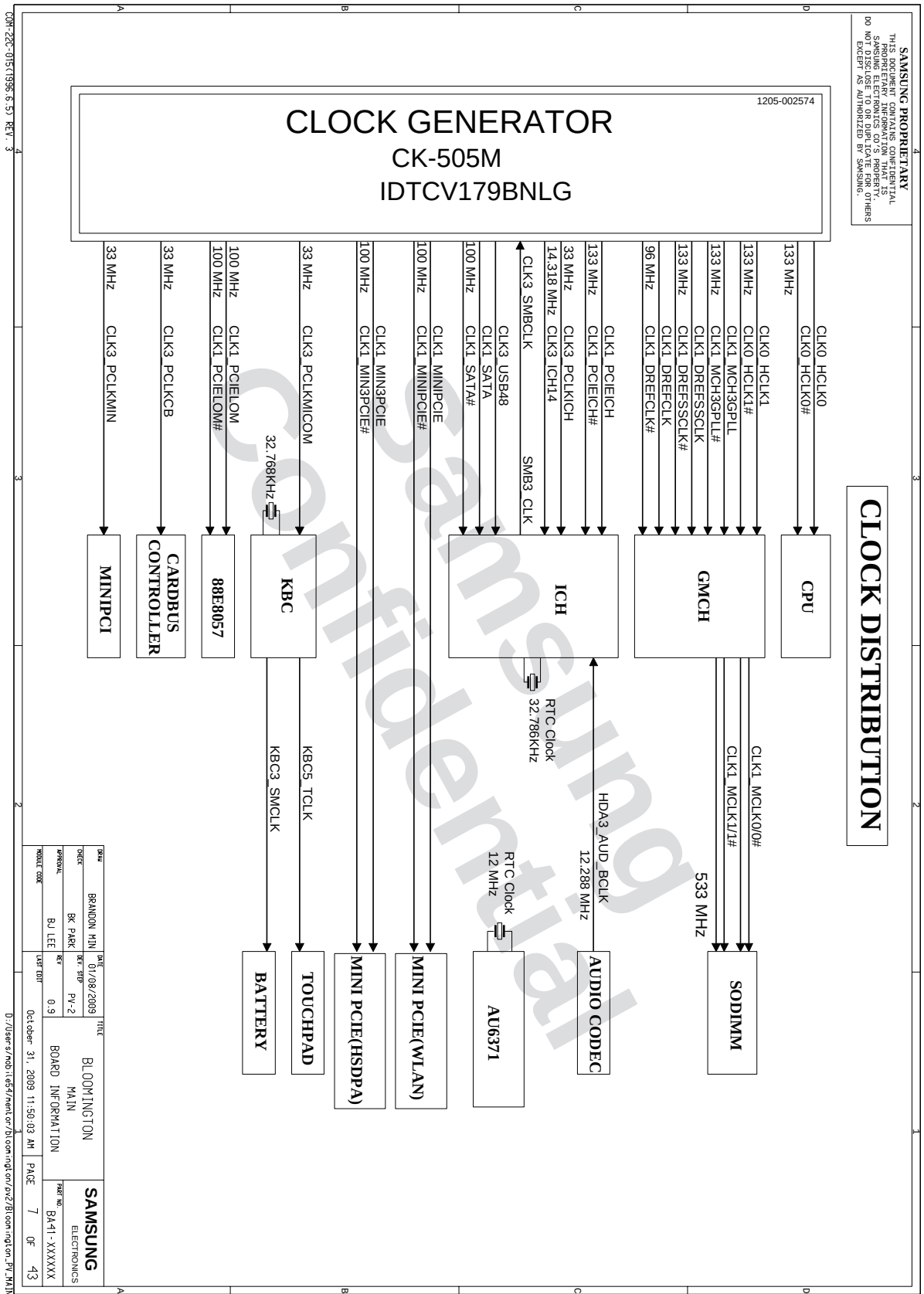
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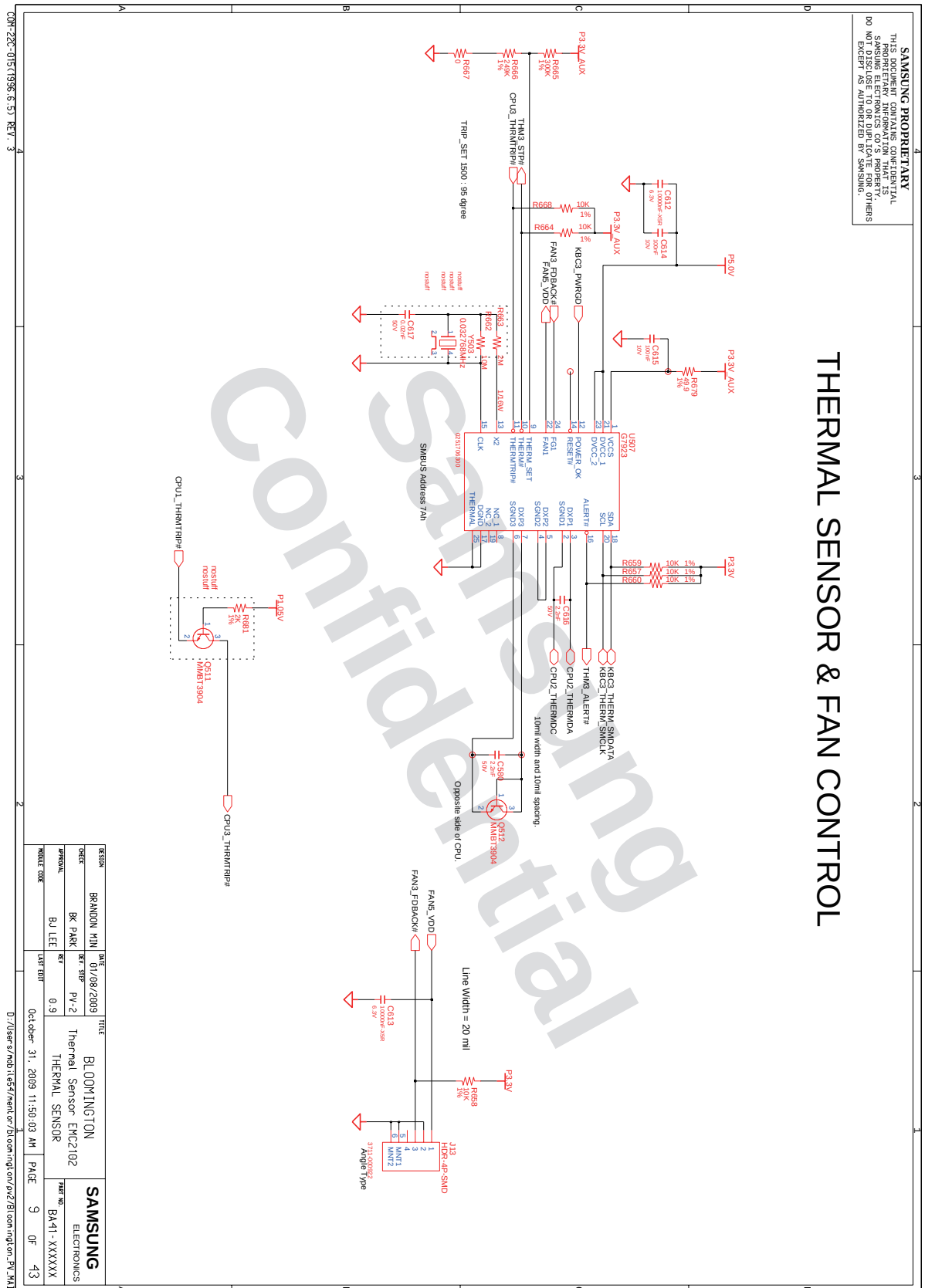


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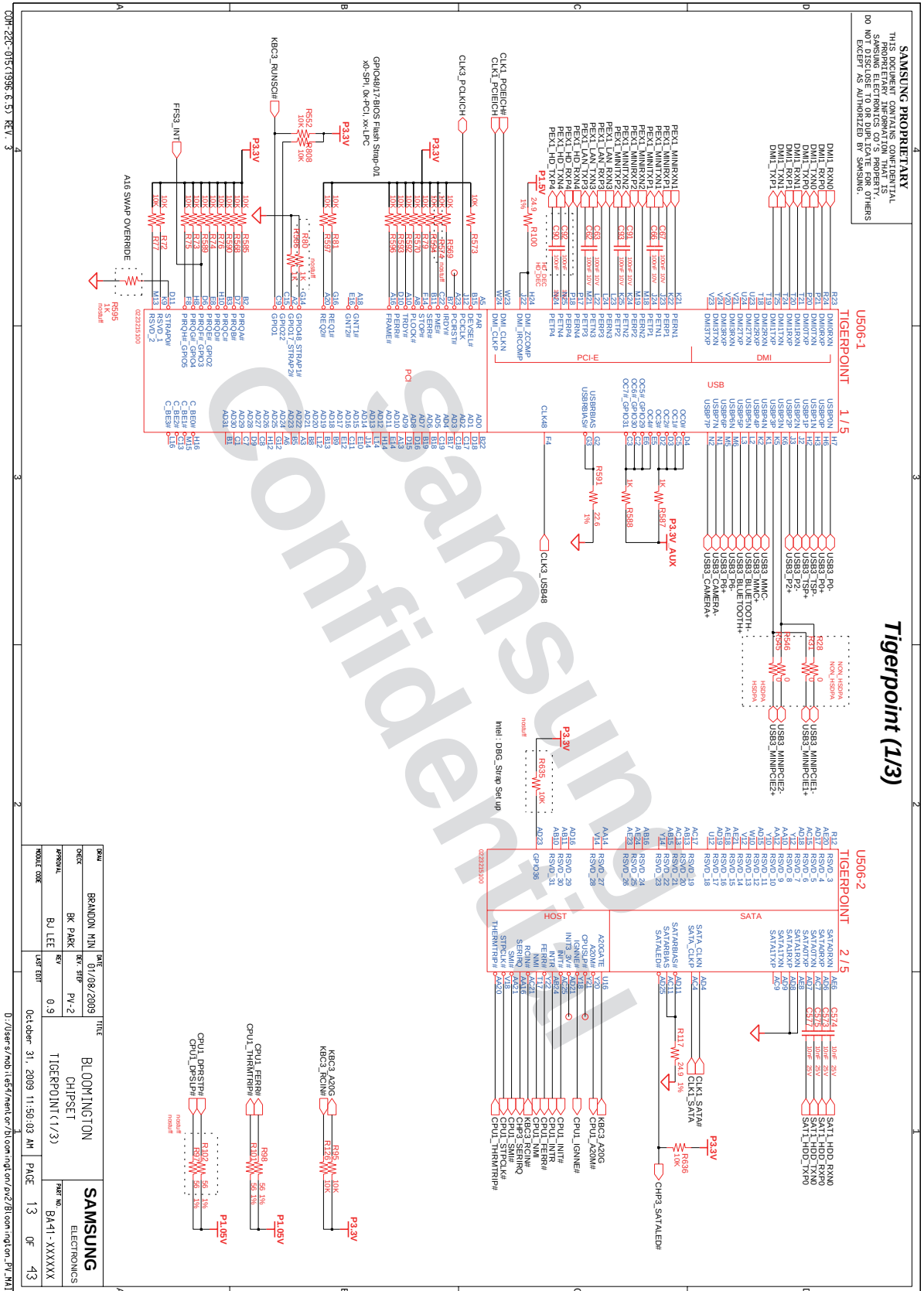
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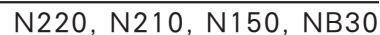
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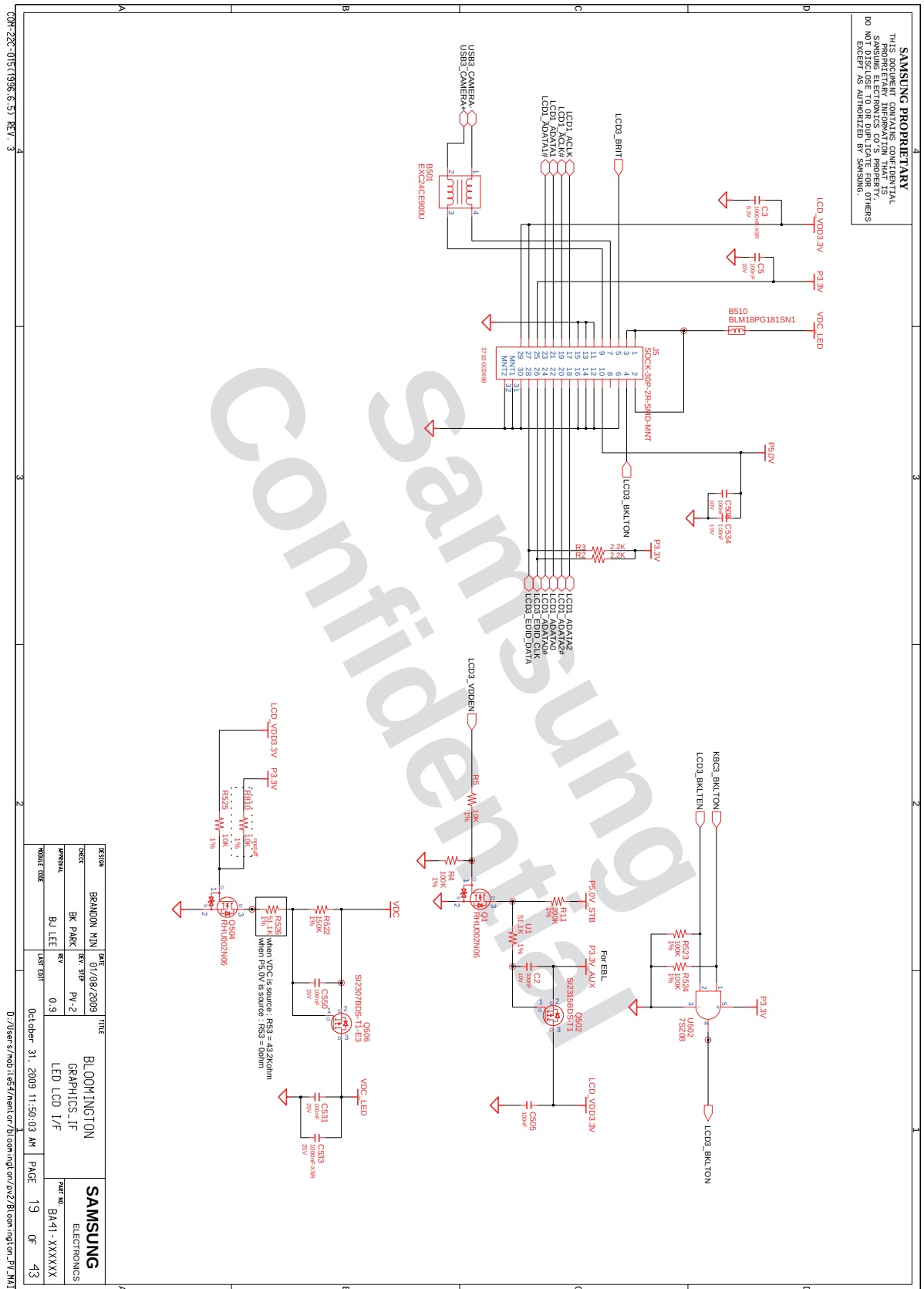


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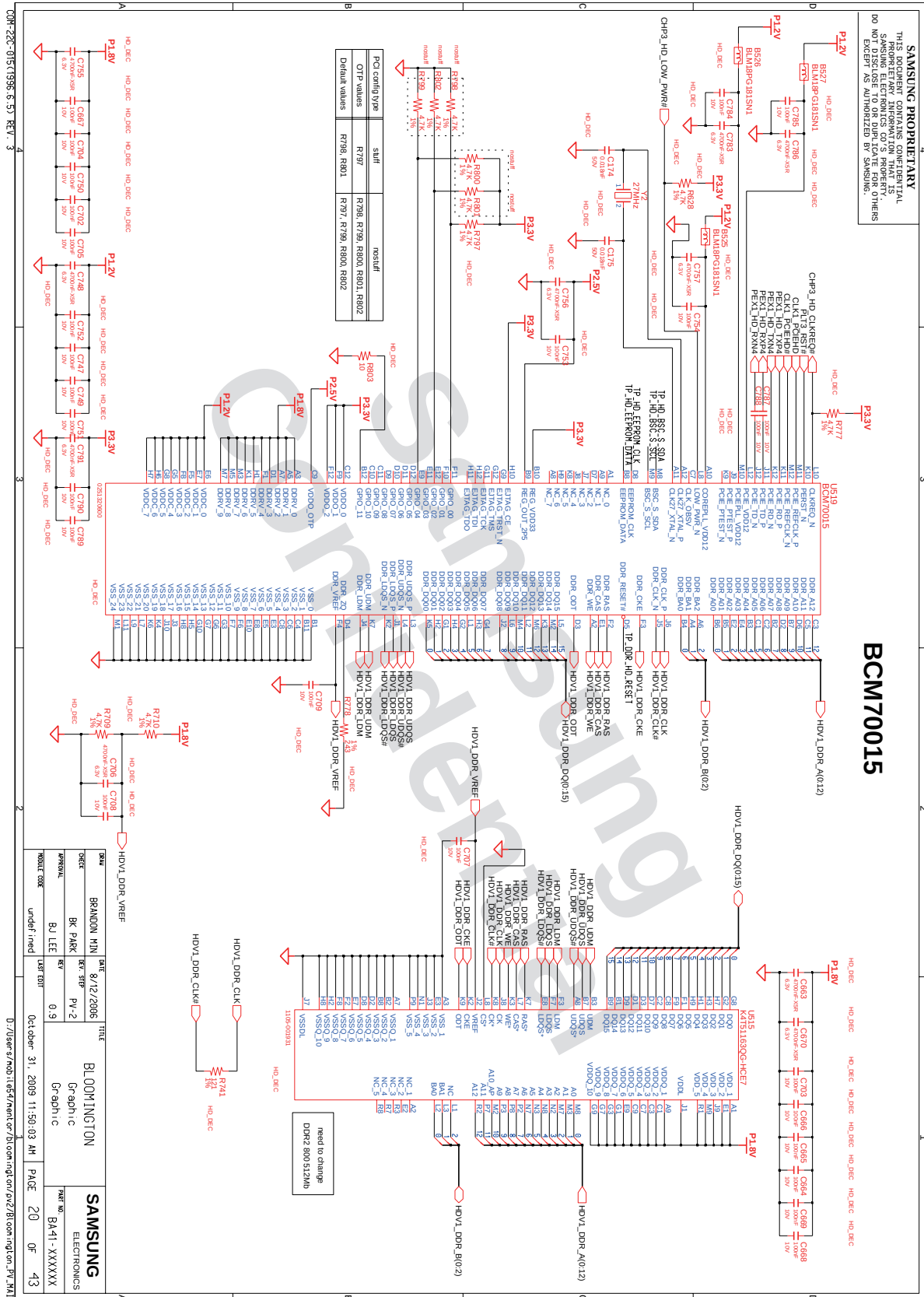


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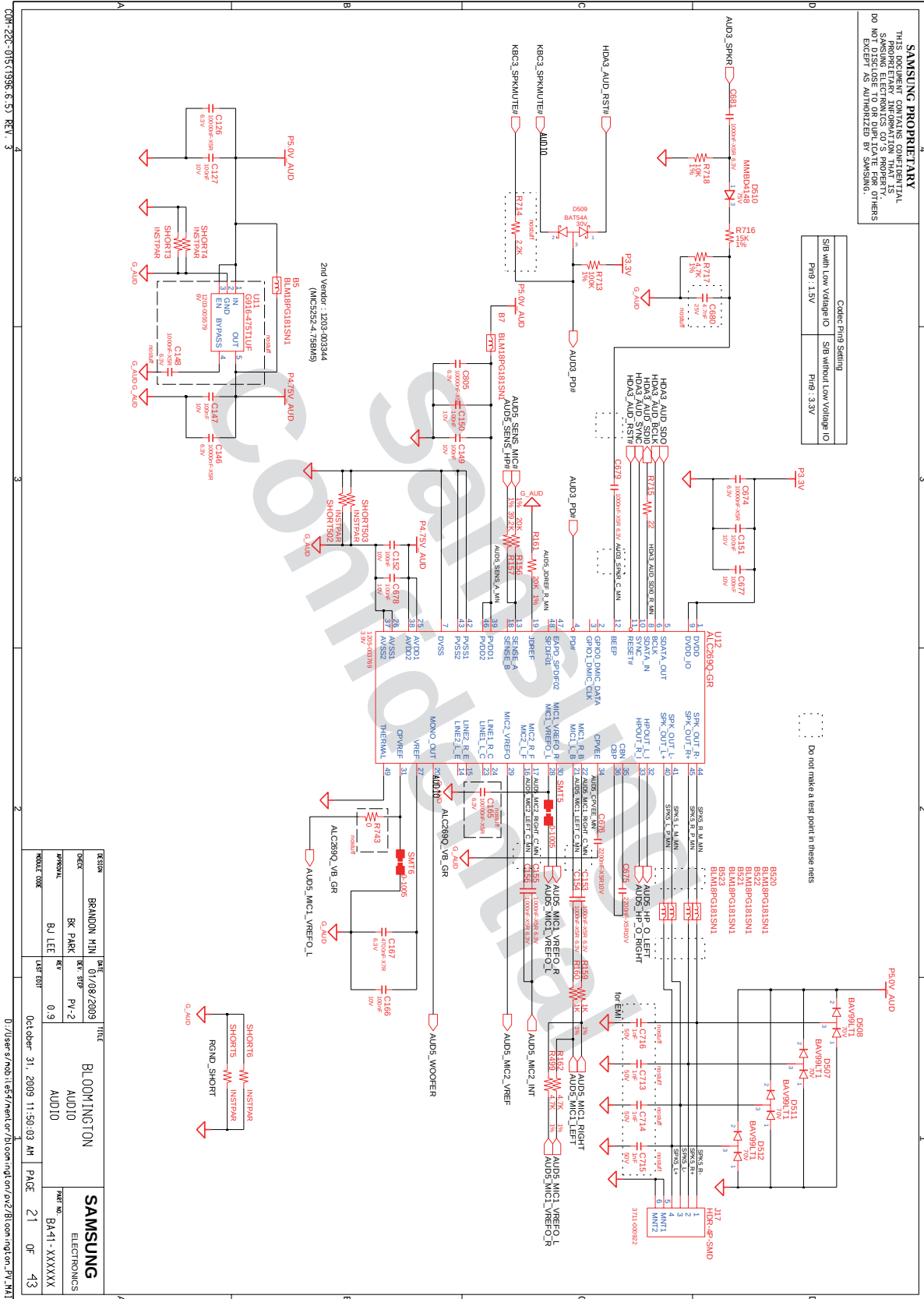
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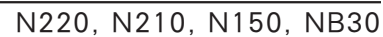
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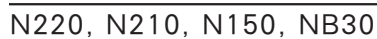
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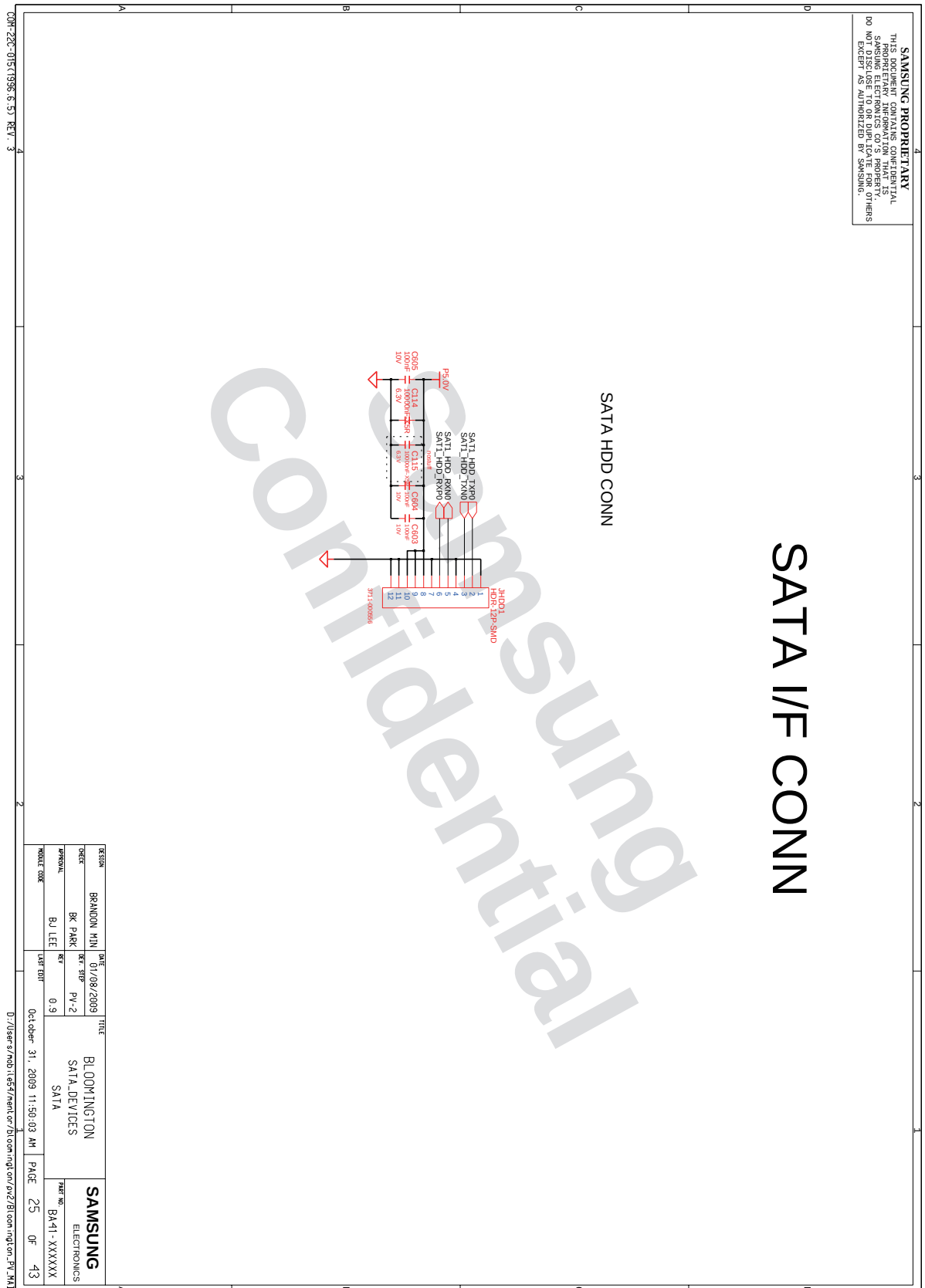


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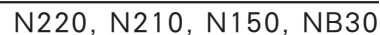


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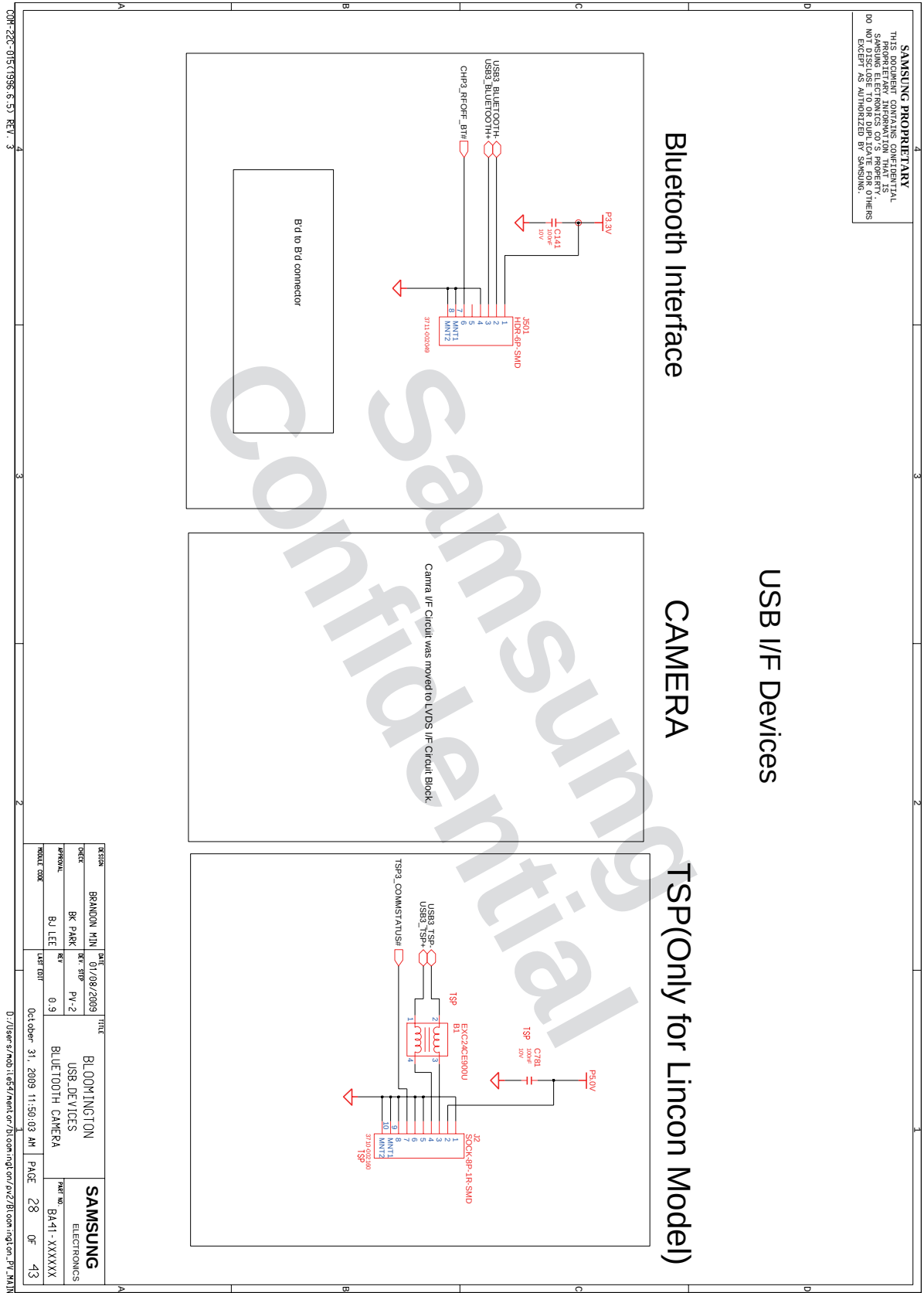
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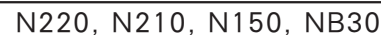
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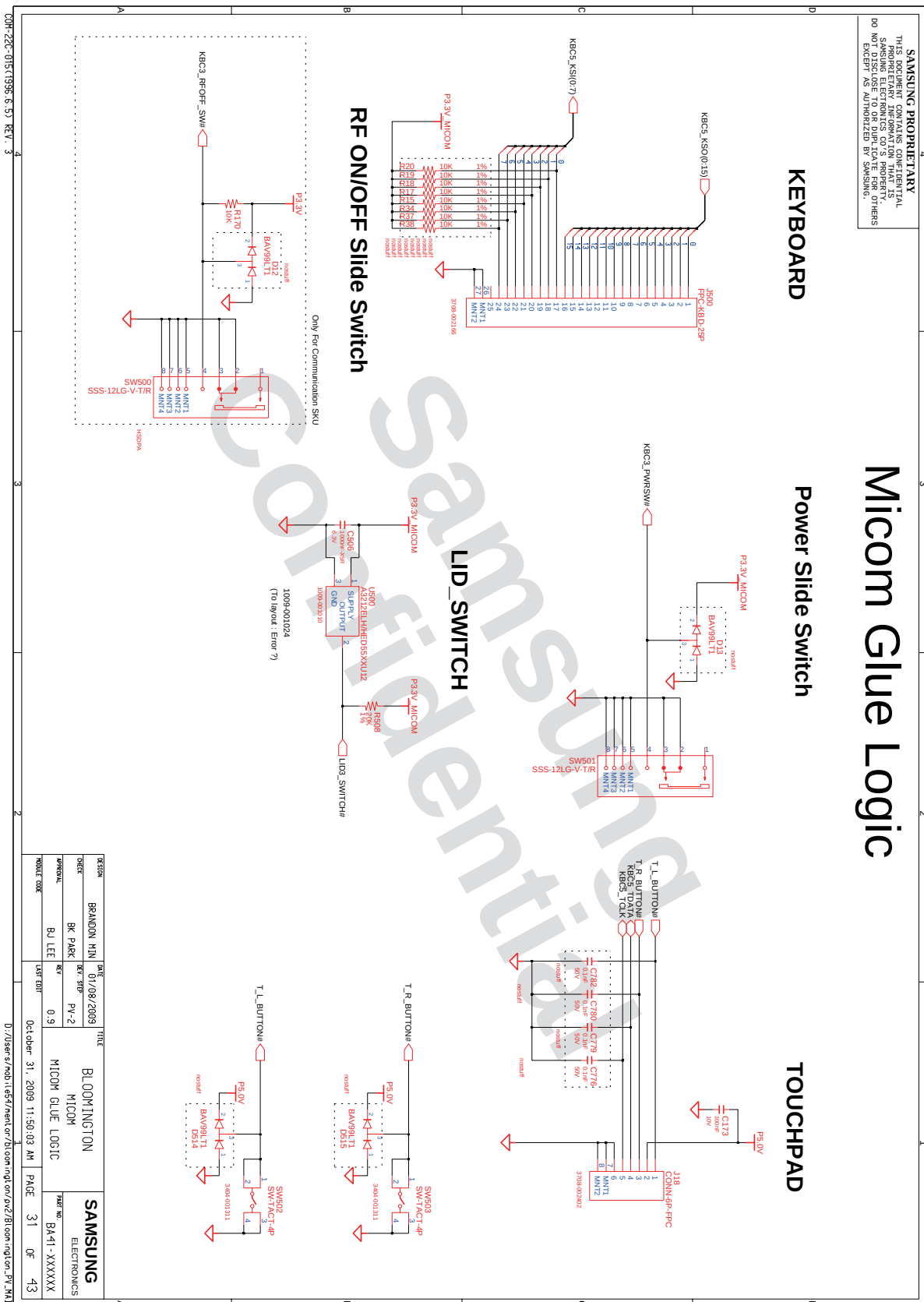


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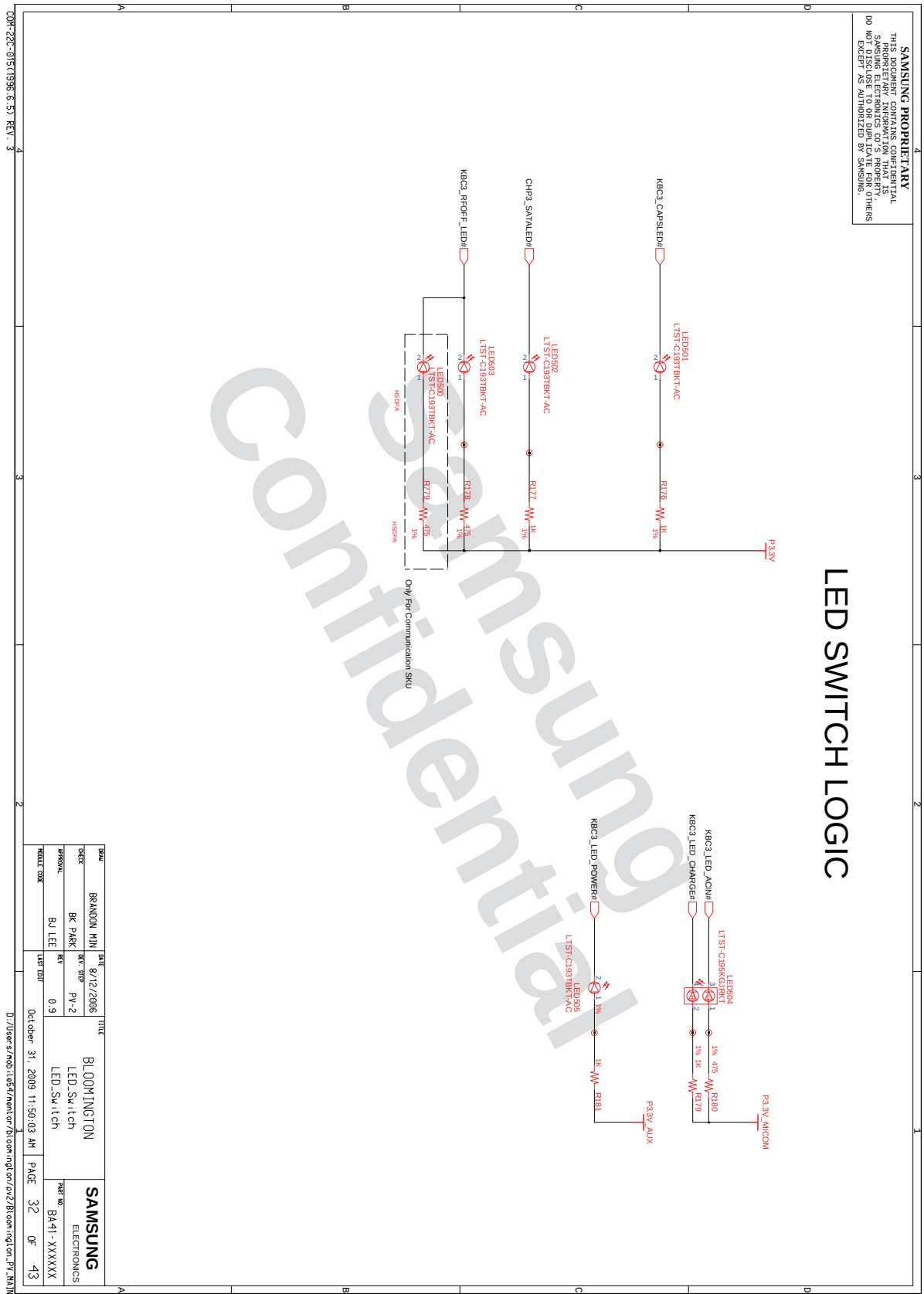


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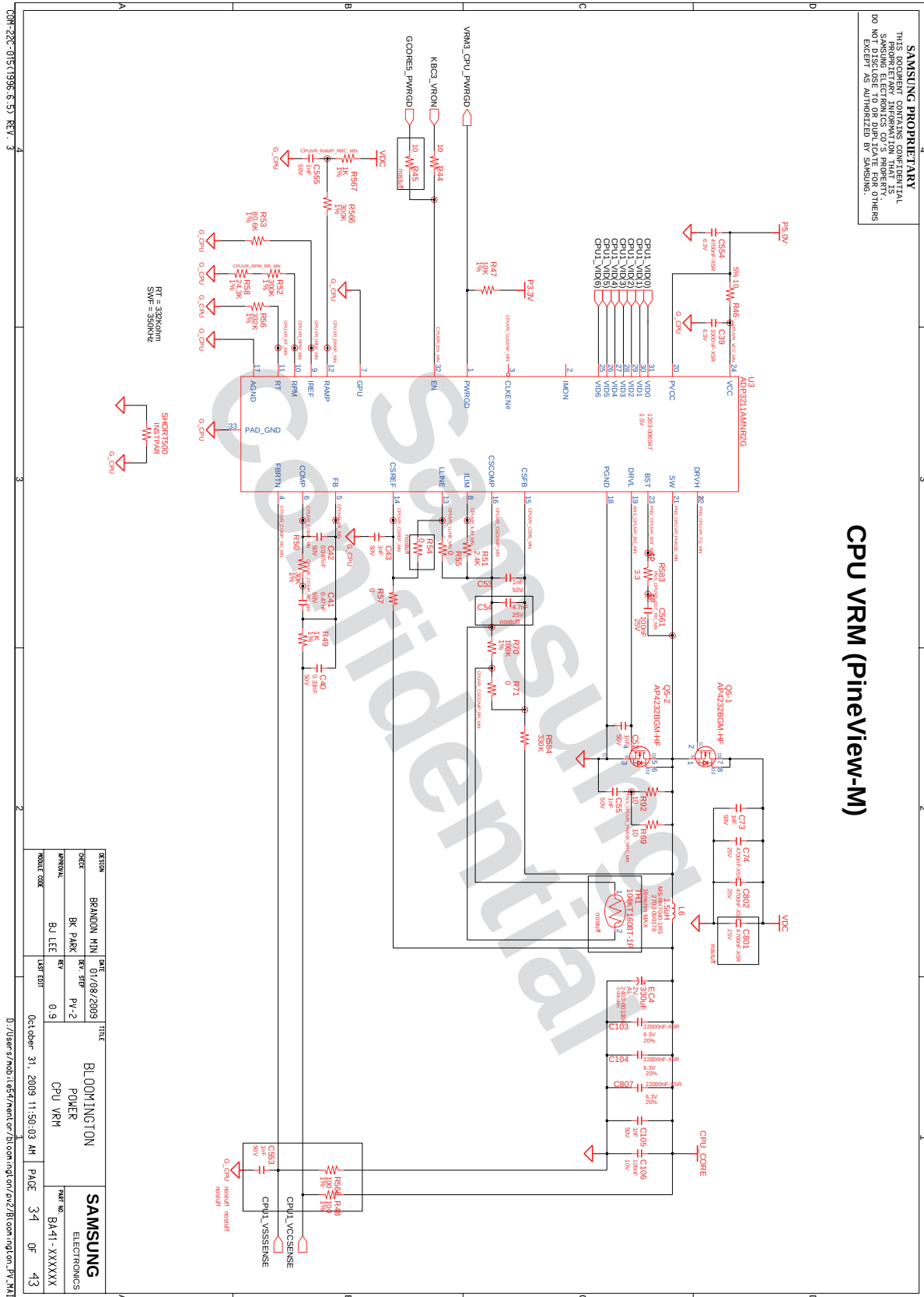
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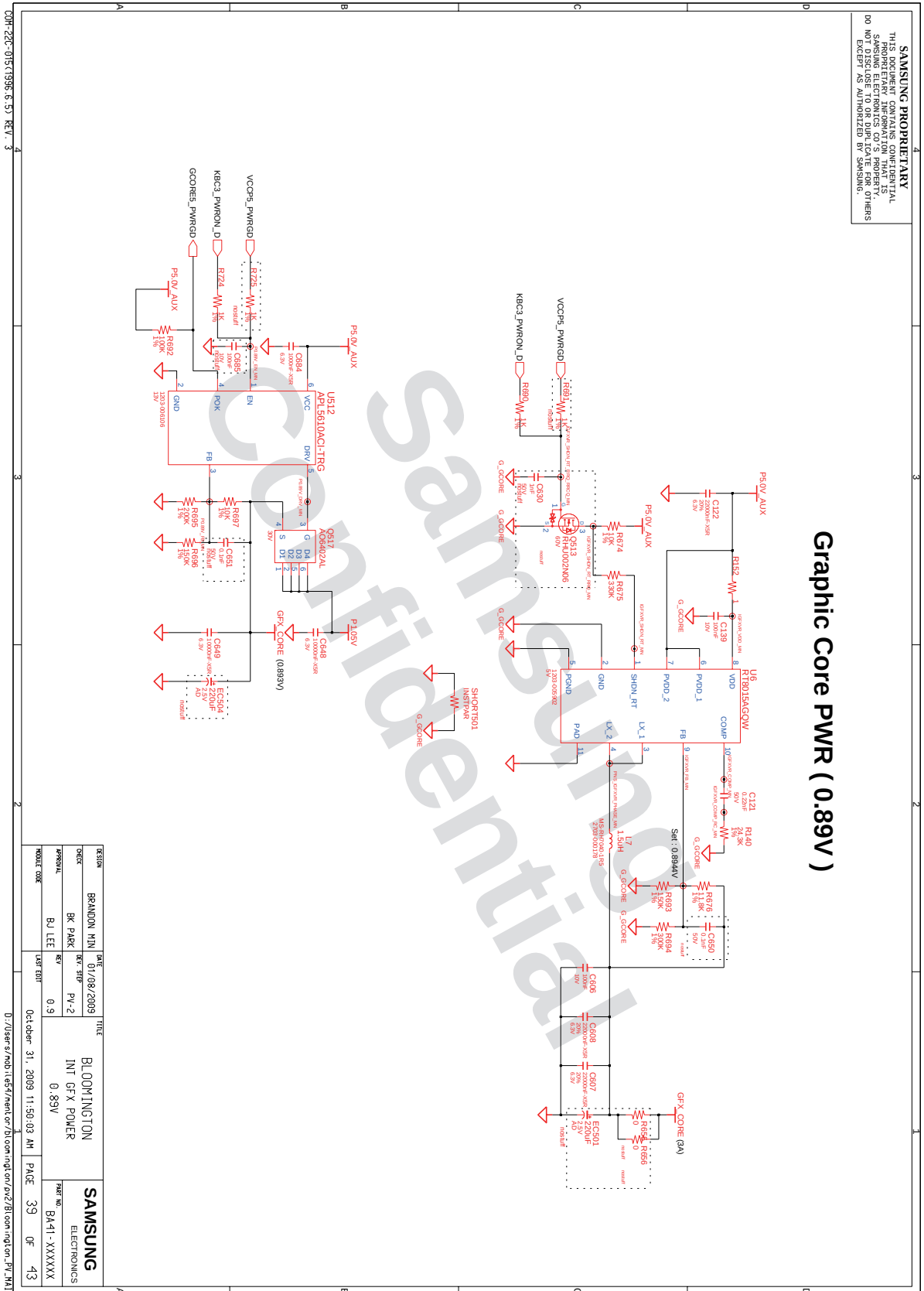
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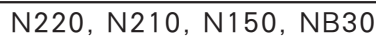
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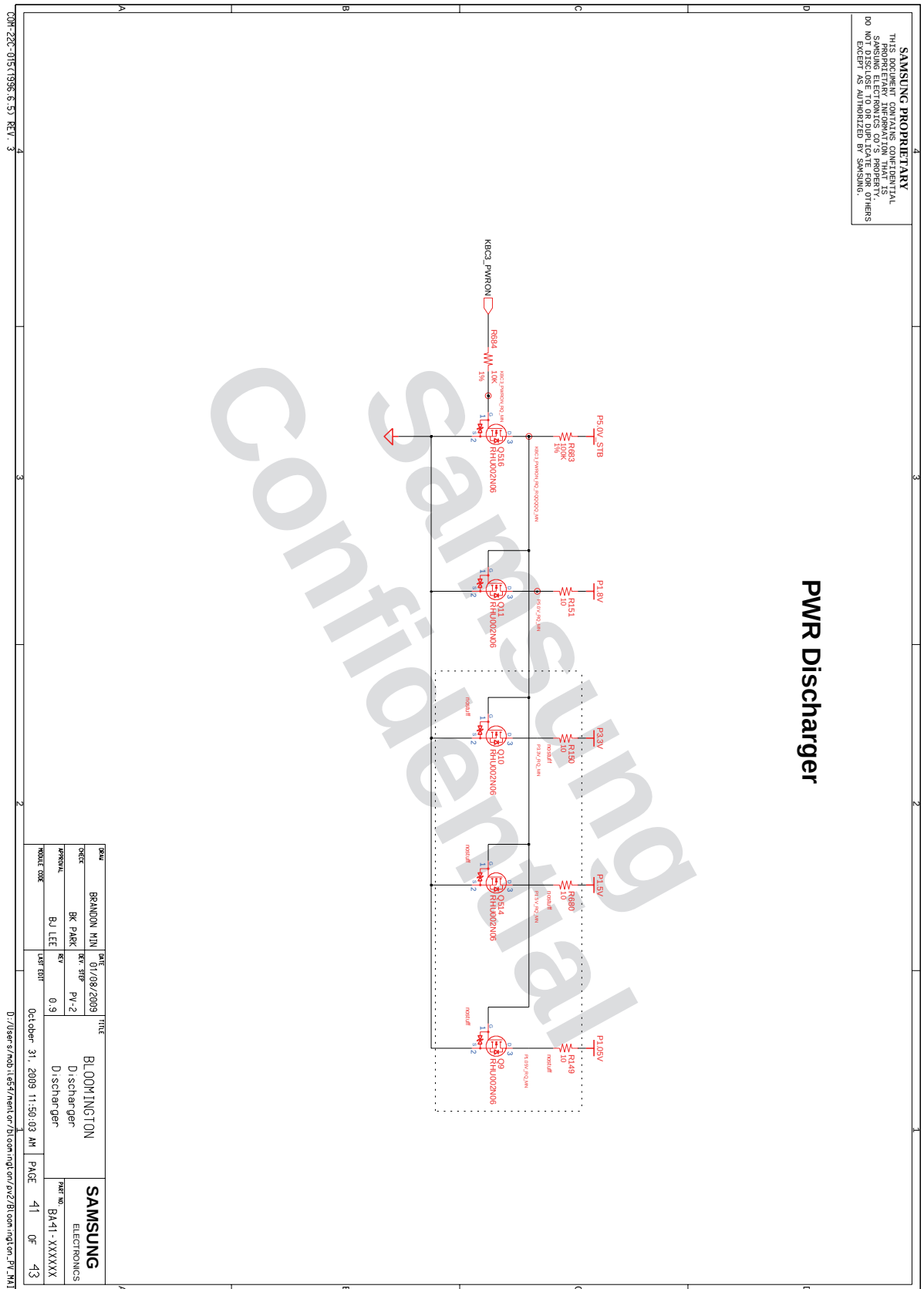


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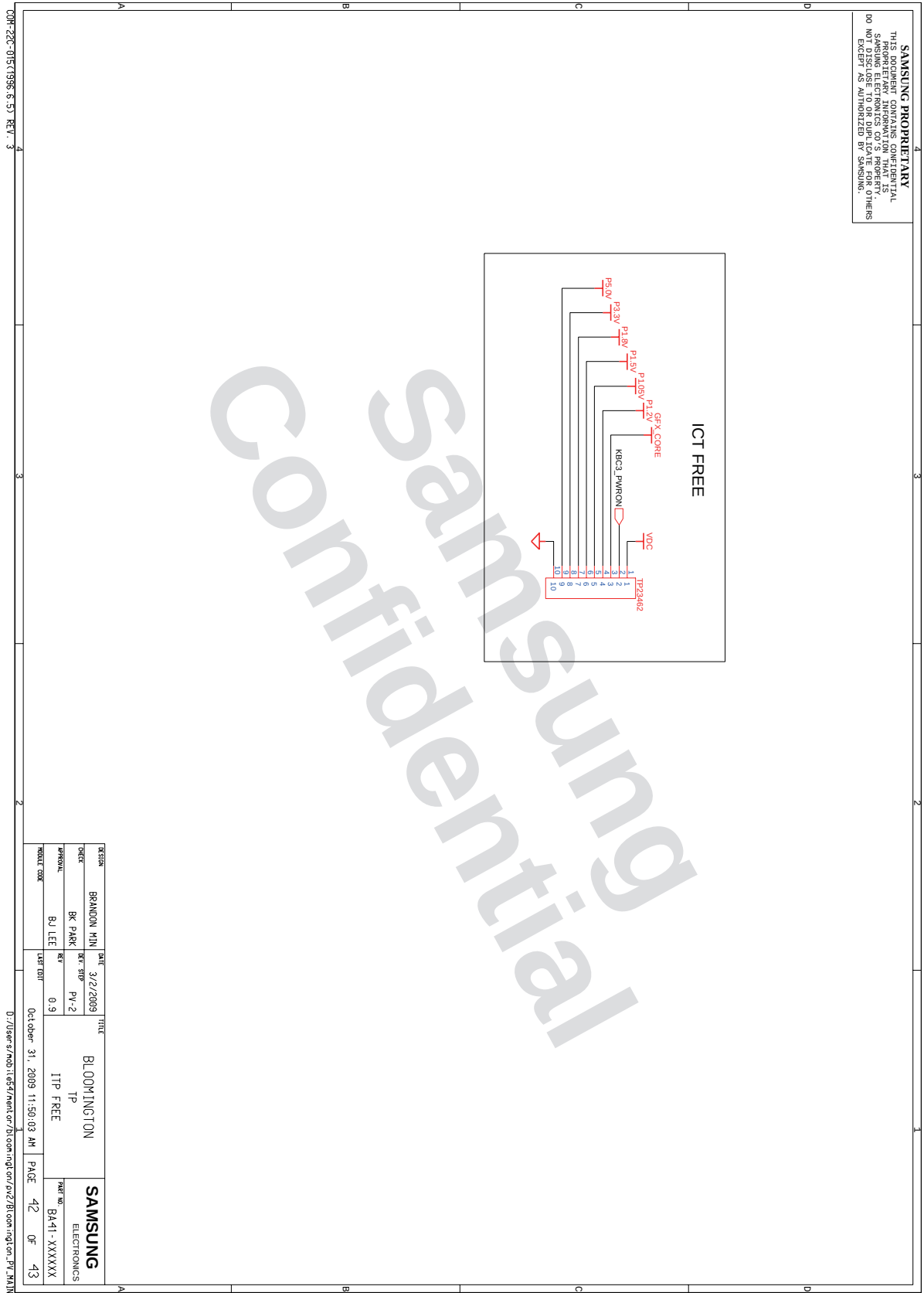


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