

OSLO2

CPU : Intel Penryn-6M (1067/800) MHz
Chip Set : Intel Cantiga & ICH9M
Remarks : Montevina Platform

Model Name : Oslo2_DDR3
PBA Name : MAIN
PCB Code : BA41-00866A
BA41-00867A
Dev. Step : MP
Revision : 1.1
T.R. Date : 2008. 05.15

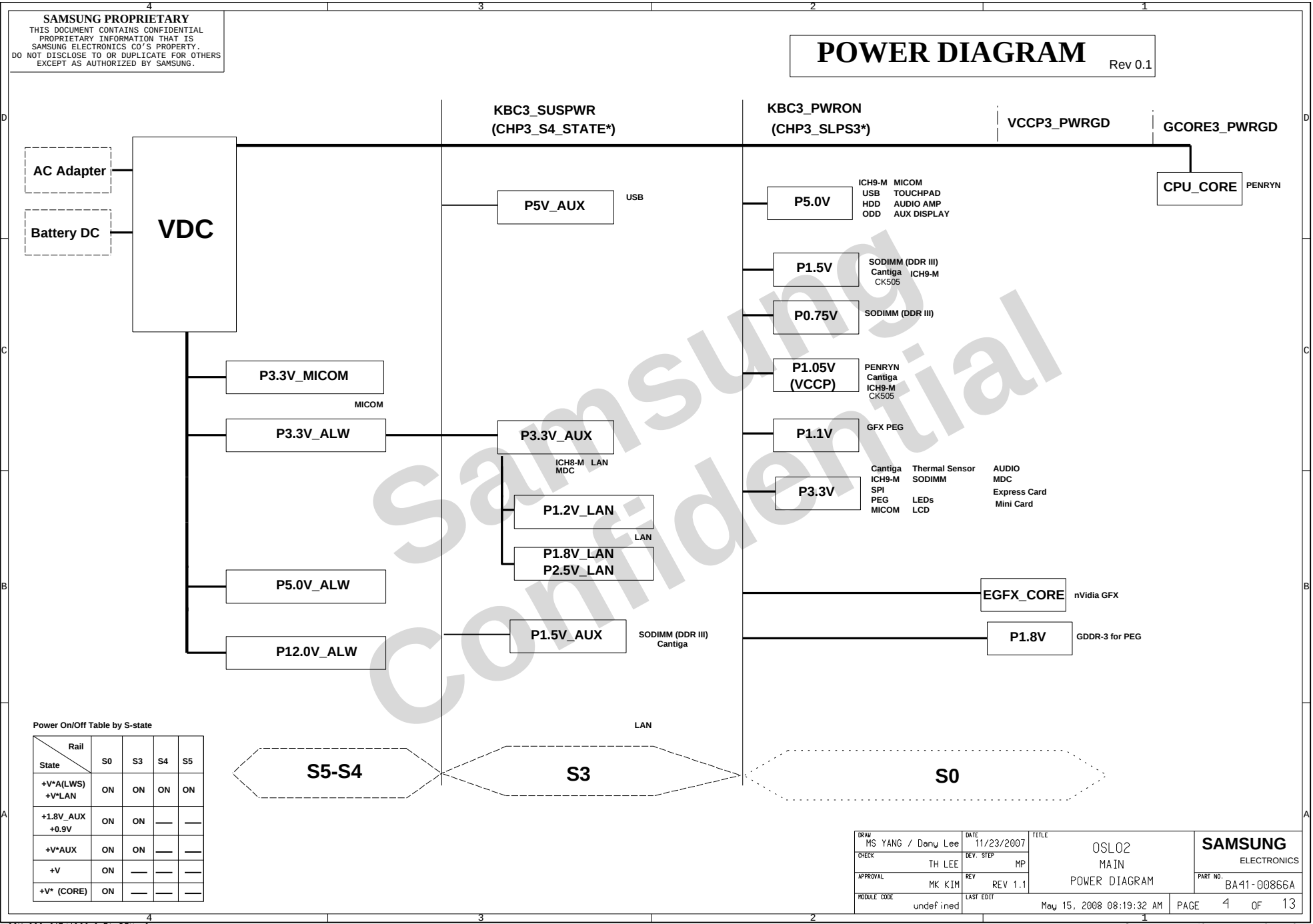
DRAW	CHECK	APPROVAL
MS YANG TH LEE	TH LEE	MK KIM

Owner : SEC Mobile R & D Signature : X

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DRAW	MS YANG / Dany Lee	DATE	11/23/2007	TITLE	OSLO2 MAIN COVER	SAMSUNG ELECTRONICS
CHECK	TH LEE	DEV. STEP	MP			PART NO. BA41-00866A
APPROVAL	MK KIM	REV	REV 1.1			
MODULE CODE		LAST EDIT	May 15, 2008 08:19:32 AM	PAGE	1 OF 13	

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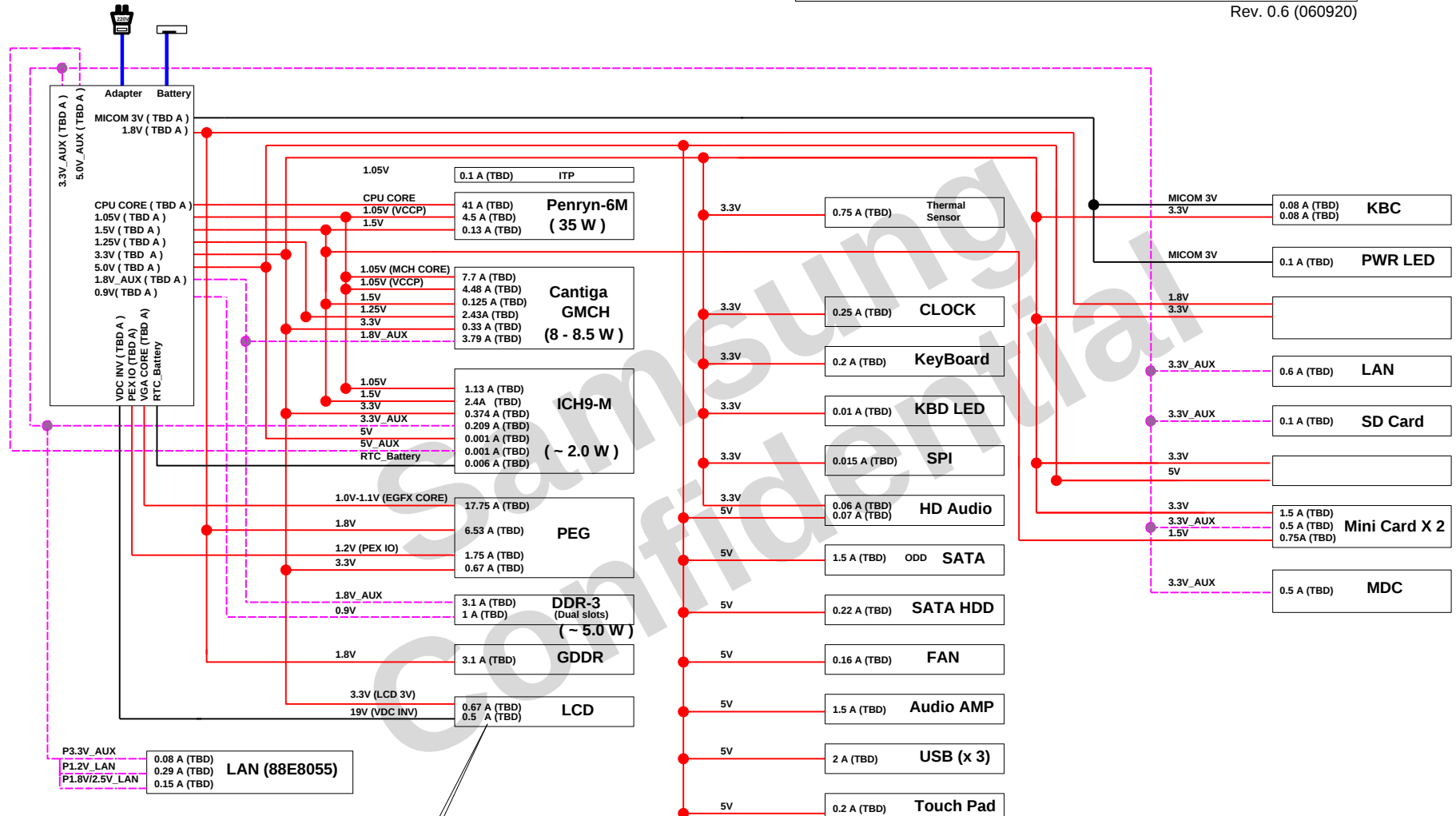


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POWER RAILS ANALYSIS

Rev. 0.6 (060920)

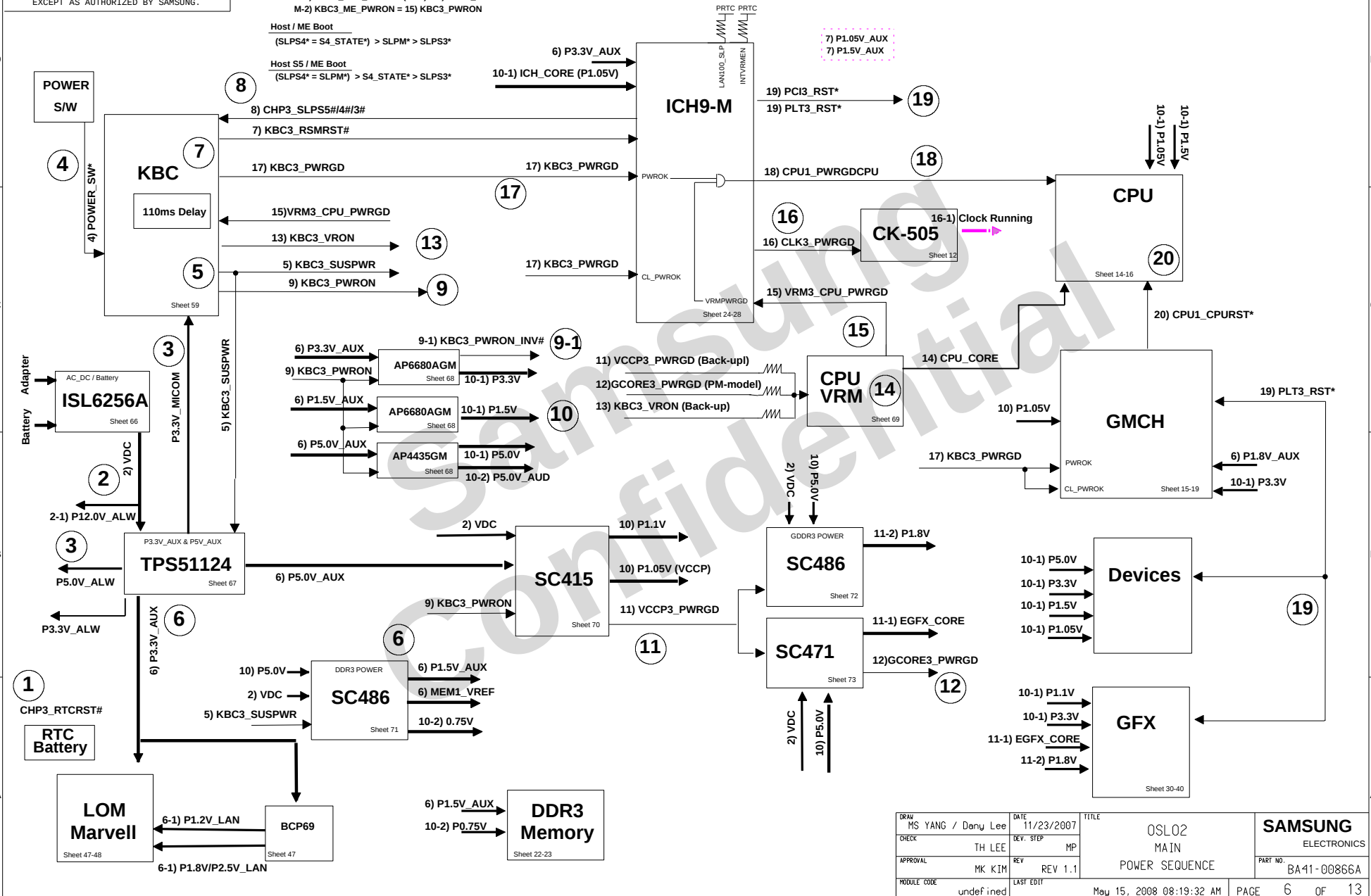


Value by Datasheet/Application notes (Value by measurement)

DRAW MS YANG / Dany Lee	DATE 11/23/2007	TITLE OSLO2 MAIN POWER RAILS	SAMSUNG ELECTRONICS
CHECK TH LEE	DEV. STEP MP		PART NO. BA411-00866A
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MODULE CODE undefined	LAST EDIT	May 15, 2008 08:19:32 AM	PAGE 5 OF 13

POWER SEQUENCE

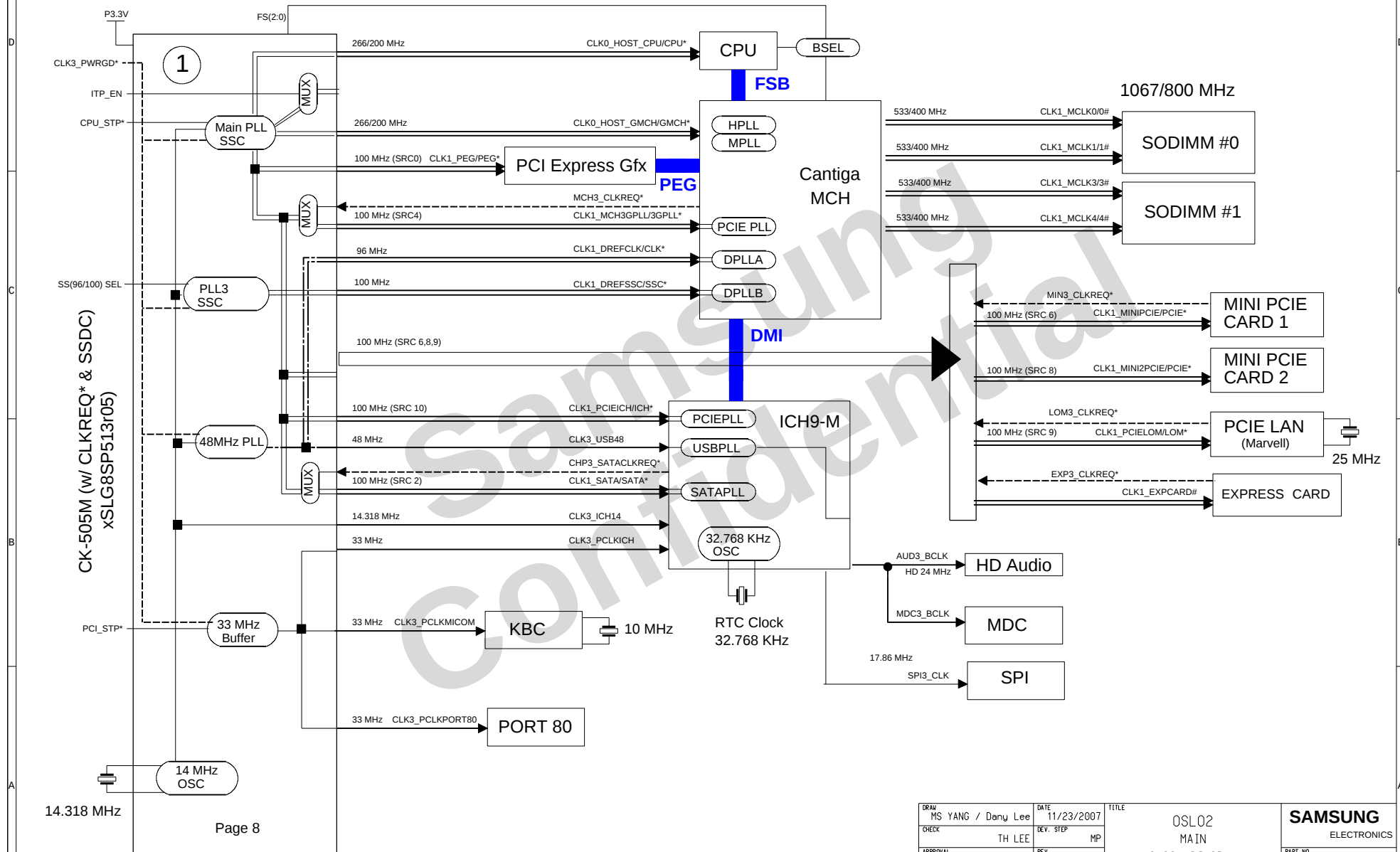
Rev. 0.9



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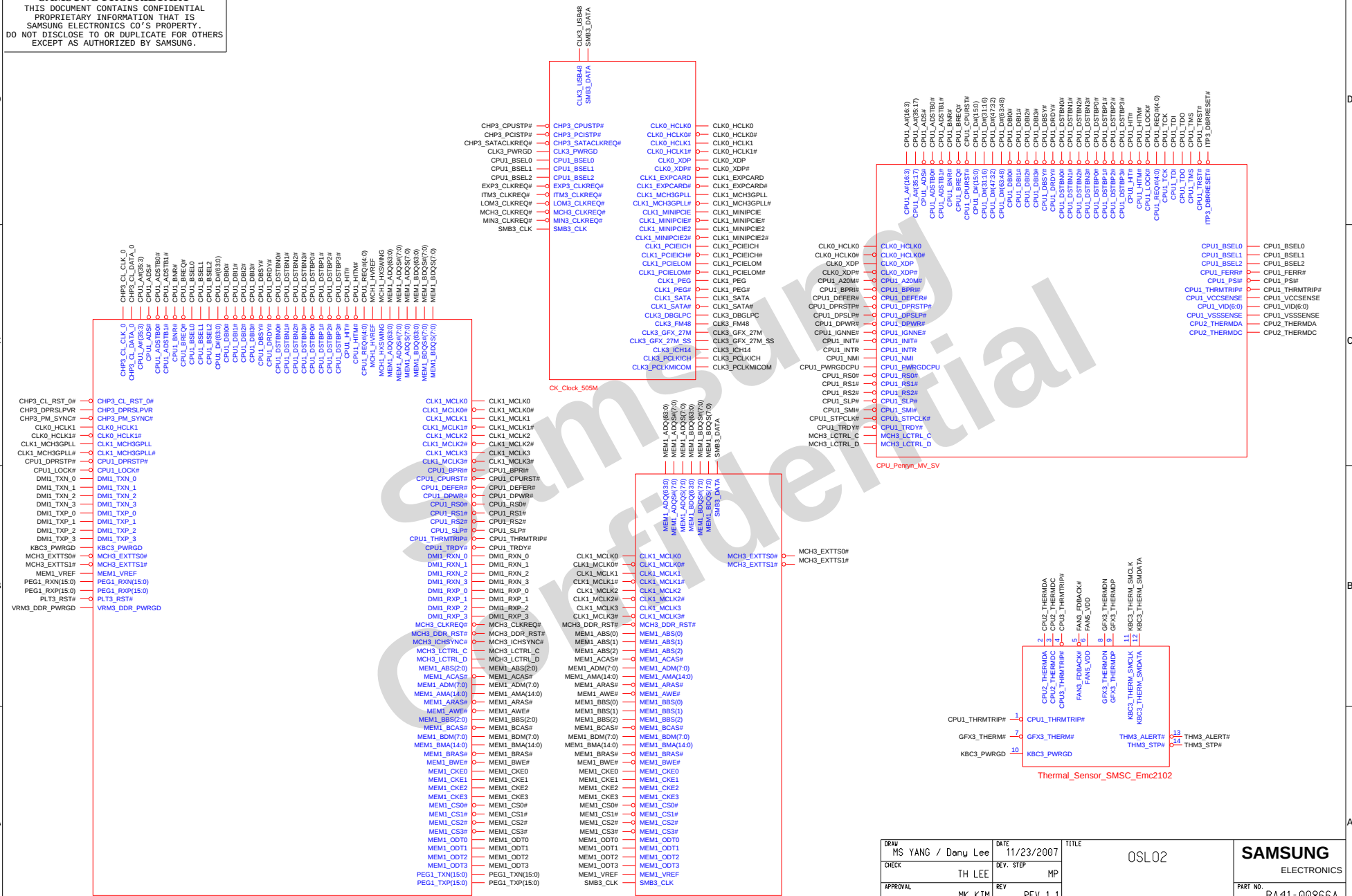
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CLOCK DISTRIBUTION Rev. 0.1



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CHECK	TH LEE	DEV. STEP	MP	MAIN	BA11-00866A	
APPROVAL	MK KIM	REV	REV 1.1	CLOCK DIAGRAM		
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D

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B

A

COM-220-015(1996.6.5) REV. 3

3

2

1

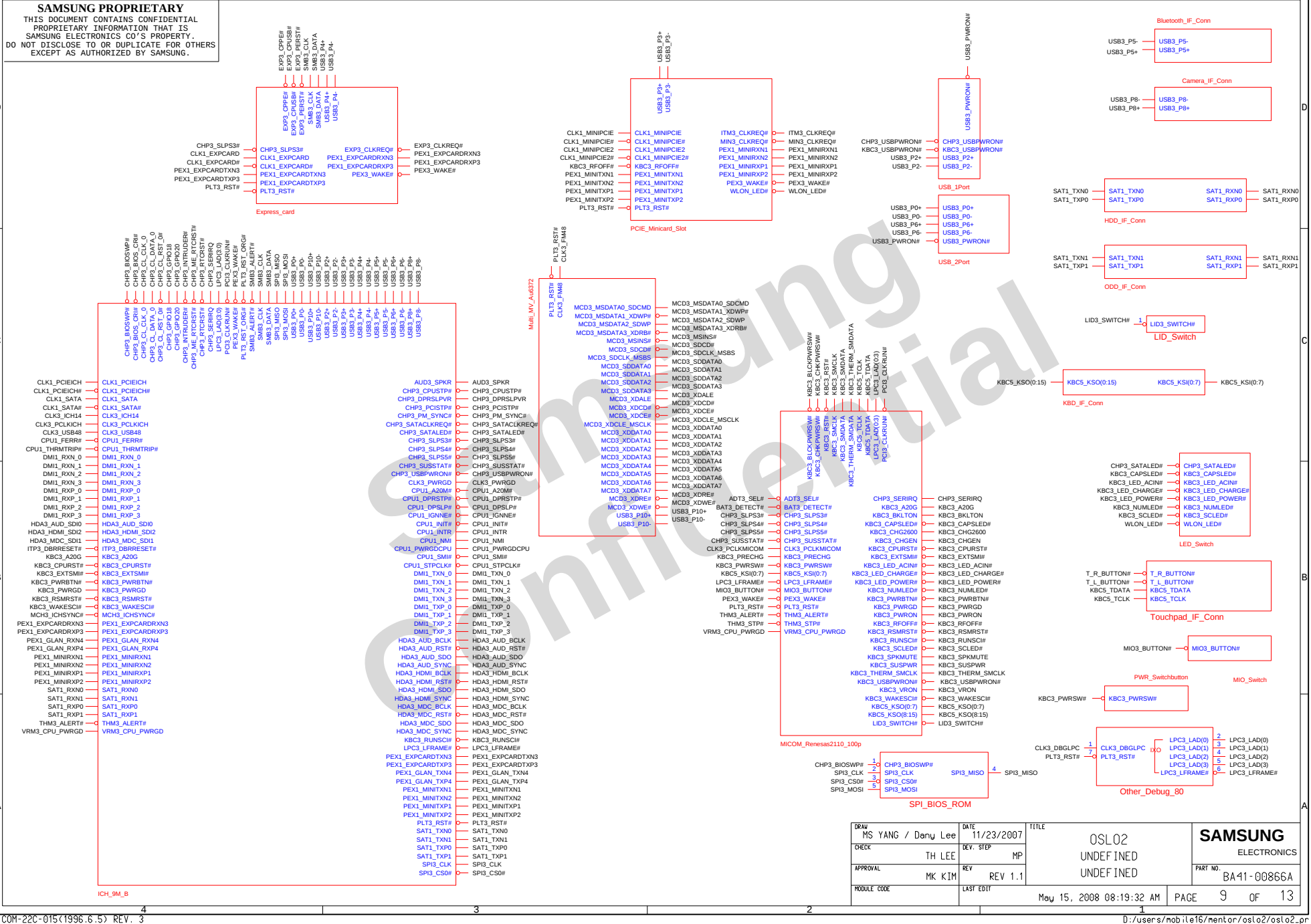
D

C

B

A

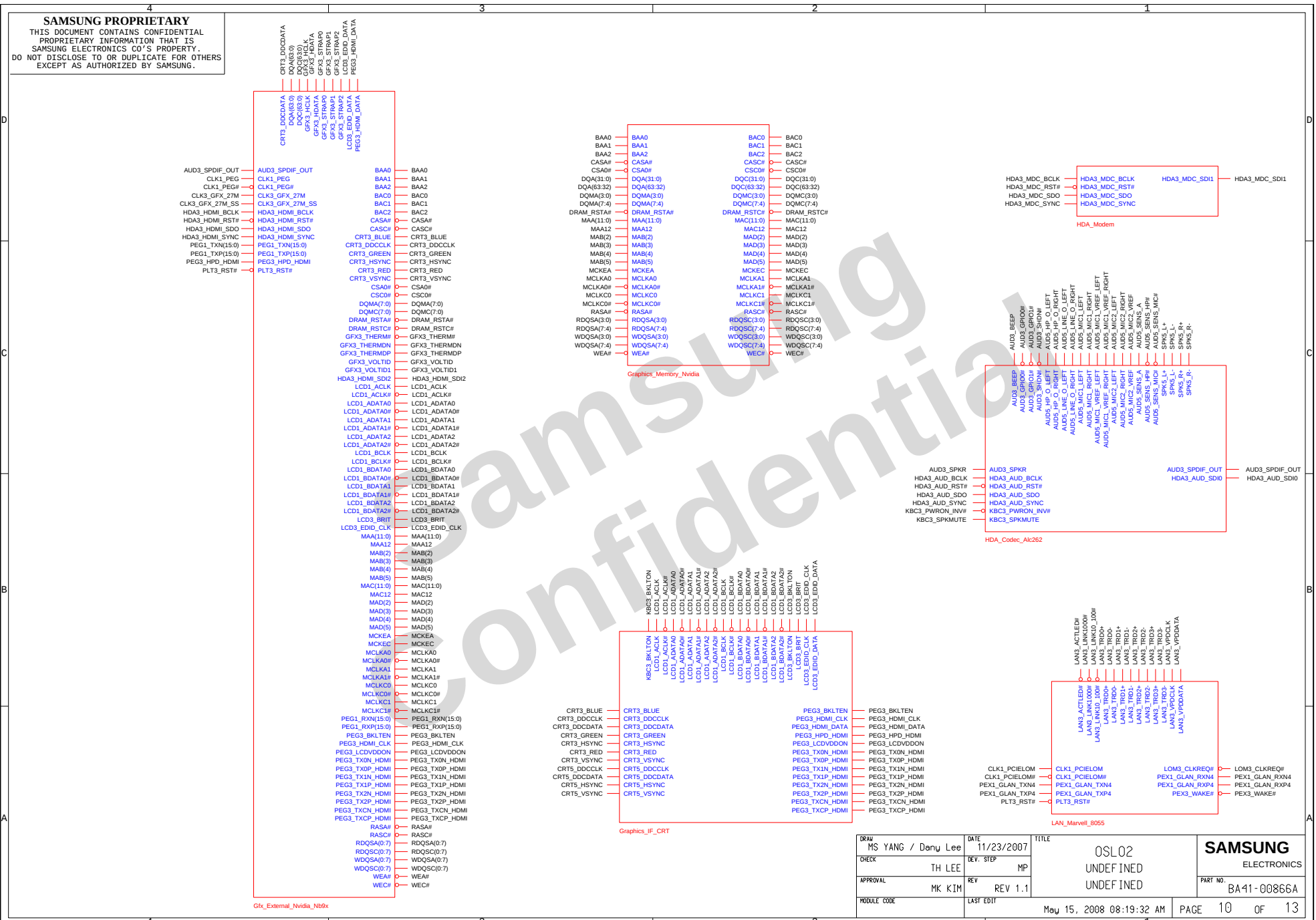
3



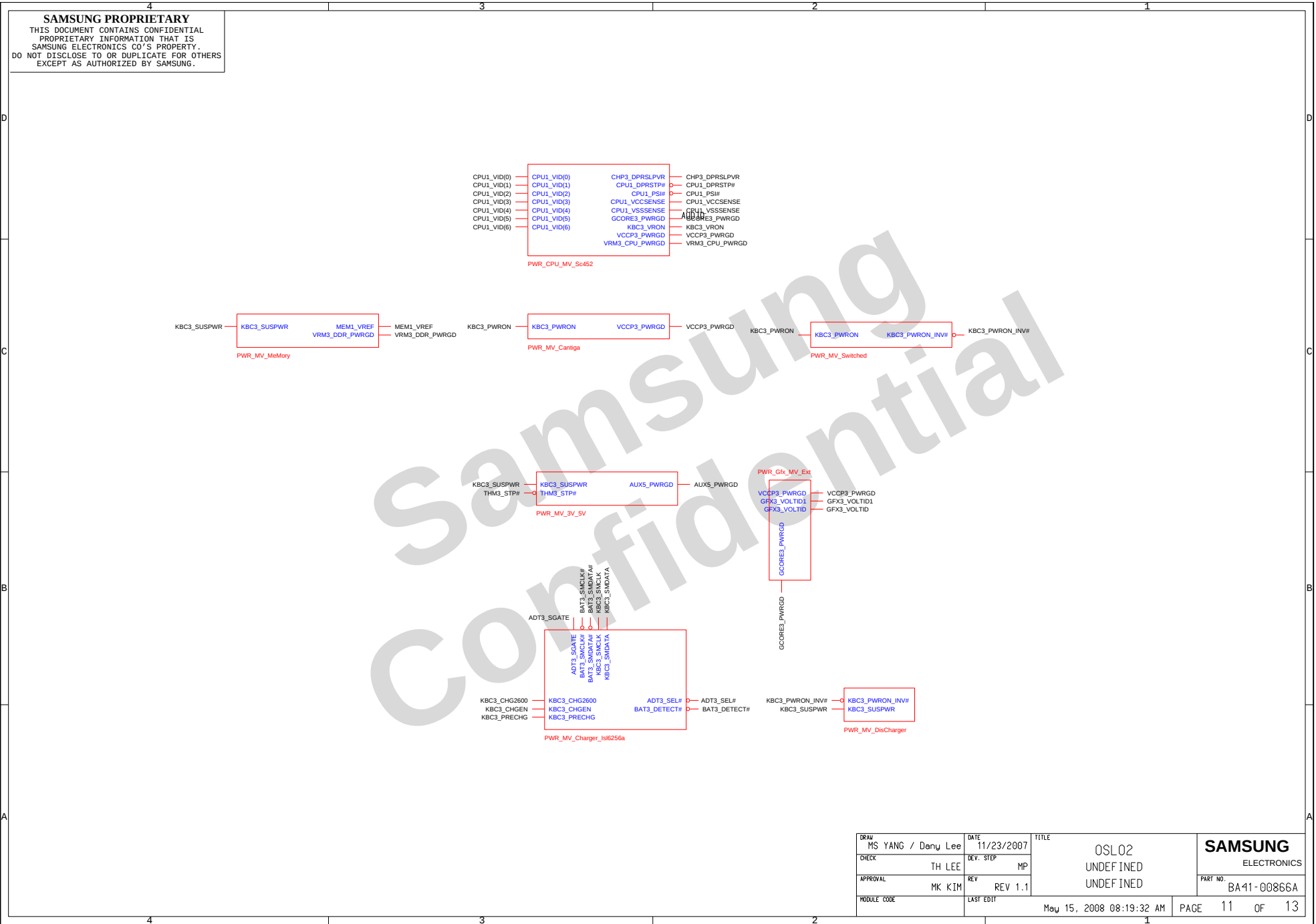
DRW	MS YANG / Dany Lee	DATE	11/23/2007	TITLE	OSLO2	SAMSUNG ELECTRONICS
CHECK	TH LEE	DEV. STEP	MP	UNDEFINED	UNDEFINED	
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MODULE CODE						

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SRP Sheet Number: 9 of 76



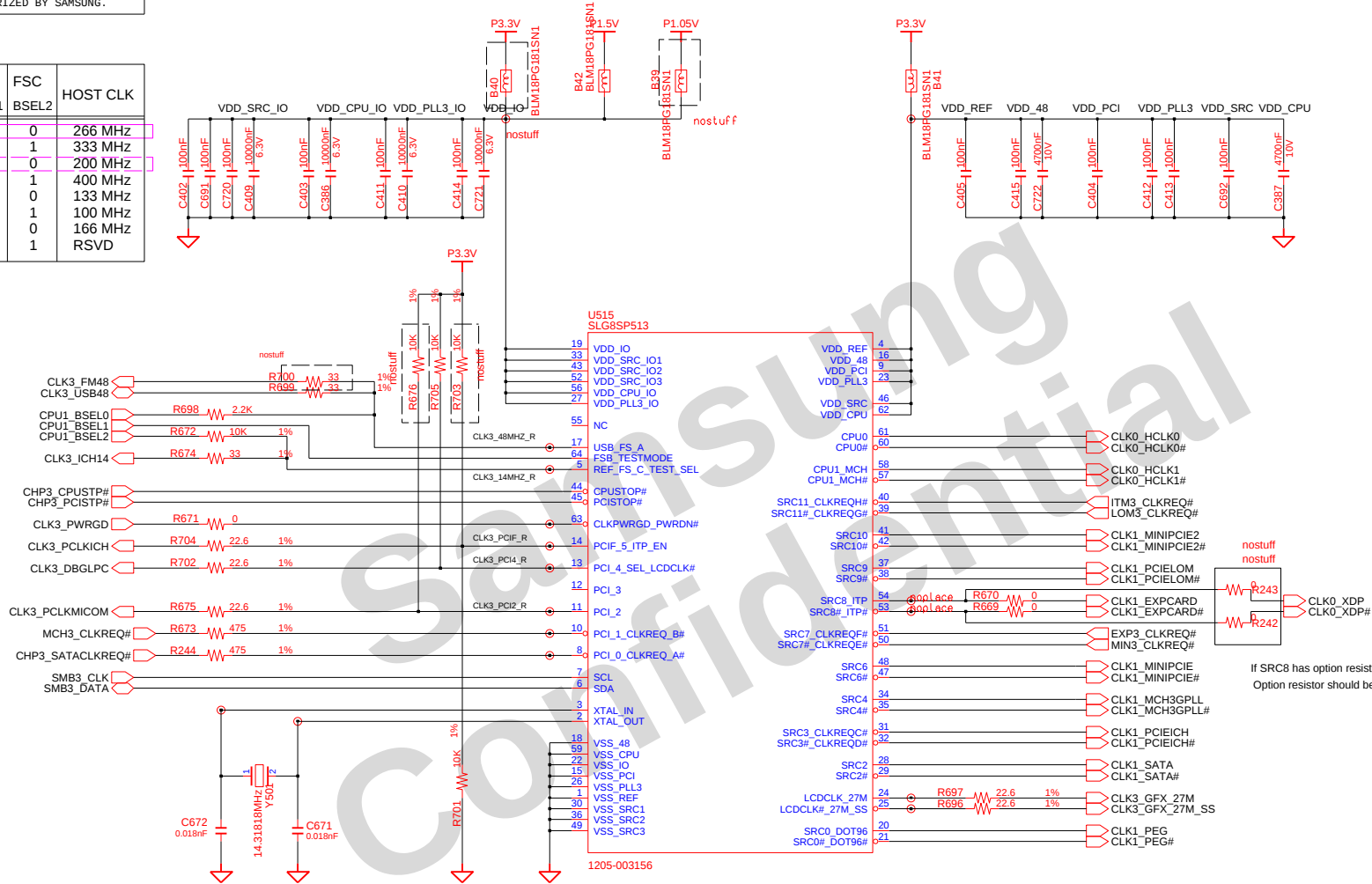
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FSA	FSB	FSC	HOST CLK
BSEL0	BSEL1	BSEL2	
0	0	0	266 MHz
0	0	1	333 MHz
0	1	0	200 MHz
0	1	1	400 MHz
1	0	0	133 MHz
1	0	1	100 MHz
1	1	0	166 MHz
1	1	1	RSVD



CLK REQ	DEVICE	SRC PORT
CLK REQ A	SATACLKREQ#	SRC2
CLK REQ B	MCH3_CLKREQ#	SRC4
CLK REQ E	MIN3_CLKREQ#	SRC6
CLK REQ F	EXP3_CLKREQ#	SRC8
CLK REQ G	LOM3_CLKREQ#	SRC9

SEL_LCDCLK*	Pin 20/21	Pin 24/25
LOW	DOT_96/DOT_96#	PEG_CLK/PEG_CLK#
HIGH	SRC_0/SRC_0#	27M & 27M_SS

Place 14.318MHz within
 500mils of CK-505

2nd source IDT, 1205-003159

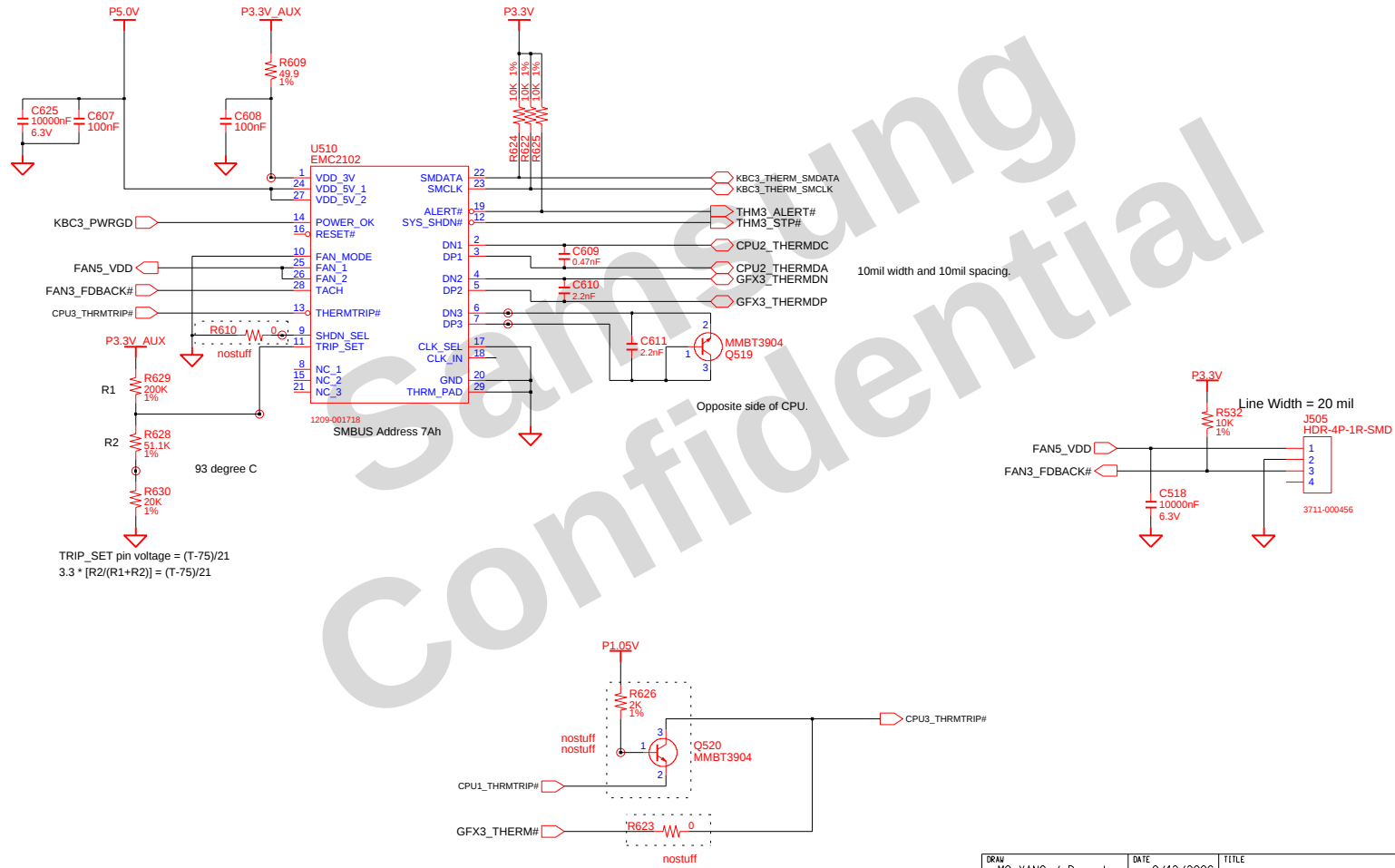
This part is 64pin QFN package.

DRAW MS YANG / Dany Lee	DATE 8/12/2006	TITLE OSL02 Main_Clock_Circuit CK-Clock_505M	SAMSUNG ELECTRONICS PART NO. BA11-00866A
CHECK TH LEE	DEV. STEP MP	REV 1.1	
APPROVAL MK KIM	REV 1.1	LAST EDIT May 15, 2008 08:19:32 AM	PAGE 1 OF 1
MODULE CODE undefined			

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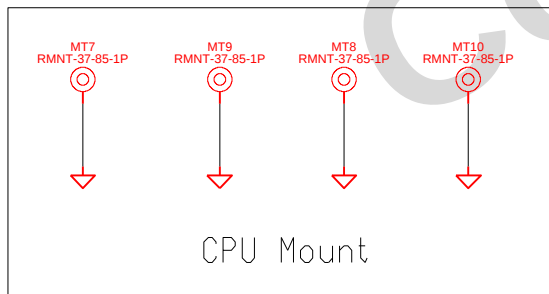
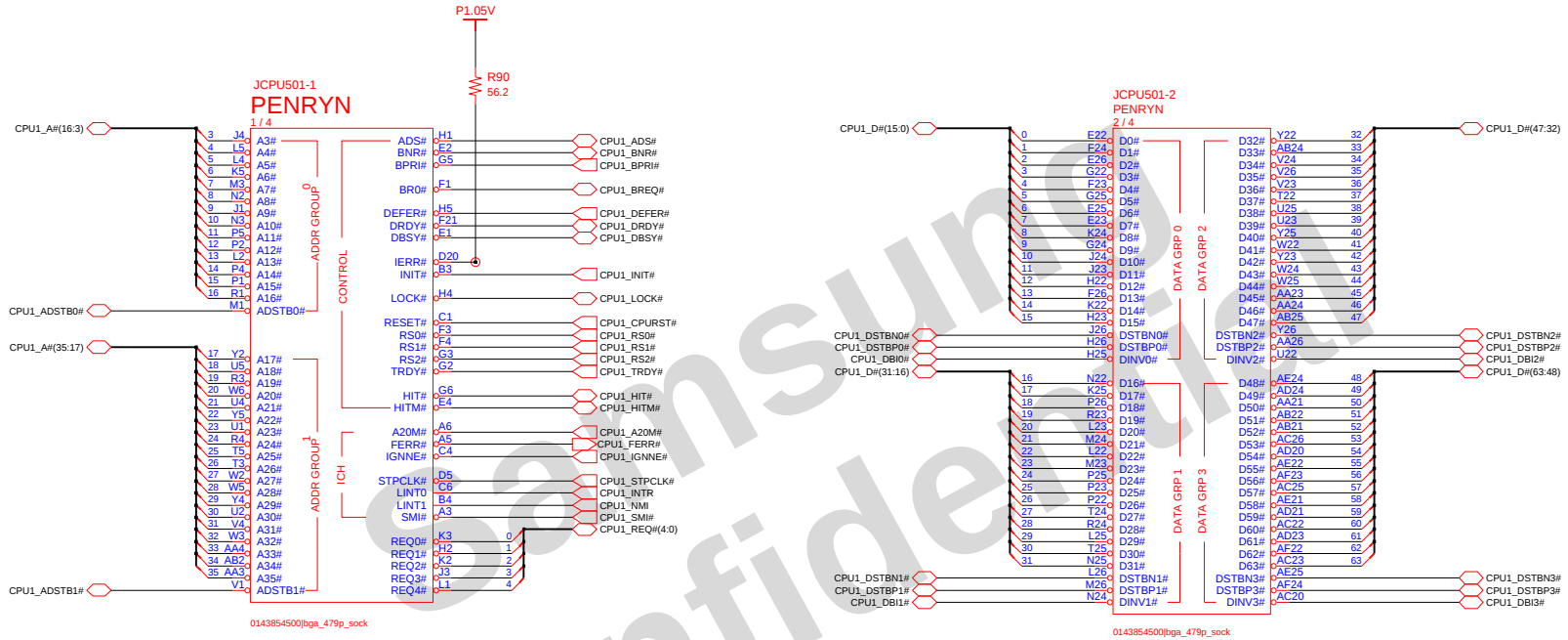
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THERMAL SENSOR & FAN CONTROL



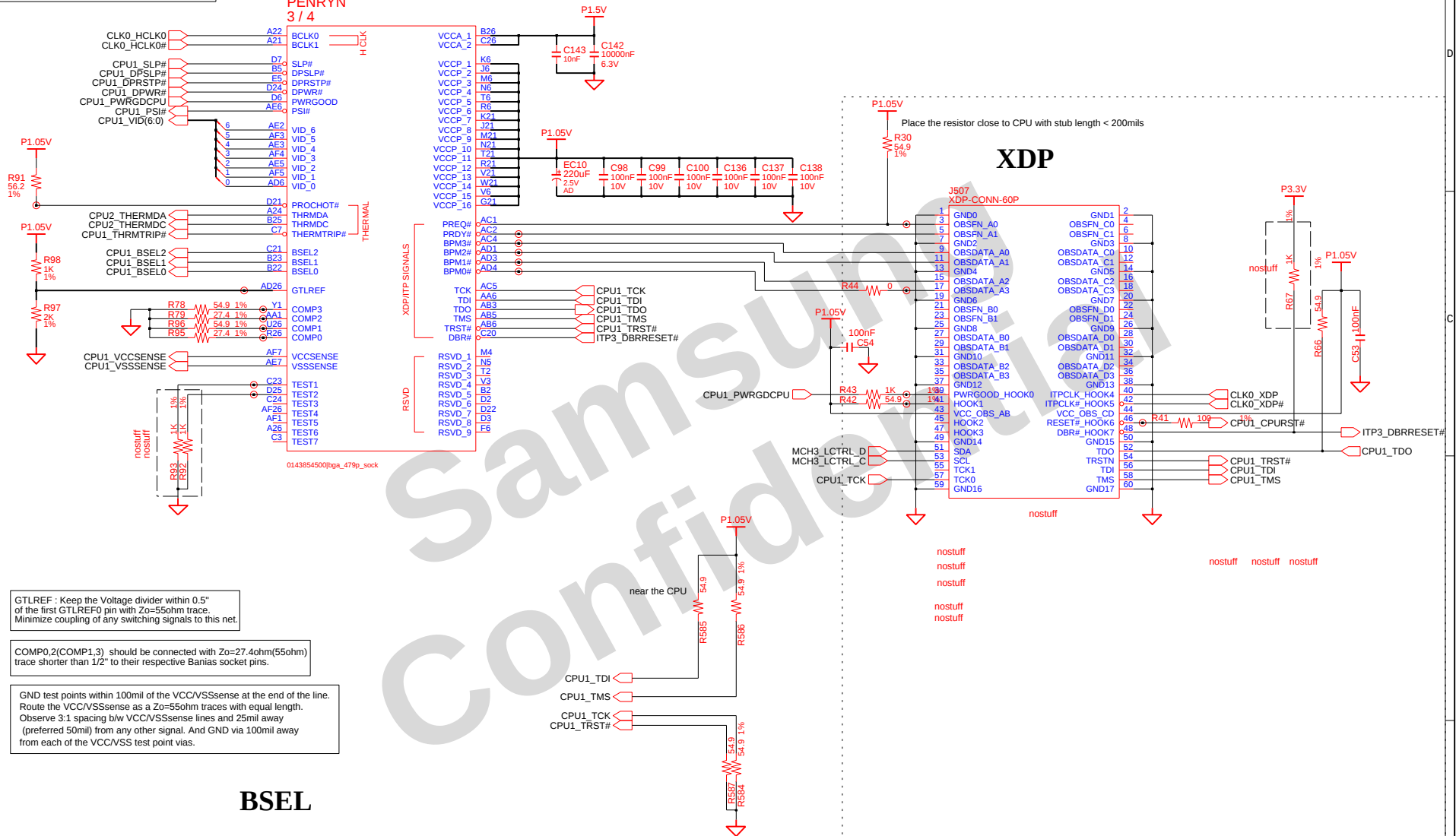
DRW	MS YANG / Dany Lee	DATE	8/12/2006	TITLE	OSL02	SAMSUNG ELECTRONICS
CHECK	TH LEE	DEV. STEP	MP	thermal_Sensor_SMSC_Emc2102	PART NO.	
APPROVAL	MK KIM	REV	REV 1.1	thermal_Sensor_SMSC_Emc2102	BA41-00866A	
MODULE CODE		LAST EDIT				
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479P CPU socket is 3704-001153



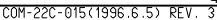
DRAW	MS YANG / Dany Lee	DATE	11/8/2006	TITLE	OSL02 CPU	SAMSUNG ELECTRONICS PART NO. BA11-00866A
CHECK	TH LEE	DEV. STEP	MP		PENRYN (1/3)	
APPROVAL	MK KIM	REV	REV 1.1			
MODULE CODE	undefined	LAST EDIT	May 15, 2008 08:19:32 AM	PAGE	1 OF 3	

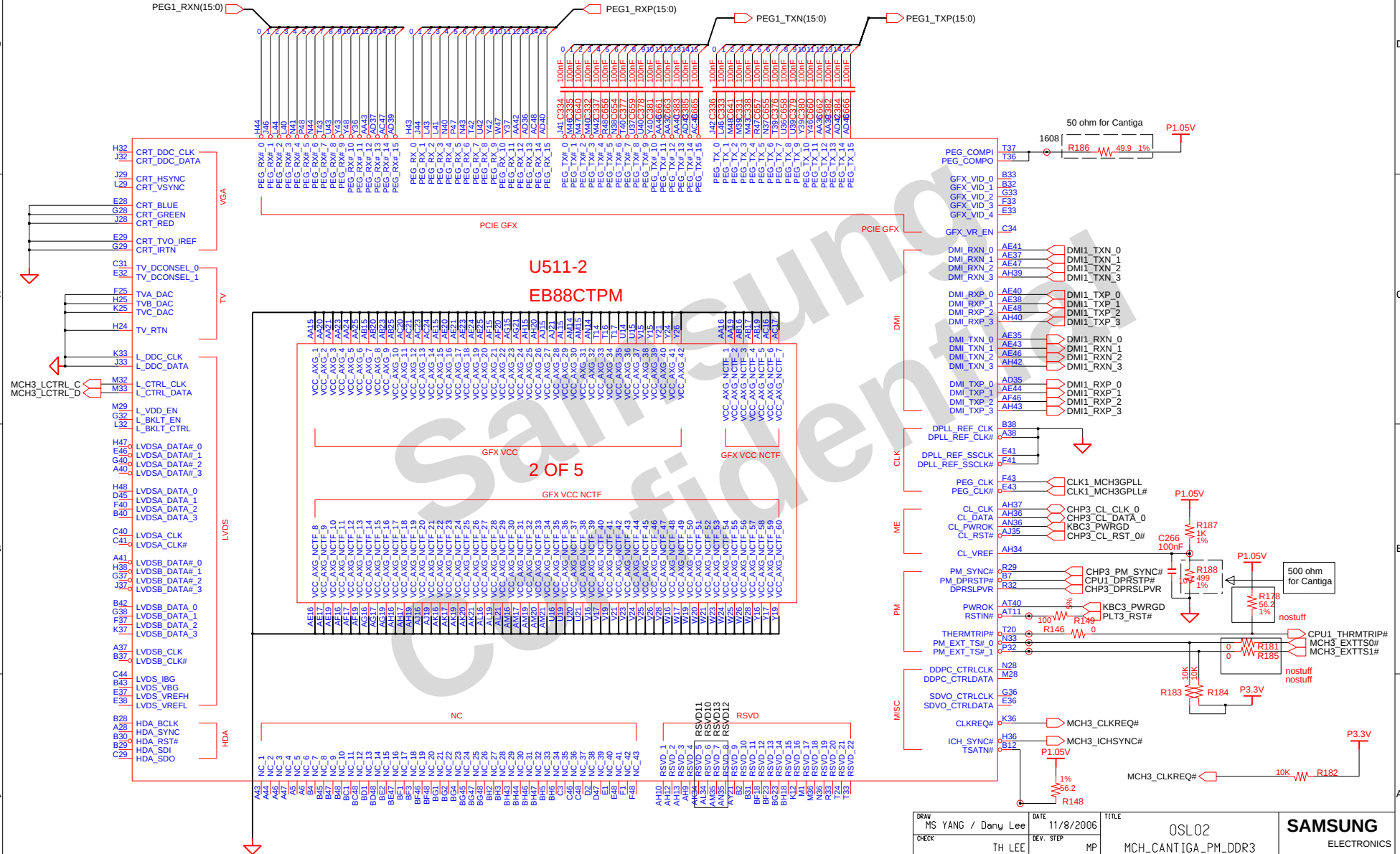
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	Pull-up	Pull-down
FSB 1067 MHz FSB 800 MHz	BSEL1	BSEL0, BSEL1, BSEL2 BSEL0, BSEL2

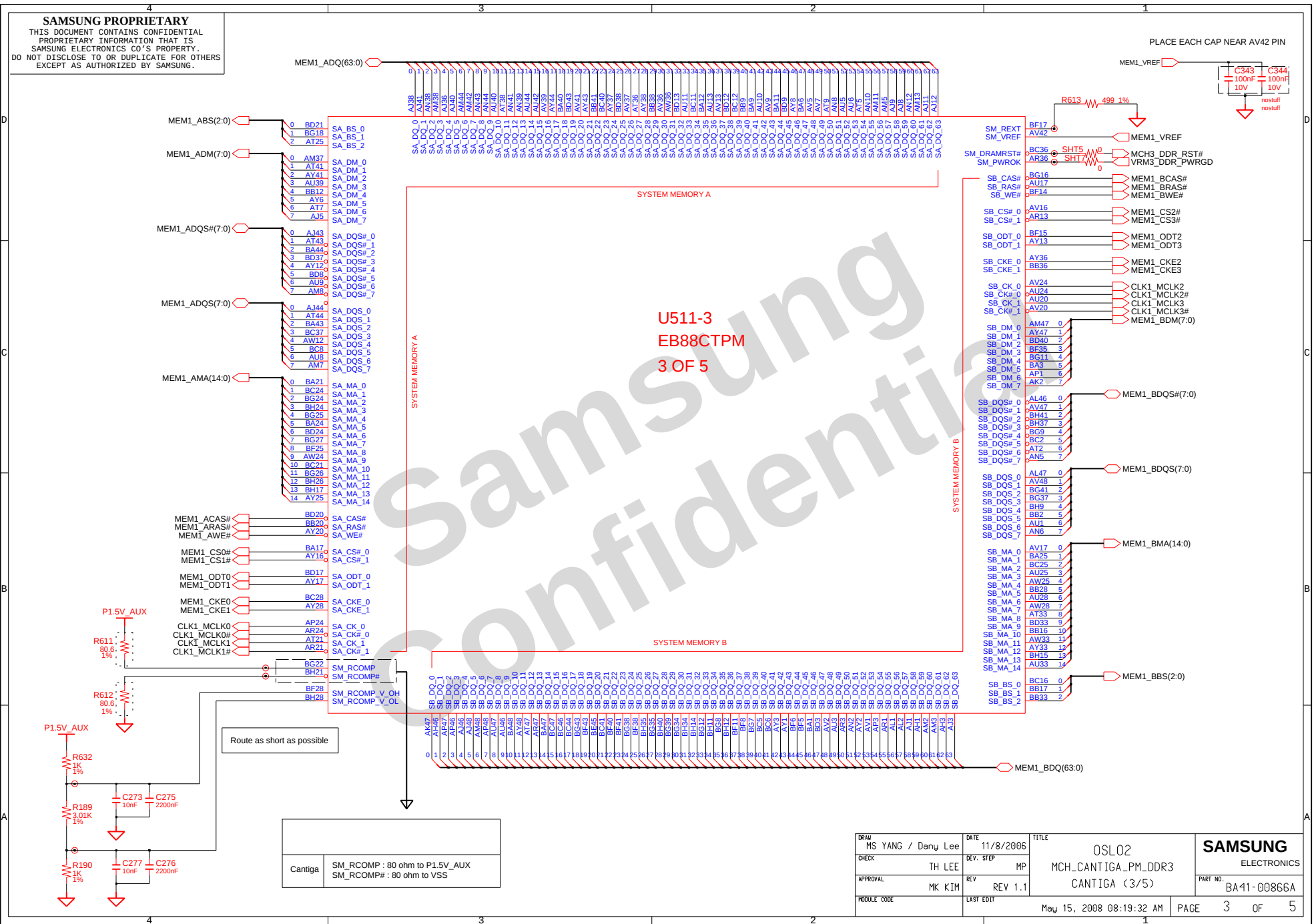
DRAW MS YANG / Dany Lee		DATE 11/8/2006		TITLE OSLO2 CPU PENRYN (2/3)			
CHECK TH LEE		REV. STEP MP					
APPROVAL MK KIM		REV REV 1.1					
MODULE CODE undefined		LAST EDIT May 15, 2008 08:19:32 AM					
				PAGE		2 OF 3	





2 x 330 uF : GFX VR side

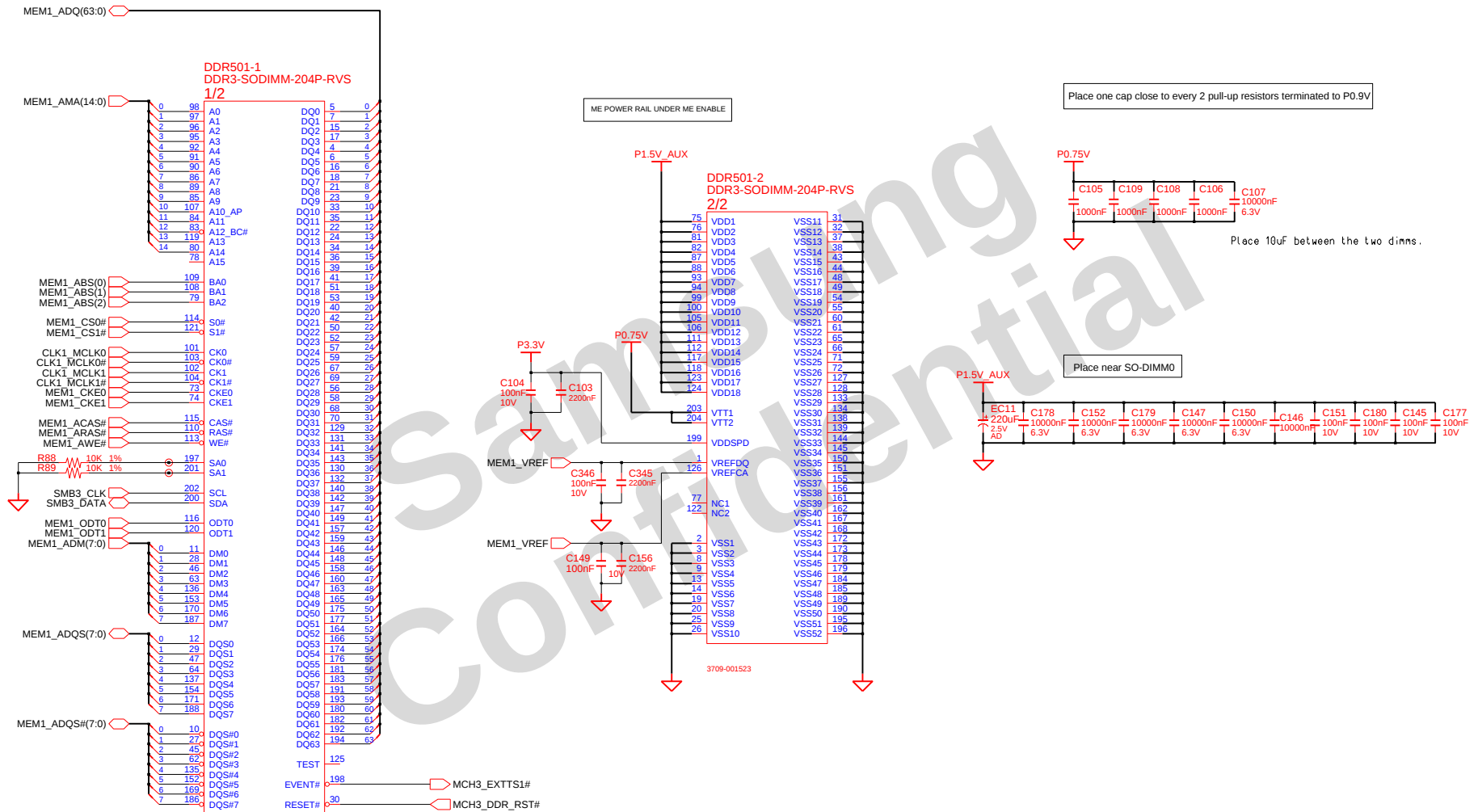
DRAW	MS YANG / Dany Lee	DATE	11/8/2006	TITLE	OSLO2	SAMSUNG ELECTRONICS PART NO. BA11-00866A
CHECK	TH LEE	DEV. STEP	MP	MCH_CANTIGA_PM_DDR3		
APPROVAL	MK KIM	REV	1.1	CANTIGA (2/5)		
MODULE CODE	undefined	LAST EDIT	May 15, 2008 08:19:32 AM	PAGE	2 OF 5	



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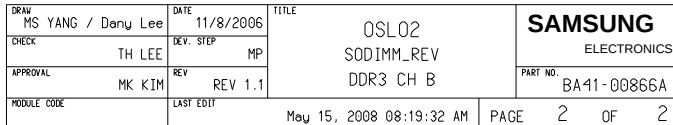
DDR SO-DIMM #0

Height : 5.2mm (Reverse)



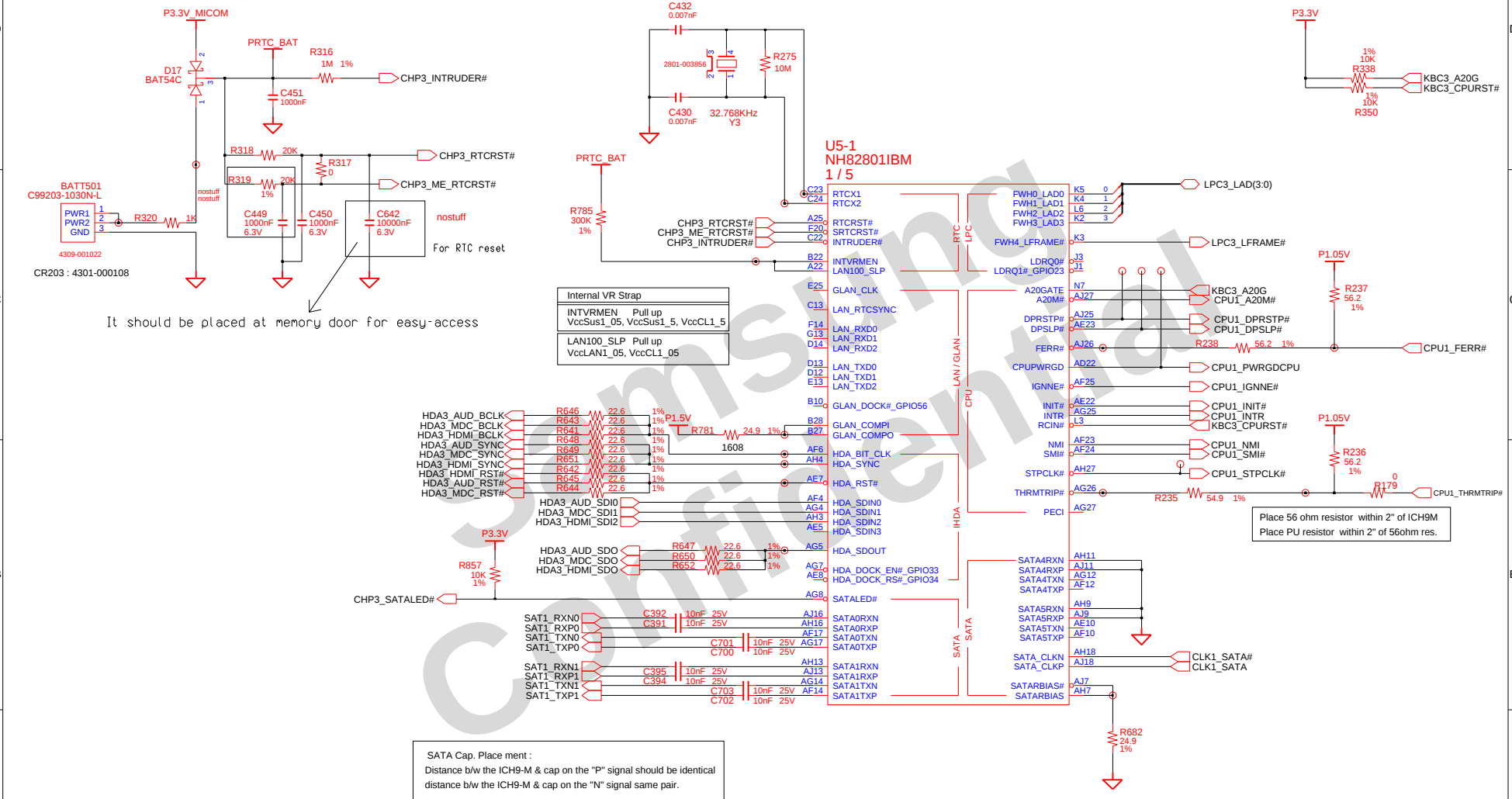
DRAW MS YANG / Dany Lee	DATE 11/8/2006	TITLE OSL02 SODIMM_REV DDR3 CH A	SAMSUNG ELECTRONICS PART NO. BA11-00866A
CHECK TH LEE	DEV. STEP MP		
APPROVAL MK KIM	REV REV 1.1		
MODULE CODE	LAST EDIT		
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DDR SO-DIMM #1
Height : 9.2mm (Reverse)



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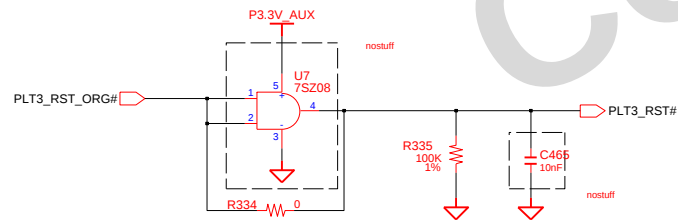
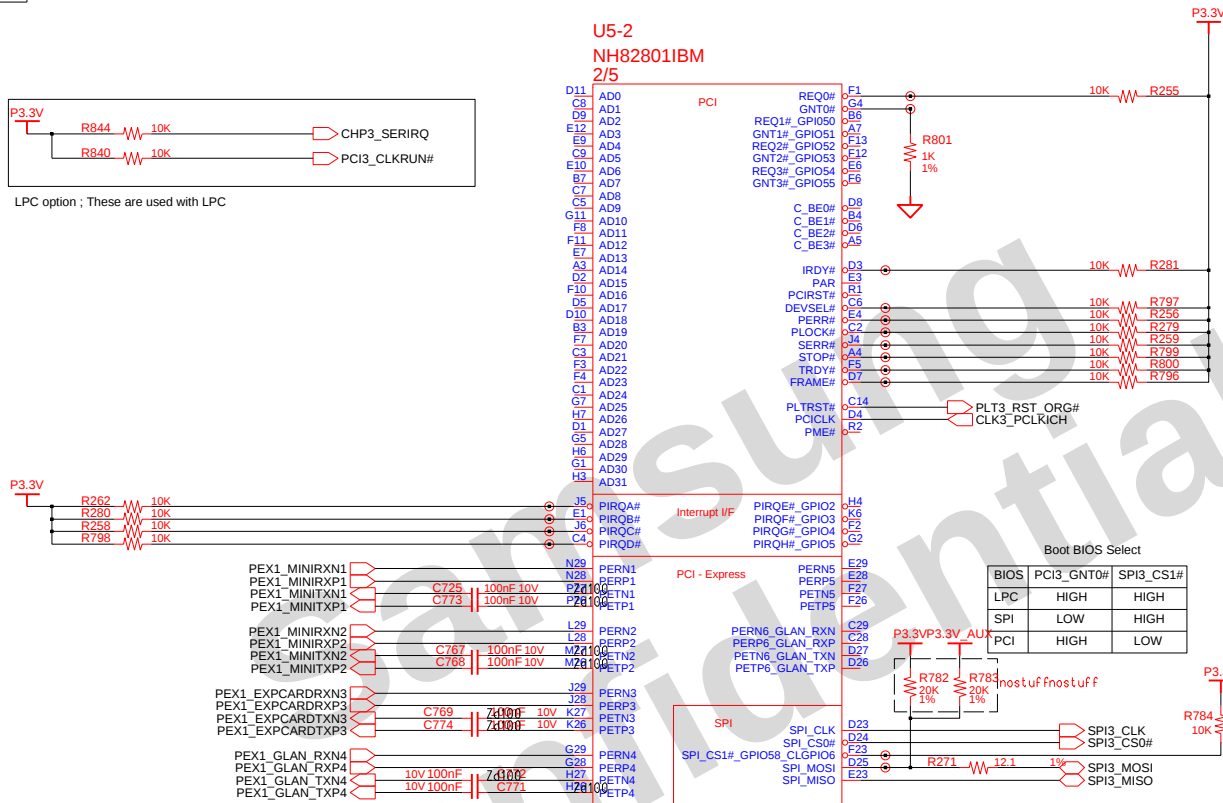
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DRAW	MS YANG / Dany Lee	DATE	11/8/2006	TITLE	OSL02	SAMSUNG ELECTRONICS PART NO. BA11-00866A
CHECK	TH LEE	DEV. STEP	MP		ICH9M_B	
APPROVAL	MK KIM	REV	REV 1.1		ICH9-M (1/5)	
MODULE CODE		LAST EDIT				
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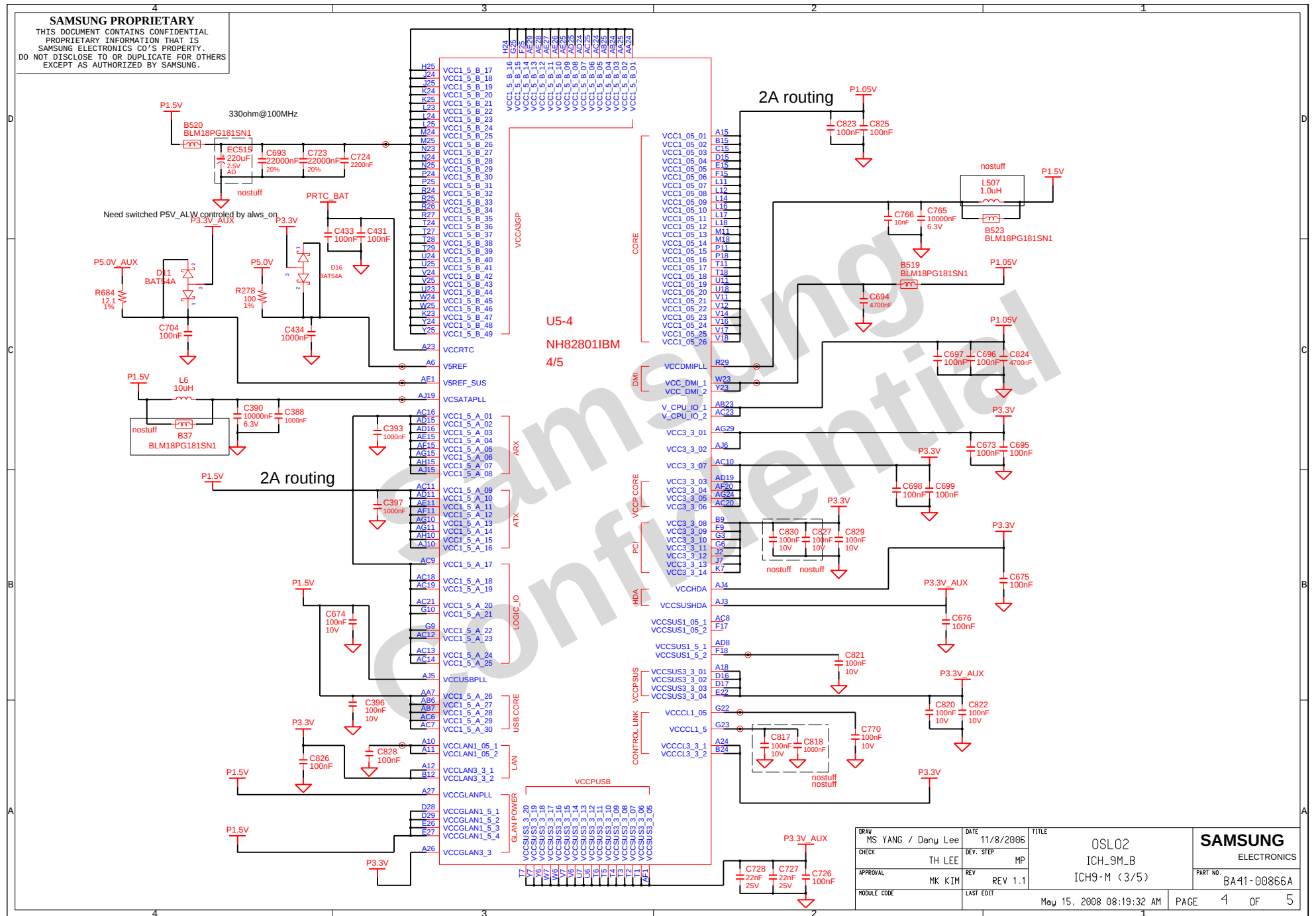


DRAW	MS YANG / Dany Lee	DATE	11/8/2006	TITLE	OSL02	SAMSUNG ELECTRONICS
CHECK	TH LEE	DEV. STEP	MP		ICH_9M_B	
APPROVAL	MK KIM	REV	1.1		ICH9-M (2/5)	PART NO. BA11-00866A
MODULE CODE	undefined	LAST EDIT				

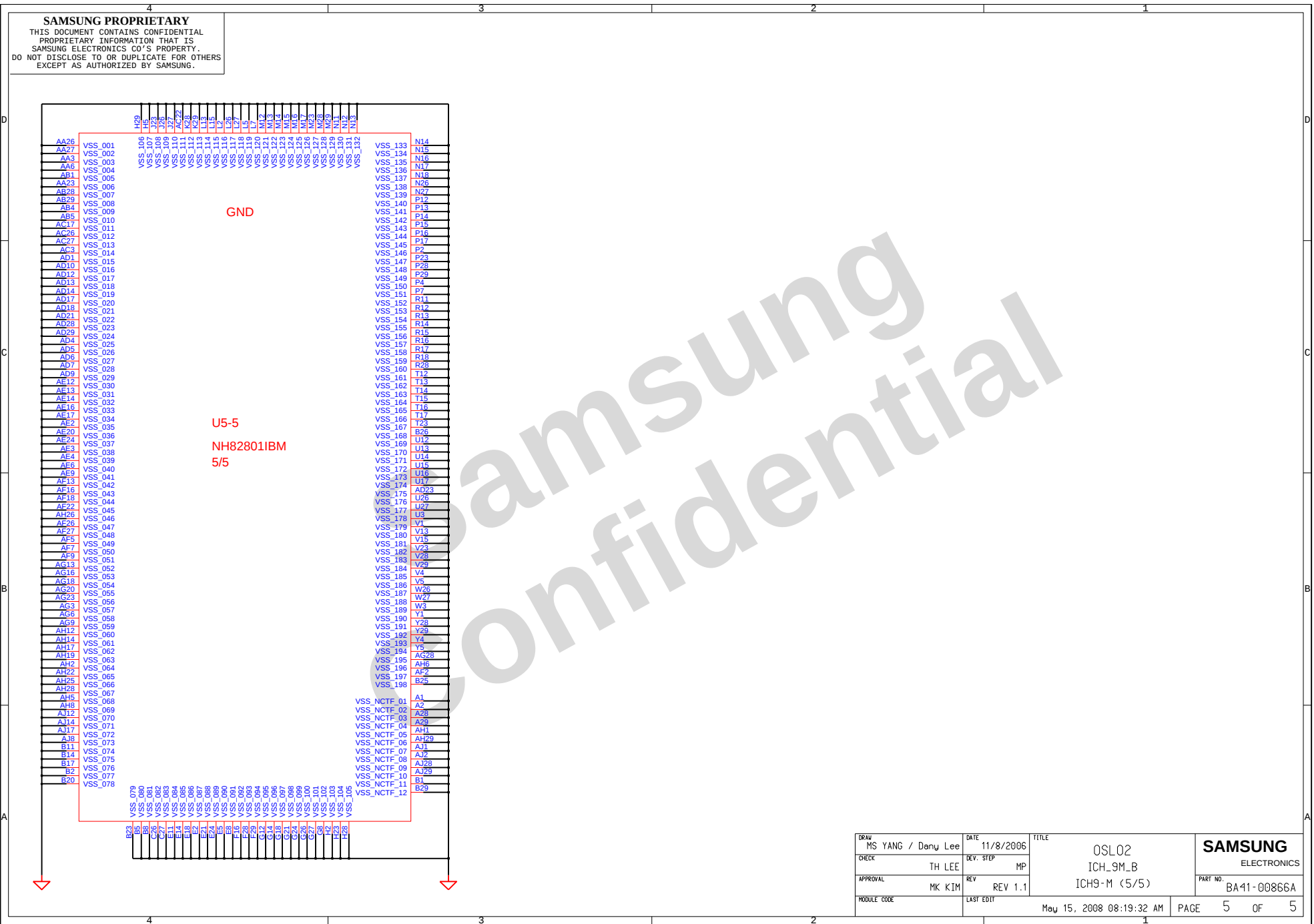
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CHECK	TH LEE	DEV. STEP	MP		ICH_9M_B	
APPROVAL	MK KIM	REV	1.1		ICH9-M (3/5)	PART NO. BA11-00866A
MODULE CODE		LAST EDIT				



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CHECK	TH LEE	DEV. STEP	MP		ICH_9M_B	
APPROVAL	MK KIM	REV	REV 1.1		ICH9-M (5/5)	PART NO. BA411-00866A
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- SPI ROM LIST (8M)-

Macronix - MX25L8005M2C-15G

-> 1107-001646

STM - M25PE80

-> 1107-001674

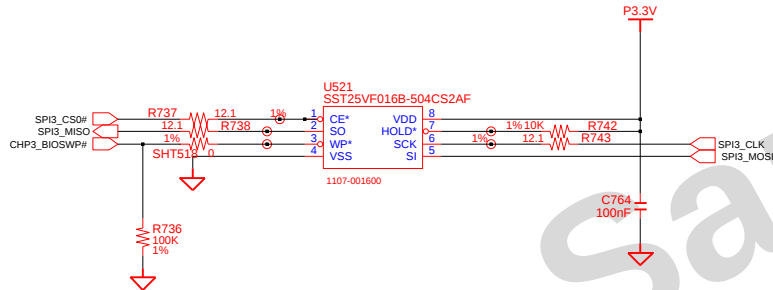
- SPI ROM LIST (16M)-

Macronix - MX25L1605A

-> 1107-001600

SST - SST25VF016B

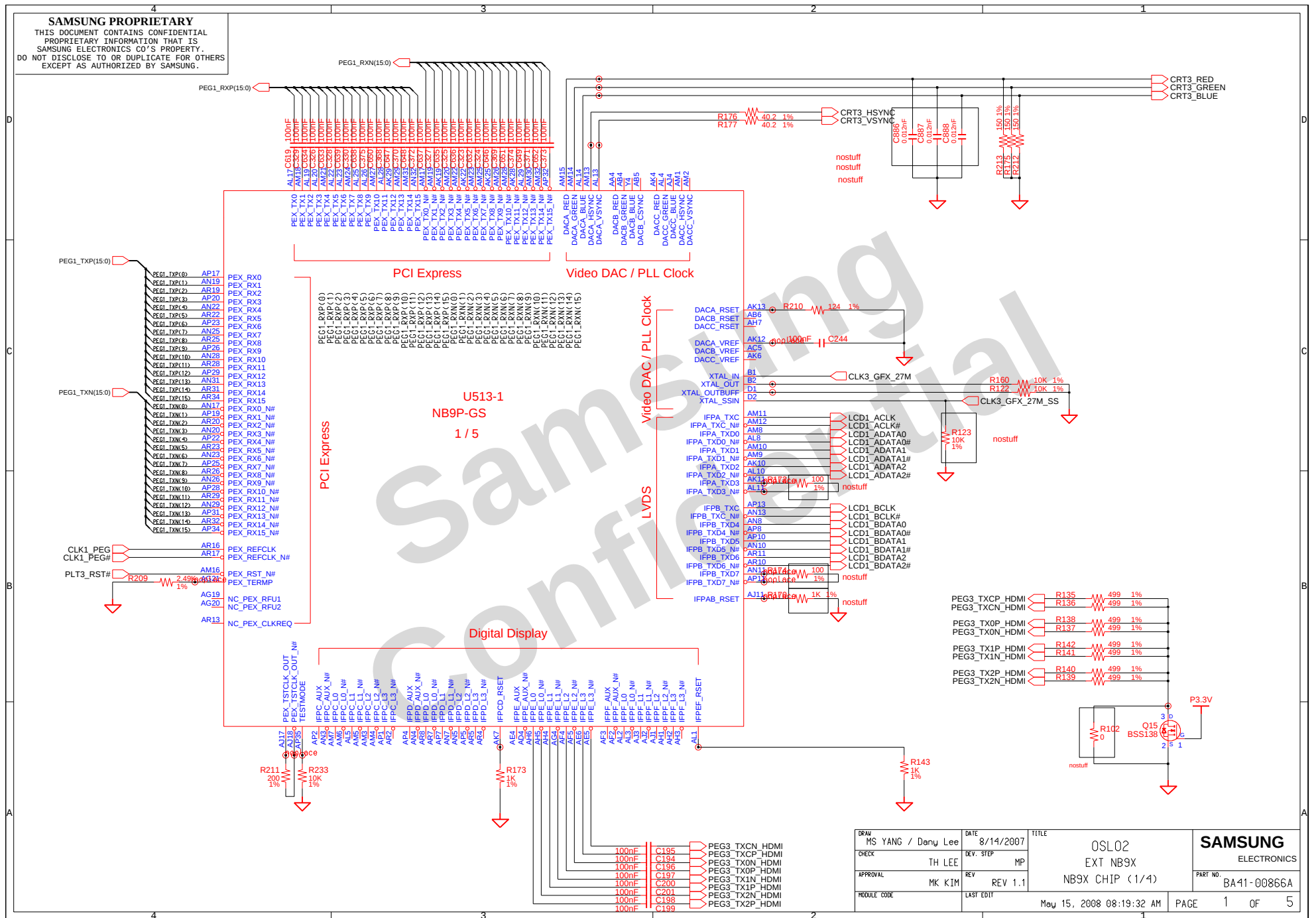
-> 1107-001600

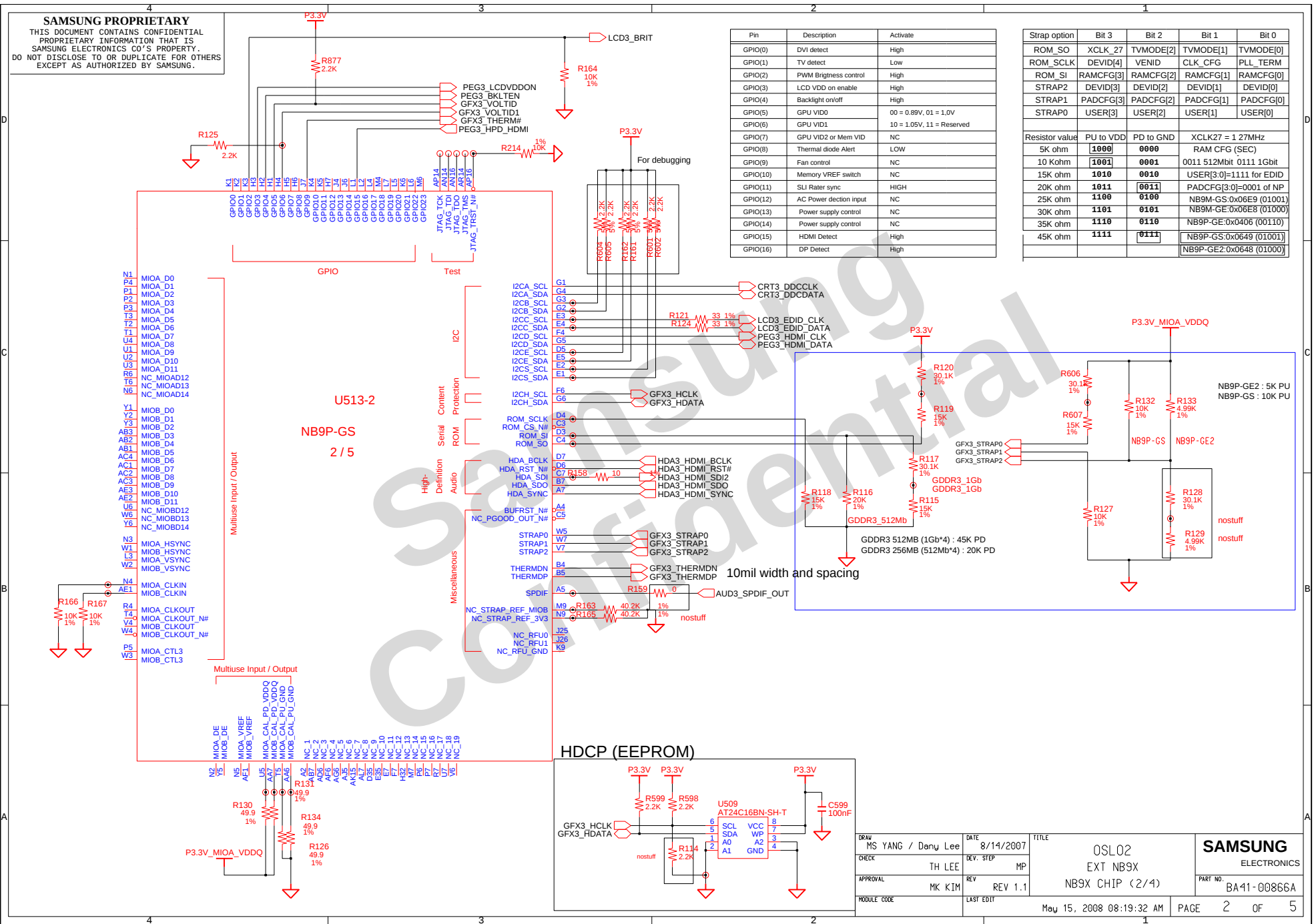


02	VERIFY REAL MODE	66	CONFIGURE ADVANCE CACHE REG.
03	DISABLE NMI	6A	DISPLAY EXTERNAL CACHE SIZE
04	GET CPU TYPE	6C	DISPLAY SHADOW MESSAGE
06	INIT. SYSTEM H/W	6E	DISPLAY NON-DISPOSABLE SEGMENT
08	INIT. CHIPSET REG.	70	DISPLAY ERROR MESSAGE
09	SET IN POST FLAG	72	CHECK FOR CONFIGURATION ERROR
0A	INIT CPU.REG	74	TEST REAL-TIME CLOCK
0B	CPU CACHE ON	76	CHECK FOR KEYBOARD EERROR
0C	INIT.CACHE TO POST	7C	SETUP HARDWARE INTERRUPT VECTOR
0E	INIT. I/O VALUE	7E	TEST COPROCESSOR IF PRESENT
0F	ENABLE THE L-BUS IDE	80	DISABLE ON-BOARD I/O PORT
10	INIT. POWER MANAGER	82	DETECT AND INSTALL EXT.RS232C
11	LOAD ALTERNATE REG.	84	DETECT AND INSTALL EXT.PARALLEL
13	PCI BUS MASTER RESET	86	RE-INIT. ON-BOARD I/O PORT
	WITH INITIAL POST VALUE	88	INIT. BIOS DATA ROM
14	INIT. KEYBOARD CONTROLLER	8A	INIT.EXTENDED BIOS DATA AREA
16	CHECK CHECKSUM	8C	INIT. FDD CONTROLLER
18	8254 TIMER INIT.	9A	SHADOW OPTION ROMS
1A	8237 DMA CONTROLLER INIT.	9C	SETUP POWER MANAGEMENT
1C	RESET INTERRUPT CONTROLLER	9E	ENABLE H/W INTERRUPT
20	TEST DRAM REFRESH	A0	SET TIME OF DAY
22	TEST 8742 KEYBOARD CONTROLLER	A4	INIT. TYPOMATIC RATE
24	SET ES SEGMENT REG. TO 4GB	A8	ERASE F2 PROMPT
26	ENABLE A20	AA	SCAN FOR F2 KEY STROKE
28	AUTO SIZING DRAM	AC	ENTER SETUP
32	COMPUTE THE CPU SPEED	AE	CLEAR IN POST FLAG
34	TEST CMOS RAM	B0	CHECK FOR ERRORS
38	SHADOW SYSTEM BIOS ROM	B2	POST DONE-PREPARE TO BOOT O/S
3A	AUTO SIZING CACHE	B4	ONE BEEP
3C	CONFIGURE ADVANCED CHIPSET REG.	B6	CHECK PASSWORD (OPTION)
3D	LOAD ALTER REG. WITH CMOS VALUE	B7	ACPI INIT
42	INIT. INTERRUPT VECTOR	BA	DMI INIT
44	INIT. BIOS INTERRUPT	BE	CLEAR SCREEN
46	CHECK ROM COPYRIGHT NOTICE	C0	TRY BOOT WITH INT19
47	INIT. I20 SUPPORT IF INSTALLED	D0	INTERRUPT HANDLER ERROR
48	CHECK VIDEO CONFIGURE AGAINST CMOS	D2	UNKNOWN INTERRUPT ERROR
49	INIT. PCI BUS AND DEVICE	D4	PENDING INTERRUPT ERROR
4A	INIT. ALL VIDEO BIOS ROM	D6	SHUTDOWN 5
4C	SHADOW VIDEO BIOS ROM	D8	SHUTDOWN ERROR
50	DISPLAY CPU TYPE AND SPEED	DA	EXTENDED BLOCK MOVE
52	TEST KEYBOARD	DC	SHUTDOWN 10
54	SET KEYCLICK IF ENABLED	89	ENABLE NMI
56	ENABLE KEYBOARD	90	INIT. HDD CONTROLLER
58	TEST FOR UNEXPECTED INTERRUPTS	91	INIT. LOCAL BUS HDD CONTROLLER
5A	DISPLAY " PRESS SETUP"	92	JUMP TO USER PATCH 2
5C	TEST RAM BETWEEN 512K AND 640K	94	DISABLE A20 ADDRESS LINE
60	TEST EXTENDED MEMORY	96	CLEAR HUGE ES SEGMENT REG.
62	TEST EXTENDED MEMORY ADDRESS LINE	98	SEARCH FOR OPTION ROMS
64	JUMP TO USER PATCH 1		

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CHECK TH LEE	DEV. STEP MP	SPI_BIOS_ROM	
APPROVAL MK KIM	REV REV 1.1	SPI_BIOS_ROM	
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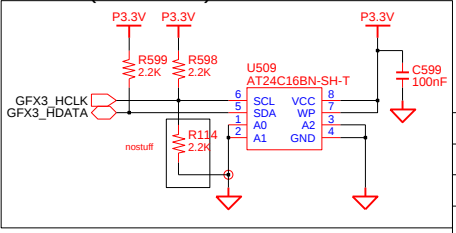
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Pin	Description	Activate
GPIO(0)	DVI detect	High
GPIO(1)	TV detect	Low
GPIO(2)	PWM Brightness control	High
GPIO(3)	LCD VDD on enable	High
GPIO(4)	Backlight on/off	High
GPIO(5)	GPU VID0	00 = 0.89V, 01 = 1.0V
GPIO(6)	GPU VID1	10 = 1.05V, 11 = Reserved
GPIO(7)	GPU VID2 or Mem VID	NC
GPIO(8)	Thermal diode Alert	LOW
GPIO(9)	Fan control	NC
GPIO(10)	Memory VREF switch	NC
GPIO(11)	SU Rater sync	HIGH
GPIO(12)	AC Power dection input	NC
GPIO(13)	Power supply control	NC
GPIO(14)	Power supply control	NC
GPIO(15)	HDMI Detect	High
GPIO(16)	DP Detect	High

Strap option	Bit 3	Bit 2	Bit 1	Bit 0
ROM_SO	XCLK_27	TVMODE[2]	TVMODE[1]	TVMODE[0]
ROM_SCLK	DEVID[4]	VENID	CLK_CFG	PLL_TERM
ROM_SI	RAMCFG[3]	RAMCFG[2]	RAMCFG[1]	RAMCFG[0]
STRAP2	DEVID[3]	DEVID[2]	DEVID[1]	DEVID[0]
STRAP1	PADCFG[3]	PADCFG[2]	PADCFG[1]	PADCFG[0]
STRAP0	USER[3]	USER[2]	USER[1]	USER[0]

Resistor value	PU to VDD	PD to GND	XCLK27 = 1.27MHz
5K ohm	1000	0000	RAM CFG (SEC)
10 Kohm	1001	0001	0011 512Mbit 0111 1Gbit
15K ohm	1010	0010	USER[3:0]=1111 for EDID
20K ohm	1011	0011	PADCFG[3:0]=0001 of NP
25K ohm	1100	0100	NB9M-GS:0x06E9 (01001)
30K ohm	1101	0101	NB9M-GS:0x06E8 (01000)
35K ohm	1110	0110	NB9P-GS:0x0406 (00110)
45K ohm	1111	0111	NB9P-GS:0x0649 (01001)
			NB9P-GS:0x0648 (01000)

HDPC (EEPROM)



DRAW	MS YANG / Dany Lee	DATE	8/14/2007	TITLE	OSL02	SAMSUNG ELECTRONICS
CHECK	TH LEE	DEV. STEP	MP		EXT NB9X	
APPROVAL	MK KIM	REV	1.1		NB9X CHIP (2/4)	PART NO. BA41-00866A
MODULE CODE		LAST EDIT				

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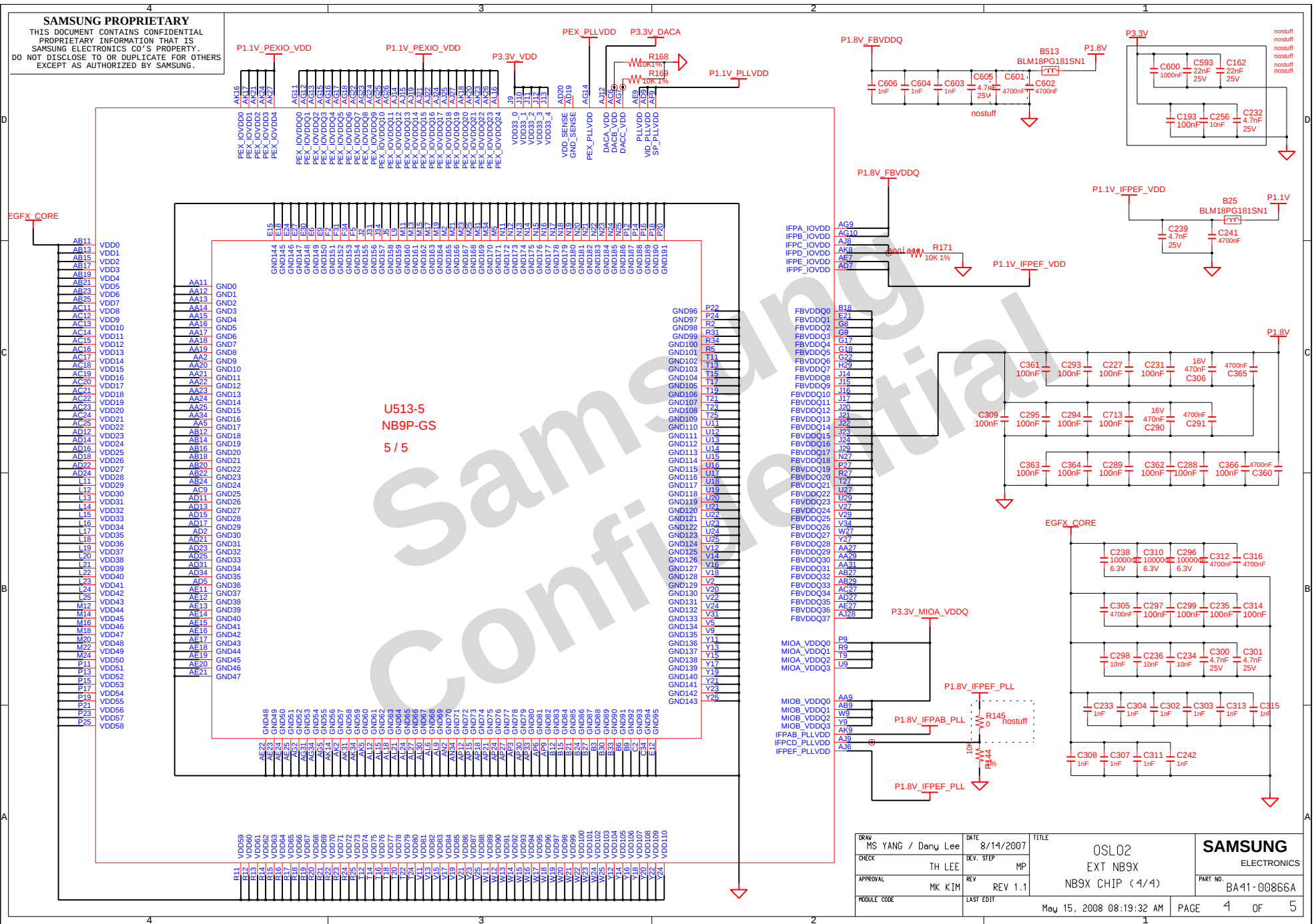
NB9F
3 / 5



NB9P-GS
4 / 5



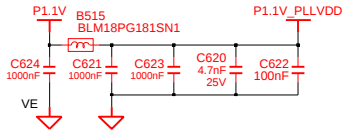
SAMSUNG
ELECTRONICS



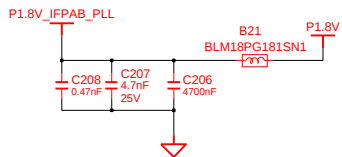
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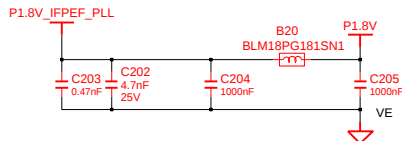
PLL_VDD



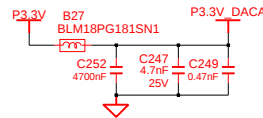
IFPAB_PLLVDD



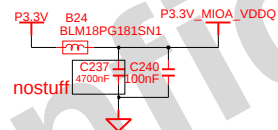
IFPEF_PLLVDD



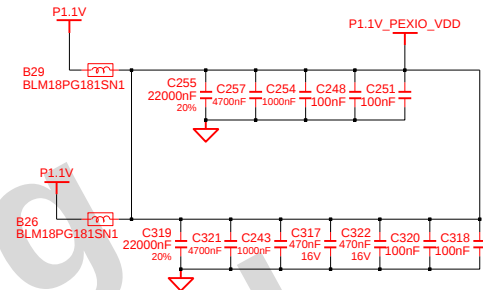
DACA/B/C



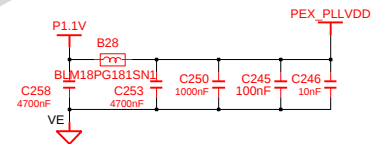
MIOA/B_VDDQ



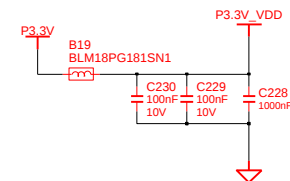
PEX_IOVDD (w/ PEX_IOVDDQ)



PEX_PLL_VDD



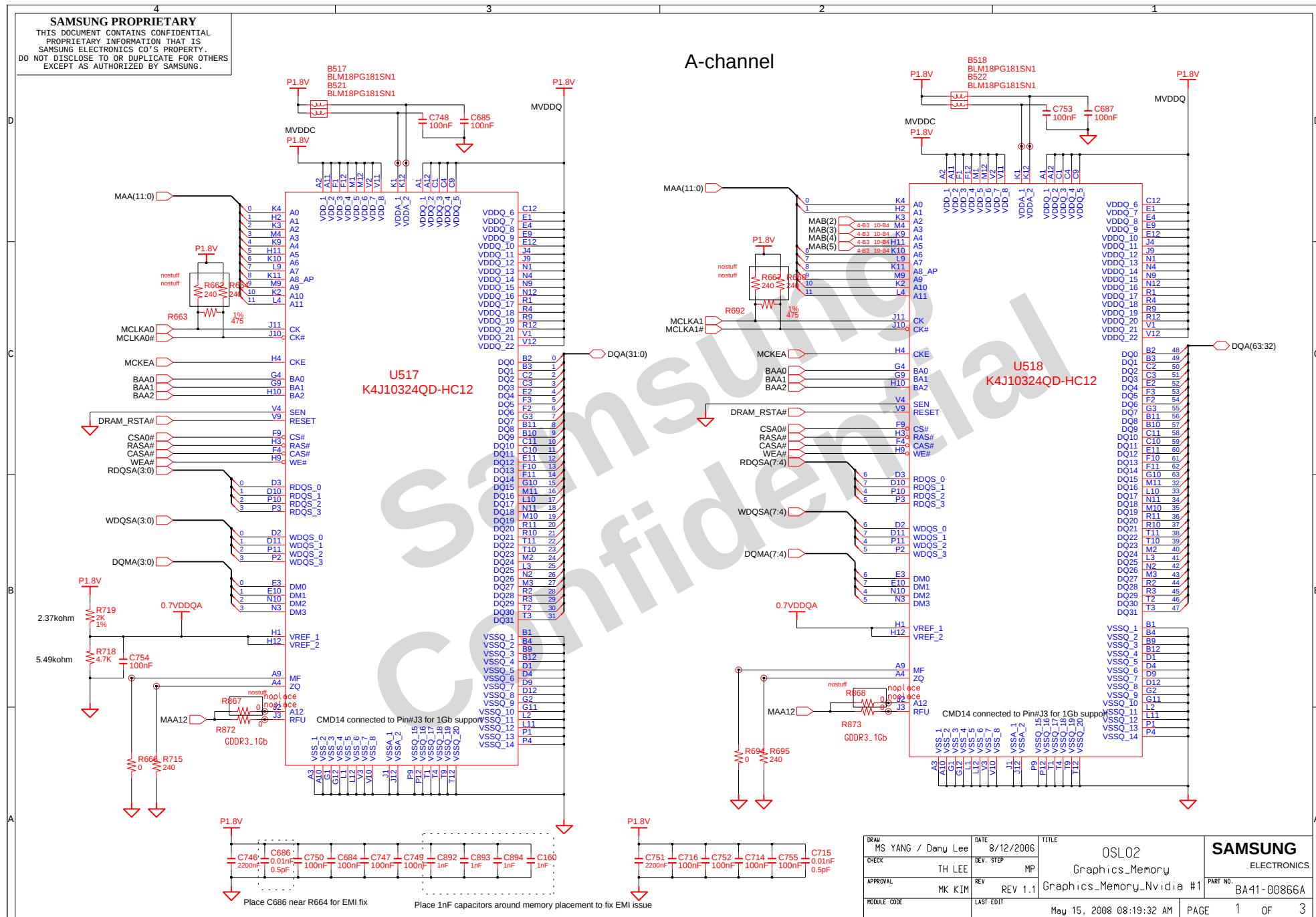
3.3V_VDD



DRAW MS YANG / Dany Lee	DATE 8/14/2007	TITLE OSL02 EXT NB9X NB9X POWER RAIL	SAMSUNG ELECTRONICS
CHECK TH LEE	DEV. STEP MP		PART NO. BA11-00866A
APPROVAL MK KIM	REV REV 1.1		
MODULE CODE	LAST EDIT	May 15, 2008 08:19:32 AM	PAGE 5 OF 5

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A-channel

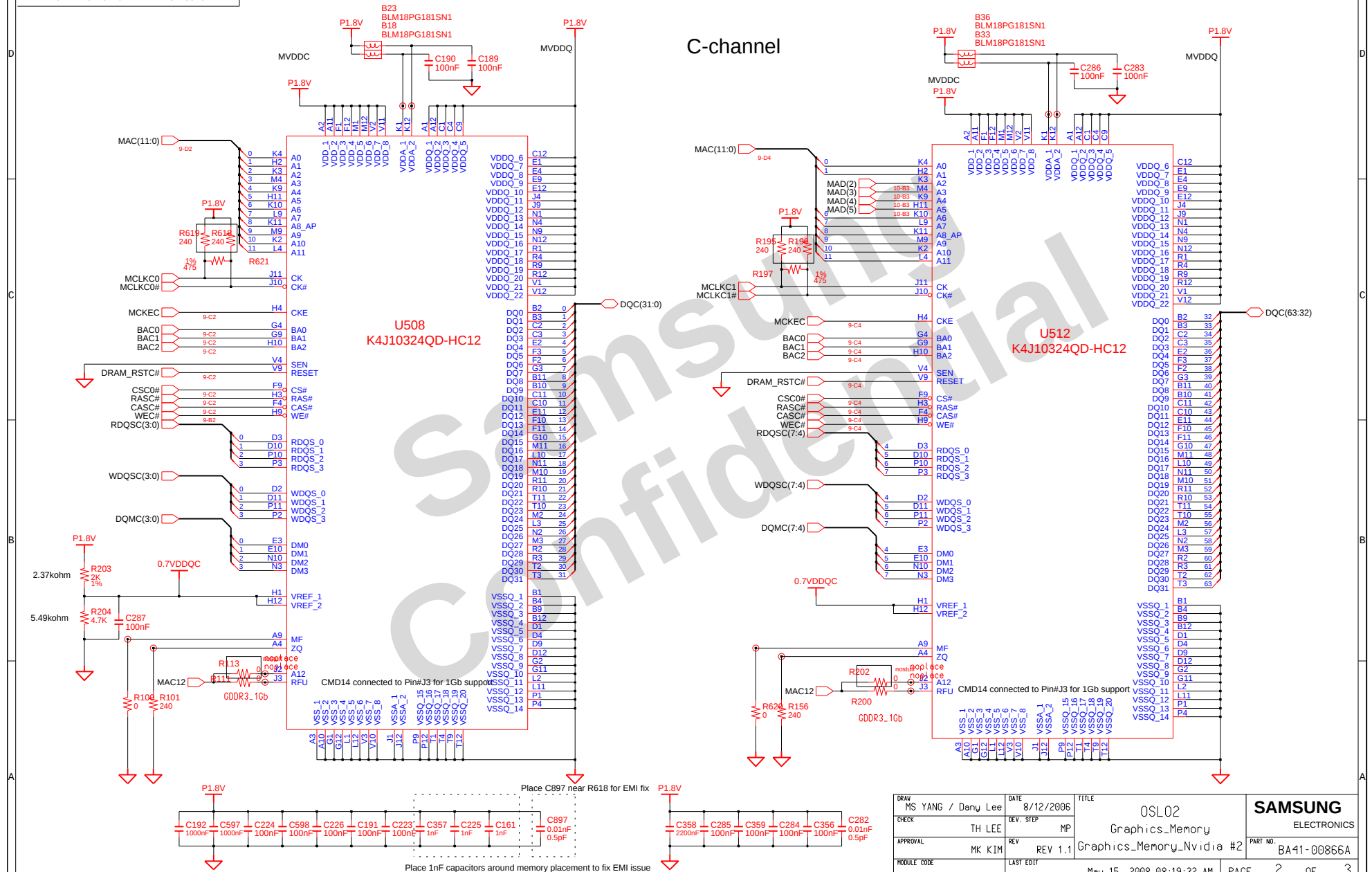


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This page is for "2channel-model".

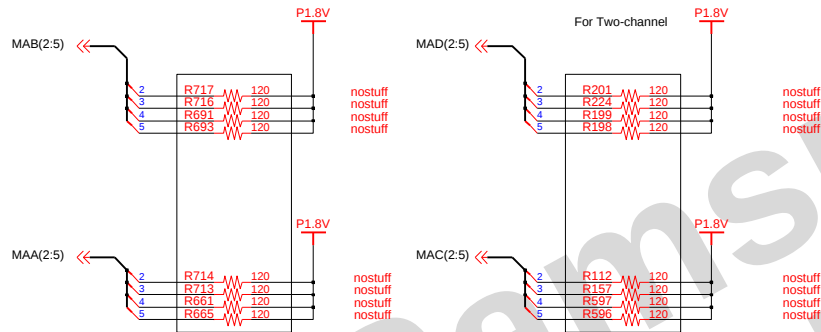
C-channel



DRAW	MS YANG / Dany Lee	DATE	8/12/2006	TITLE	OSL02	SAMSUNG ELECTRONICS
CHECK	TH LEE	DEV. STEP	MP		Graphics_Memory	
APPROVAL	MK KIM	REV	REV 1.1		Graphics_Memory_Nvidia #2	PART NO.
MODULE CODE		LAST EDIT				BA41-00866A
May 15, 2008 08:19:32 AM						PAGE 2 OF 3

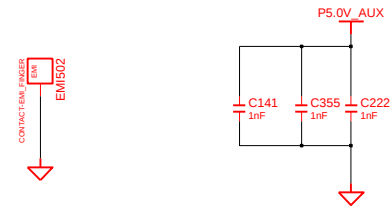
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EMI solution @ OSLO2

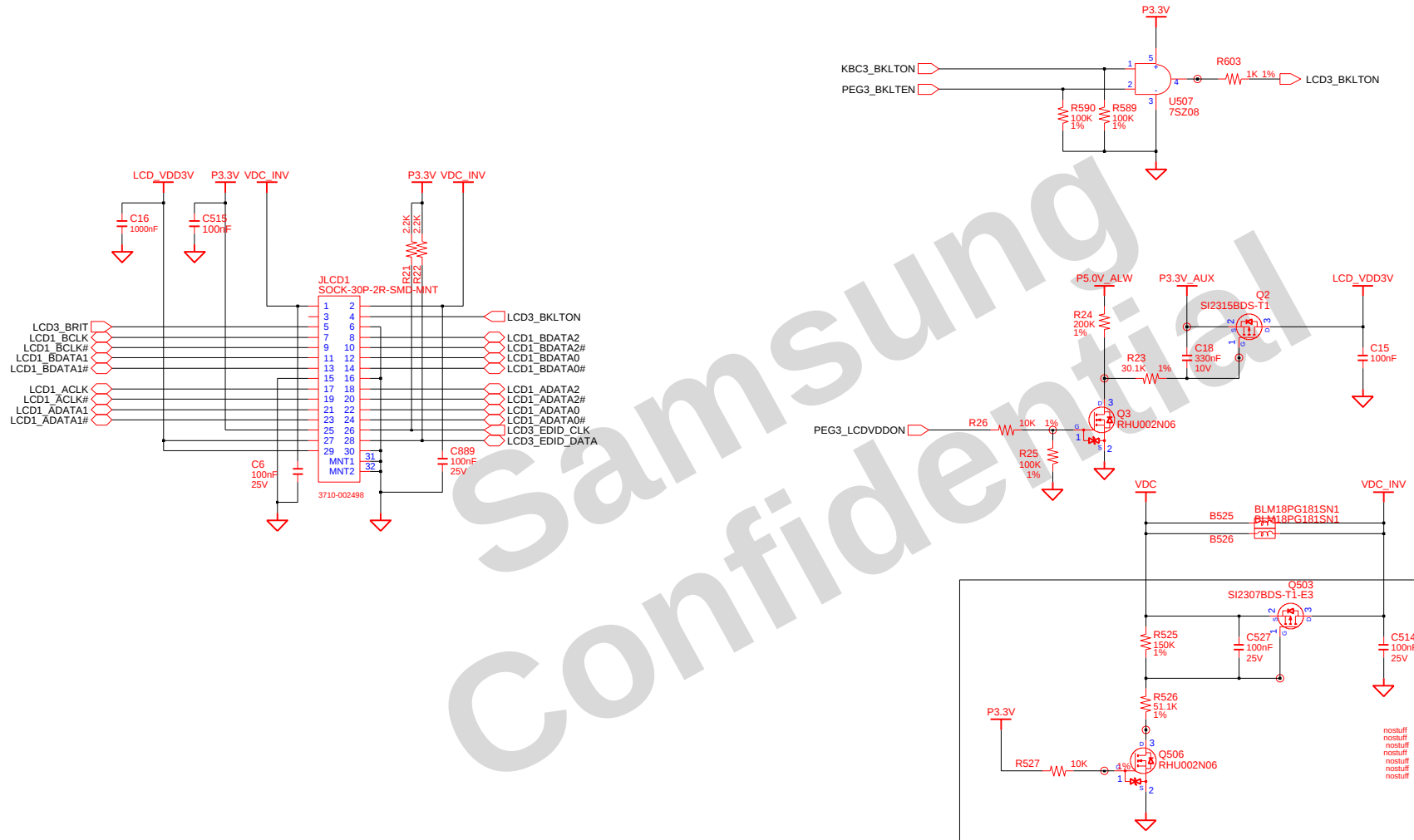
Place three 1nF capacitor around C-channel component



DRAW MS YANG / Dany Lee	DATE 8/12/2006	TITLE OSLO2	SAMSUNG ELECTRONICS PART NO. BA41-00866A
CHECK TH LEE	DEV. STEP MP	Graphics_Memory	
APPROVAL MK KIM	REV REV 1.1	Graphics_Memory_Nvidia #3	
MODULE CODE	LAST EDIT	May 15, 2008 08:19:32 AM	
		PAGE 3 OF 3	

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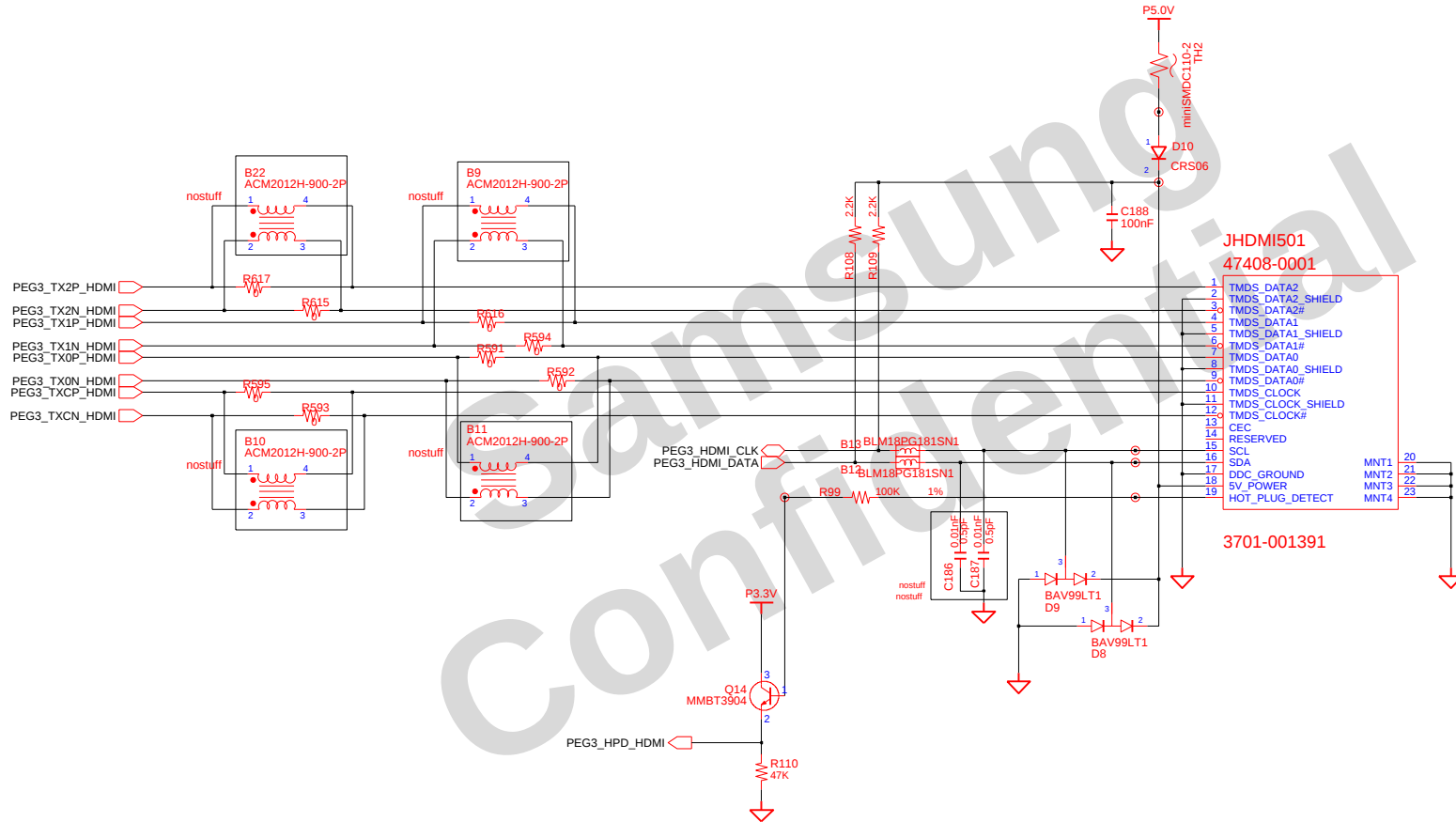
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DRAW	MS YANG / Dany Lee	DATE	8/17/2007	TITLE	OSL02 UNDEF INED	SAMSUNG ELECTRONICS PART NO. BA11-00866A
CHECK	TH LEE	DEV. STEP	MP			
APPROVAL	MK KIM	REV	REV 1.1			
MODULE CODE		LAST EDIT				
May 15, 2008 08:19:32 AM						PAGE 12

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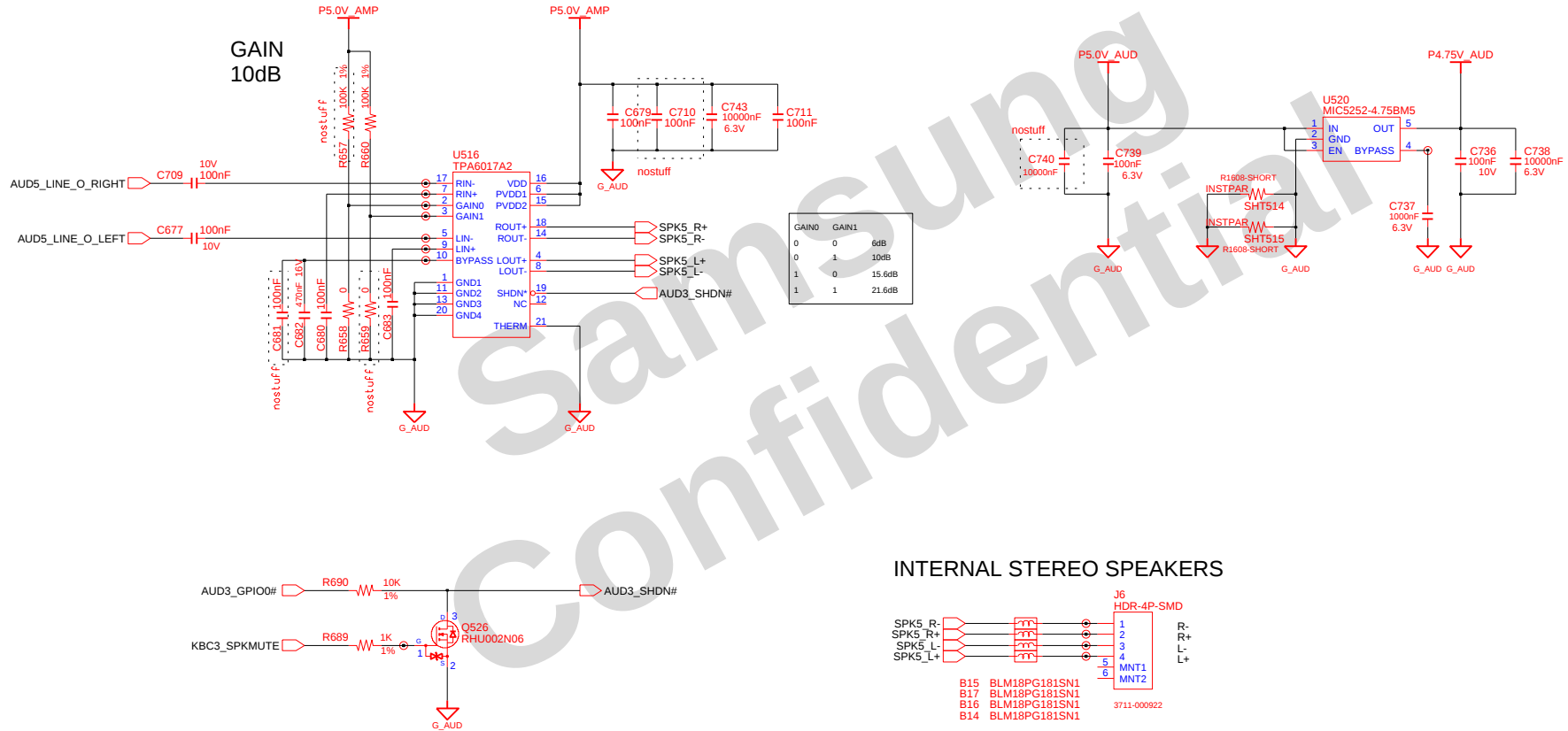
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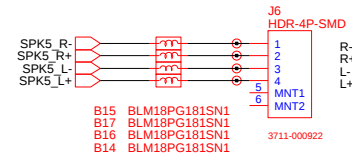
DRAW MS YANG / Dany Lee	DATE 8/17/2007	TITLE OSL02 UNDEFINED	SAMSUNG ELECTRONICS PART NO. BA11-00866A
CHECK TH LEE	DEV. STEP MP		
APPROVAL MK KIM	REV REV 1.1		
MODULE CODE	LAST EDIT		
May 15, 2008 08:19:32 AM			PAGE 13 of 13

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INTERNAL STEREO SPEAKERS

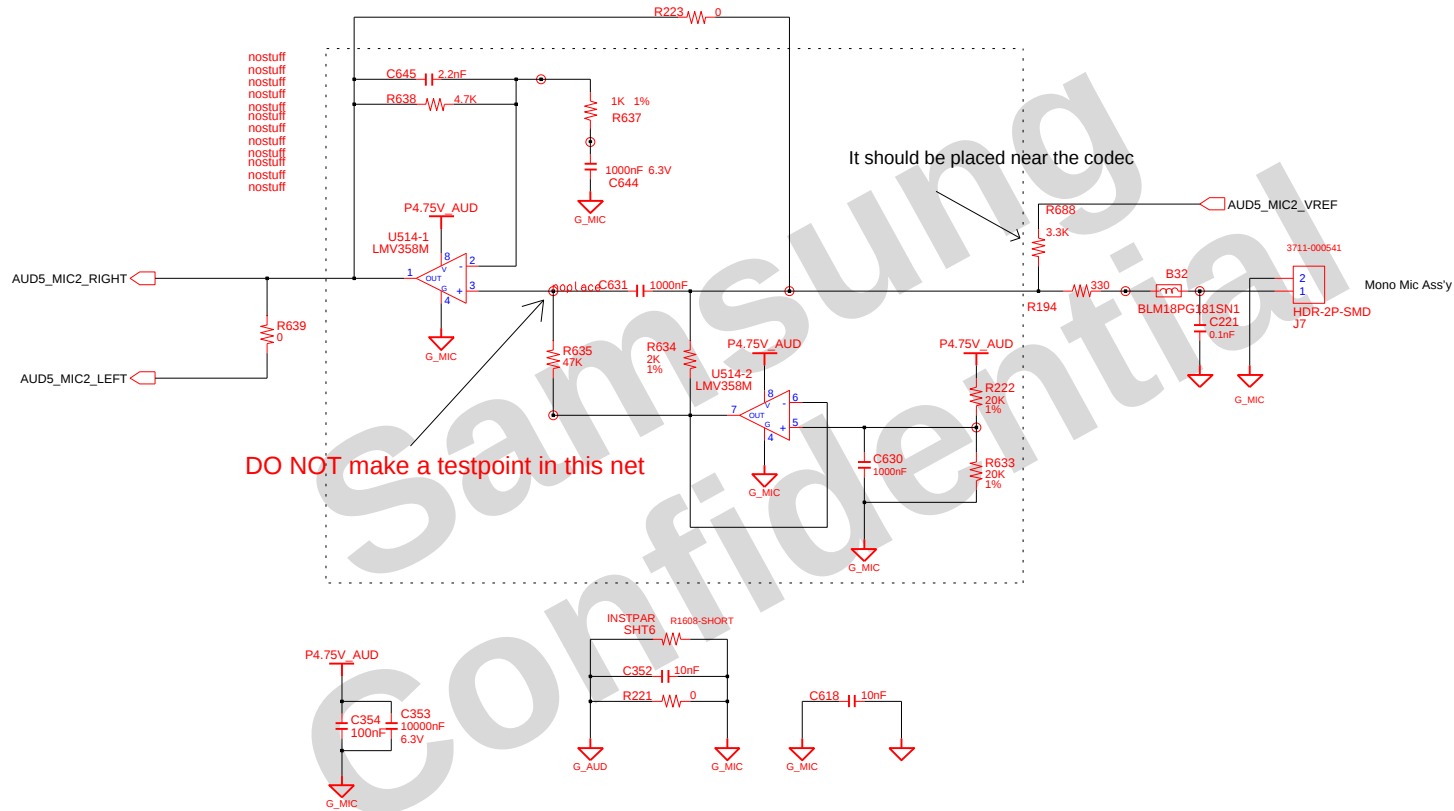


DRAW	MS YANG / Dany Lee	DATE	8/12/2006	TITLE	OSL02	SAMSUNG ELECTRONICS PART NO. BA41-00866A
CHECK	TH LEE	DEV. STEP	MP		HDA_Codec_Alc262	
APPROVAL	MK KIM	REV	1.1		HDA_Codec_Alc262 #2	
MODULE CODE		LAST EDIT				

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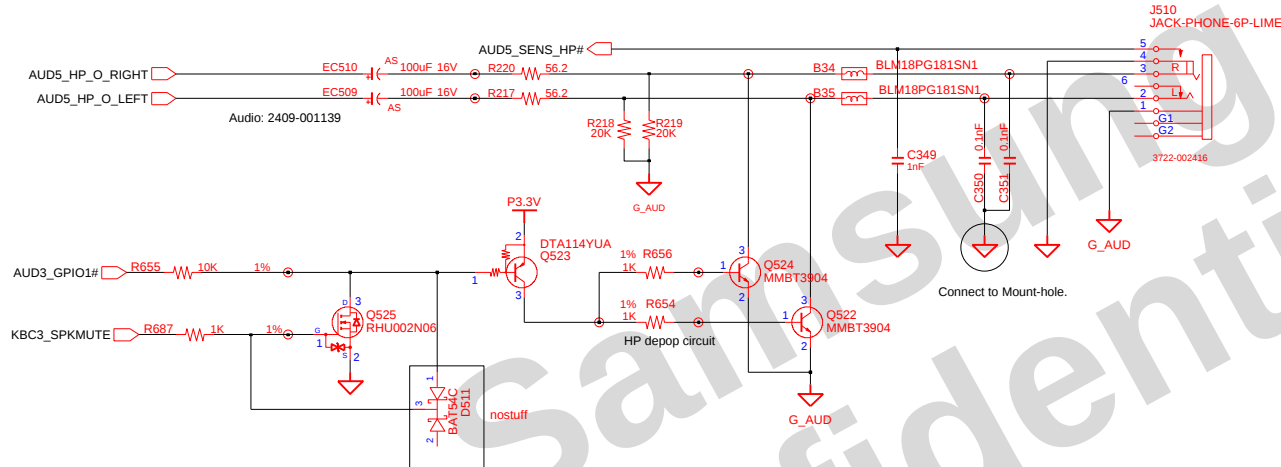


DRW	MS YANG / Dany Lee	DATE	8/12/2006	TITLE	OSL02	SAMSUNG ELECTRONICS
CHECK	TH LEE	DEV. STEP	MP		HDA_Codec_Alc262	
APPROVAL	MK KIM	REV	1.1		HDA_Codec_Alc262 #3	PART NO.
MODULE CODE	undefined	LAST EDIT				BA11-00866A
				May 15, 2008 08:19:32 AM	PAGE	3 OF 5

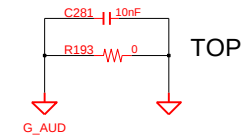
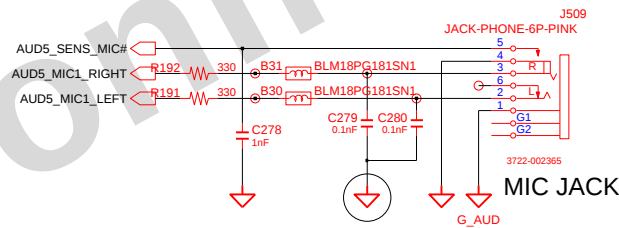
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HEADPHONE



Connect to Mount-hole.



The traces led to Audio Jacks have the width over 10mil

DRW MS YANG / Dany Lee	DATE 8/12/2006	TITLE OSL02	SAMSUNG ELECTRONICS PART NO. BA11-00866A
CHECK TH LEE	DEV. STEP MP	HDA_Codec_Alc262	
APPROVAL MK KIM	REV REV 1.1	HDA_Codec_Alc262 #4	
MODULE CODE	LAST EDIT		
May 15, 2008 08:19:32 AM			PAGE 4 OF 5

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DMS MS YANG / Dany Lee		DATE 8/12/2006		TITLE OSL02 HDA_Codec_Alc262 HDA_Codec_Alc262 #5			
CHECK TH LEE		DEV. STEP MP					
APPROVAL MK KIM		REV REV 1.1		PART NO.		BA11-00866A	
MODULE CODE undef ined		LAST EDIT May 15, 2008 08:19:32 AM		PAGE		5 OF 5	

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MDC Connector

J10
SOCK-12P-2R-SMD

HDA3_MDC_SDO
HDA3_MDC_SYNC
HDA3_MDC_SDI1
HDA3_MDC_RST#

R264 33 1%

P3.3V_AUX

C439 100nF
C440 10000nF 6.3V

HDA3_MDC_BCLK

MNT1
MNT2
MNT3
MNT4
MNT5
MNT6

3710-002133

M3 HEAD DIA LENGTH

CONTACT PATTERN

MDC-model
MDC-model
MDC-model
MDC-model
MDC-model
MDC-model
MDC-model
MDC-model
MDC-model
MDC-model

J2 HDR-2P-1R
B511
CIC21J601NE
CIC21J601NE
C546 1nF 3KV
C547 1nF 3KV

MNT1
MNT2

To MDC card.

To RJ11 cable. (Connect with BA39-00593A)

J3 HDR-2P-1R
B511
CIC21J601NE
CIC21J601NE
C546 1nF 3KV
C547 1nF 3KV

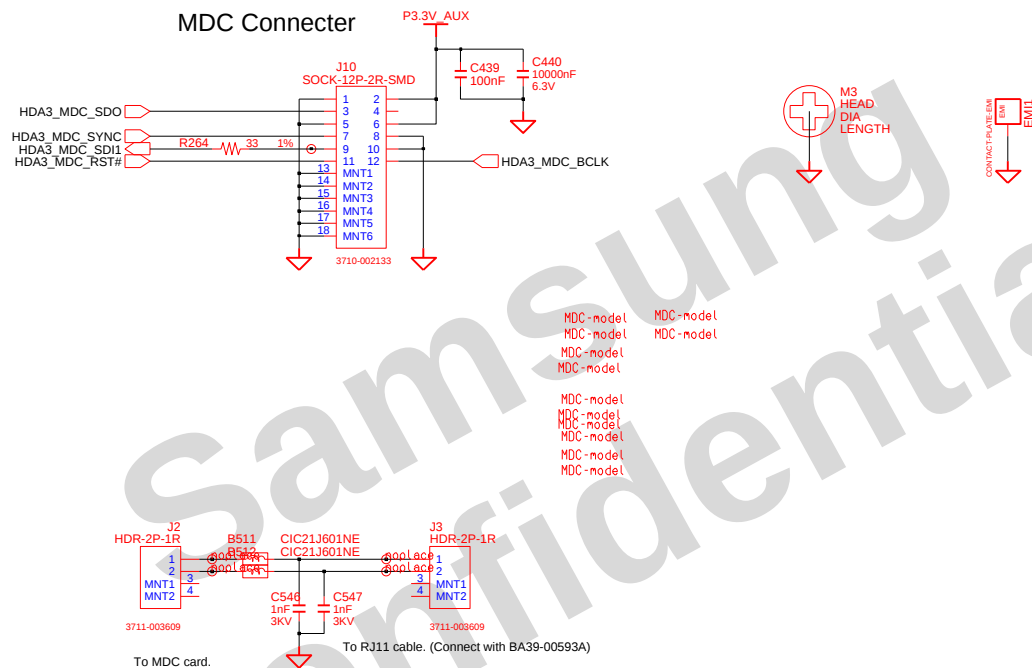
MNT1
MNT2

DRAW	DATE	TITLE	SAMSUNG
MS YANG / Dany Lee	8/12/2006	OSL02	ELECTRONICS
CHECK TH LEE	REV. STEP MP	HDA_Modem	PART NO. BA41-00866A
APPROVAL MK KIM	REV 1.1	HDA_Modem	
MODULE CODE	May 15, 2008 08:19:32 AM PAGE 1 OF 1		

COM-22C-015(1996.6.5) REV. 3

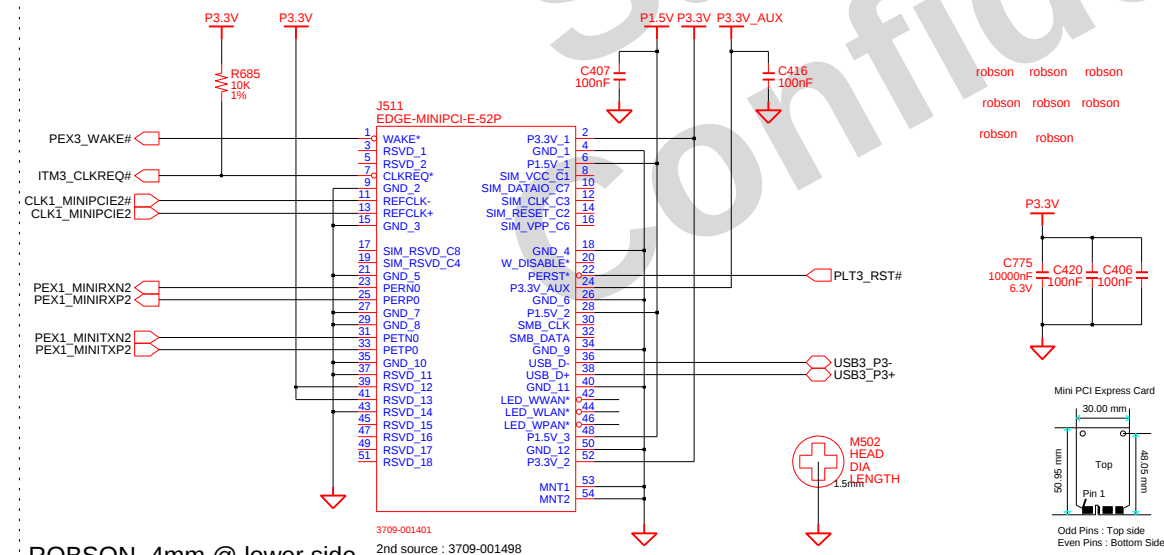
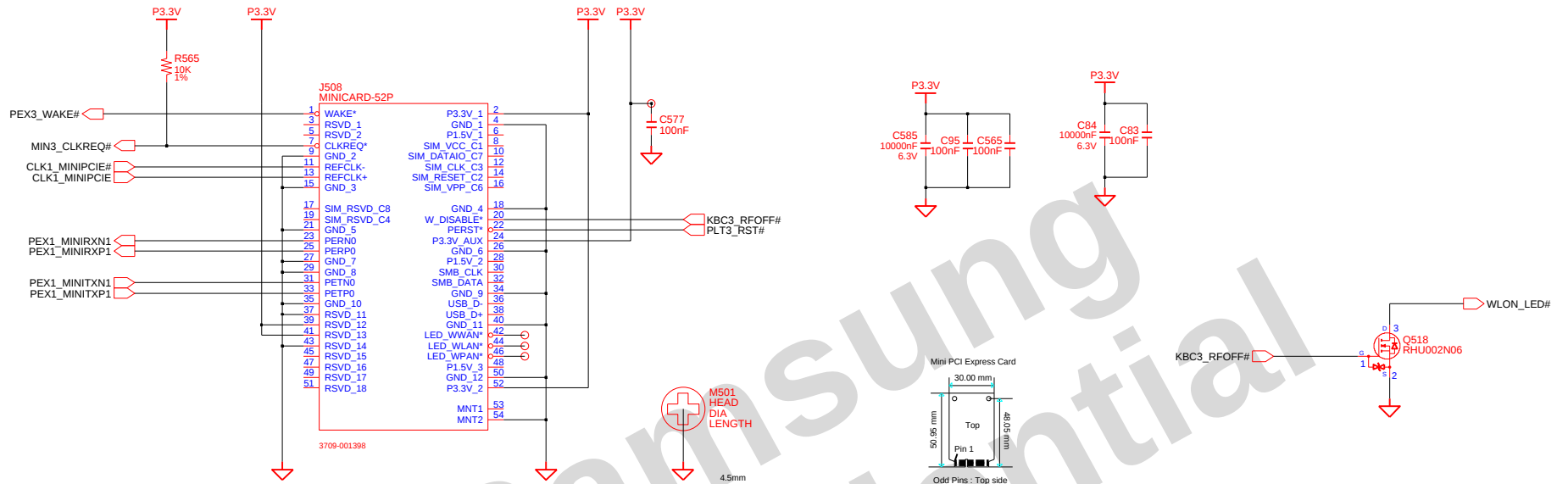
D:/users/mobile16/mentor/oslo2/oslo2_pr/design_blocks/HDA_Modem SRP Sheet Number: 46 of 46

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DRAW	MS YANG / Dany Lee	DATE	8/12/2006	TITLE	OSLO2 HDA_Modem HDA_Modem	MS SAMSUNG ELECTRONICS
CHECK	TH LEE	DEV. STEP	MP			
APPROVAL	MK KIM	REV	REV 1.1			
MODULE CODE	LAST EDIT					
				Mon 15, 2008 08:19:32 AM	PAGE	1 OF 1

WLAN, 7mm @ Upper side

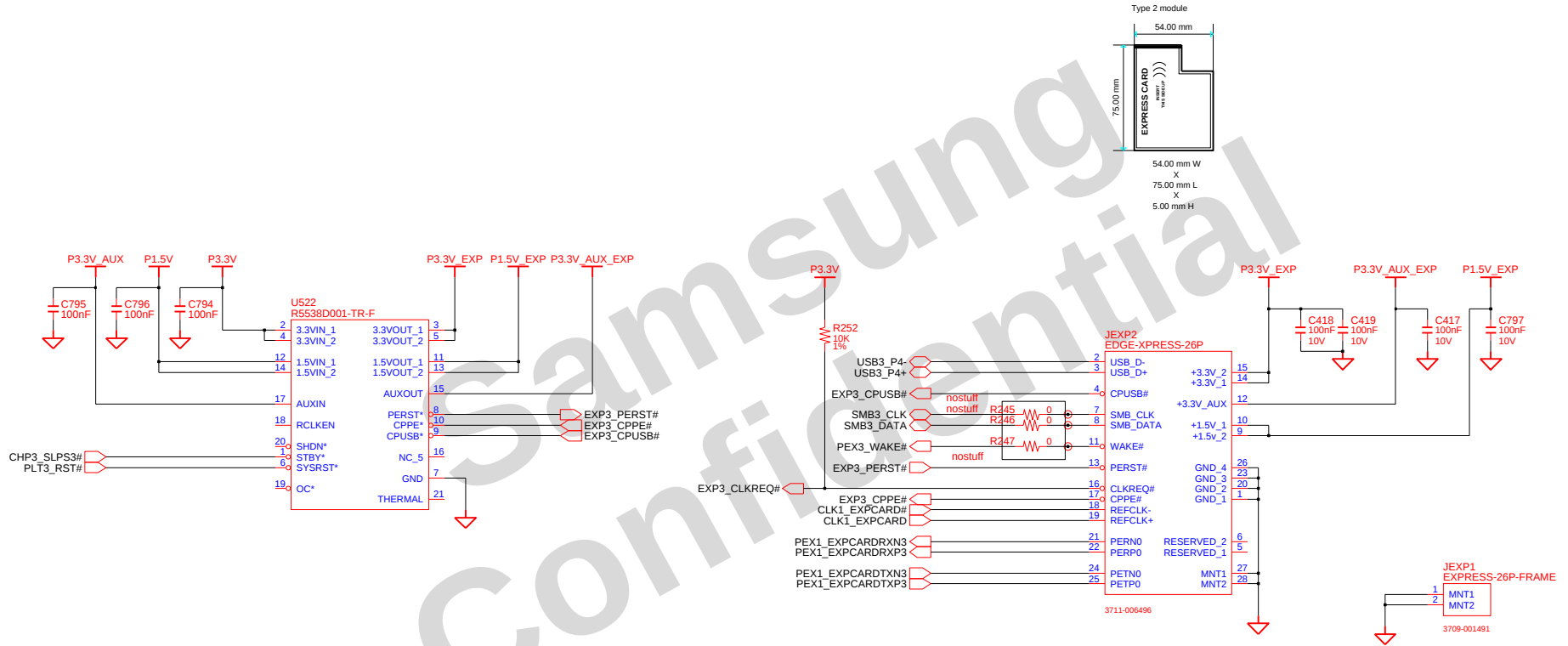


ROBSON, 4mm @ lower side

DRAW	MS YANG / Dany Lee	DATE	8/12/2006	TITLE	OSLO2	SAMSUNG ELECTRONICS
CHECK	TH LEE	DEV. STEP	MP	PCIE_Minicard_Slot	PCIE_Minicard_Slot	
APPROVAL	MK KIM	REV	REV 1.1			PART NO. BA41-00866A
MODULE CODE	undefined	LAST EDIT				
				May 15, 2008 08:19:32 AM	PAGE	1 OF 1

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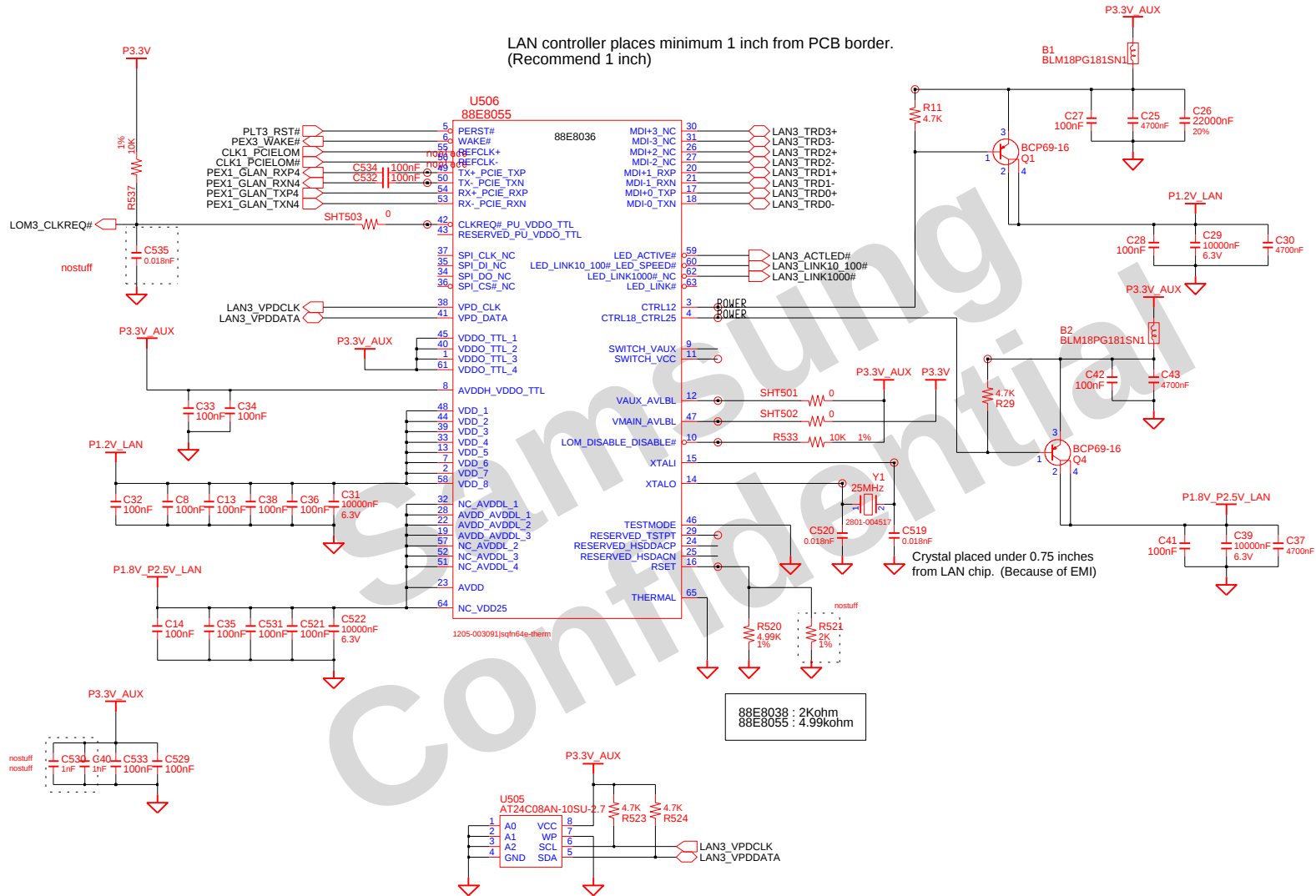


DRW	MS YANG / Dany Lee	DATE		TITLE	OSL02	SAMSUNG ELECTRONICS PART NO. BA11-00866A
CHECK	TH LEE	DEV. STEP	MP			
APPROVAL	MK KIM	REV	REV 1.1			
MODULE CODE	undefined	LAST EDIT		May 15, 2008 08:19:32 AM	PAGE 0F	

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LAN controller places minimum 1 inch from PCB border.
(Recommend 1 inch)



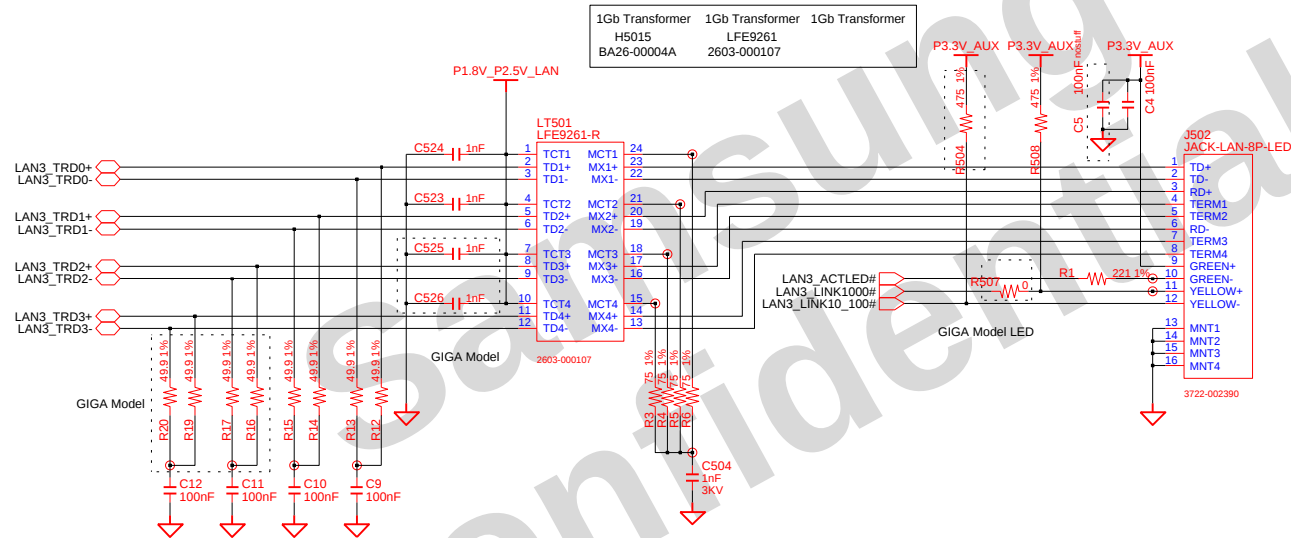
DRAW	MS YANG / Dany Lee	DATE	8/12/2006	TITLE	OSL02	SAMSUNG ELECTRONICS PART NO. BA11-00866A
CHECK	TH LEE	DEV. STEP	MP		LAN_Marvell_8055	
APPROVAL	MK KIM	REV	1.1		LAN_Marvell_8055	
MODULE CODE		LAST EDIT				

May 15, 2008 08:19:32 AM PAGE 1 OF 2

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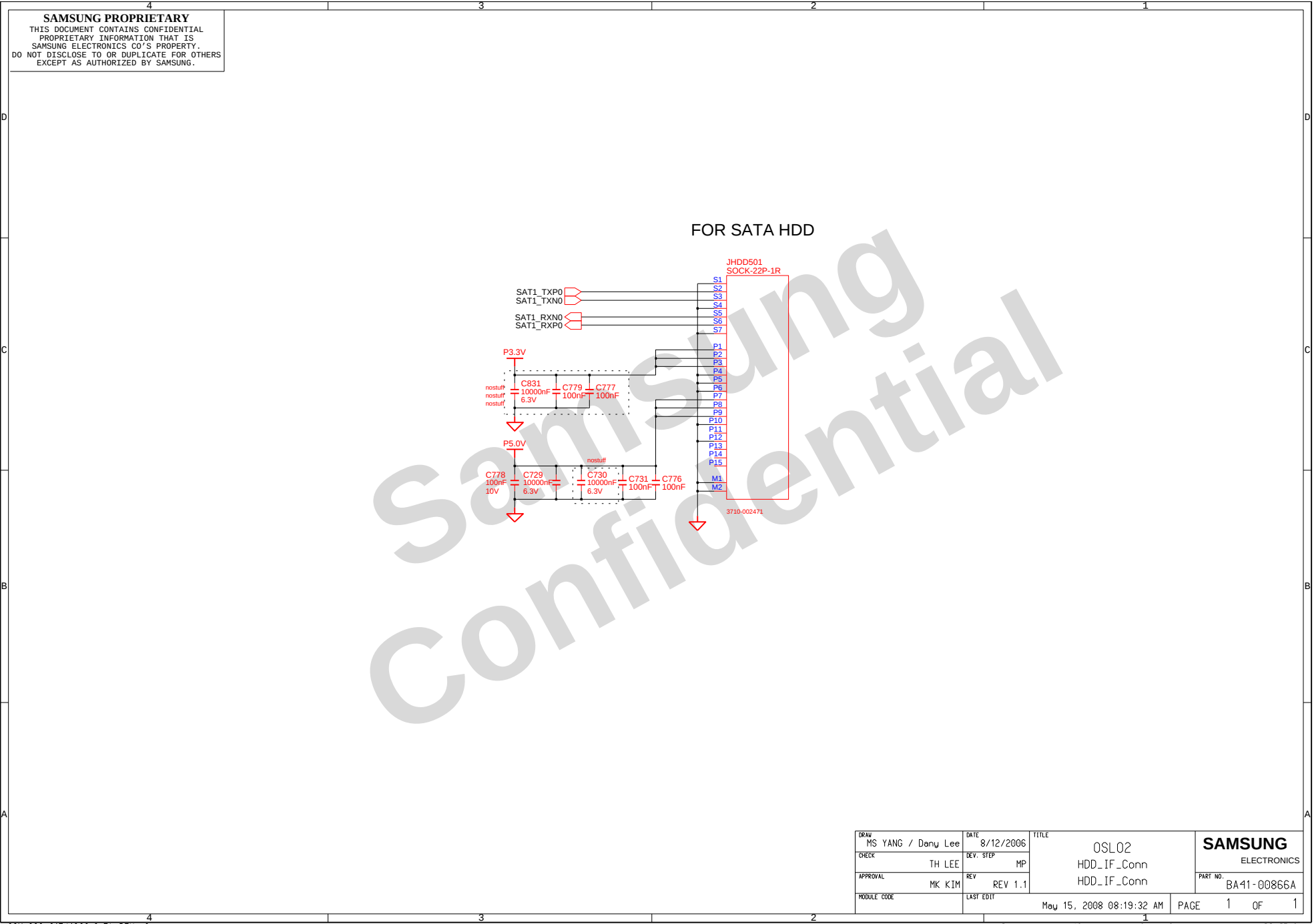
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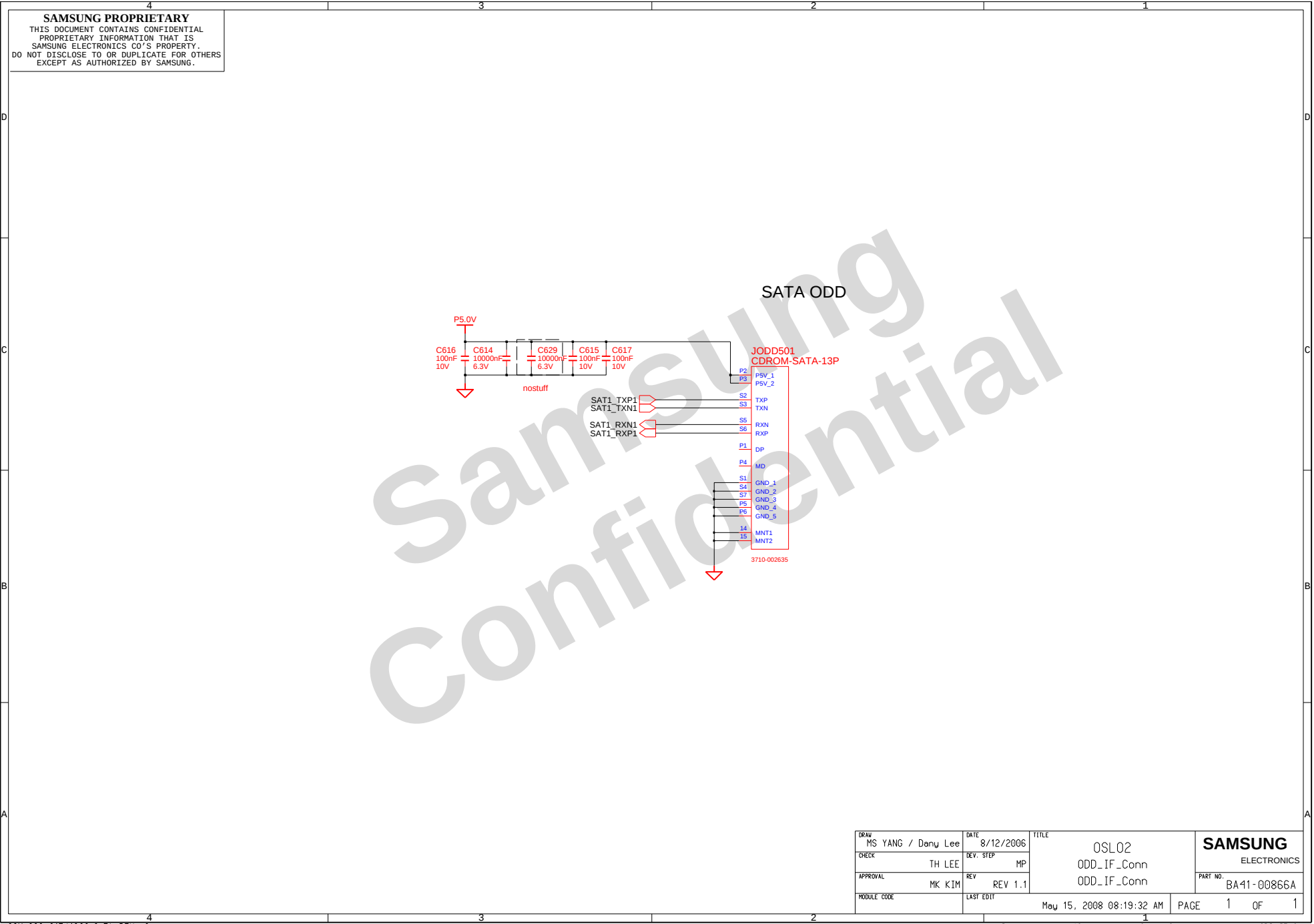
The distance between LAN controller
and transformer is designed to extend less than two inches.



Place near to the Marvell chip.

DRAW	MS YANG / Dany Lee	DATE	8/12/2006	TITLE	OSL02	SAMSUNG ELECTRONICS PART NO. BA11-00866A
CHECK	TH LEE	DEV. STEP	MP		LAN_Marvell_8055	
APPROVAL	MK KIM	REV	REV 1.1		LAN_Marvell_8055	
MODULE CODE		LAST EDIT				
May 15, 2008 08:19:32 AM						PAGE 2 OF 2

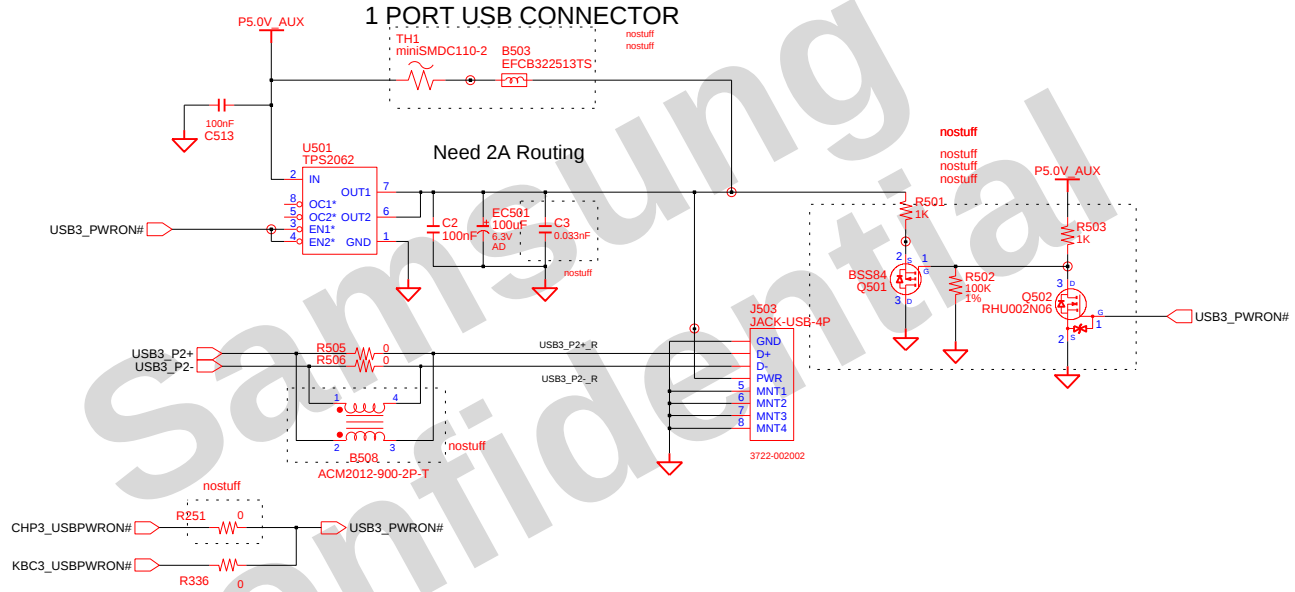




DRW MS YANG / Dany Lee	DATE 8/12/2006	TITLE OSL02	SAMSUNG ELECTRONICS PART NO. BA41-00866A
CHECK TH LEE	DEV. STEP MP	ODD_IF_Conn	
APPROVAL MK KIM	REV REV 1.1	ODD_IF_Conn	
MODULE CODE	LAST EDIT	May 15, 2008 08:19:32 AM	
PAGE 1 OF 1			

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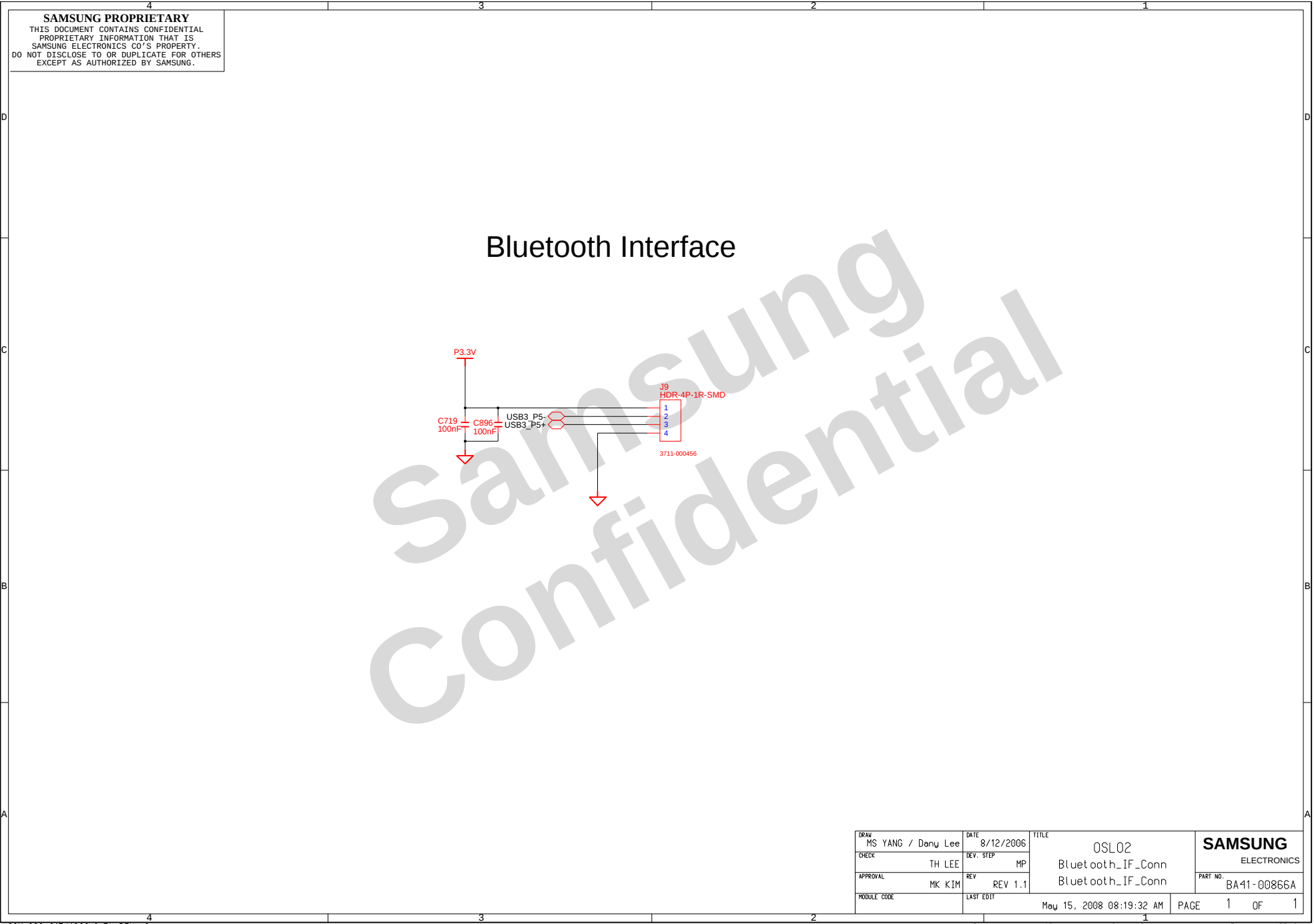
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DRAW MS YANG / Dany Lee	DATE 8/12/2006	TITLE OSL02 USB_1Port USB_1Port	SAMSUNG ELECTRONICS
CHECK TH LEE	DEV. STEP MP		PART NO. BA11-00866A
APPROVAL MK KIM	REV REV 1.1		
MODULE CODE	LAST EDIT	May 15, 2008 08:19:32 AM	PAGE 1 OF 1

[illegible]

D:/users/mobile16/mentor/oslo2/oslo2_pr/design_blocks/USB_2Port
SRP Sheet Number: 55 of 76

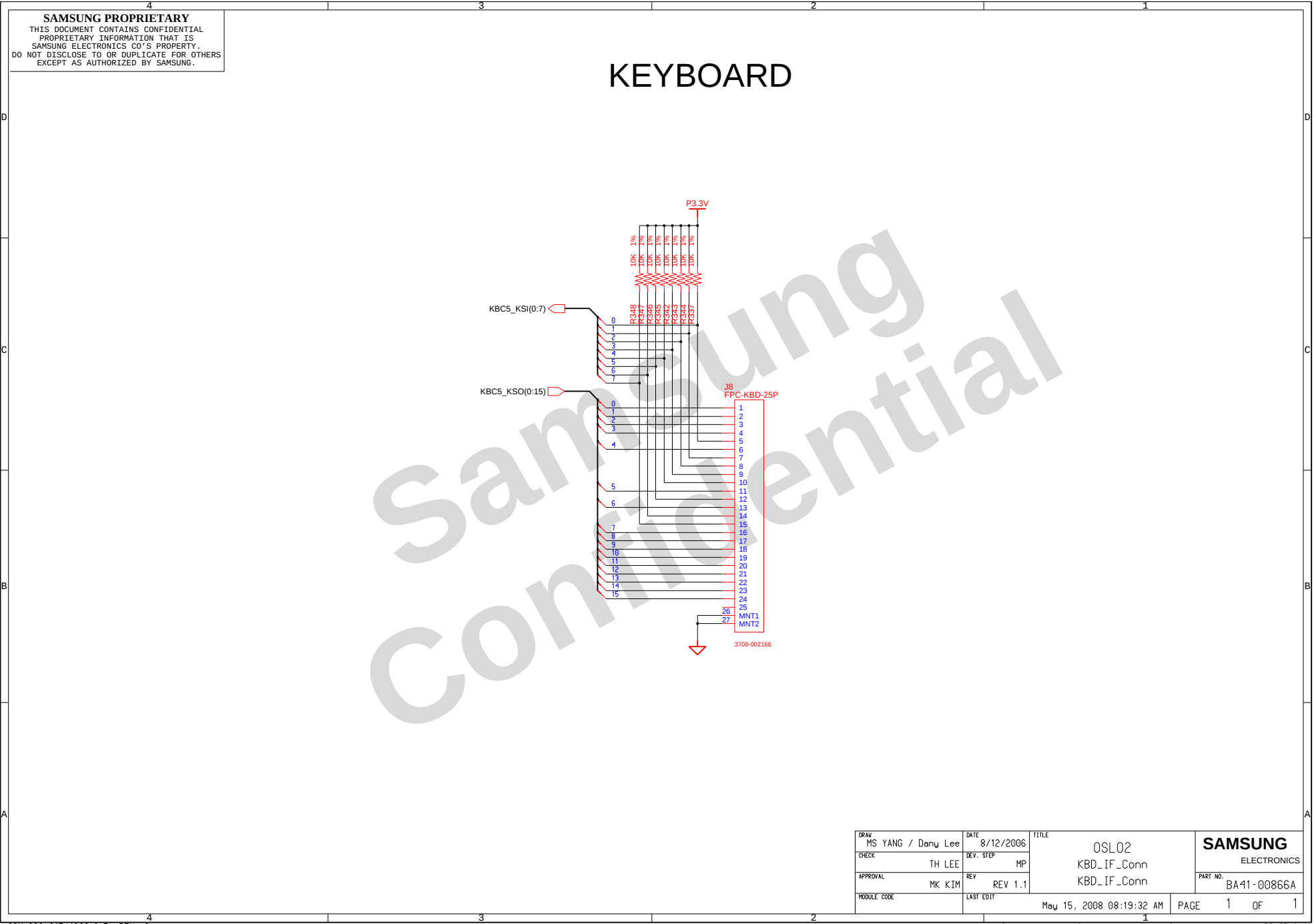


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[illegible]

DRW	MS YANG / Dany Lee	DATE	8/12/2006	TITLE	OSL02		
CHECK	TH LEE	REV. STEP	MP		Camera_IF_Conn		
APPROVAL	MK KIM	REV	REV 1.1		Camera_IF_Conn	PART NO.	BA41-00866A
MODULE CODE		LAST EDIT		May 15, 2008 08:19:32 AM	PAGE	1	OF 1

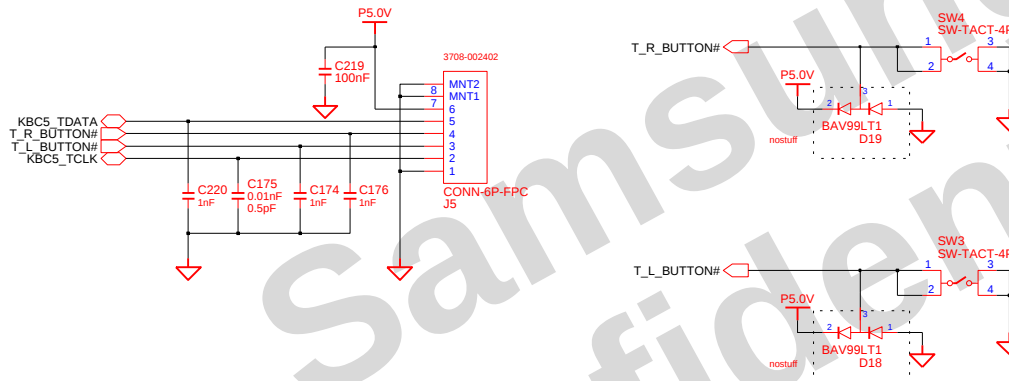




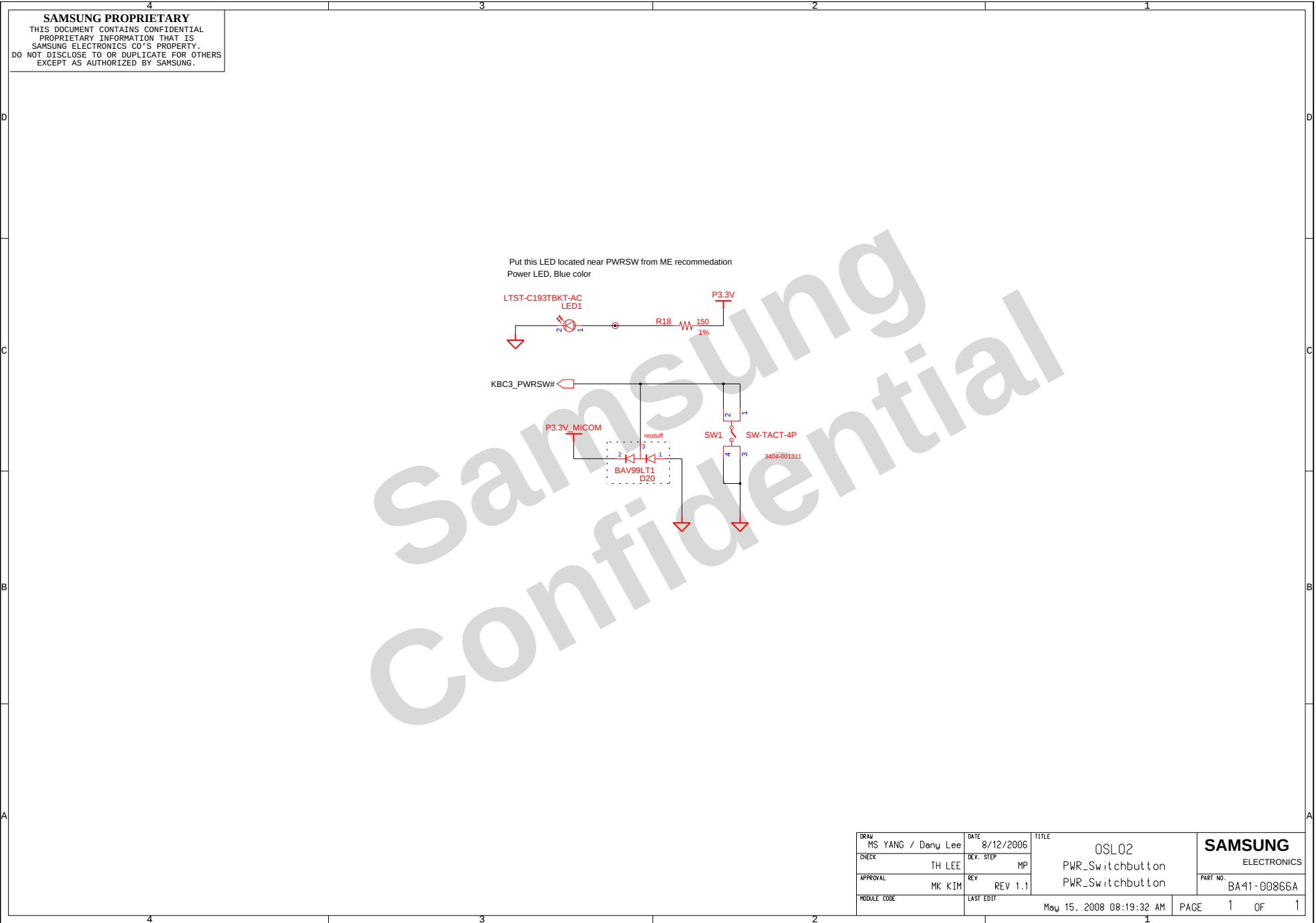
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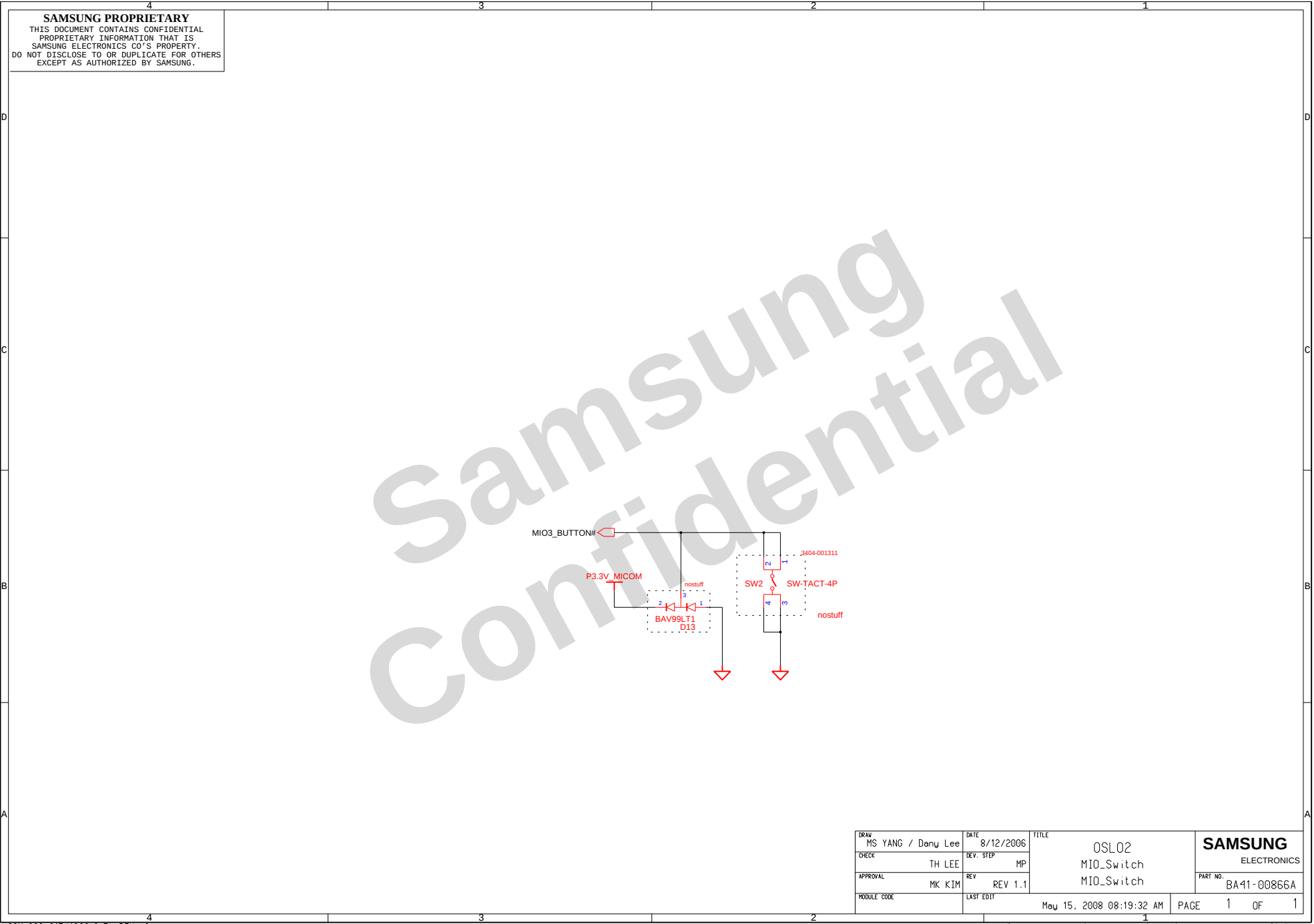
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TOUCHPAD



DRAW MS YANG / Dany Lee	DATE 8/12/2006	TITLE OSL02 Touchpad_IF_Conn Touchpad_IF_Conn	SAMSUNG ELECTRONICS PART NO. BA41-00866A
CHECK TH LEE	DEV. STEP MP	REV REV 1.1	
APPROVAL MK KIM	LAST EDIT	May 15, 2008 08:19:32 AM	PAGE 1 OF 1
MODULE CODE			



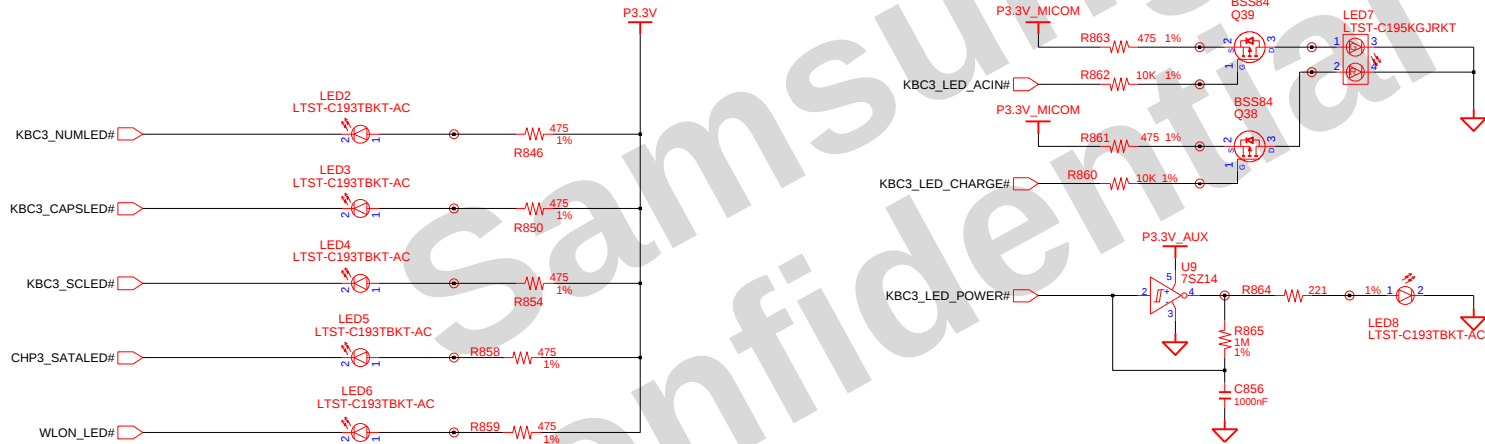


DRAW	MS YANG / Dany Lee	DATE	8/12/2006	TITLE	OSL02	SAMSUNG ELECTRONICS
CHECK	TH LEE	DEV. STEP	MP	MIO_Switch	MIO_Switch	
APPROVAL	MK KIM	REV	REV 1.1			PART NO. BA41-00866A
MODULE CODE		LAST EDIT	May 15, 2008 08:19:32 AM	PAGE	1 OF 1	1

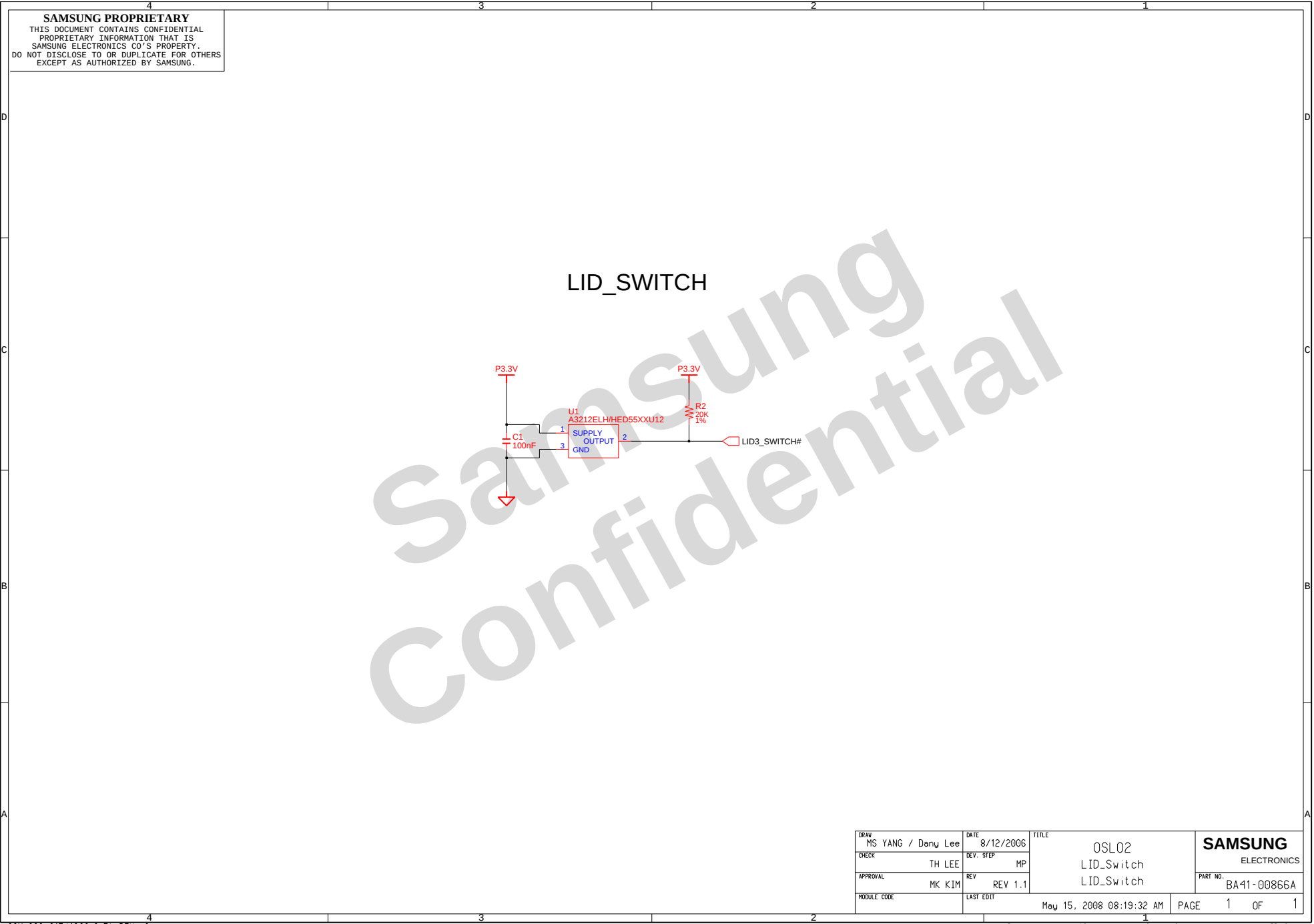
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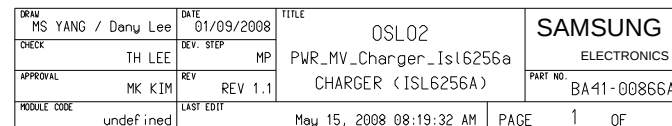
ADAPTERIN/CHARGING LED



DRAW MS YANG / Dany Lee	DATE 8/12/2006	TITLE OSL02 LED_Switch LED_Switch	SAMSUNG ELECTRONICS
CHECK TH LEE	DEV. STEP MP		PART NO. BA41-00866A
APPROVAL MK KIM	REV REV 1.1		
MODULE CODE	LAST EDIT	May 15, 2008 08:19:32 AM	PAGE 1 OF 1



CHARGER & POWER MANAGEMENT

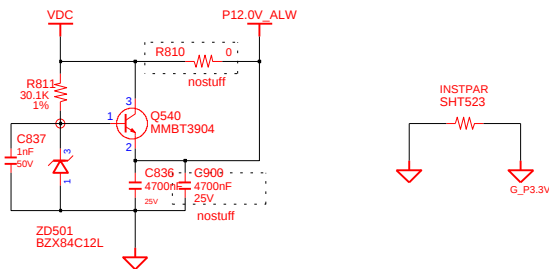


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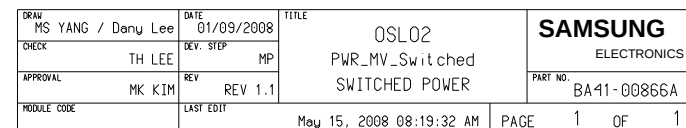
P3.3V_AUX & P5.0V_AUX

TONSEL VREF2
5V / 3.3V : 280KHz / 430KHz
(4.5A)

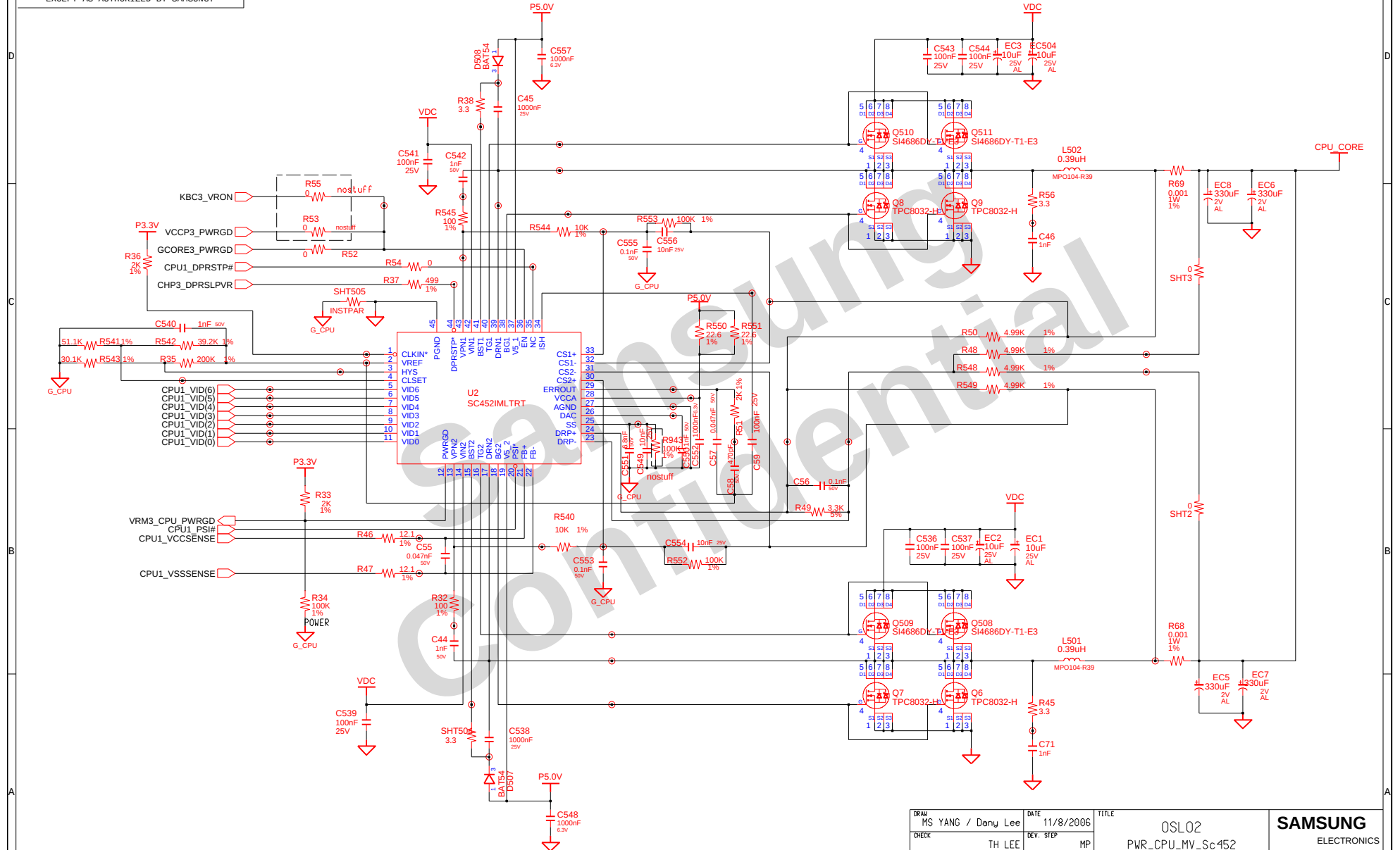
P12V ALW



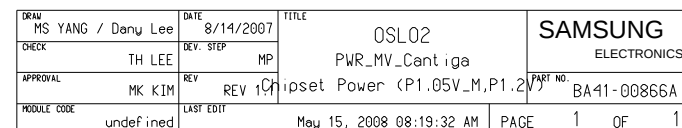
DRAW MS YANG / Dany Lee		DATE 11/8/2006		TITLE OSL02 PWR_MV_3V_5V P3.3V_AUX / P5.0V_AUX		 SAMSUNG ELECTRONICS	
CHECK TH LEE		DEV. STEP MP					
APPROVAL MK KIM		REV REV 1.1					
MODULE CODE undefined		LAST EDIT May 15, 2008 08:19:32 AM					
				PART NO. BA41-00866A		PAGE 1 OF 1	



CPU VRM [SEMTECH]



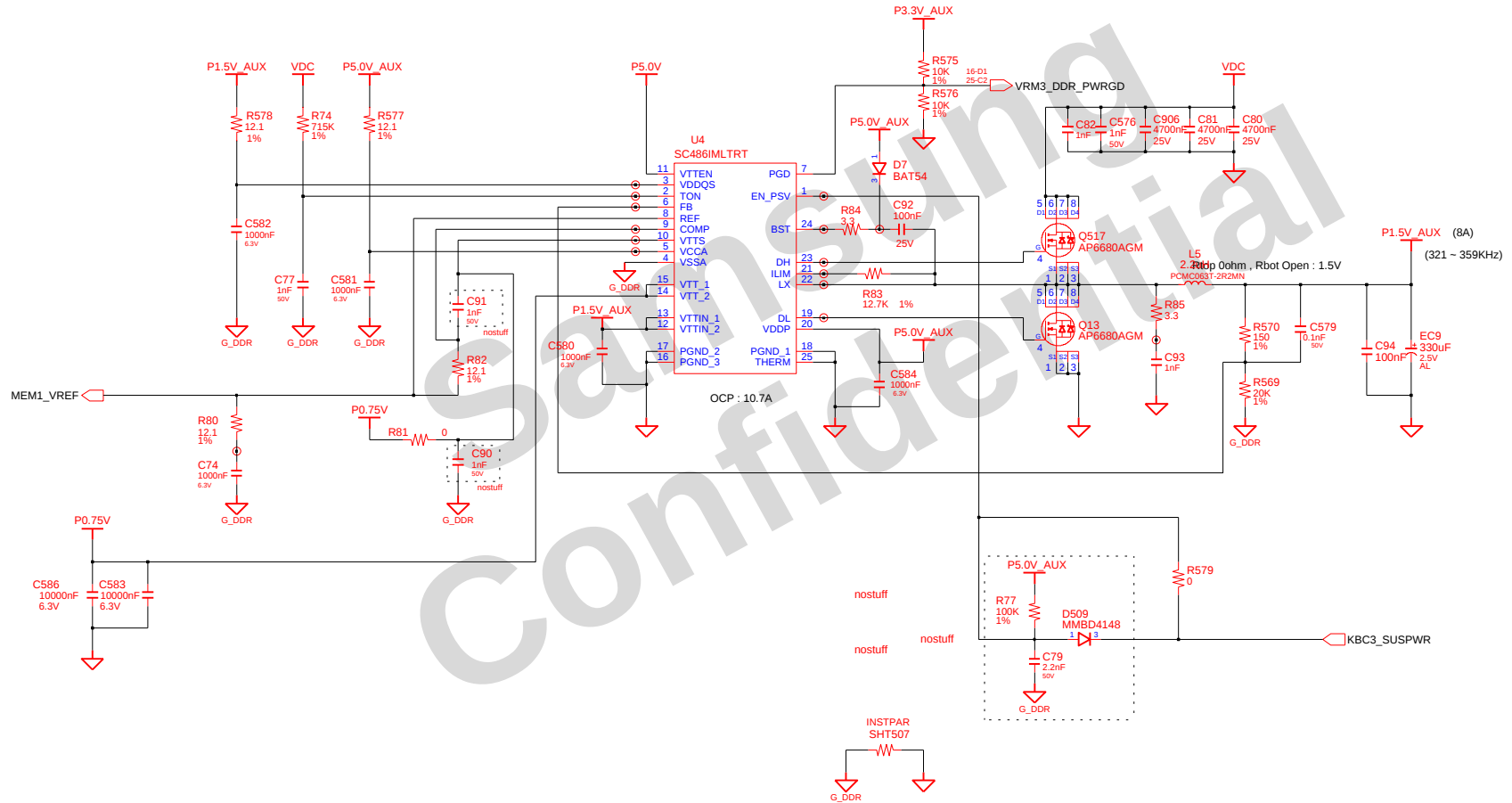
DRAW	MS YANG / Dany Lee	DATE	11/8/2006	TITLE	OSL02	SAMSUNG ELECTRONICS PART NO. BA11-00866A
CHECK	TH LEE	DEV. STEP	MP		PWR_CPU_MV_Sc452	
APPROVAL	MK KIM	REV	1.1		CPU1 VRM (IMVP6+)	
MODULE CODE	undefined	LAST EDIT				
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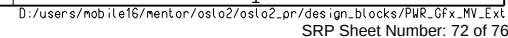
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DDR3 Power

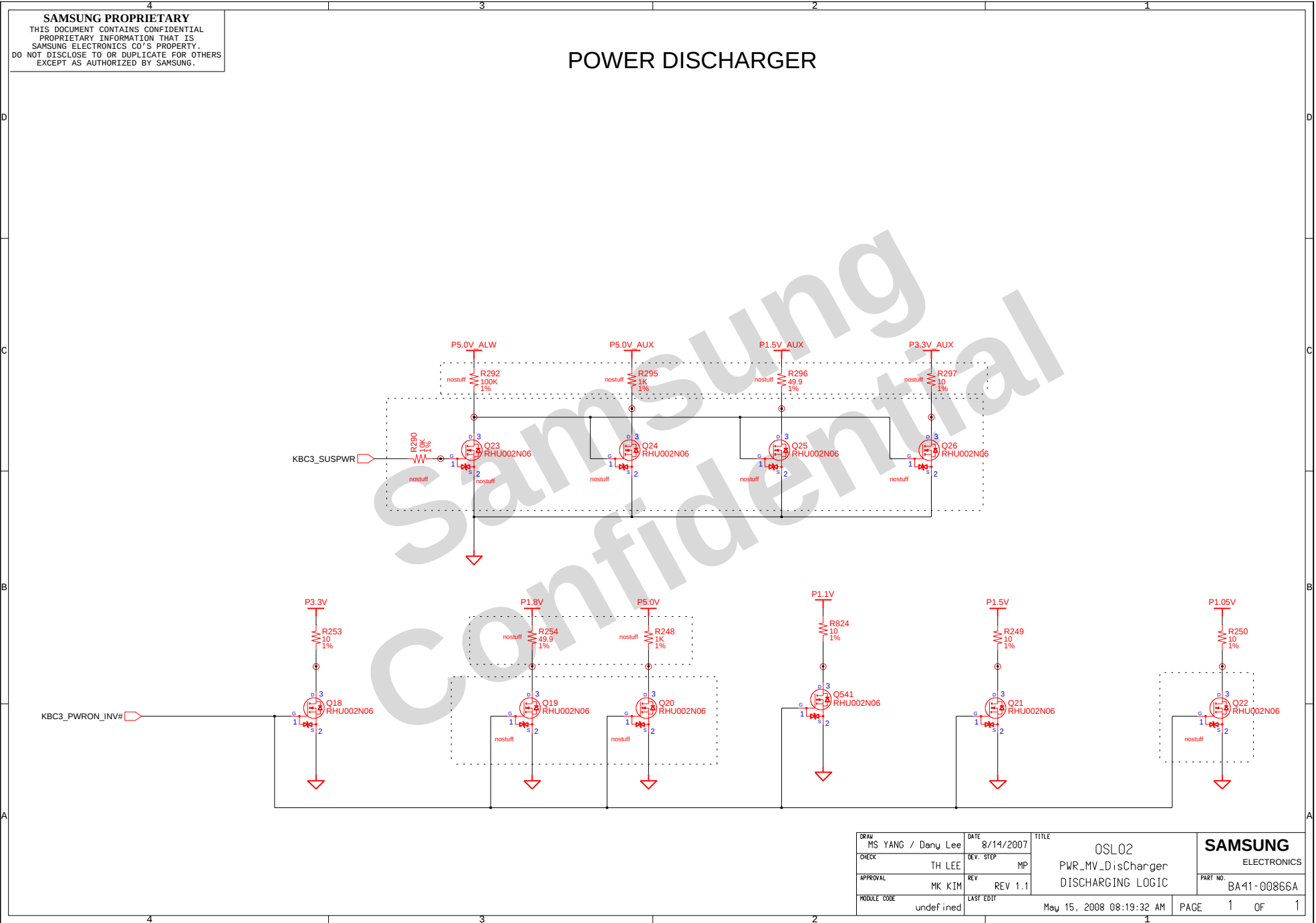


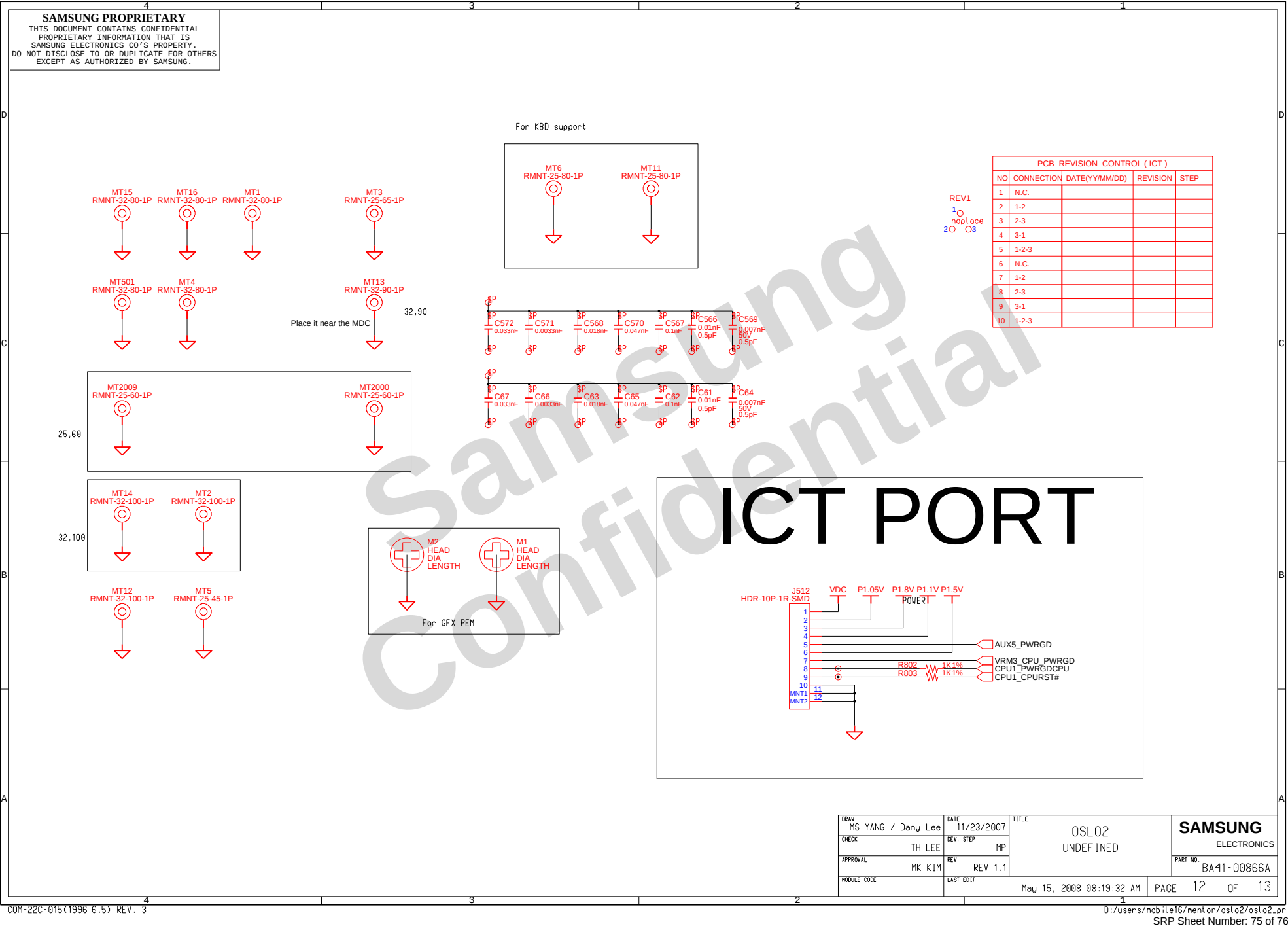
DRAW	MS YANG / Dany Lee	DATE	11/8/2006	TITLE	OSL02	SAMSUNG ELECTRONICS
CHECK	TH LEE	DEV. STEP	MP		PWR_MV_Memory	
APPROVAL	MK KIM	REV	1.1		DDR3 POWER (P1.5V_AUX)	
MODULE CODE	undefined	LAST EDIT				
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COM-22C-015(1996.6.5) REV. 3



36)	SAMSUNG ELECTRONICS PART NO. BA41-00866A
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- ☐ CRT5_DDCDATA
- ☐ CRT5_HSYNC
- ☐ CRT5_VSYNC

- ☐ KBC5_KSO(4)
- ☐ KBC5_KSO(5)
- ☐ KBC5_KSO(6)
- ☐ KBC5_KSO(7)

 P1.8V
 P1.8V
 P1.8V
 P1.8V
 P1.8V
 P1.8V_FBVD
 P1.8V_FBVD

GROUND
GROUND
GROUND
GROUND
GROUND
GROUND
GROUND
GROUND
GROUND

GROUND
GROUND
GROUND
GROUND
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GROUND
GROUND

☐ KBC3_CHC2600
☐ MCD3_SDDATA0
☐ MCD3_SDDATA1
☐ MCD3_SDDATA2
☐ MCD3_SDDATA3
☐ MCD3_XDDATA0
☐ MCD3_XDDATA1
☐ MCD3_XDDATA2
☐ MCD3_XDDATA3
☐ MCD3_XDDATA4
☐ MCD3_XDDATA5
☐ MCD3_XDDATA6
☐ MCD3_XDDATA7
☐ MCD3_MSDATA2_S
☐ MCD3_MSDATA1_X
☐ MCD3_MSDATA3_X
☐ MCD3_SDCLK_MSB
☐ MCD3_XDCLK_MSC
☐ MCD3_MSINS#
☐ MCD3_SDCD#
☐ MCD3_XDALE
☐ MCD3_XDCD#
☐ MCD3_XDCE#

any Lee	DATE 11/23/2
TH LEE	DEV. STEP
	DEV.

DESIGN MS YANG / Dany Lee	DATE 11/23/2007	TITLE OSLO2 UNDEFINED		SAMSUNG ELECTRONICS	
CHECK TH LEE	DEV. STEP MP				
APPROVAL MK KIM	REV REV 1.1				
MODULE CODE	LAST EDIT				
		May 15, 2008 08:19:32 AM		PAGE	13 OF 13

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