

SONY PROPRIETARY INFORMATION

SONY CONFIDENTIAL

REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APPR.
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IRX-3400 SCHEMATICS

MBX-147

FOR MP

REV. 1.154

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○ 4 CLKGEN	8 (CLK GEN-1)	200 - 299	CLOCK GENERATOR
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○ 9 COMBO	24 (COMBO-1)	1800 - 1999	I.LINK & MS-DUO & PCCARD(1/2)
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○ 10 LAN	26 (LAN-1)	2000 - 2199	GIGABIT/FAST ETHERNET
○ 11 LPC	27 (LPC-1)	2500 - 2599	EC/KBC
○ 12 CN	28 (CN-1)	3000 - 3099	KEYBOARD & TP, ETC & LID
	29 (CN-2)	3100 - 3199	IDE CN (SATA, AEGIS / PATA)
	30 (CN-3)	3200 - 3259	LCD CTRL & CN
	31 (CN-4)	3260 - 3299	VGA & I.LINK
	32 (CN-5)	3300 - 3399	PC CARD & EXPRESS CARD
	33 (CN-6)	3400 - 3499	MINI CARD (WIRELESS-LAN)
	34 (CN-7)	3600 - 3699	DOCK CN
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	36 (CN-9)	3900 - 3999	SW & LED & AUDIO
○ 13 POWER	37 (POWER-1)	4000 - 4099	+VCPUCORE
	38 (POWER-2)	5100 - 5199	+VGFXCORE / +VGFXMEM
	39 (POWER-3)	4100 - 4199	+1.5VSUS / +1.05VSUS
	40 (POWER-4)	4200 - 4299	+1.8VSUS / +VDDRVT
	41 (POWER-5)	4300 - 4399	+1.2VSUS / +2.5VSUS
	42 (POWER-6)	4400 - 4499	+3VALW / +5VALW
	43 (POWER-7)	4500 - 4599	LOAD SW
	44 (POWER-8)	4600 - 4699	TIMING SEQUENCER
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○ 15 VARIANCE	50 (VARIANCE-1)		VARIANCE

REVISIONS

STUDY	:	0.010	2005/	6/09
		0.020	2005/	6/10
		0.030	2005/	6/14
		0.040	2005/	6/20
		0.050	2005/	6/27
EVT	:	0.100	2005/	7/01
		0.110	2005/	7/11
		0.120	2005/	7/14
		0.130	2005/	7/25
		0.140	2005/	8/01
		0.150	2005/	8/03
		0.160	2005/	8/05
		0.170	2005/	8/08
		0.180	2005/	8/09
		0.190	2005/	8/10
		0.1A0	2005/	8/11
		0.1B0	2005/	8/16
		0.1B1	2005/	8/19
		0.1B2	2005/	8/22
		0.1B3	2005/	8/25
		0.1B4	2005/	8/30
DVT	:	0.200	2005/	9/17
		0.210	2005/	9/26
		0.220	2005/	9/29
		0.230	2005/	9/30
		0.240	2005/	10/2
		0.241	2005/	10/2
		0.242	2005/	10/11
		0.243	2005/	10/18
		0.244	2005/	10/18.2
		0.245	2005/	10/20
		0.245	2005/	10/20
DVT2	:	0.300	2005/	10/24
		0.310	2005/	10/26
PVT	:	1.100	2005/	11/08
		1.110	2005/	11/10
		1.111	2005/	11/15
		1.120	2005/	11/17
		1.130	2005/	11/19
		1.140	2005/	11/19.2
		1.150	2005/	11/21
		1.151	2005/	11/28
		1.152	2005/	11/30
		1.153	2005/	12/12
MP	:	1.154	2006/	01/16

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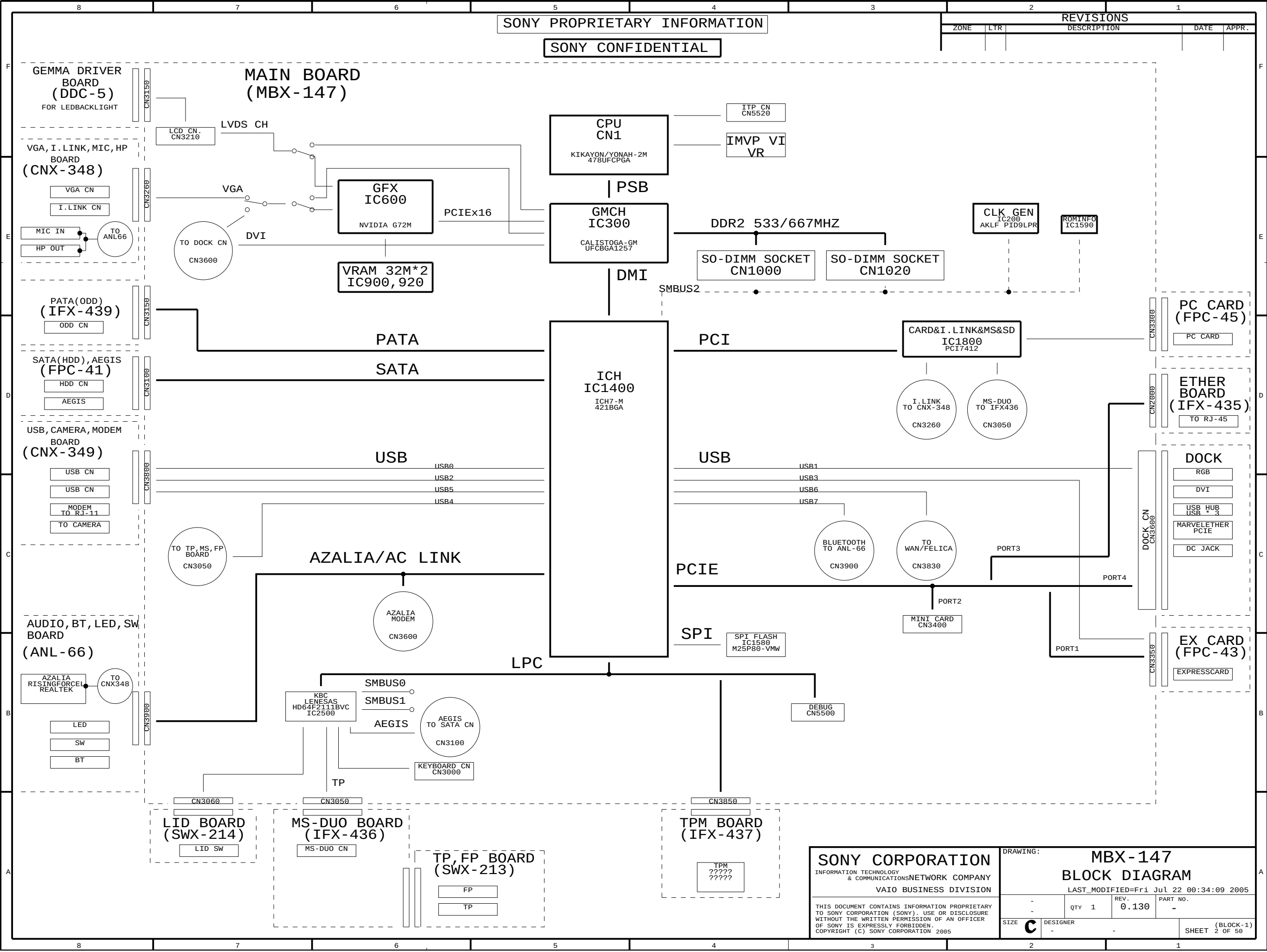
MBX-147

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LAST MODIFIED=Mon Jan 16 20:39:24 2006

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ZS-260			

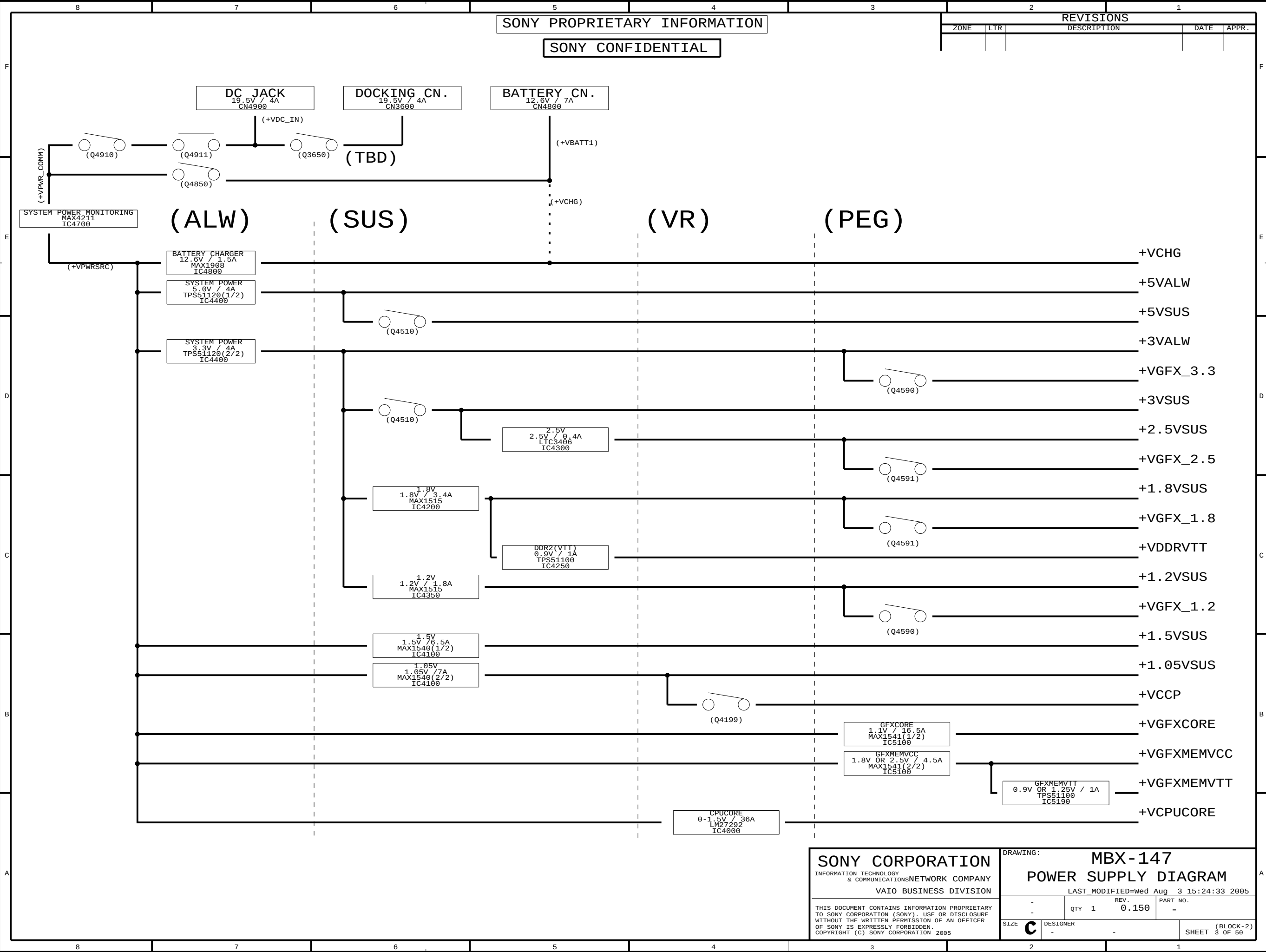
SIZE C	DESIGNER	SHEET 1 OF 50
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DRAWING:		MBX-147	
		BLOCK DIAGRAM	
LAST_MODIFIED=Fri Jul 22 00:34:09 2005			
QTY 1	REV. 0.130	PART NO. -	
SIZE C	DESIGNER -	(BLOCK-1) SHEET 2 OF 50	



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REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APPR.

(EVT)

(REV. 0.100 -> 0.110)

P.07 CHG PARTS : TH110
P.11 DEL REF : C467
P.16 DEL REF : R710,734,762,763
ADD REF : RB760
P.21 CHG REF : RB1463 -> RB1457
PIN SWAP FOR A/W : RB1450 - 1457
DEL REF : R1440
P.22 DEL REF : R1513,RB1560,R1558,1559
CHG REF : R1402 -> R1562
CHG PARTS : Q1580,1581 -> Q1580(2IN1)
CHG PARTS : R1520,1005 -> 0603
CHG NOTES : R1550 MT -> DNI
P.23 CHG NET : C1645.2 +1.05VSUS -> +VCCP
P.25 ADD REF : R1907
P.27 CHG NET : RB2540.5 PM_SLP_S5# -> PM_RSMRST#
P.28 ADD REF : F3050
P.32 CHG NET : CN3350,35,37P USB_P7 -> USB_P3
PIN SWAP : CN3800,32P <-> 33P
P.35 PIN SWAP : IC3800,2P <-> 3P, 5P <-> 8P
CHG PARTS : CN3830
ADD REF : F3811,3830,3831
P.36 CHG NET : CN3900,48,49P USB_P3 -> USB_P7
CHG NOTES : JR3900 DNI -> MT
P.37 DEL REF : SL4050,4051,4070,4071
CHG PARTS : TH4050,4070

(REV. 0.110 -> 0.120)

P.08 CHG NET : IC200.18 D_MECH_DET# -> DOCKED#
P.09 PIN SWAP FOR A/W : IC300,DMI-PORT
CHG NOTES : R309 DNI <-> MT
P.14 PIN SWAP FOR A/W : PEG_TX,RX+,-(POLARITY INVERSION)
P.18 PIN SWAP FOR A/W : FBAD,FBADQM,FBADQS
P.23 ADD REF : JR1611
P.26 DEL REF : RB3931
ADD REF : R3940,3941
P.27 DEL REF : R2500,2502,2515, C2511
P.31 CHG NET : CN3260,17,16,14,13 TPA(<+<->-) <-> TPB
P.34 REF : Q340, R3640
P.36 DEL REF : R2003
ADD REF : JR2001,2002
P.43 ADD REF : R4591,4594,Q4593,4594
P.44 CHG NET : IC4660.2,C4660.2 +3VSUS -> +3VALW

P.ALL CHG PARTS : CHANGE PARTS SIZE 0603 -> 1005
0-262-375-01 -> 1-107-819-81
0-262-376-01 -> 1-119-923-81
0-262-377-01 -> 1-127-772-81
0-262-378-01 -> 1-164-936-81
0-262-379-01 -> 1-164-937-81
0-262-380-01 -> 1-164-943-81

(REV. 0.120 -> 0.130)

P.06 CHG NOTES : JR0 DNI -> MT
P.14 PIN SWAP FOR A/W : IC631,611,FL651,652
DEL PARTS : RB631,633,644
P.15 CHG PARTS : R670,671,6603 -> 1005
P.18 MOV PARTS : TP930,931,960,961
PIN SWAP FOR A/W : IC920 : DQ<8> - DQ<15>
CHG PARTS : R990,991,0603 -> 1005
P.27 ADD NET : IC2500,50 ANL66_ID
P.29 CHG PARTS : CN3100,30P ->20P
ADD PARTS : Q3100
P.30 PIN SWAP FOR A/W : FL3220,3221
CHG PARTS : R3211,3212,0.5% -> 5%
P.33 CHG PARTS : CN3400,3401
CHG NET : CN3400.18 OFFPAGE -> DGND
P.34 DEL PARTS : RB3630.4,5 OFFPAGE -> DGND
PIN SWAP FOR A/W : CN3600 TMDs+,-
P.35 ADD PARTS : R3802,3803
PIN SWAP : CN3830
P.36 DEL PARTS : R3900,D3901,FB3970,3972,3976
CHG PARTS : Q3904,R3902
ADD PARTS : F3970,3972,3976
P.39 PIN SWAP : Q4199
ADD PARTS : R4199,C4199,Q4198
P.41 CHG CIRCUIT ALL : +1.2VSUS(AROUND IC4350)
P.44 ADD NET : IC4610.2 VR_ON#

POWER CHG PARTS : CHG SIZE R(0.5%) 0603 -> 1005

(REV. 0.130 -> 0.140)

P.22 ADD PARTS : SL1508,RB1540,1544
DEL PARTS : R1540, R1547
CHG NET : IC2500.40, RB2530.1 MODEL_ID_EC<1> -> CAM_OFF
P.27 CHG NET : IC2500.47 CAM_OFF -> VAUD_OFF#
CHG PARTS : CN3000
P.28 PIN SWAP : CN3100,1-12
P.29 DEL PARTS : R3650 - 3652,Q3650,3651
P.36 ADD PARTS : R3990
ADD NET : CN3900.25 VAUD_OFF#
CHG NET : CN3900.36 PWRGDSUS -> ANL66_ID

PIN SWAP FOR A/W(FROM A/WKER)
P.8 RB208
P.20 RB1050 - 1056,RB1070 - 1076
P.21 RB1450 - 1455,RB1460 - 1461
P.22 RB1520,1550
P.27 RB2521,2540,2550
P.30 FL3220,3221

(REV. 0.140 -> 0.150)

P.22 CHG PARTS : IC1580
P.31 PIN SWAP FOR A/W : FL3260
P.31 PIN SWAP FOR A/W : CN3260 TPA0 <-> TPB0
P.34 PIN SWAP FOR A/W : CN3600 PCIE_DOCK1_TX <-> CLK_PCIE_DOCK
CHG NET : CN3600 DOCKEDP,N -> GND

(REV. 0.150 -> 0.160)

P.8 ADD PARTS : R250-255
DEL PARTS : RB206
P.10 ADD PARTS : TP380-385
P.14 DEL PARTS : C608,609
P.16 CHG PARTS : R752-754,R776-779
CHG NOTES : R754,778,779 DNI -> MT
P.18 CHG PARTS : TP SIZE 0.3 -> D0.6
P.28 PIN SWAP : CN3000
P.33 CHG PARTS : CN3400,3401
P.36 PARTS CHG : Q3906
P.37 CHG NET : IC4090,17
P.46 CHG PARTS : Q4850,D4860,FB4860-4863

(REV. 0.160 -> 0.170)

P.14 DEL PARTS : RB651

PIN SWAP FOR A/W(FROM A/WKER)
P.21 RB1454
P.28 D3000 - 3004

(REV. 0.170 -> 0.180)

P.6 ADD PARTS : R50,Q51
CHG PARTS : Q50
P.16 ADD PARTS : R780
P.27 CHG NET : RB2550.4,5
P.34 CHG PARTS : Q3610
P.35 PIN SWAP : CN3850

(REV. 0.180 -> 0.190)

P.16 MOV PARTS : R780

(REV. 0.190 -> 0.1A0)

P.29 CHG PARTS : Q3100
P.41 CHG NET : AROUND IC4300

(REV. 0.1A0 -> 0.1B0)

P.8 CHG PARTS : R250
CHG NOTES : R252 MT -> DNI
P.16 ADD PARTS : R755
CHG NOTES : R730,R720 MT -> DNI
CHG NOTES : R719,R731 DNI -> MT
P.34 PIN SWAP : CN3600 D_VGA_RED,GRN,BLU
P.40 CHG PARTS : R4202
P.41 CHG PARTS : R4354

(REV. 0.1B0 -> 0.1B1)

P.19 CHG PARTS : CN1000
P.37 CHG PARTS : L4050,4070,R4011,4050,4070,C4090-4093
P.38 CHG PARTS : R5110,5123,5124,5125,5151,5152,5153,C5130,C5131

(REV. 0.1B1 -> 0.1B2)

P.18 CHG PARTS : IC900,920
P.38 CHG PARTS : L5110

(REV. 0.1B2 -> 0.1B3)

P.26 CHG NOTES : C2000-C2002 MT -> DNI
P.28 CHG NOTES : C3050,3051 MT -> DNI
P.33 CHG NOTES : C3301,3303,3360,3362,3363 MT -> DNI
P.35 CHG NOTES : C3410-3412,3430,3431,3450 MT -> DNI
P.36 CHG NOTES : C3830-3832,3850 MT -> DNI
P.46 CHG NOTES : C4850 MT -> DNI
P.47 CHG NOTES : C4900 MT -> DNI

(REV. 0.1B3 -> 0.1B4)

P.35 CHG NOTES : C3830 DNI -> MT

(DVT)

(REV. 0.1B4 -> 0.200)

P.6 DEL PARTS : JR0,1
ADD PARTS : SL50,51
CHG PARTS : C1,2
P.8 DEL PARTS : JR202
ADD PARTS : SL202
P.9 DEL PARTS : JR302
ADD PARTS : SL302
CHG PARTS : C340,341,344,345
P.14 PIN SWAP : AROUND IC611(VGA),631(LVDS)
DEL PARTS : TP731 AND ADD NET : HOTPLUG
P.18 CHG PARTS : C905,906,925,926
P.19 CHG PARTS : CN1020
P.21 CHG PARTS : C1400,1401,1425
DEL PARTS : JR1500
ADD PARTS : SL1560
CHG NOTES : R1509 MT -> DNI
P.32 CHG PARTS : C1513,1519,1540
P.34 PIN SWAP : IC1800,C8 <-> F8
P.35 CHG PARTS : C1823
P.27 CHG PARTS : C2510
P.32 CHG NET : C3304,3305
P.34 CHG NET : CN3600,20 DGND -> HOTPLUG
P.35 CHG NOTES : JR3810 MT->DNI,JR3811 DNI->MT
CHG PARTS : C3830
P.37 CHG PARTS : C4007,4009
P.39 CHG PARTS : C4121,C4151
P.38 CHG PARTS : C5101,C5151
P.41 CHG PARTS : IC4300 AND AROUND
P.42 CHG PARTS : IC4400 AND AROUND
P.45 ADD PARTS : D4730
DEL PARTS : Q4760
CHG NET : AROUND IC4760
P.46 CHG PARTS : C4723,4722,R4740,4741
PIN SWAP : CN4800

(REV. 0.200 -> 0.210)

P.6 CHG PARTS : C1,2
P.7 CHG PARTS : R100,101,C150
P.11 CHG PARTS : C420,451,453,468
CHG NOTES : C410 DNI -> MT
P.12 CHG PARTS : C495
P.22 ADD SGRP : IC1580 SGRP=03
P.23 DEL PARTS : JR1611
CHG PARTS : C1620,1642
P.24 ADD PARTS : C1860-1864
P.27 CHG NOTES : R2580 DNI -> MT
P.28 PIN SWAP : CN3050.1P-12P
P.29 CHG PARTS : Q3151,3152
DEL PARTS : JR3151,3152
P.32 CHG PARTS : C3354
P.36 CHG PARTS : C3970
P.37 ADD PARTS : Q4054,4074,R4060,4077
CHG PARTS & ADD SGRP : L4050,4070 SGRP=01
ADD SGRP : C4090-4093 SGRP=02
P.38 DEL PARTS & CHG PARTS : Q5171 & Q5170(2IN1)
P.45 CHG PARTS : Q4740
P.46 PIN SWAP : D4860

(REV. 0.210 -> 0.220)

P.9 ADD PARTS : C346-348,R343,344,JR340
P.10 DEL PARTS : TP383,384
P.11 ADD PARTS : C414
CHG PARTS : C410
P.12 ADD PARTS : C496,497,498
P.13 CHG PARTS : R513
P.14 CHG PARTS : FL651,652,R609
P.19 ADD SGRP : CN1000,1020 SGRP=04,05
P.22 CHG REF : JR1500 -> R1501
P.24 CHG PARTS : IC1800
P.25 CHG PARTS : IC1800
P.31 CHG PARTS : D3260
P.35 DEL PARTS : JR3810,3811
CHG PARTS : F3810
P.36 DEL PARTS : JR3900
P.37 CHG PARTS : Q4010
P.39 CHG PARTS : L4140,Q4190,4198
DEL PARTS : L4110
PIN SWAP : R4156,4157,4170,4171,SL4170
ADD PARTS : SL4151,4152,4170,4171,JR4170
P.43 CHG PARTS : Q4500,4592
P.45 CHG PARTS : Q4721
P.46 CHG PARTS : Q4860
P.47 CHG PARTS : Q4914
P.48 CHG PARTS : Q5003,5050

(REV. 0.220 -> 0.230)

P.12 ADD PARTS : C475-479
P.14 PIN SWAP : FL651,652
P.15 ADD PARTS : C667,668
P.22 CHG NET : RB1550,1,8P->NC, IC1400 F19P->PCIE_WAKE_F#
P.25 CHG PARTS : C1915,C1916
P.31 CHG PARTS : C3260,3261
P.37 CHG PARTS : R4052,4072
P.38 CHG PARTS : C5130,5131
CHG NET : IC5100,20,3VALW->+3VSUS
CHG NET : R5177,20,3VALW->+3VSUS
DEL PARTS : C5150,R5140,5154-5156,5173
ADD PARTS : R5160,5161,C5163
P.40 CHG PARTS : R4207,C4209
ADD PARTS : JR4255
P.42 CHG PARTS : R4411,4412,4413,4421,4422,4423
P.43 CHG PARTS : R4510,4520,4550,4552-4554,4591-4593
P.44 CHG PARTS & CHG REF : JR4020 -> SL4020
P.45 CHG PARTS : D4710,4730,R4710,4723,4740,4741,C4740
CHG NET : R4714.1 IC4700.4->IC4700.5
IC4700.3 C4711.2->DGND
DEL PARTS : D4711,4713,R4711,Q4711
P.46 DEL PARTS : R4812,4813
CHG PARTS : D4830
P.46 CHG PARTS : Q4860
P.47 CHG PARTS : Q4914
ADD PARTS : C4901
P.48 CHG PARTS : Q5003,5050

(REV. 0.230 -> 0.240)

P.17 ADD PARTS : C838
P.22 CHG PARTS : RB1540,1544
P.38 ADD PARTS : R5190,5191,5184
CHG NET : IC5100.34,35P

(REV. 0.240 -> 0.241)

P.29 CHG P/N : CN3100(SAME PARTS)
P.30 CHG P/N : CN3201(SAME PARTS)
P.32 CHG P/N : CN3300(SAME PARTS)
P.34 CHG P/N : CN3600(SAME PARTS)
P.49 CHG P/N : CN5520(SAME PARTS)

(REV. 0.241 -> 0.242)

P.7 CHG PARTS : C100-125,22UF ->10UF
CHG NOTES : MT->DNI C102,204,107,109,110-113
P.11 CHG PARTS : L450,453,JR401
P.15 CHG PARTS : R682
P.31 CHG PARTS : Q3280
P.43 CHG PARTS : Q4570

(REV. 0.242 -> 0.243)

P.39 CHG PARTS : L4140,R4151
P.41 CHG PARTS : R4354,R4357

(REV. 0.243 -> 0.244)

P.30 CHG PARTS : FL3220,3221

(REV. 0.244 -> 0.245)

P.16 CHG NOTES : MT->DNI R703,706
DNI->MT R704,705
P.30 CHG PARTS : FL3220,3221,CN3200
P.39 CHG PARTS : IC4100
CHG NOTES : DNI->MT R4190
P.42 CHG PARTS : R4430,4450
P.46 CHG PARTS : CN4800

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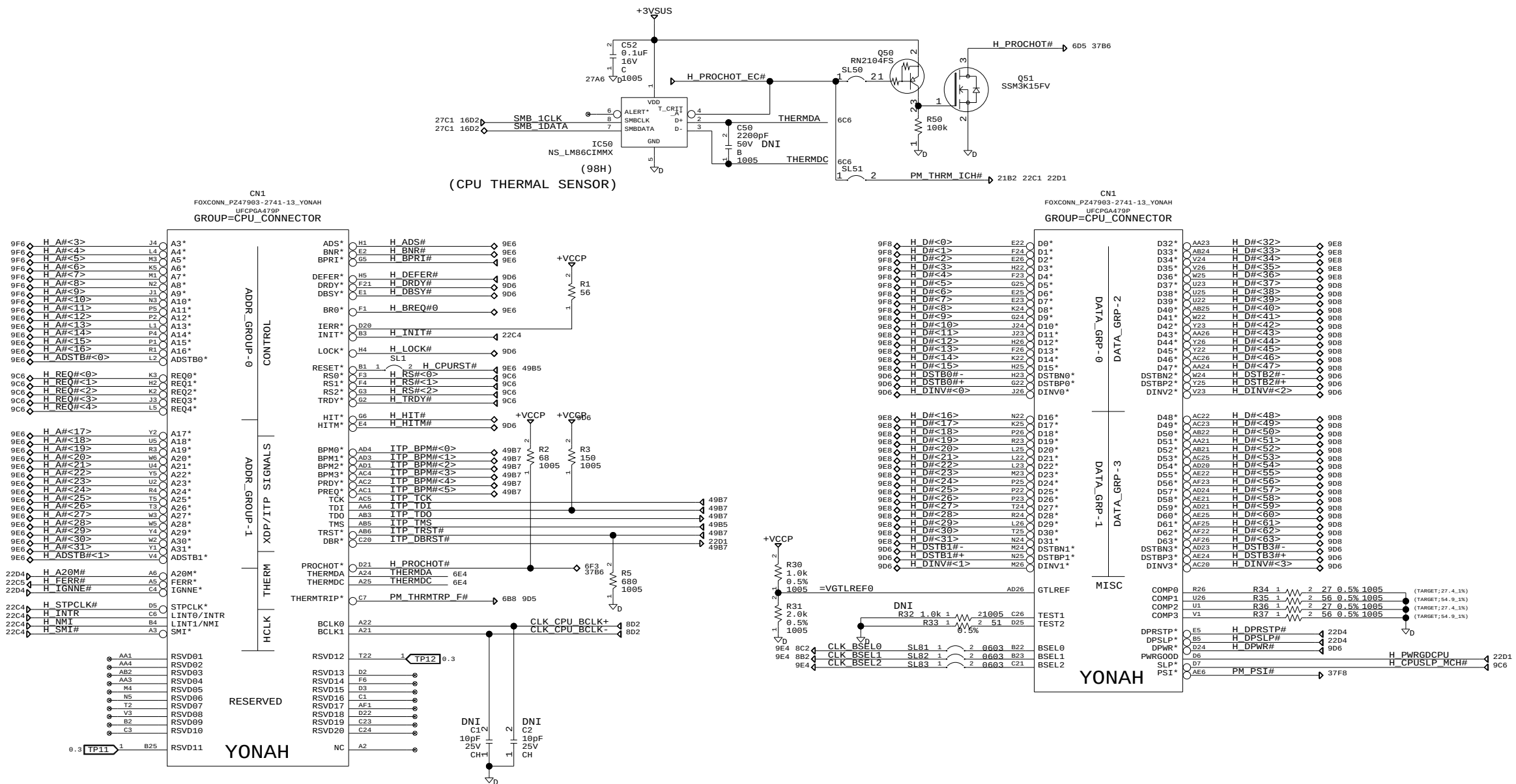
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DRAWING:

(REF: 0- 100)

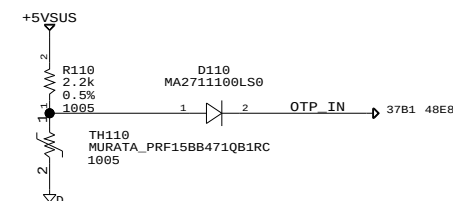
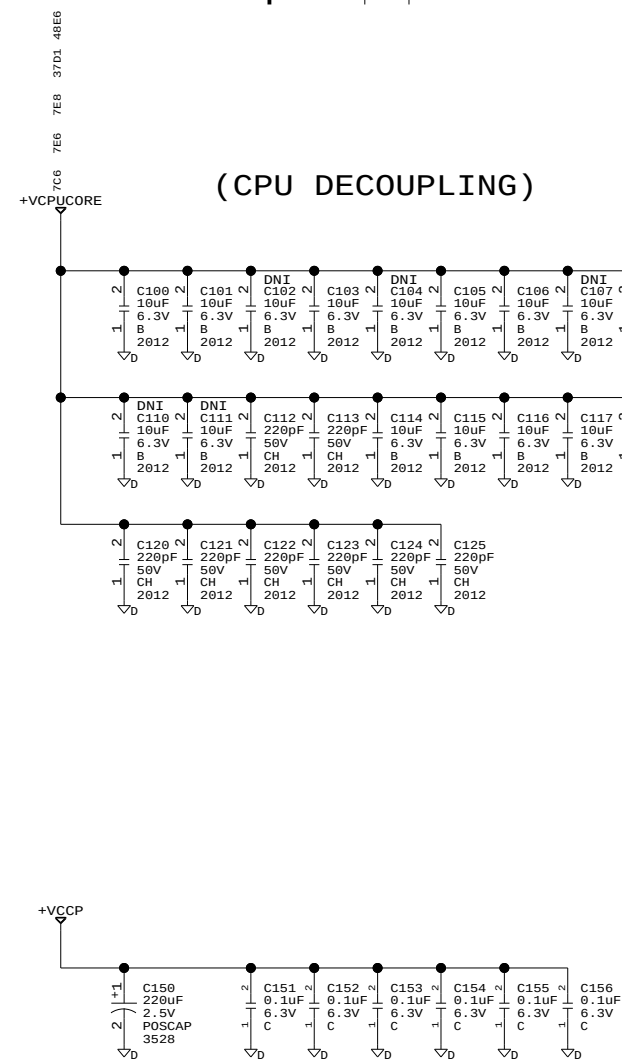
MBX-147
CPU (1/2)

LAST_MODIFIED=Mon Jan 16 20:46:47 2006

SIZE	QTY	REV.	PART NO.
C	1	0.210	-
DESIGNER			

SHEET 6 OF 59 (CPU-1)

(CPU DECOUPLING)



DRAWING:		MBX-147 CPU (2/2)		A	
LAST_MODIFIED=Mon Jan 16 20:46:49 2006					
-	QTY 1	REV. 1.154	PART NO.		
-					
SIZE	DESIGNER			(CPU-2)	
C	-			SHEET 7 OF 50	

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.

(READ ADDRESS D2)

FSB	FSA	CPUCLK
0	0	100MHz
1	0	133MHz
1	1	200MHz
1	1	166MHz

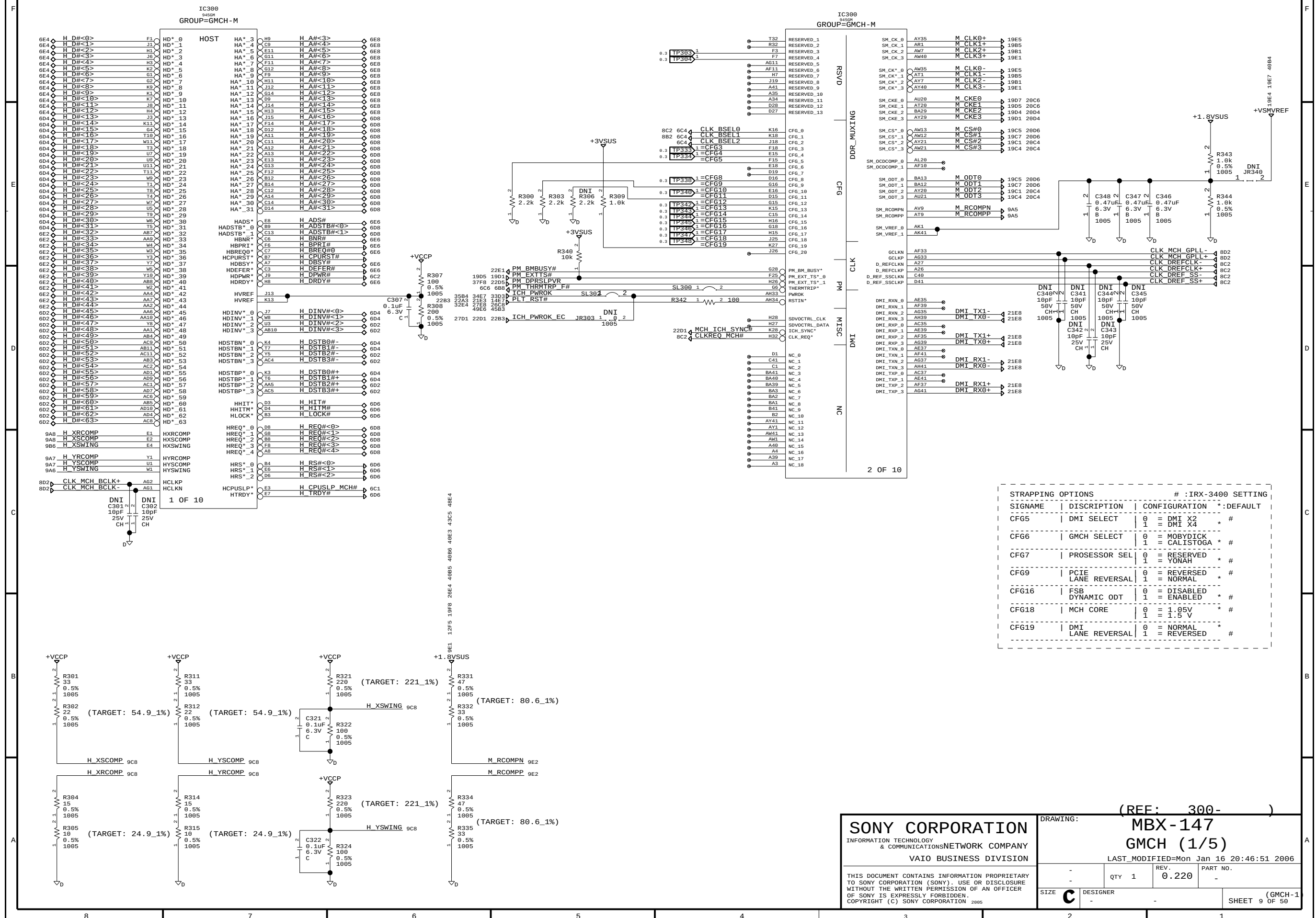
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LAST_MODIFIED=Mon Jan 16 20:46:50 2006			
-	QTY 1	REV. 1.151	PART NO. -
SIZE C	DESIGNER -	SHEET (CLKGEN-1) 8 OF 50	

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REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APPR.



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REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APPR.

IC300
945GM
GROUP=GMCH-M

19E7	M A DQ<0>	AJ35	SA DQ_0	SA BS_0	AU12	M A BS<0>	19C7 20D6
19E7	M A DQ<1>	AJ34	SA DQ_1	SA BS_1	AV14	M A BS<1>	19C5 20C6
19E7	M A DQ<2>	AM31	SA DQ_2	SA BS_2	BA20	M A BS<2>	19D7 20C6
19E7	M A DQ<3>	AM33	SA DQ_3				
19E5	M A DQ<4>	AJ36	SA DQ_4	SA CAS*	AY13	M A CAS#	19C7 20D6
19E5	M A DQ<5>	AK35	SA DQ_5	SA DM_0	AJ33	M A DM<0>	19E5
19E5	M A DQ<6>	AJ32	SA DQ_6	SA DM_1	AK35	M A DM<1>	19E5
19E5	M A DQ<7>	AH31	SA DQ_7	SA DM_2	AL26	M A DM<2>	19D5
19E7	M A DQ<8>	AK35	SA DQ_8	SA DM_3	AN22	M A DM<3>	19D7
19E7	M A DQ<9>	AP33	SA DQ_9	SA DM_4	AM14	M A DM<4>	19C5
19E7	M A DQ<10>	AR31	SA DQ_10	SA DM_5	AL9	M A DM<5>	19C7
19E7	M A DQ<11>	AP31	SA DQ_11	SA DM_6	AR3	M A DM<6>	19B5
19E5	M A DQ<12>	AK36	SA DQ_12	SA DM_7	AM4	M A DM<7>	19B7
19E5	M A DQ<13>	AK36	SA DQ_13				
19E5	M A DQ<14>	AK34	SA DQ_14	SA DQS_0	AK33	M A DQS0+	19E7
19E5	M A DQ<15>	AK33	SA DQ_15	SA DQS_1	AT33	M A DQS1+	19E7
19D7	M A DQ<16>	AK26	SA DQ_16	SA DQS_2	AN28	M A DQS2+	19D7
19D7	M A DQ<17>	AL27	SA DQ_17	SA DQS_3	AM22	M A DQS3+	19D5
19D7	M A DQ<18>	AK26	SA DQ_18	SA DQS_4	AN12	M A DQS4+	19C7
19D7	M A DQ<19>	AN24	SA DQ_19	SA DQS_5	AN8	M A DQS5+	19C5
19D5	M A DQ<20>	AK28	SA DQ_20	SA DQS_6	AP3	M A DQS6+	19B7
19D5	M A DQ<21>	AL28	SA DQ_21	SA DQS_7	AG5	M A DQS7+	19B7 19B5
19D5	M A DQ<22>	AM24	SA DQ_22	SA DQS* 0	AK32	M A DQS0-	19E7
19D5	M A DQ<23>	AP26	SA DQ_23	SA DQS* 1	AU33	M A DQS1-	19E7
19D7	M A DQ<24>	AP23	SA DQ_24	SA DQS* 2	AK27	M A DQS2-	19D7
19D7	M A DQ<25>	AL22	SA DQ_25	SA DQS* 3	AM21	M A DQS3-	19D5
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19D7	M A DQ<27>	AN20	SA DQ_27	SA DQS* 5	AL8	M A DQS5-	19C5
19D5	M A DQ<28>	AT23	SA DQ_28	SA DQS* 6	AK3	M A DQS6-	19B7
19D5	M A DQ<29>	AP24	SA DQ_29	SA DQS* 7	AH5	M A DQS7-	19B7 19B5
19D5	M A DQ<30>	AP20	SA DQ_30				
19D5	M A DQ<31>	AT21	SA DQ_31	SA MA_0	AV16	M A A<0>	19C5 20C6
19C7	M A DQ<32>	AR12	SA DQ_32	SA MA_1	AU14	M A A<1>	19C7 20D6
19C7	M A DQ<33>	AR14	SA DQ_33	SA MA_2	AM16	M A A<2>	19C5 20D6
19C7	M A DQ<34>	AP13	SA DQ_34	SA MA_3	BA16	M A A<3>	19C7 20D6
19C7	M A DQ<35>	AP12	SA DQ_35	SA MA_4	BA17	M A A<4>	19C5 20D6
19C5	M A DQ<36>	AT13	SA DQ_36	SA MA_5	AU16	M A A<5>	19C7 20C6
19C5	M A DQ<37>	AT12	SA DQ_37	SA MA_6	AV17	M A A<6>	19D5 20D6
19C5	M A DQ<38>	AL14	SA DQ_38	SA MA_7	AU17	M A A<7>	19D5 20D6
19C5	M A DQ<39>	AL12	SA DQ_39	SA MA_8	AW17	M A A<8>	19D7 20C6
19C7	M A DQ<40>	AK9	SA DQ_40	SA MA_9	AT16	M A A<9>	19D7 20C6
19C7	M A DQ<41>	AN7	SA DQ_41	SA MA_10	AU13	M A A<10>	19C7 20D6
19B7	M A DQ<42>	AK8	SA DQ_42	SA MA_11	AT17	M A A<11>	19D5 20C6
19B7	M A DQ<43>	AK7	SA DQ_43	SA MA_12	AV20	M A A<12>	19D7 20C6
19C5	M A DQ<44>	AP9	SA DQ_44	SA MA_13	AV12	M A A<13>	19C5 20D6
19C5	M A DQ<45>	AN9	SA DQ_45				
19B5	M A DQ<46>	AT5	SA DQ_46	SA RAS*	AK14	M A RAS#	19C5 20D6
19B5	M A DQ<47>	AL5	SA DQ_47	SA RCVENIN*	AK23	=A RCVENIN#	=B RCVENOUT#
19B7	M A DQ<48>	AY2	SA DQ_48	SA RCVENOUT*	AK24	=A RCVENIN#	=B RCVENOUT#
19B7	M A DQ<49>	AM2	SA DQ_49	SA WE*	AY14	M A WE#	19C7 20D6
19B7	M A DQ<50>	AP1	SA DQ_50				
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19B5	M A DQ<52>	AV2	SA DQ_52				
19B5	M A DQ<53>	AT3	SA DQ_53				
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19B7	M A DQ<56>	AG7	SA DQ_56				
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19B5	M A DQ<59>	AF6	SA DQ_59				
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19B5	M A DQ<61>	AH6	SA DQ_61				
19B5	M A DQ<62>	AF4	SA DQ_62				
19B5	M A DQ<63>	AF8	SA DQ_63				

DDR
SYSTEM
MEMORY-A

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19B5 19D5	M A DQS7+	1	TP380	0.3
19B5 19C5	M A DQS7-	1	TP381	0.3
19B5 19B7	M A DQ<63>	1	TP382	0.3

19E4 19E4	M B DQ<0>	1	TP385	0.3
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IC300
945GM
GROUP=GMCH-M

19E4	19B7	M B DQ<0>	AK39	SB DQ_0	AT24	M B BS<0>	19C4 20C4
19E4	M B DQ<1>	AJ37	SB DQ_1	SB BS_1	AV23	M B BS<1>	19C1 20C4
19E4	M B DQ<2>	AP39	SB DQ_2	SB BS_2	AY28	M B BS<2>	19D4 20D4
19E4	M B DQ<3>	AM41	SB DQ_3				
19E1	M B DQ<4>	AJ38	SB DQ_4	SB CAS*	AR24	M B CAS#	19C4 20C4
19E1	M B DQ<5>	AK38	SB DQ_5	SB DM_0	AK36	M B DM<0>	19E1
19E1	M B DQ<6>	AM41	SB DQ_6	SB DM_1	AR38	M B DM<1>	19E1
19E1	M B DQ<7>	AP41	SB DQ_7	SB DM_2	AT36	M B DM<2>	19D1
19E4	M B DQ<8>	AT40	SB DQ_8	SB DM_3	BA31	M B DM<3>	19D4
19E4	M B DQ<9>	AV41	SB DQ_9	SB DM_4	AL17	M B DM<4>	19C1
19E4	M B DQ<10>	AU38	SB DQ_10	SB DM_5	AH8	M B DM<5>	19C4
19E4	M B DQ<11>	AV38	SB DQ_11	SB DM_6	BA5	M B DM<6>	19B1
19E1	M B DQ<12>	AP38	SB DQ_12	SB DM_7	AN4	M B DM<7>	19B4
19E1	M B DQ<13>	AR40	SB DQ_13				
19E1	M B DQ<14>	AW38	SB DQ_14	SB DQS_0	AK39	M B DQS0+	19E4
19E1	M B DQ<15>	AY38	SB DQ_15	SB DQS_1	AT39	M B DQS1+	19E4
19D4	M B DQ<16>	BA38	SB DQ_16	SB DQS_2	AU35	M B DQS2+	19D4
19D4	M B DQ<17>	AV36	SB DQ_17	SB DQS_3	AR29	M B DQS3+	19D1
19D4	M B DQ<18>	AR36	SB DQ_18	SB DQS_4	AR16	M B DQS4+	19C4
19D4	M B DQ<19>	AP36	SB DQ_19	SB DQS_5	AR10	M B DQS5+	19C1
19D1	M B DQ<20>	BA36	SB DQ_20	SB DQS_6	AR7	M B DQS6+	19B4
19D1	M B DQ<21>	AU36	SB DQ_21	SB DQS* 0	AN5	M B DQS7+	19B1
19D1	M B DQ<22>	AP35	SB DQ_22	SB DQS* 1	AM40	M B DQS0-	19E4
19D1	M B DQ<23>	AP34	SB DQ_23	SB DQS* 2	AU39	M B DQS1-	19E4
19D4	M B DQ<24>	AY33	SB DQ_24	SB DQS* 3	AT35	M B DQS2-	19D4
19D4	M B DQ<25>	BA33	SB DQ_25	SB DQS* 4	AP29	M B DQS3-	19D1
19D4	M B DQ<26>	AT31	SB DQ_26	SB DQS* 5	AP16	M B DQS4-	19C4
19D4	M B DQ<27>	AU29	SB DQ_27	SB DQS* 6	AT19	M B DQS5-	19C1
19D1	M B DQ<28>	AU31	SB DQ_28	SB DQS* 7	AT7	M B DQS6-	19B4
19D1	M B DQ<29>	AW31	SB DQ_29		AP5	M B DQS7-	19B1
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19C4	M B DQ<32>	AN19	SB DQ_32	SB MA_1	AM24	M B A<1>	19C4 20D4
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19C4	M B DQ<34>	AP14	SB DQ_34	SB MA_3	AR28	M B A<3>	19C4 20D4
19C4	M B DQ<35>	AM14	SB DQ_35	SB MA_4	AT27	M B A<4>	19C1 20D4
19C1	M B DQ<36>	AN17	SB DQ_36	SB MA_5	AT26	M B A<5>	19C4 20D4
19C1	M B DQ<37>	AM16	SB DQ_37	SB MA_6	AU27	M B A<6>	19D1 20D4
19C1	M B DQ<38>	AP15	SB DQ_38	SB MA_7	AV28	M B A<7>	19D1 20D4
19C1	M B DQ<39>	AL15	SB DQ_39	SB MA_8	AV27	M B A<8>	19D4 20D4
19C4	M B DQ<40>	AJ11	SB DQ_40	SB MA_9	AW27	M B A<9>	19D4 20D4
19C4	M B DQ<41>	AM10	SB DQ_41	SB MA_10	AV24	M B A<10>	19C4 20C4
19B4	M B DQ<42>	AJ9	SB DQ_42	SB MA_11	BA27	M B A<11>	19D1 20D4
19B4	M B DQ<43>	AM10	SB DQ_43	SB MA_12	AT27	M B A<12>	19D4 20D4
19C1	M B DQ<44>	AK13	SB DQ_44	SB MA_13	AR23	M B A<13>	19C1 20C4
19C1	M B DQ<45>	AH11	SB DQ_45				
19B1	M B DQ<46>	AK10	SB DQ_46	SB RAS*	AU23	M B RAS#	19C1 20C4
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19B4	M B DQ<49>	AW10	SB DQ_49	SB WE*	AR27	M B WE#	19C4 20C4
19B4	M B DQ<50>	BA4	SB DQ_50				
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19B1	M B DQ<61>	AK5	SB DQ_61				
19B1	M B DQ<62>	AJ5	SB DQ_62				
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DDR
SYSTEM
MEMORY-B

5 OF 10

(REF: - 399)

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DRAWING:

MBX-147
GMCH (2/5)

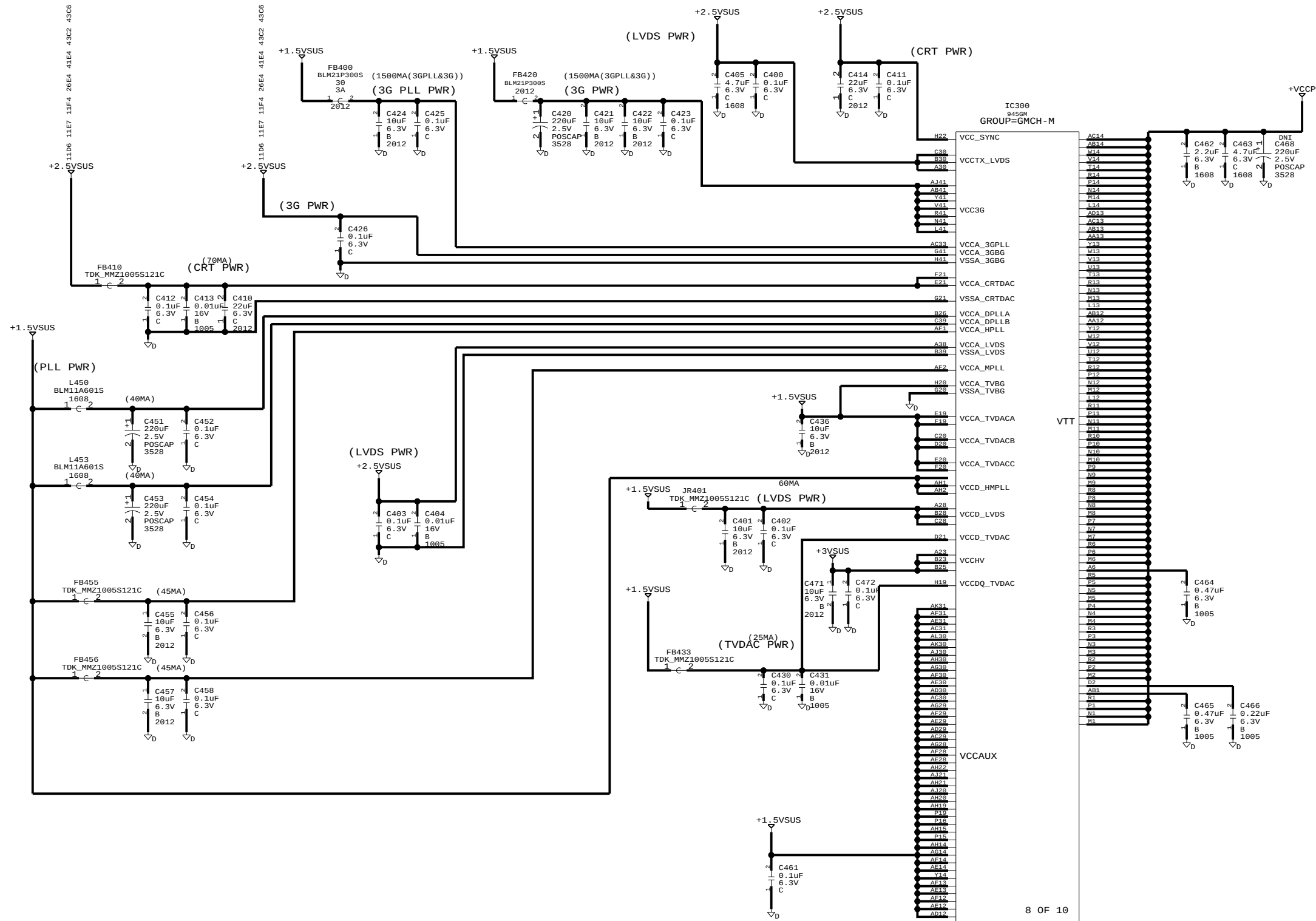
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QTY	1	REV.	0.160	PART NO.	-
SIZE	C	DESIGNER	-	SHEET	10 OF 50

(GMCH-2)

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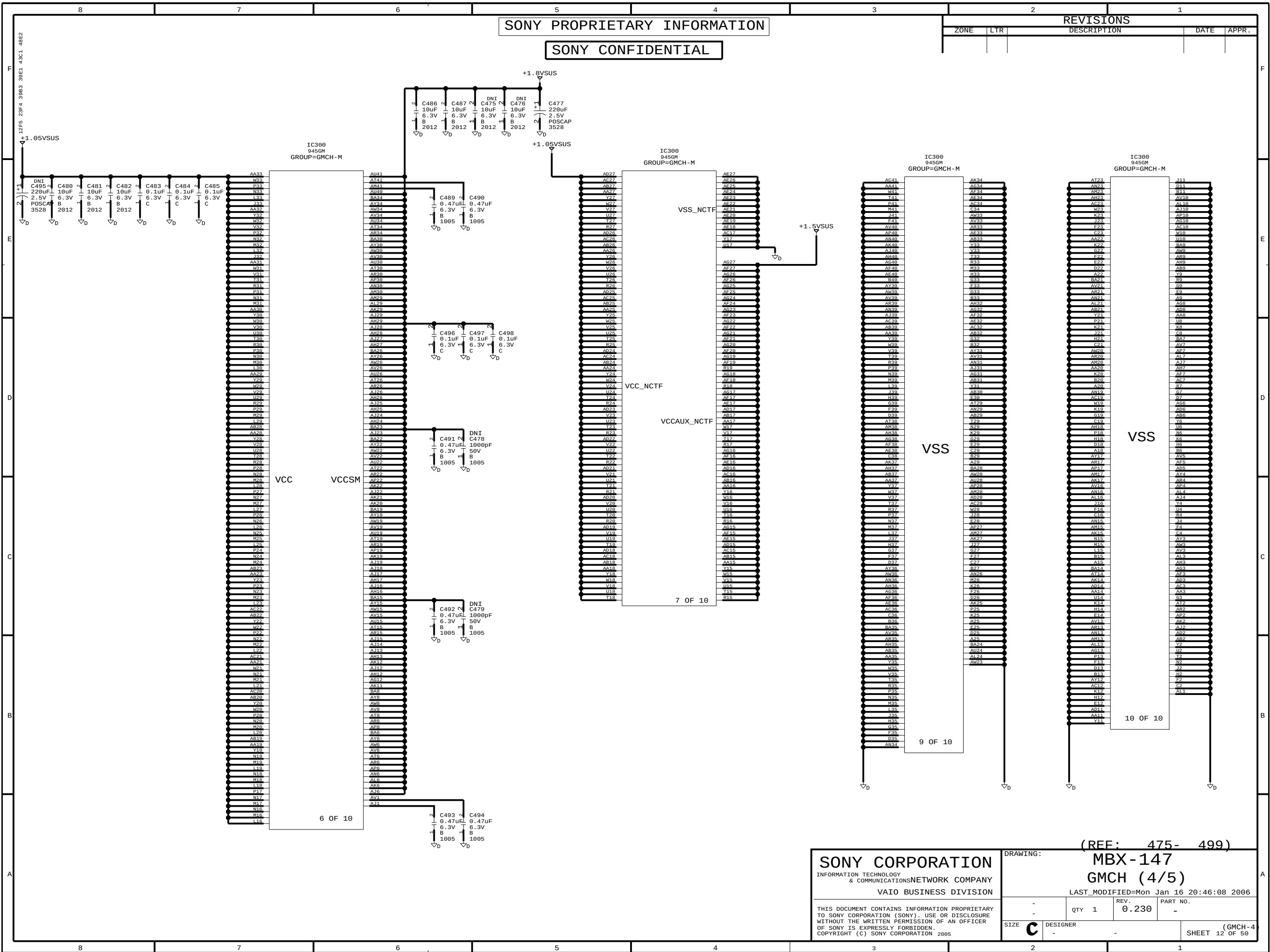
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LAST_MODIFIED=Mon Jan 16 20:46:06 2006			
-	QTY 1	REV. 0.242	PART NO. -
SIZE C	DESIGNER -		SHEET 11 OF 50 (GMCH-3)

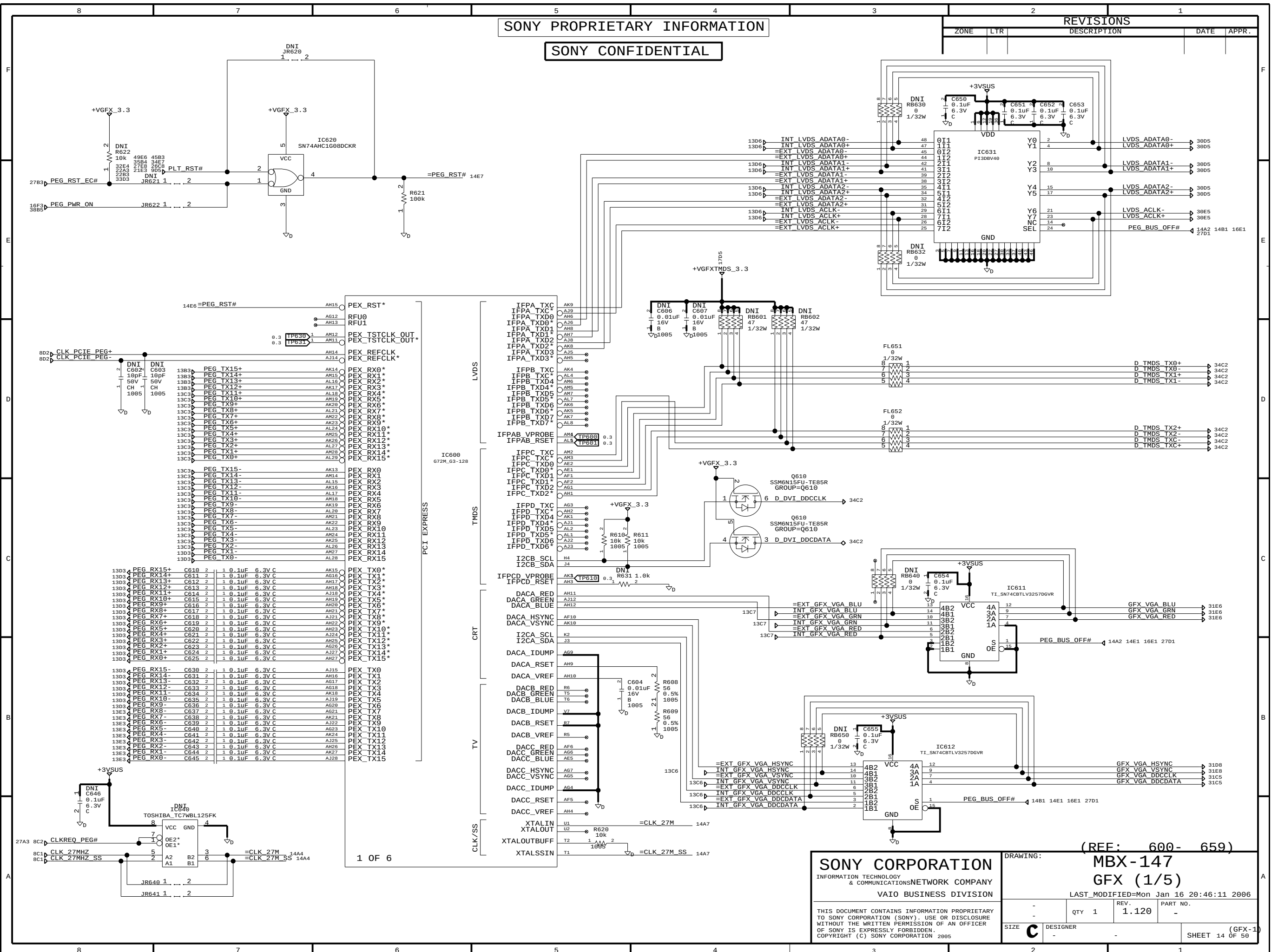


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ZONE	LTR	DESCRIPTION	DATE	APPR.



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DRAWING:

(REF: 600- 659)

MBX-147
GFX (1/5)

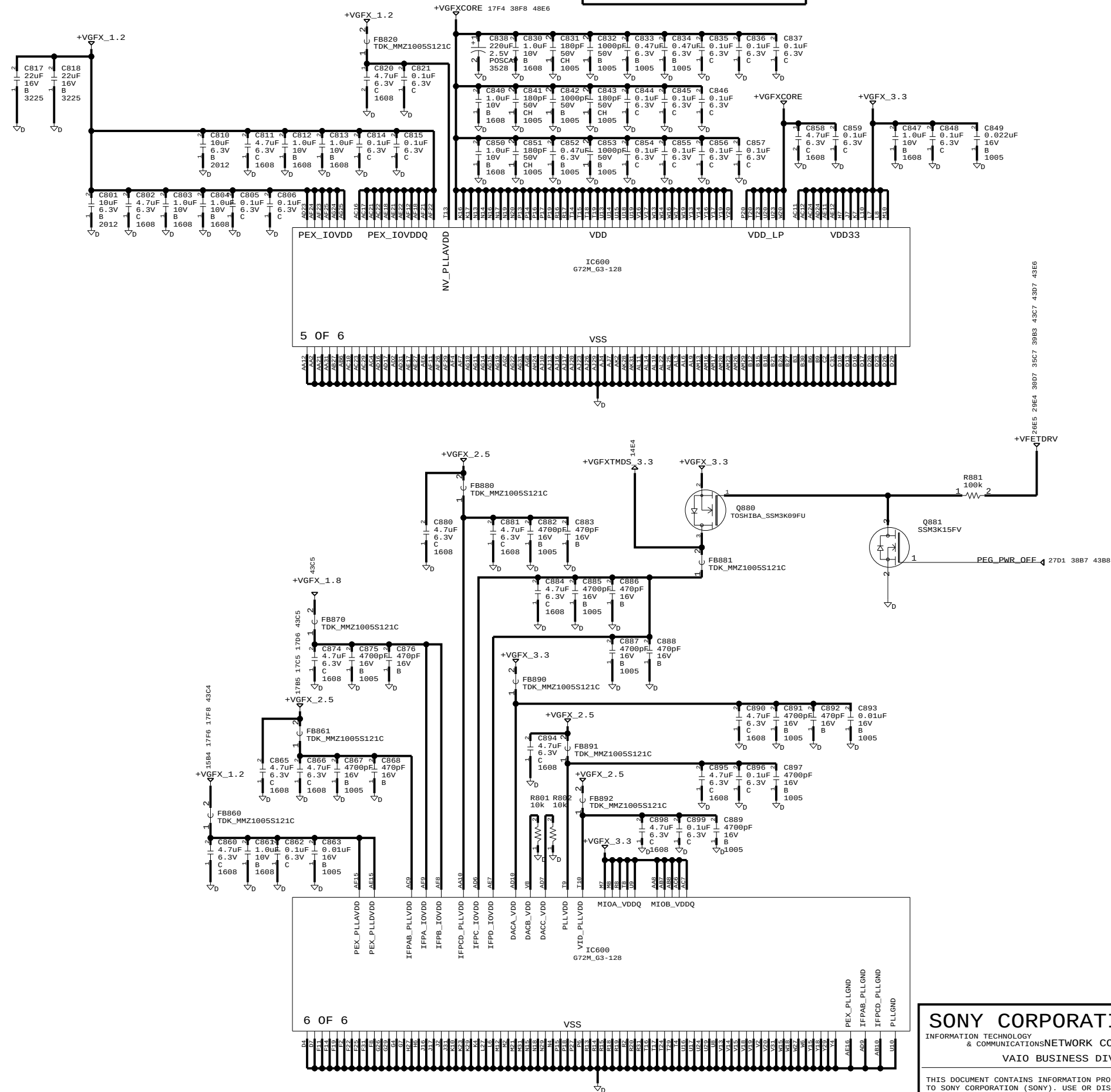
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SIZE	DESIGNER	QTY	REV.	PART NO.	SHEET
C	-	1	1.120	-	14 OF 50

(GFX-1)

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DRAWING:			
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LAST_MODIFIED=Mon Jan 16 20:46:13 2006			
-	QTY 1	REV. 0.310	PART NO. -
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SIZE C	DESIGNER	(GFX-3)	
-	-	SHEET 16 OF 50	



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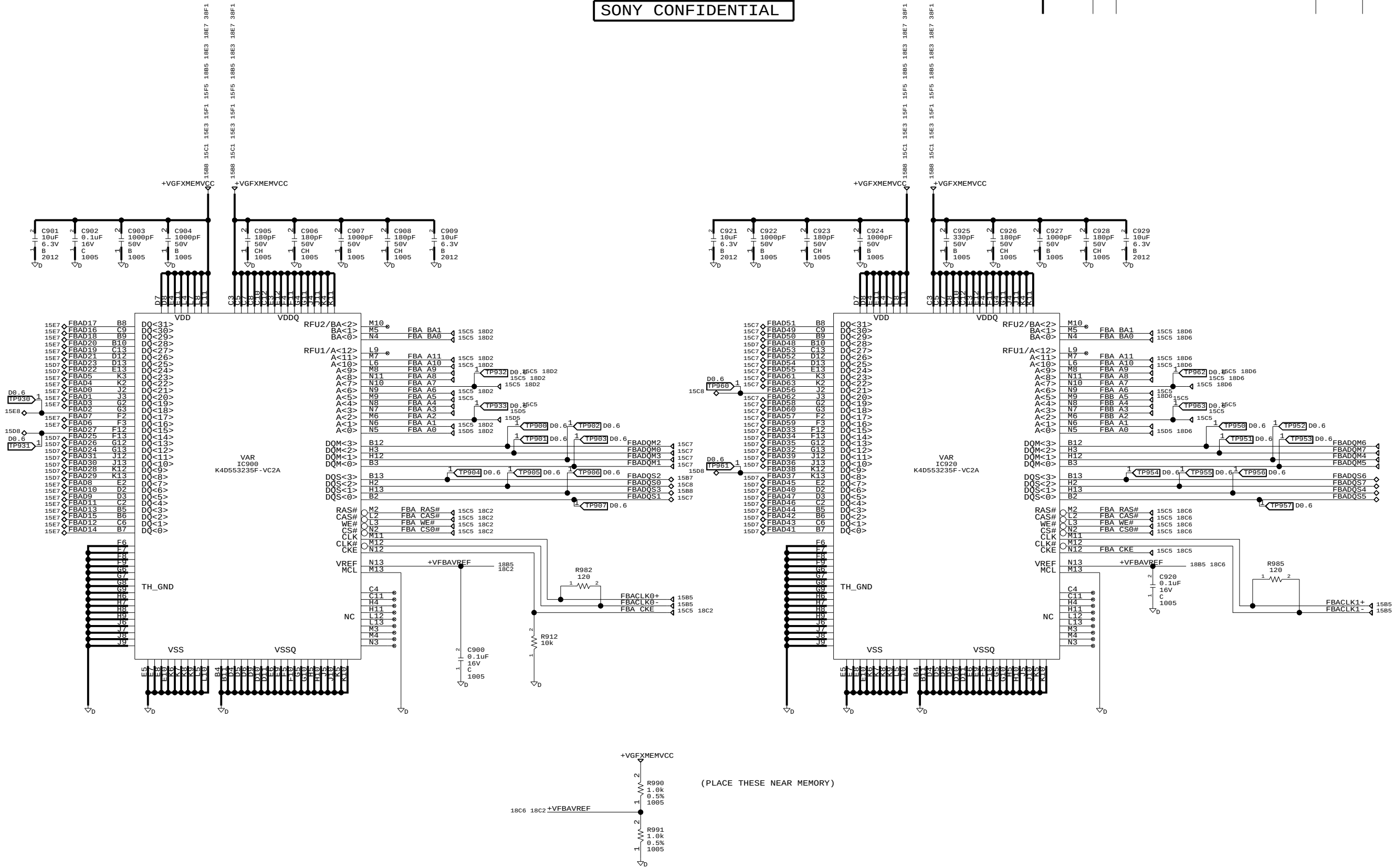
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SIZE	DESIGNER			(GFX-4)	
C	-	-	SHEET 17		OF 50

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ZONE	LTR	DESCRIPTION	DATE	APPR.

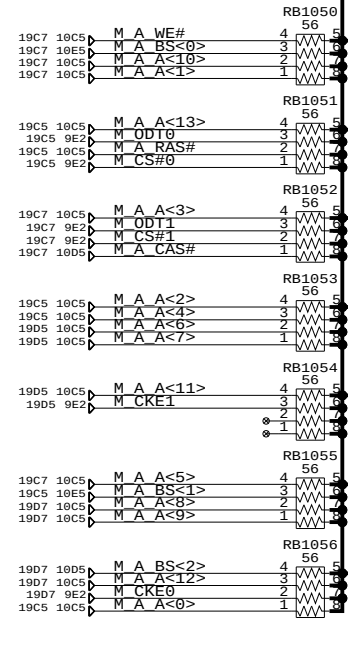


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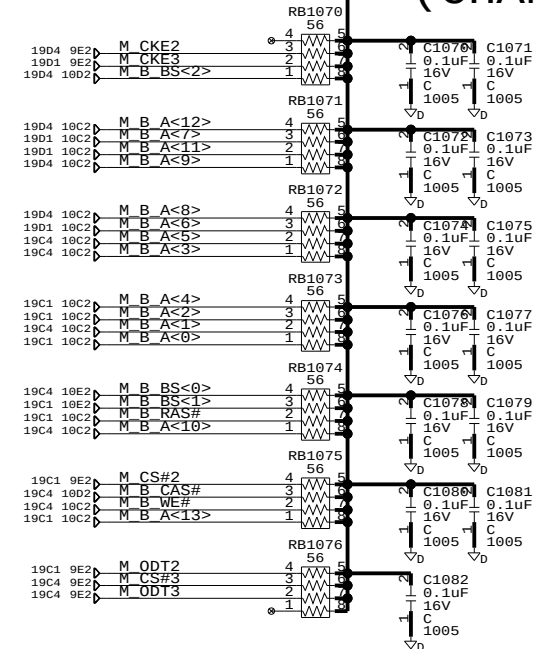
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QTY 1	REV. 1.154	-	
SIZE C	DESIGNER	-	
SHEET 18 OF 50		(GFX-5)	

(CHANNEL A)



(56 OHM PULL-UP)

(CHANNEL B)



(REF: - 1299)

MBX-147
DDR2 MEMORY(2/2)

LAST_MODIFIED=Mon Jan 16 20:46:20 2006

LAST MODIFIED=Mon Jan 10 20:40:20 2000			
-	QTY 1	REV. 0.100	PART NO. -
SIZE C	DESIGNER -		(MEMORY-2) SHEET 20 OF 50

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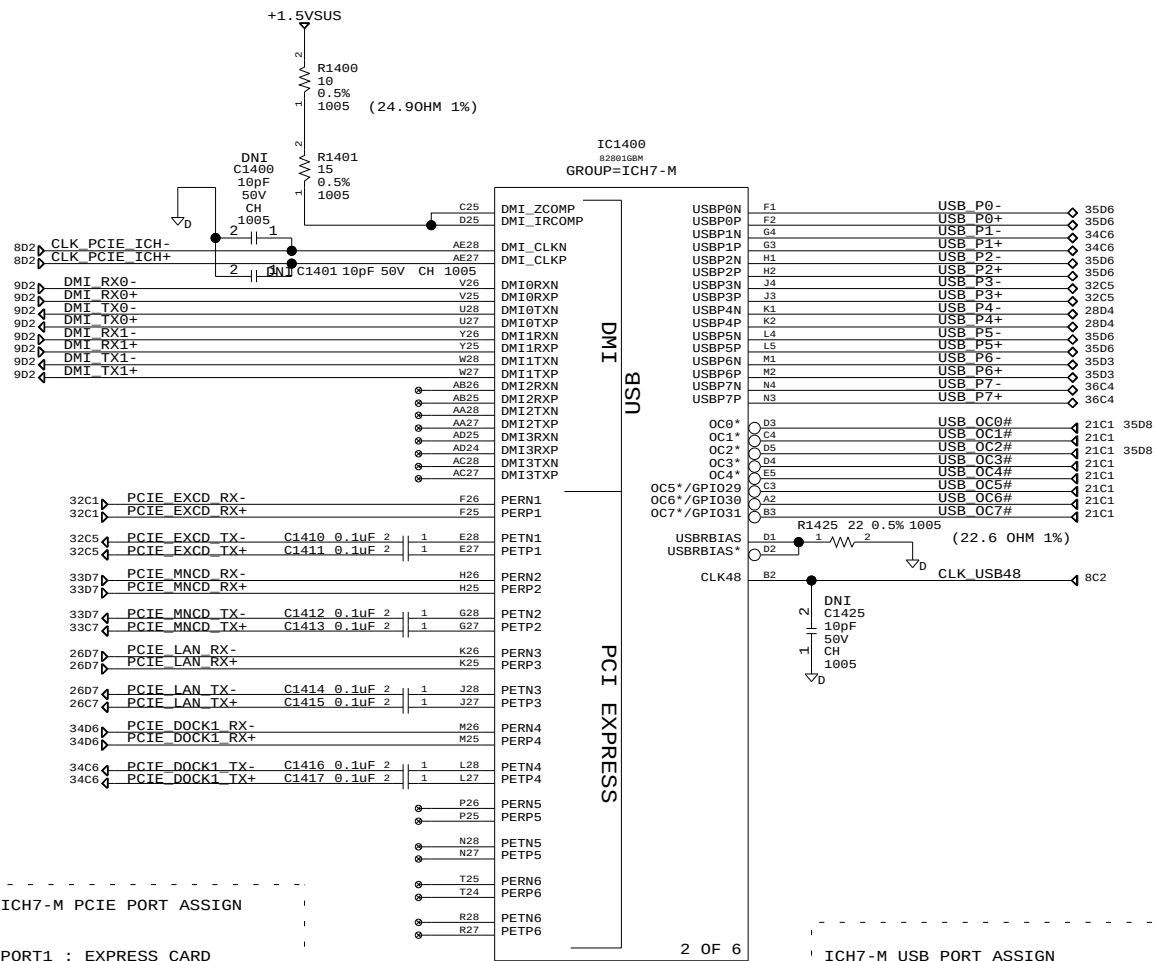
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REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APPR.



ICH7-M PCIE PORT ASSIGN

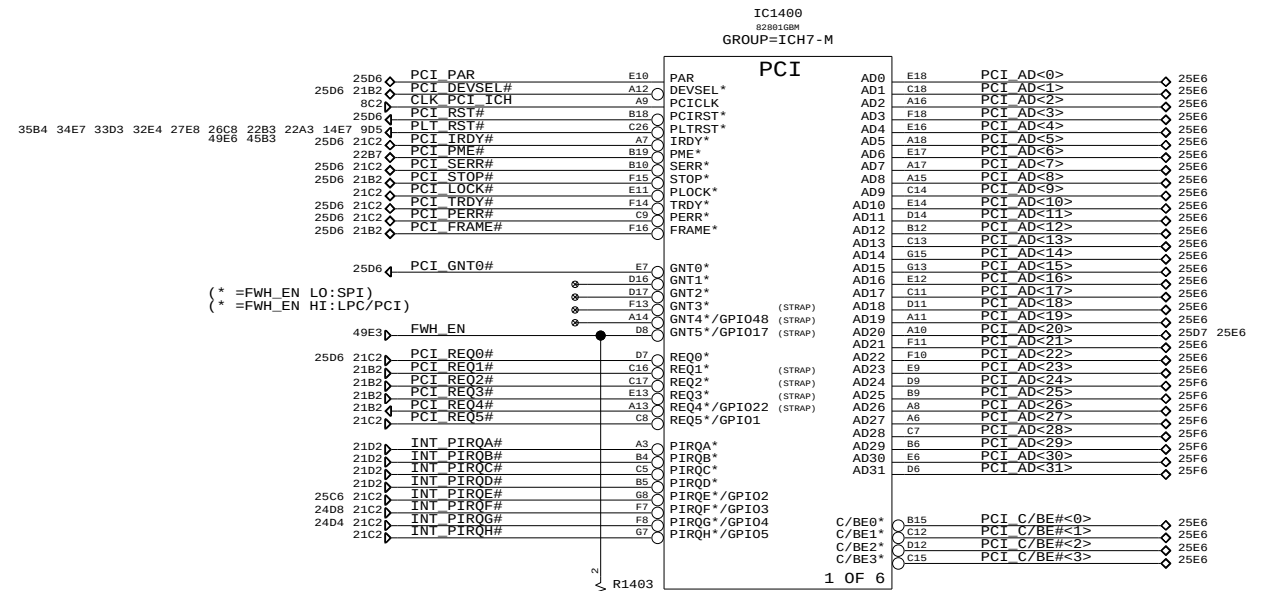
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PORT2 : MINI CARD
PORT3 : LAN
PORT4 : DOCK(LAN)

ICH7-M USB PORT ASSIGN

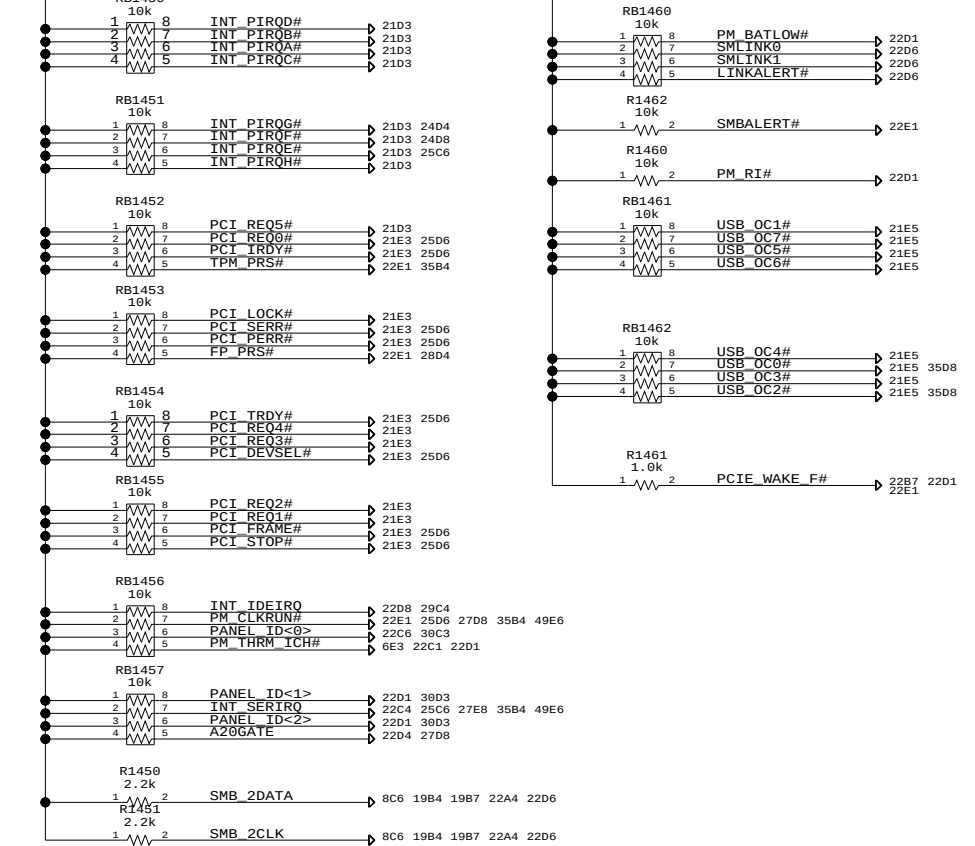
PORT0 : MAIN UNIT CN
PORT1 : DOCK CN
PORT2 : MAIN UNIT CN
PORT3 : EXPRESS CARD
PORT4 : FINGER PRINT SENSOR
PORT5 : CAMERA
PORT6 : FELICA / WAN
PORT7 : BLUE TOOTH

REQ/GNT BUS NO DEV.NO AD<XX> PIRQ

CARD A	0	8	4	20	E
I.LINK	-	8	4	20	G
MEMORY STICK	-	8	4	20	F
INT LAN	-	8	8	-	E
DRAM CONT	-	0	0	-	
PEG BRIDGE	-	0	1	-	
INT GFX	-	0	2	-	
AZALIA	-	0	27	-	D
USB UHCI1	-	0	29	-	B
USB UHCI2	-	0	29	-	D
USB UHCI3	-	0	29	-	F
USB UHCI4	-	0	29	-	B
USB EHCI	-	0	29	-	H



(PULLUPS)



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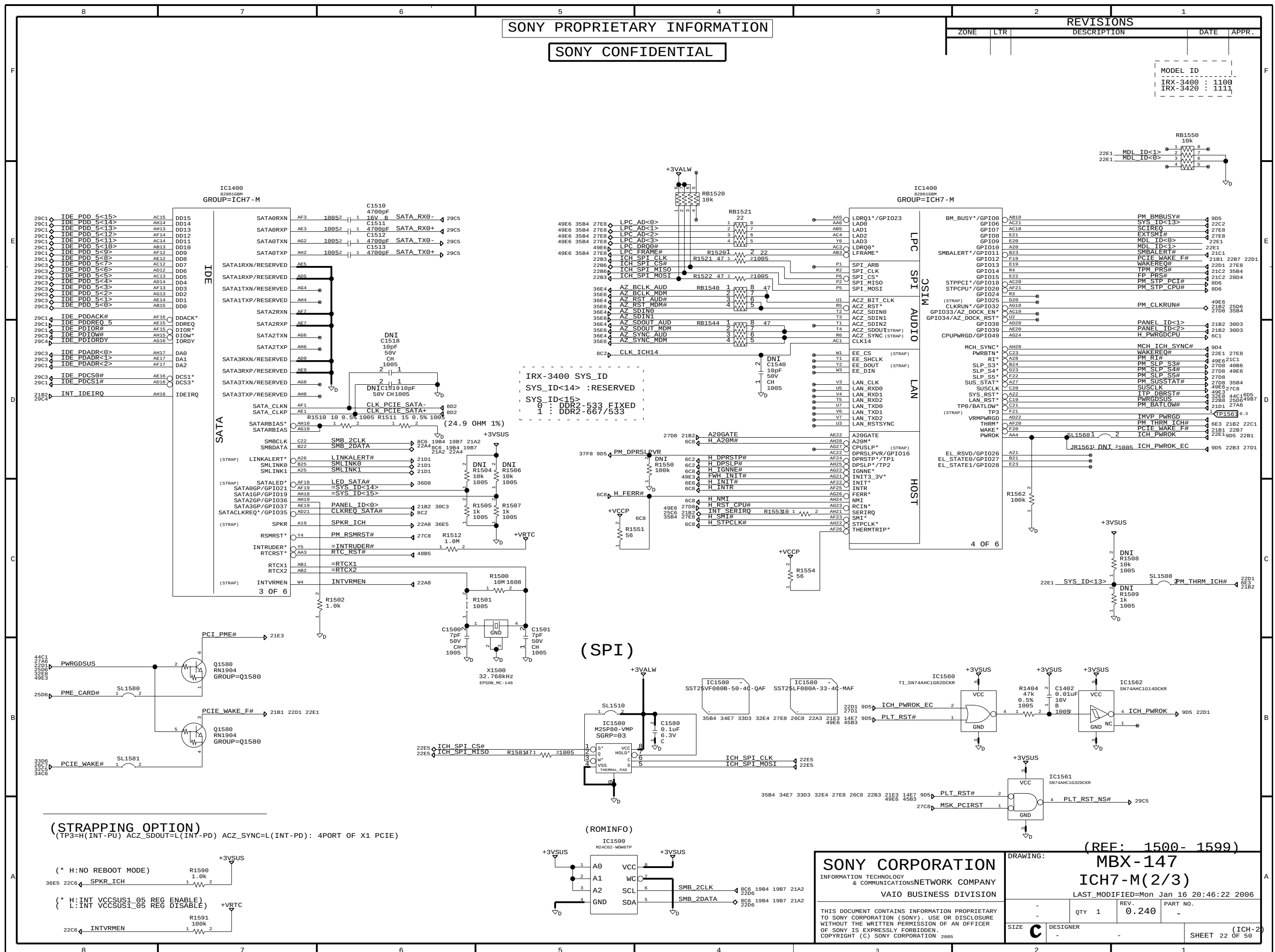
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MBX-147
ICH7-M(1/3)

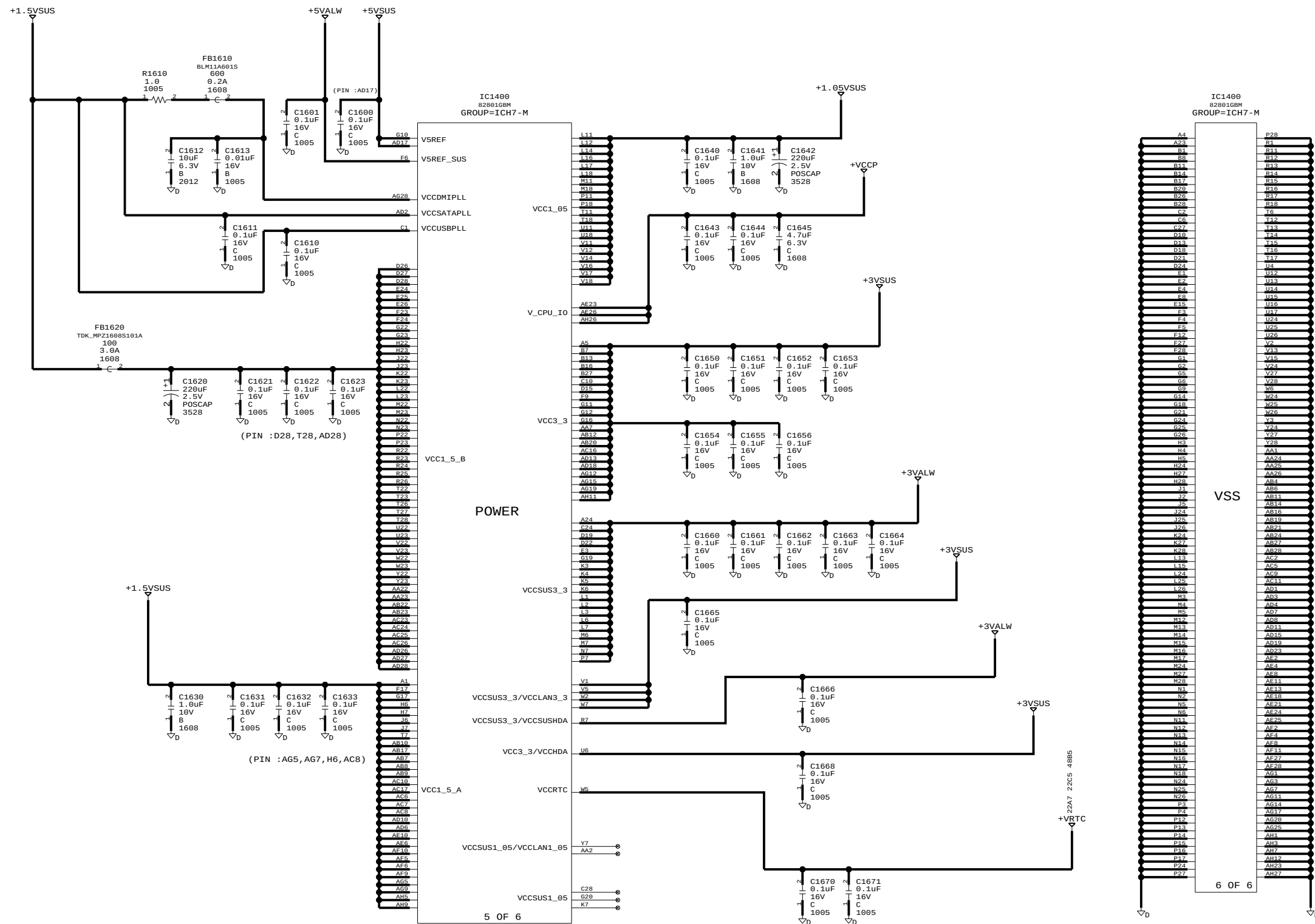
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SHEET 21 OF 50



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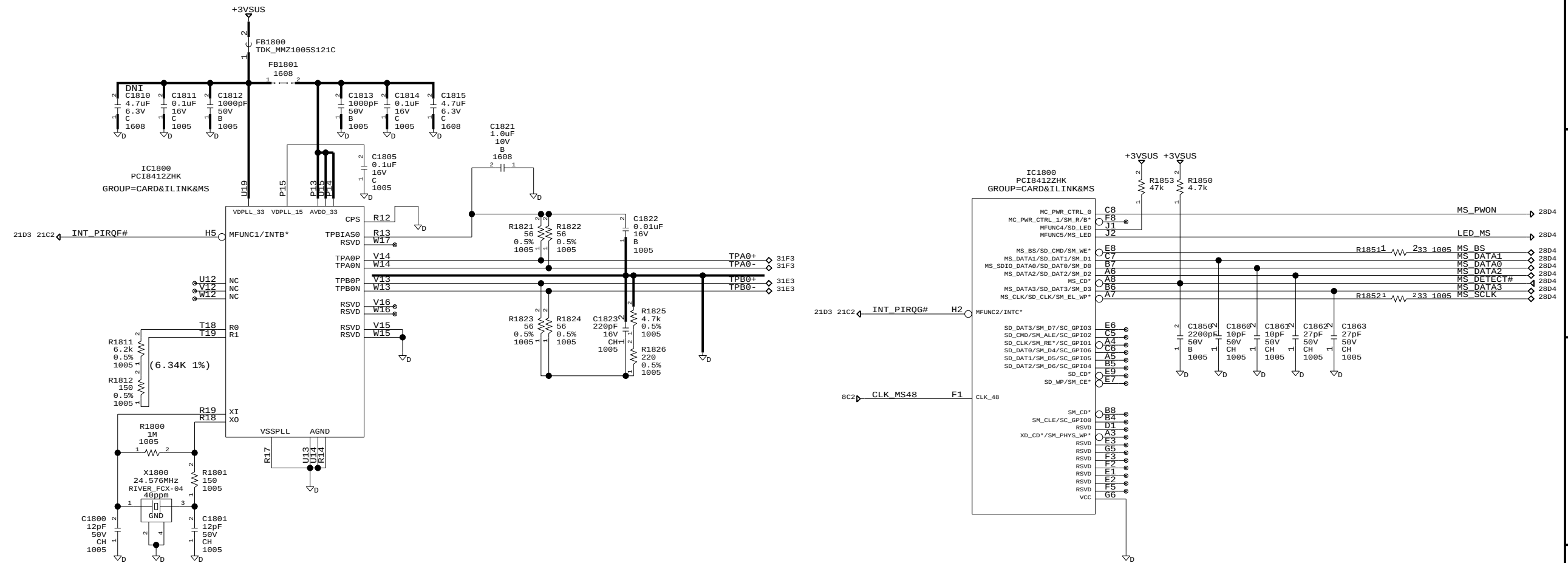


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DRAWING:			
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LAST_MODIFIED=Mon Jan 16 20:46:24 2006			
-	QTY 1	REV. 0.210	PART NO. -
SIZE C	DESIGNER		SHEET 23 OF 50 (ICH-3)

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DRAWING:				(REF: 1800-)			
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LAST_MODIFIED=Mon Jan 16 20:46:25 2006							
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SIZE	C	DESIGNER			SHEET (COMBO-1) 24 OF 50		

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ZONE	LTR	DESCRIPTION	DATE	APPR.
3VSUS				
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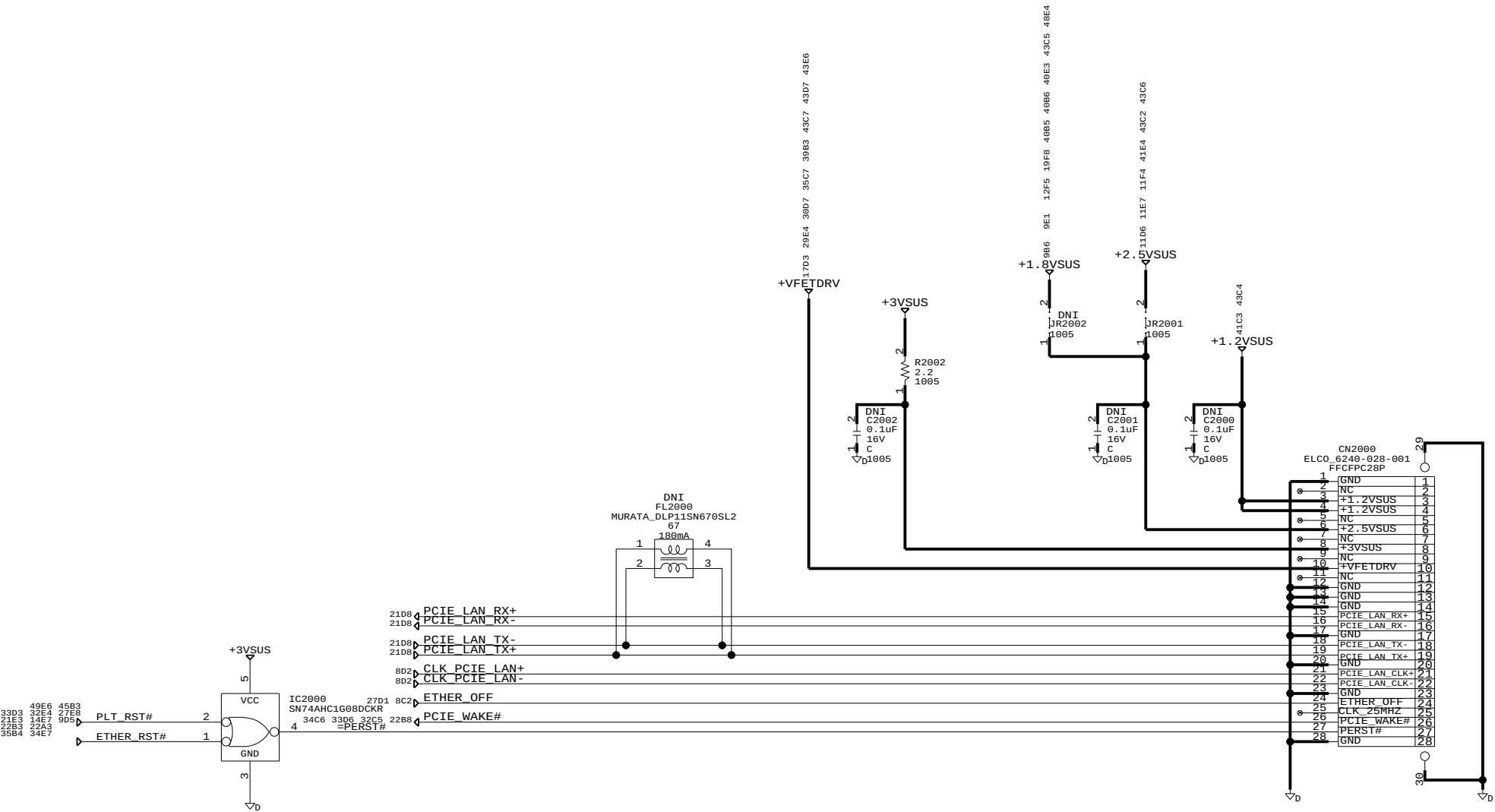
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-	QTY 1	REV. 0.230	PART NO. -
SIZE C	DESIGNER	SHEET (COMBO-2) 25 OF 50	

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REV.	QTY	REV.	PART NO.
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SIZE	DESIGNER	SHEET
C	-	26 OF 50

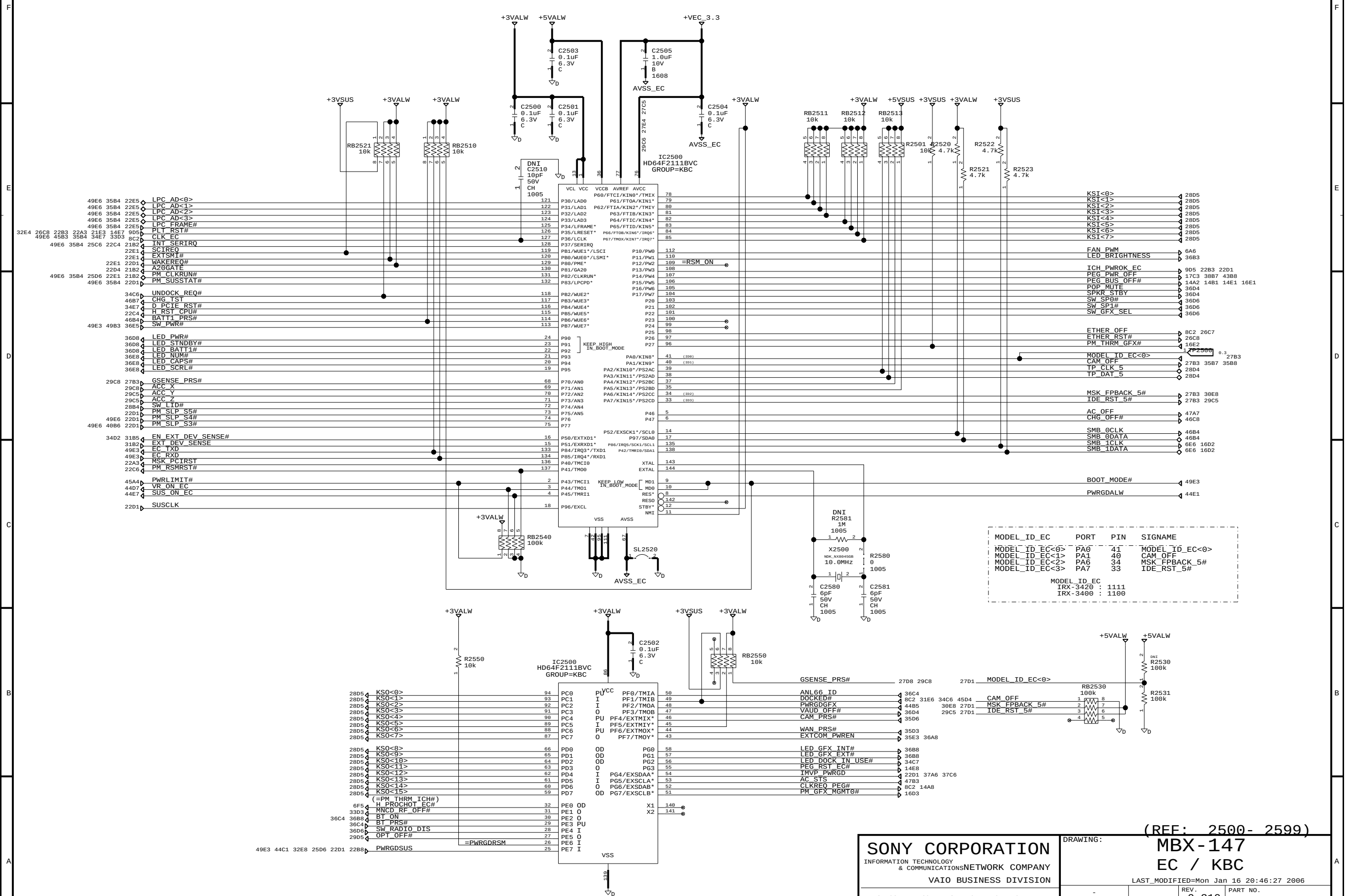
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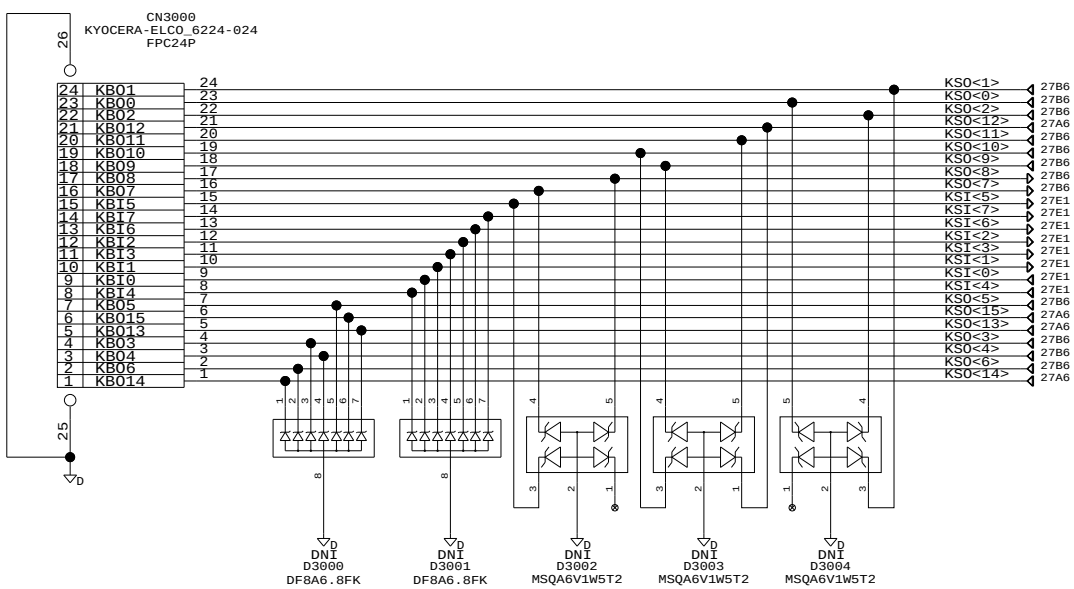
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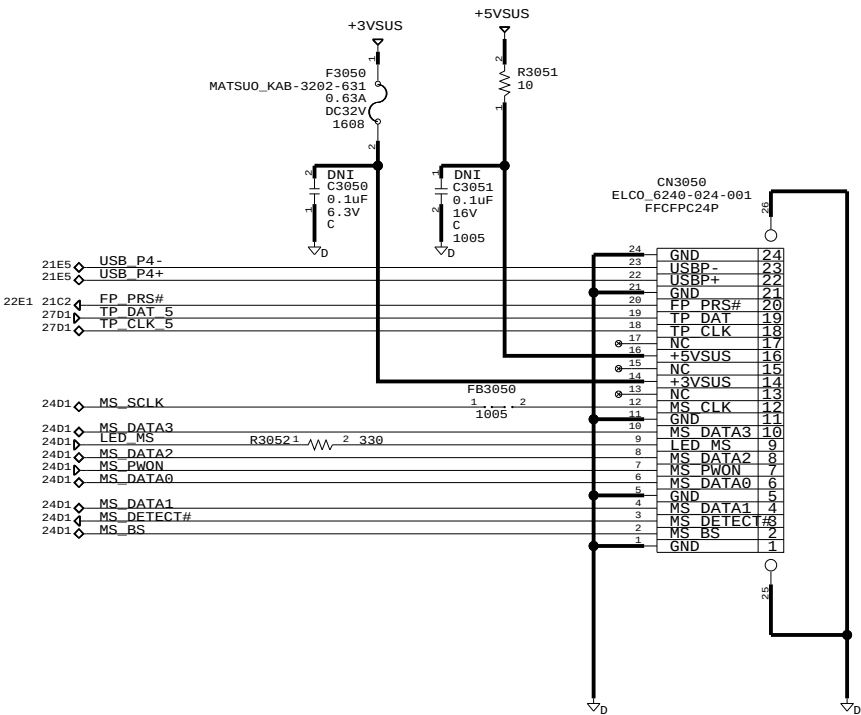
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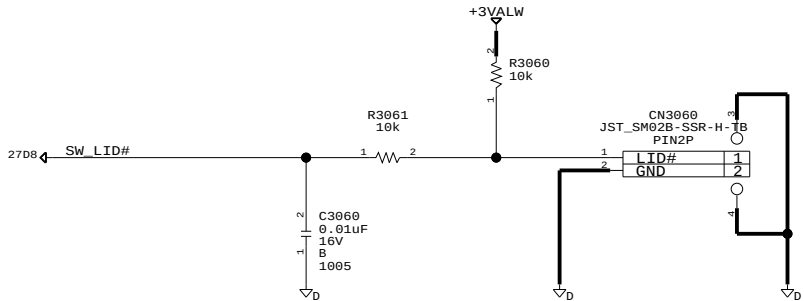
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(TP, FP, MS-DUO)
(TO SWX-213)



(LID)
(TO SWX-214)



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(REF: 3000- 3099)

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SIZE	C	DESIGNER	-	SHEET	28 OF 50

(CN-1)

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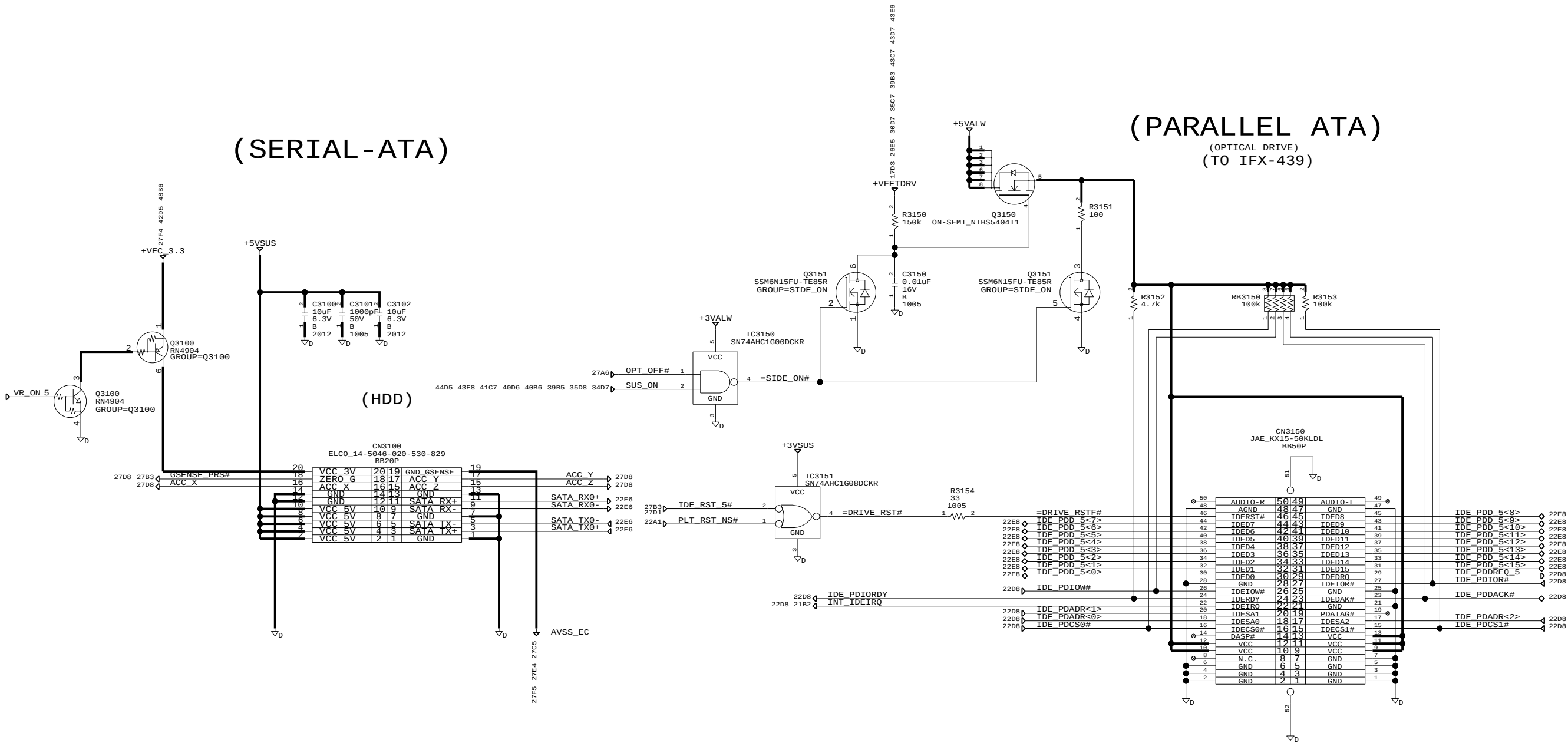
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(TO IFX-439)

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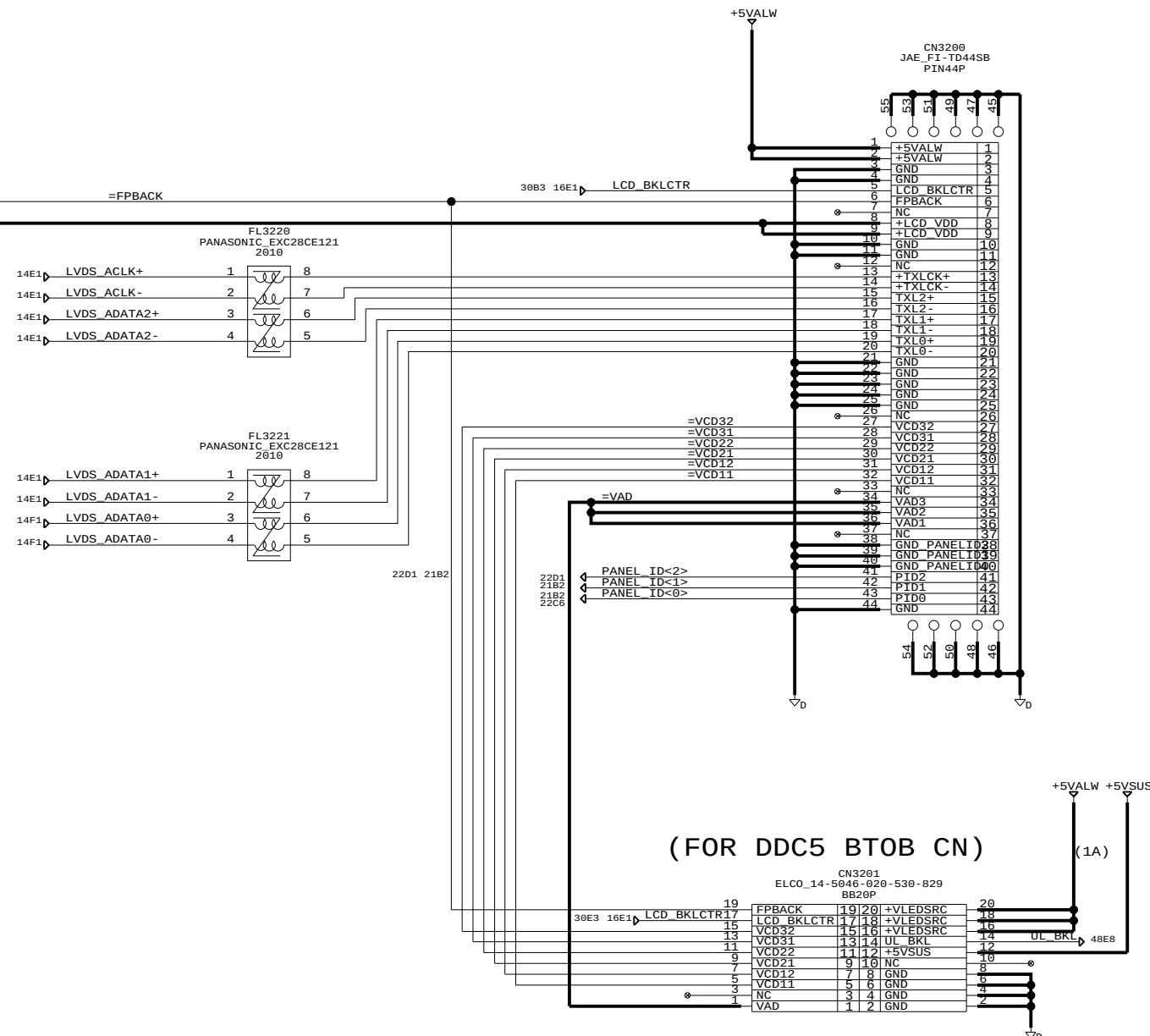
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DRAWING:

(REF: 3100- 3199)
MBX-147
IDE CN(SATA, AEGIS/PATA)

LAST_MODIFIED=Mon Jan 16 20:46:28 2006

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SIZE	C	DESIGNER	-	SHEET	29 OF 50

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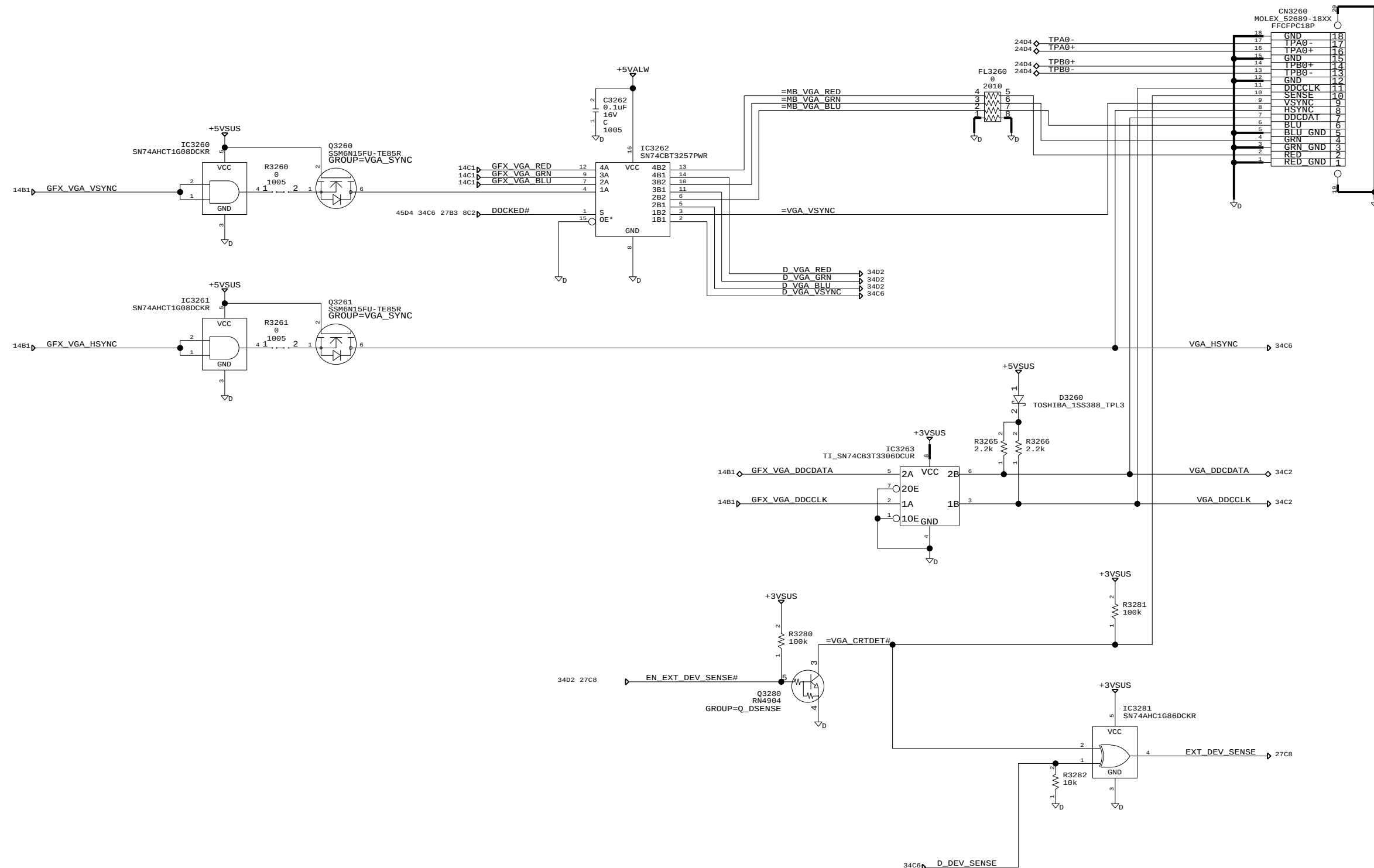
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SIZE C	DESIGNER -		(CN-3) SHEET 30 OF 50

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(VGA, I.LINK)
(TO CNX-348)



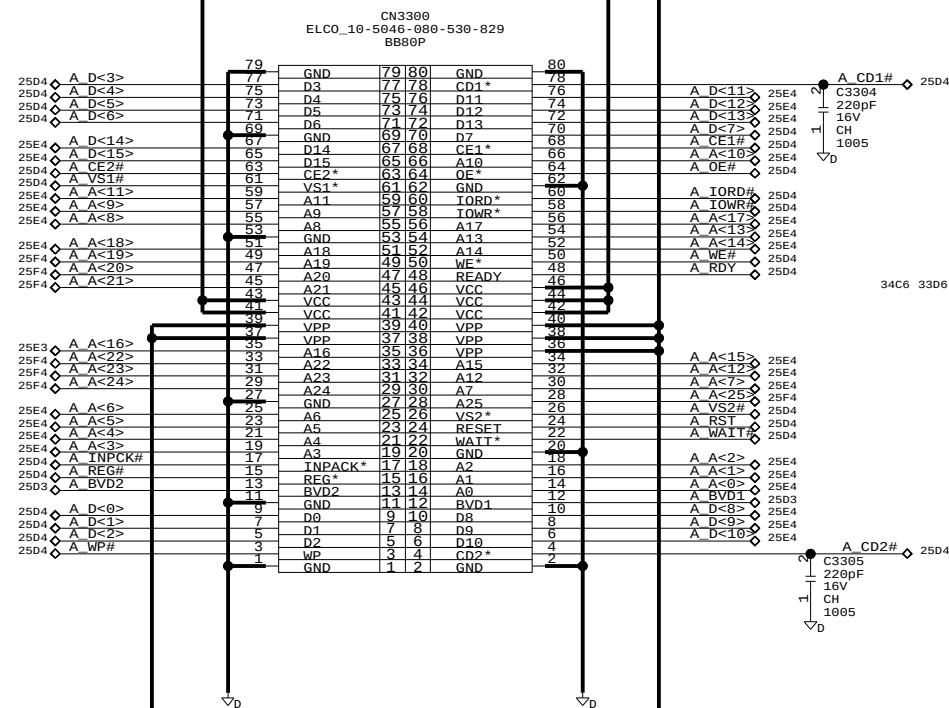
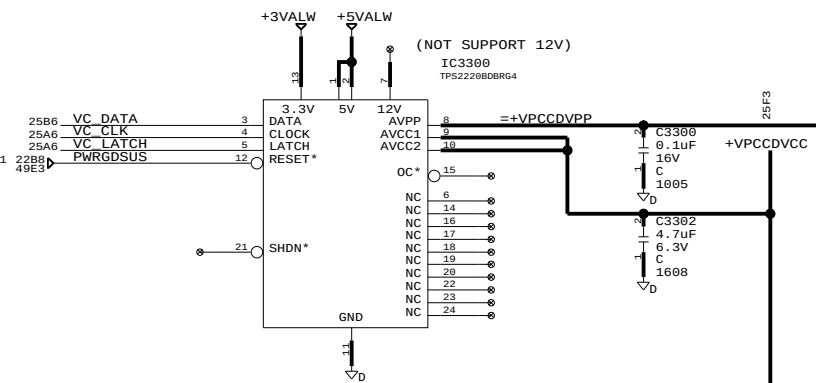
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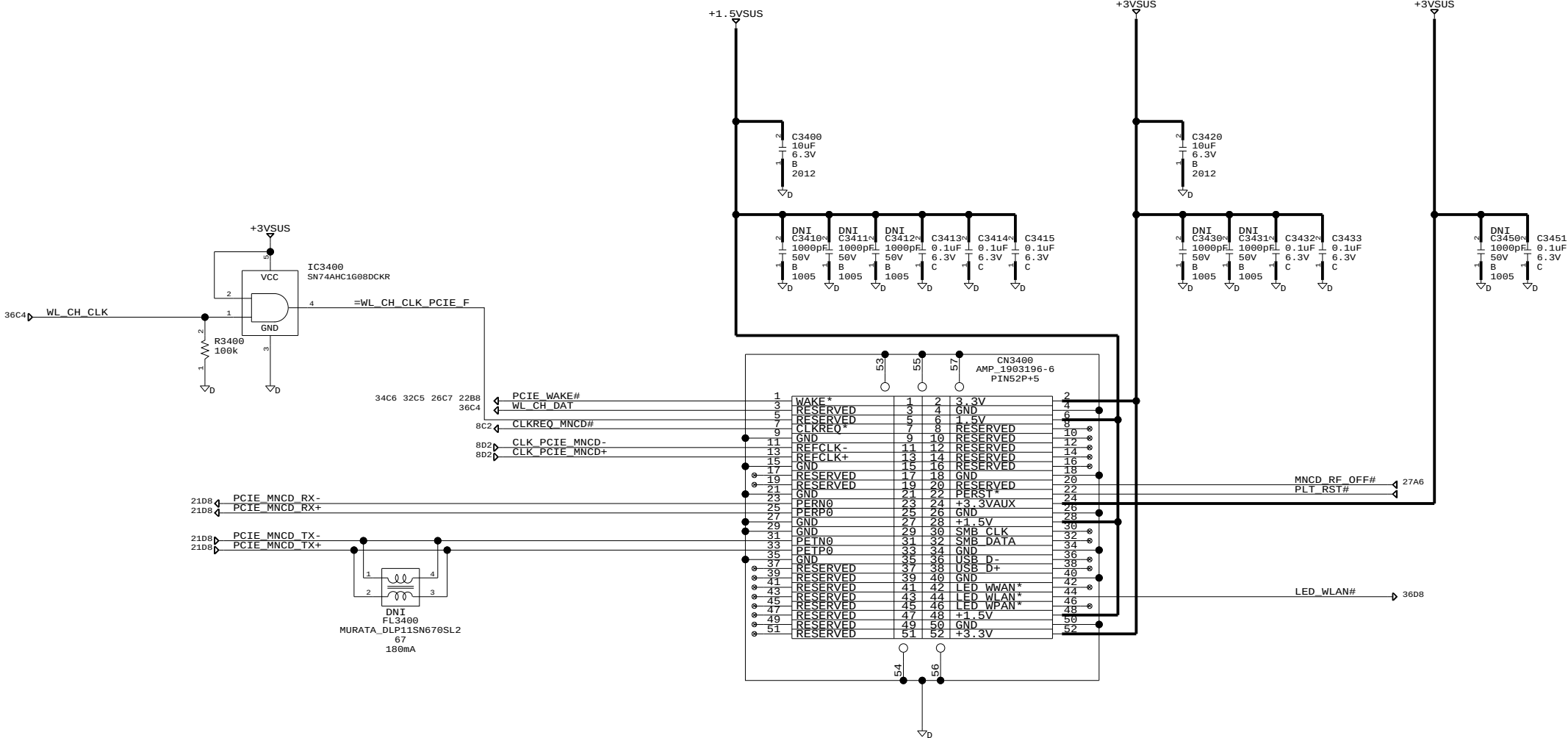


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MINI CARD (WIRELESS-LAN)

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QTY	1	REV.	1.120	PART NO.	-
SIZE	C	DESIGNER	-	SHEET	33 OF 50 (CN-6)

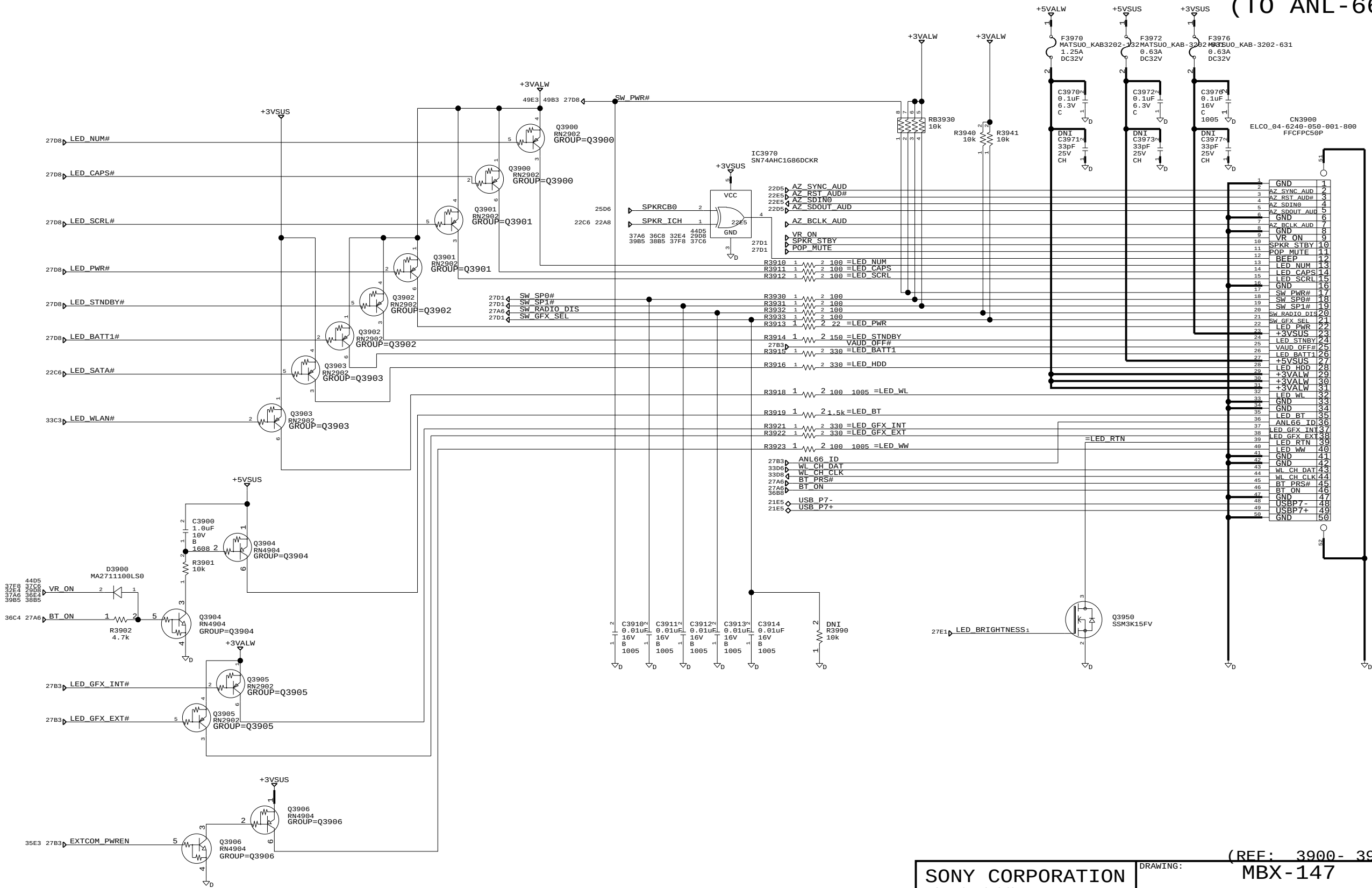
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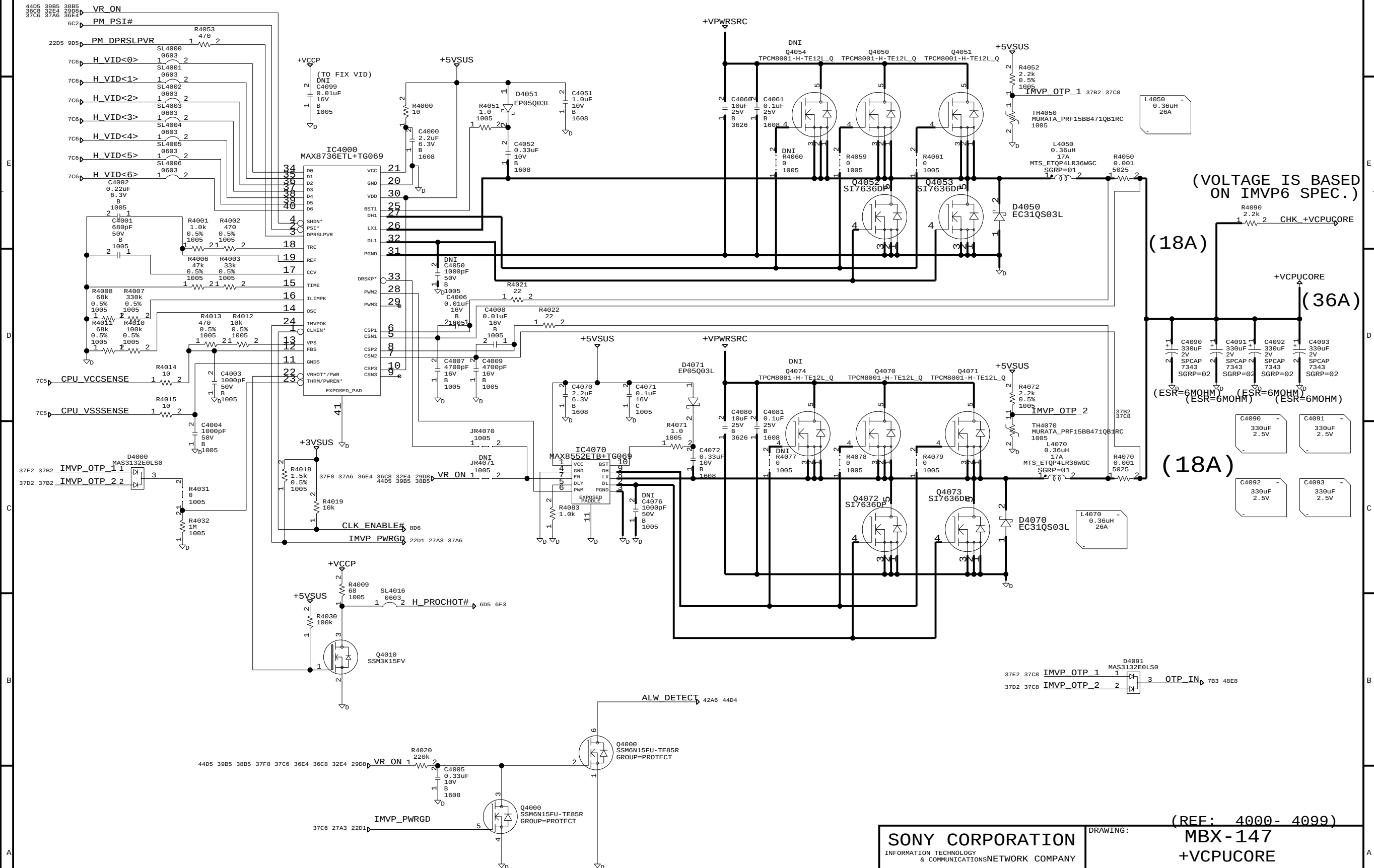
(CN-9)
SHEET 36 OF 50

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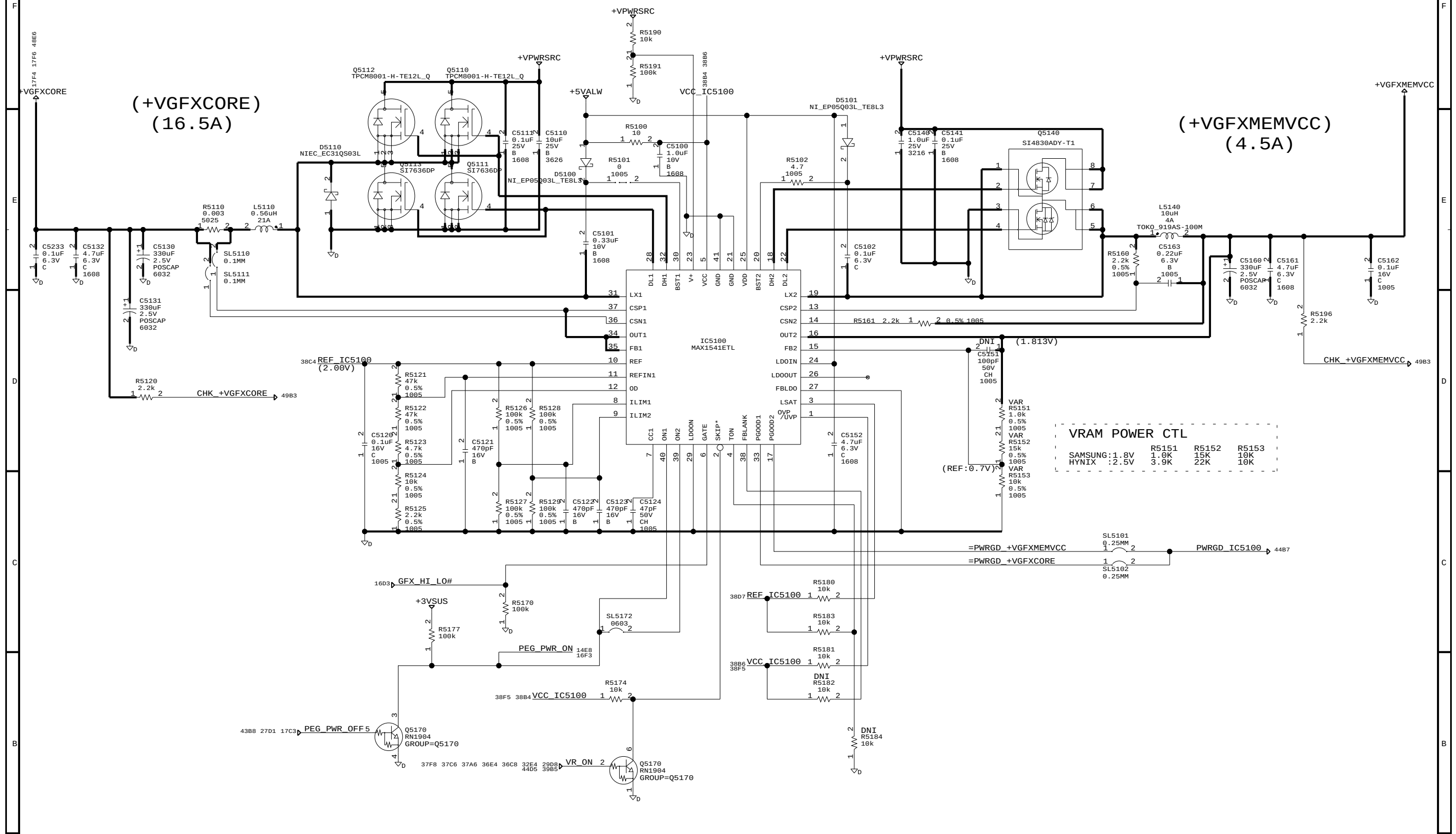
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DESIGNER			

SHEET 37 OF 50 (POWER-1)

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ZONE	LTR	DESCRIPTION	DATE	APPR.



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SIZE C	DESIGNER -	SHEET (POWER-2) 38 OF 50	

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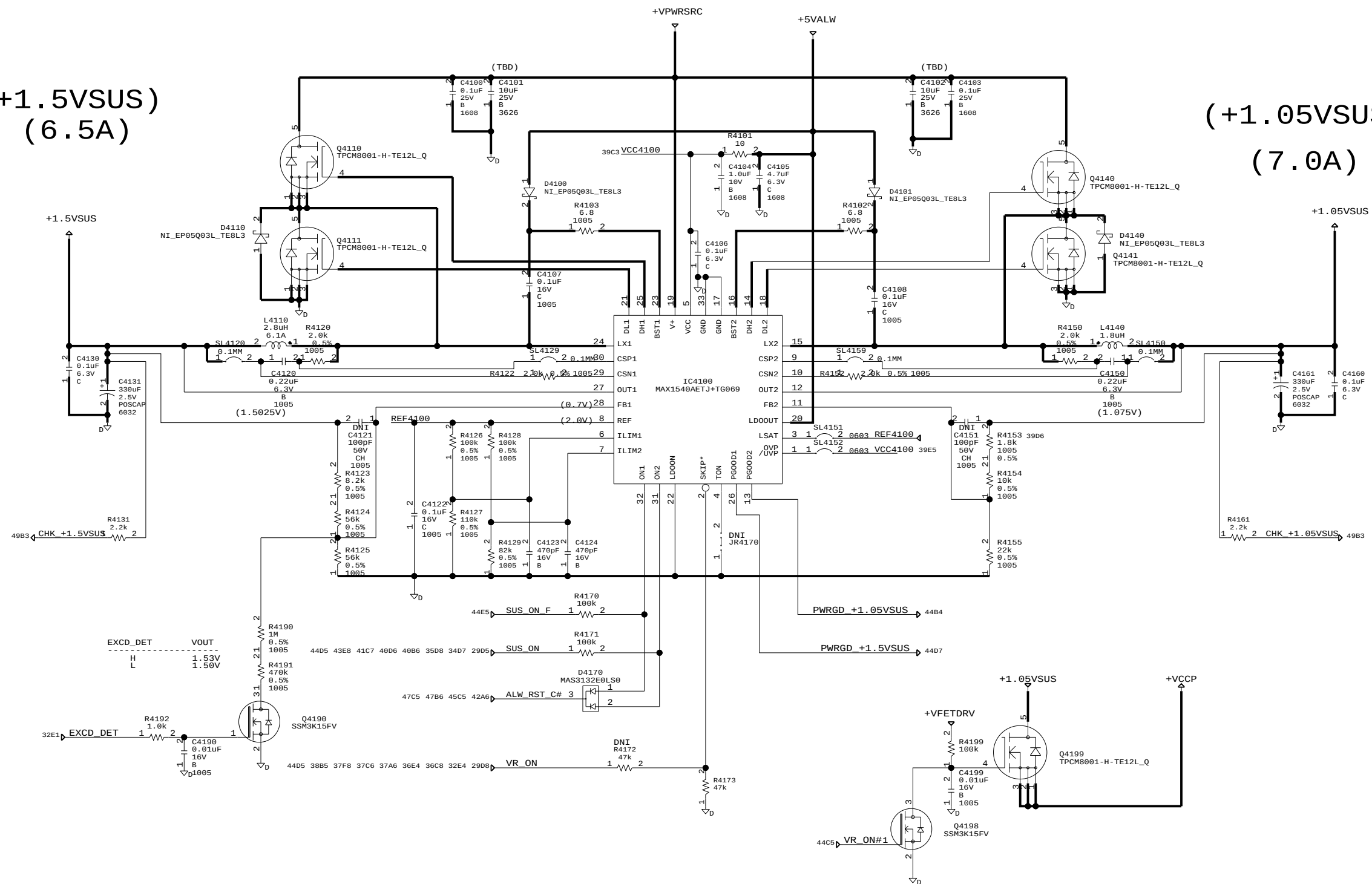
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ZONE	LTR	DESCRIPTION	DATE	APPR.

(+1.5VSUS)
(6.5A)

(+1.05VSUS)
(7.0A)



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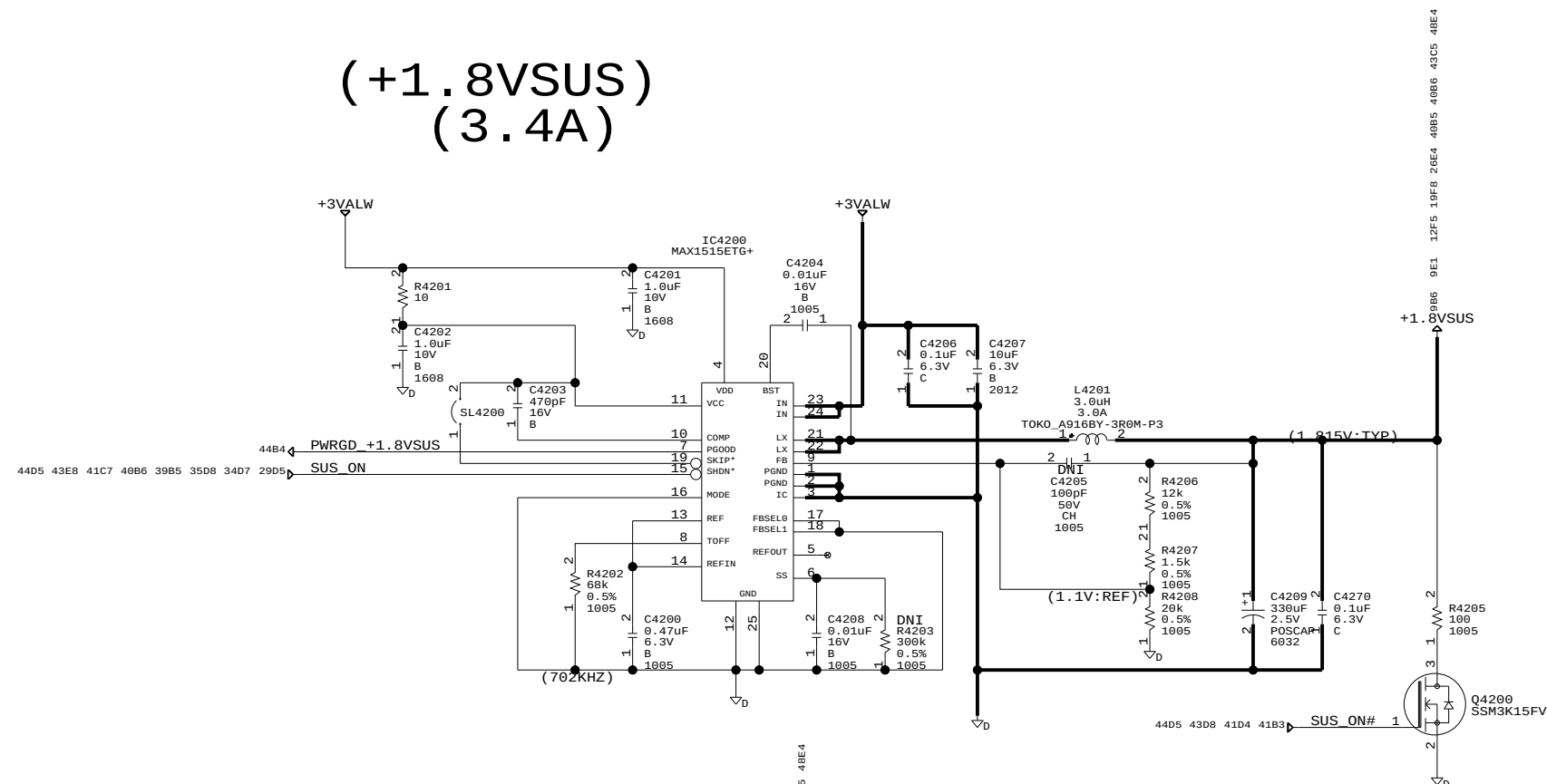
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SIZE	QTY 1	REV. 1.120	PART NO.
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SHEET 39 OF 50		(POWER-3)	

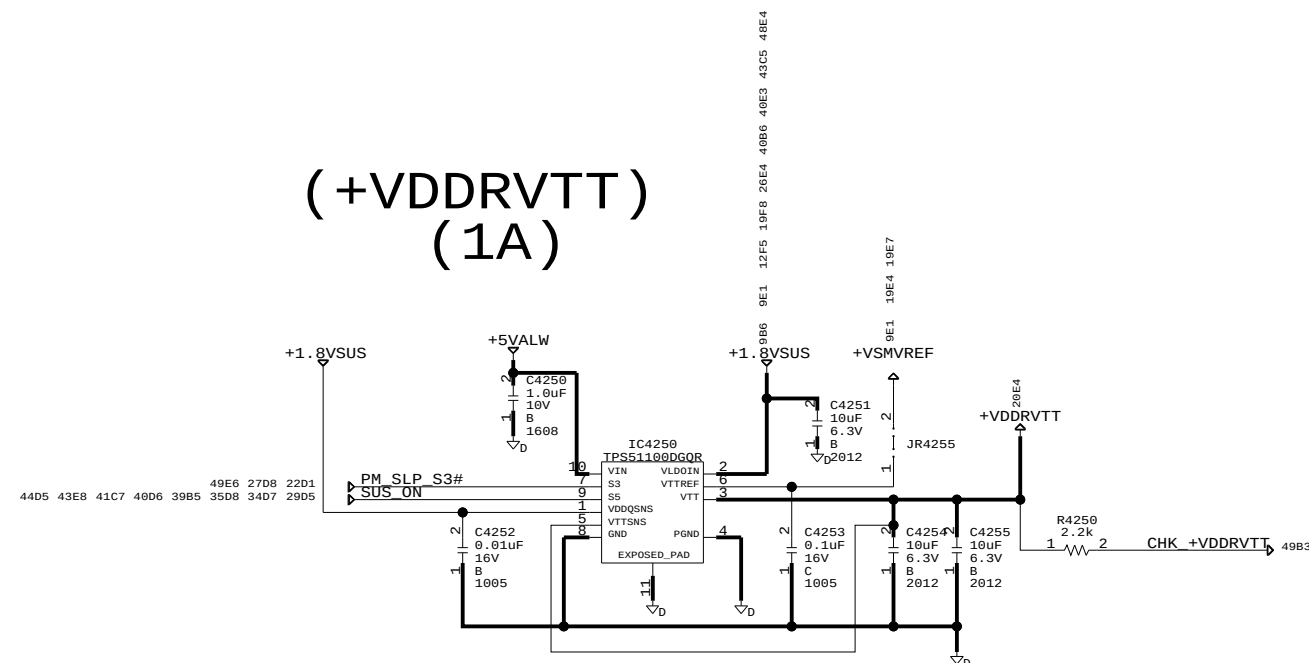
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ZONE	LTR	DESCRIPTION	DATE	APPR.

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(3.4A)



(+VDDRVTT)
(1A)



DRAWING:			
MBX-147 +1.8VSUS / +VDDRVTT			
LAST_MODIFIED=Mon Jan 16 20:46:38 2006			
-	QTY 1	REV. 0.230	PART NO. -
SIZE C	DESIGNER -	SHEET (POWER-4) 40 OF 50	

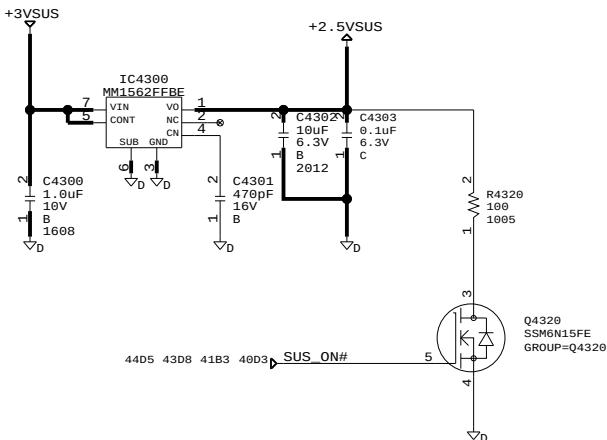
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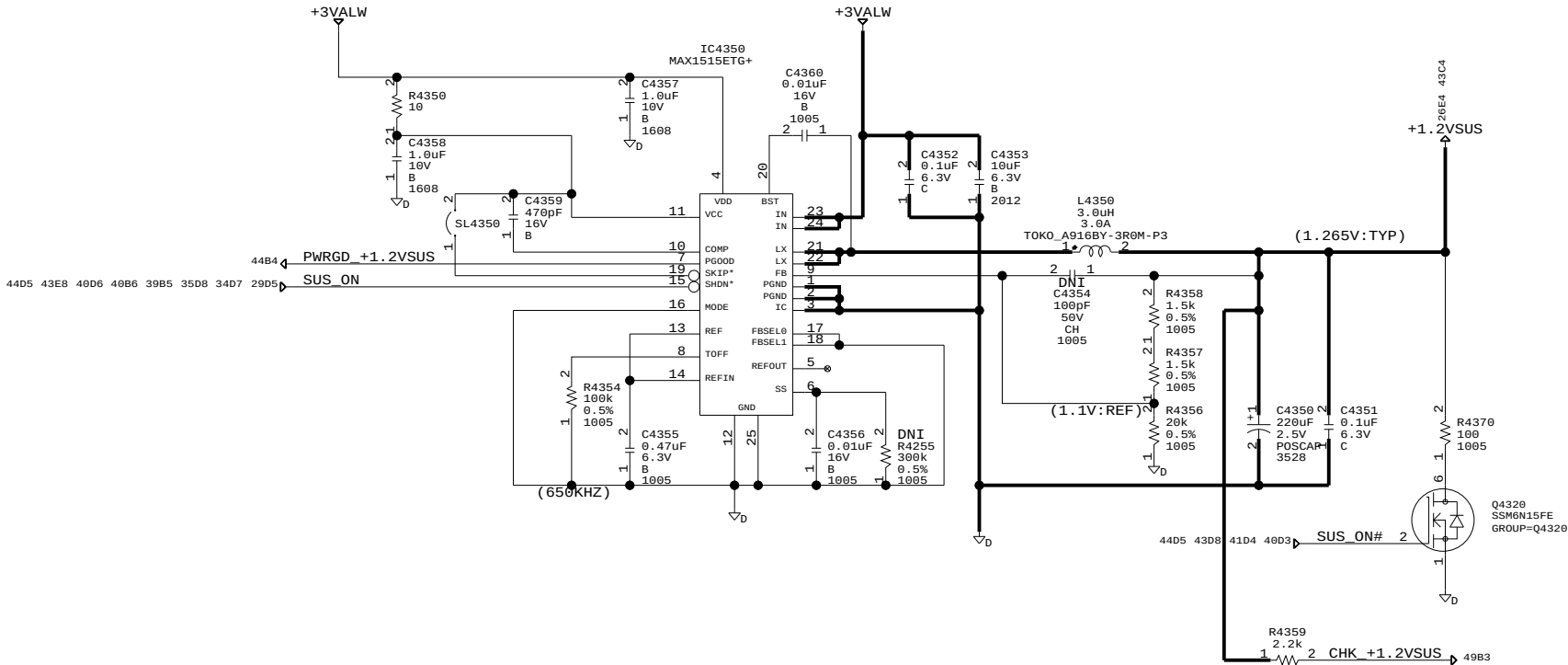
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ZONE	LTR	DESCRIPTION	DATE	APPR.

(+2.5VSUS)
(0.4A)



(+1.2VSUS)
(1.8A)



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LAST_MODIFIED=Mon Jan 16 20:46:38 2006			
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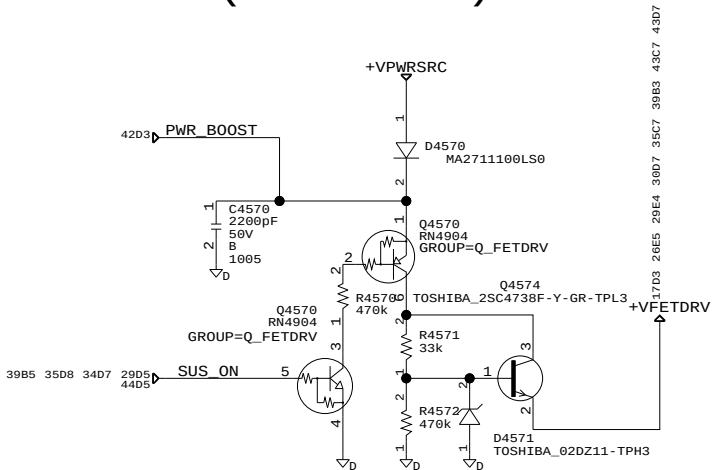
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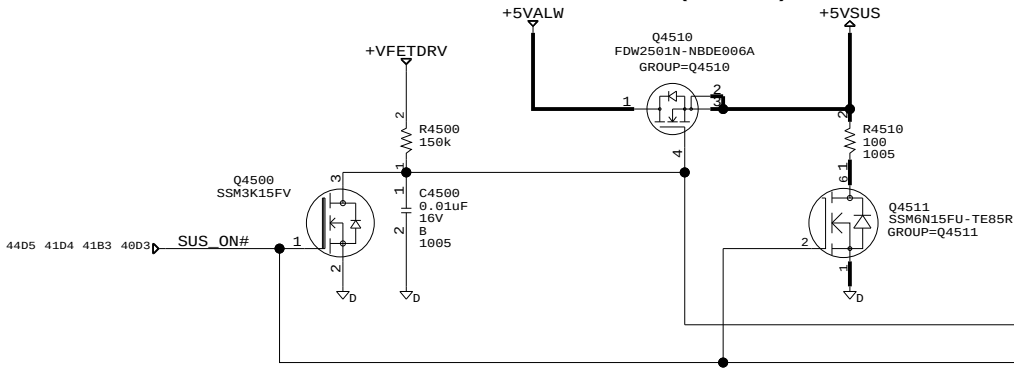
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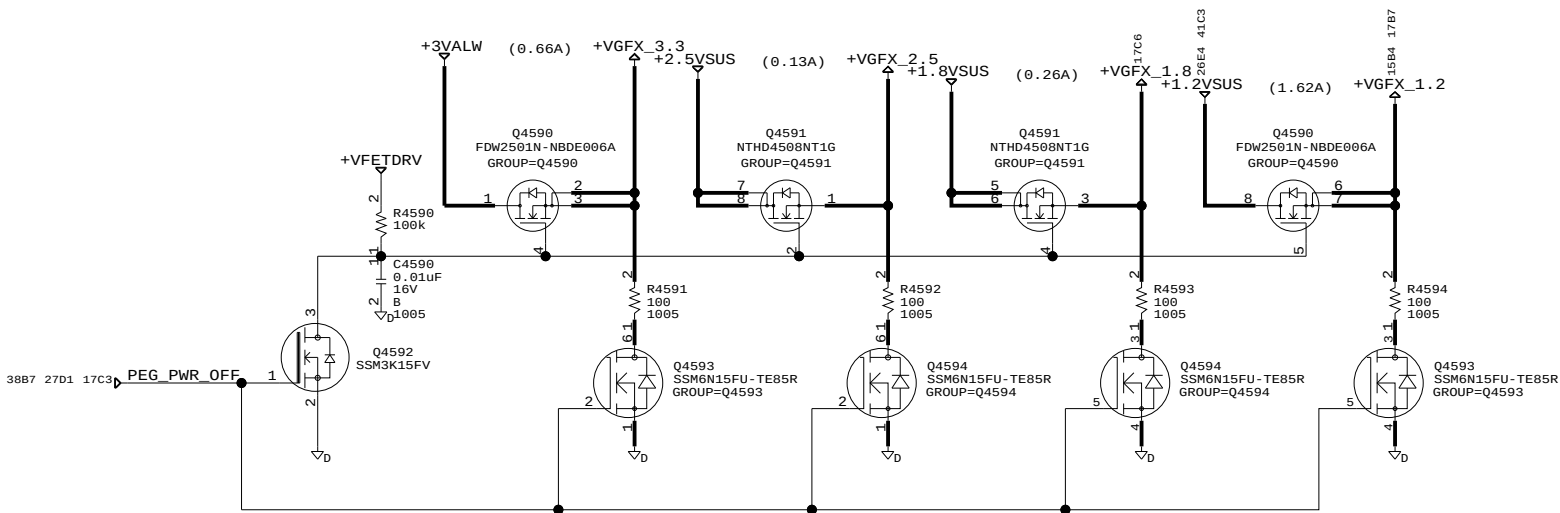
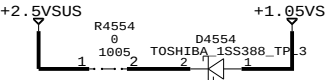
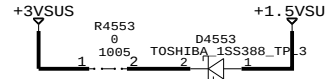
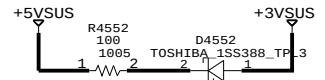
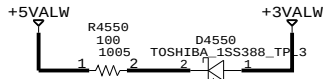
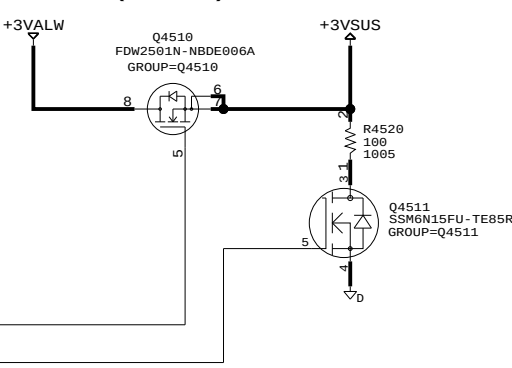
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(+5VSUS)
(1.1A)



(+3VSUS)
(1.1A)



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SIZE	C	DESIGNER	-	SHEET	(POWER-7) 43 OF 50

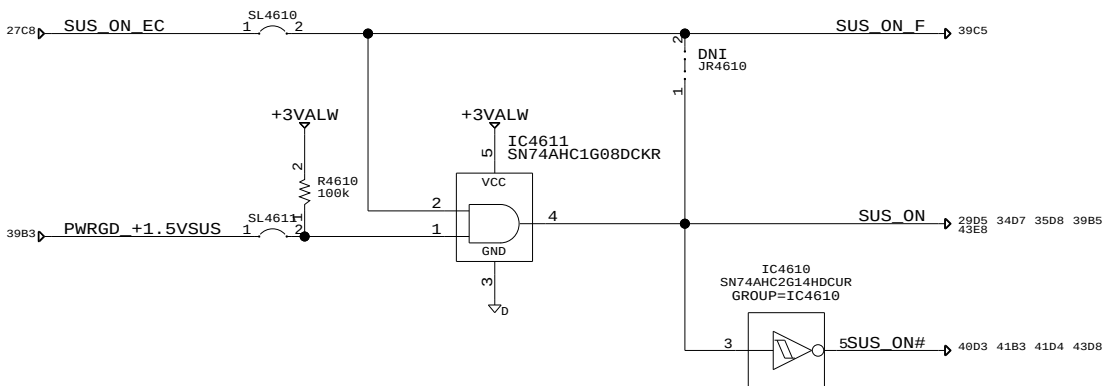
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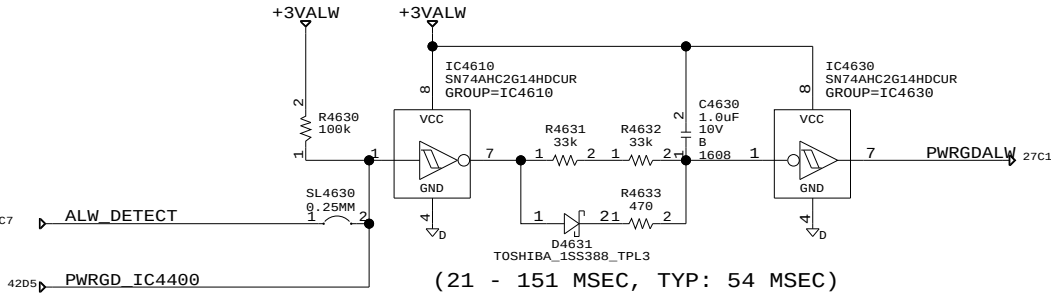
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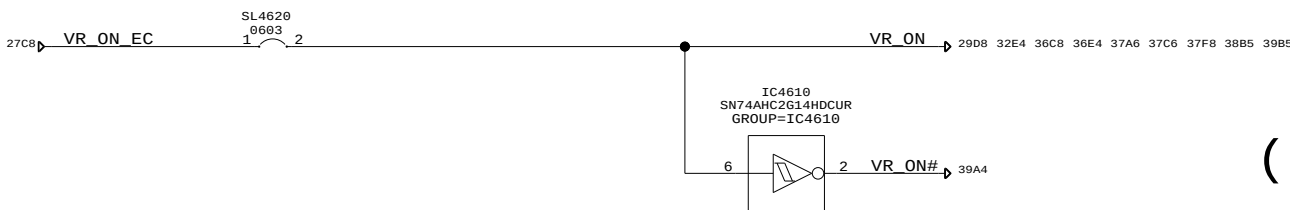
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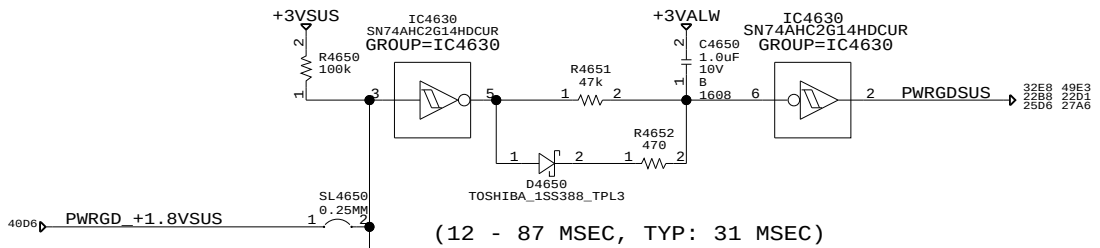
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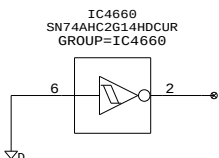
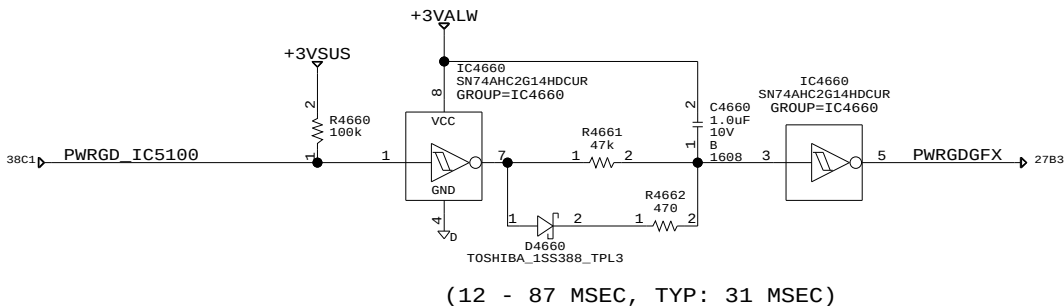
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(PWRGDGFX)



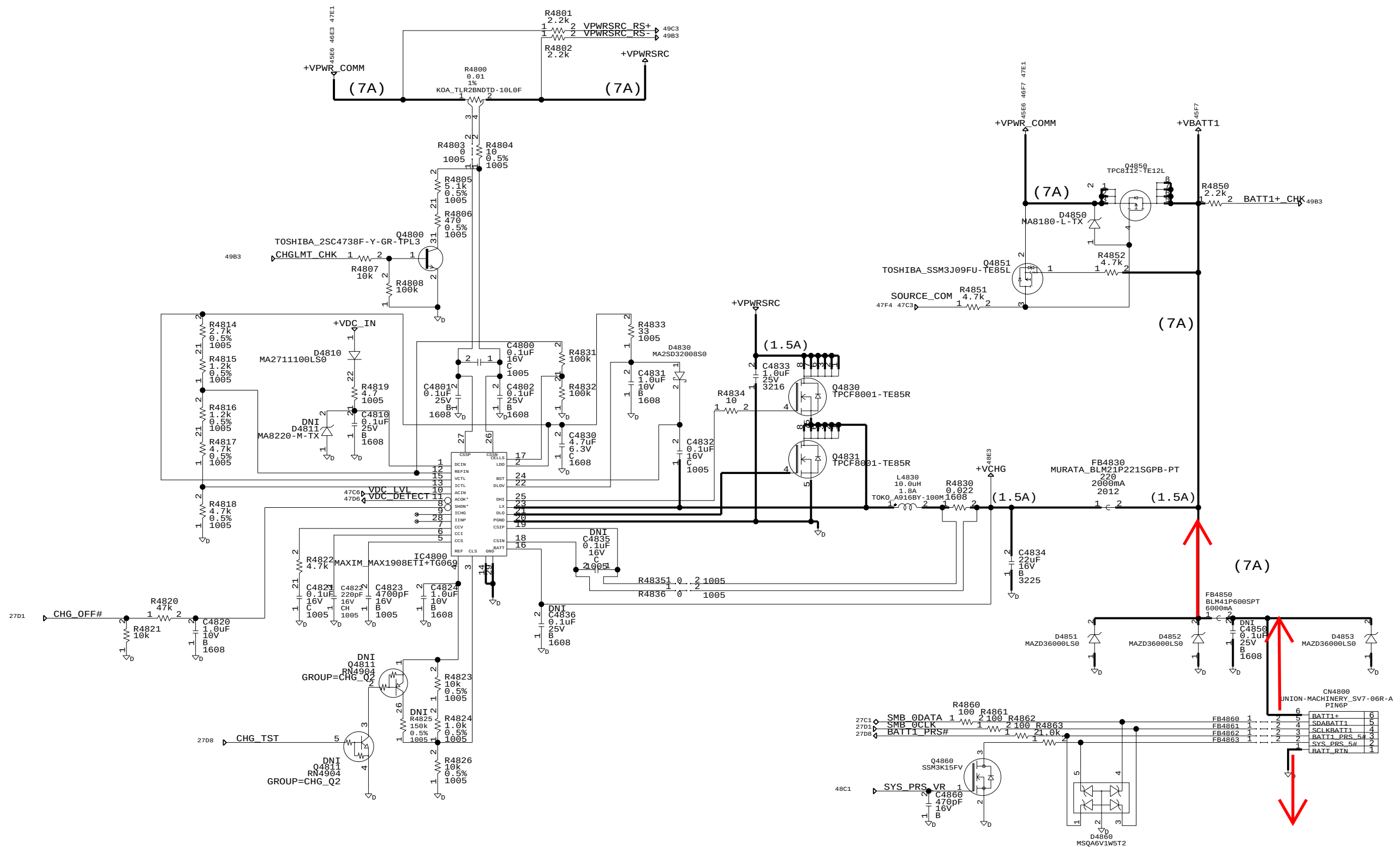
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LAST MODIFIED=Mon Jan 16 20:46:41 2006

SIZE	QTY 1	REV. 1.120	PART NO. -
DESIGNER	SHEET 44 OF 50		

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(REF: 4800- 4899)

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BATTERY CHARGER

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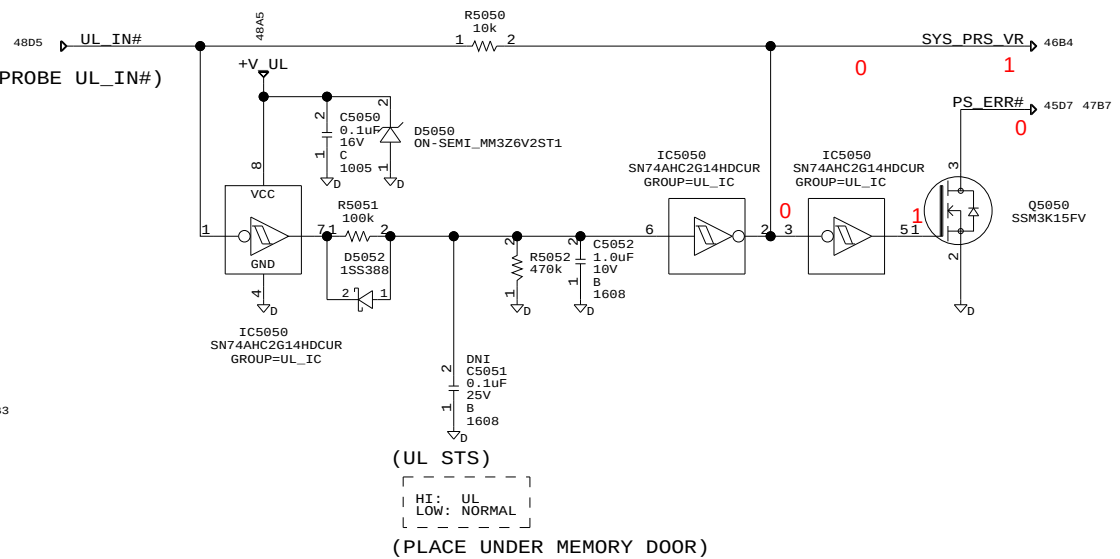
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SIZE C	DESIGNER -			SHEET (POWER-10) 46 OF 50

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SIZE C	DESIGNER -	SHEET 47 OF 50 (POWER-11)	

(OVP)



(BACKUP BATTERY)



DRAWING:		(REF: 5000- 5099)	
		MBX-147	
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LAST_MODIFIED=Mon		Jan 16 20:46:44 2006	
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SIZE C	DESIGNER -	SHEET (POWER-12) 48 OF 50	

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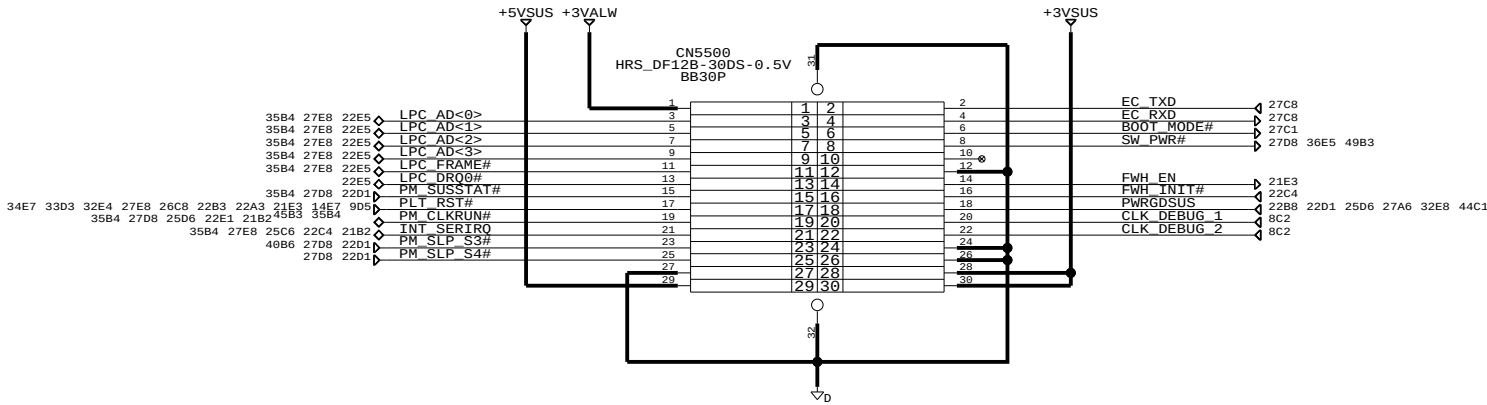
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SONY CONFIDENTIAL

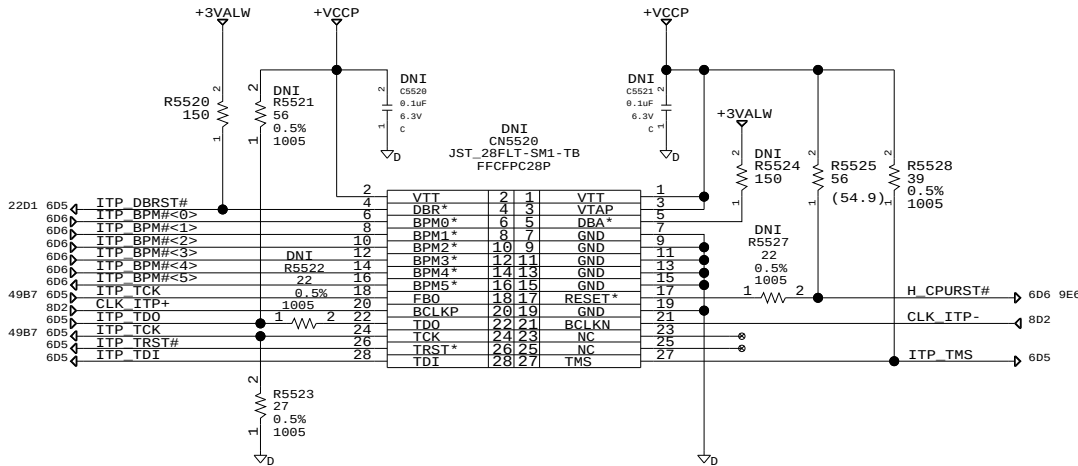
REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APPR.

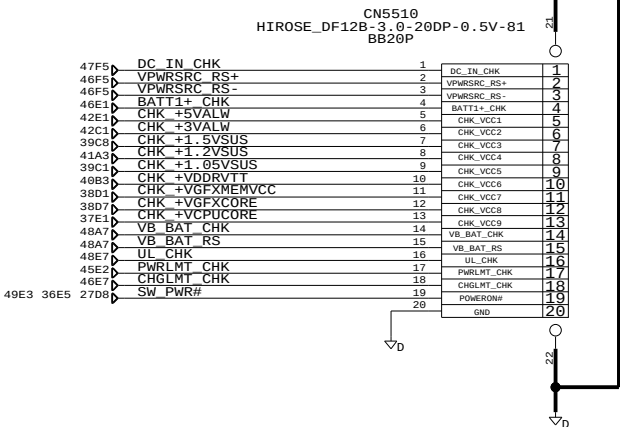
(DEBUG CONNECTOR)



(ITP CONNECTOR)



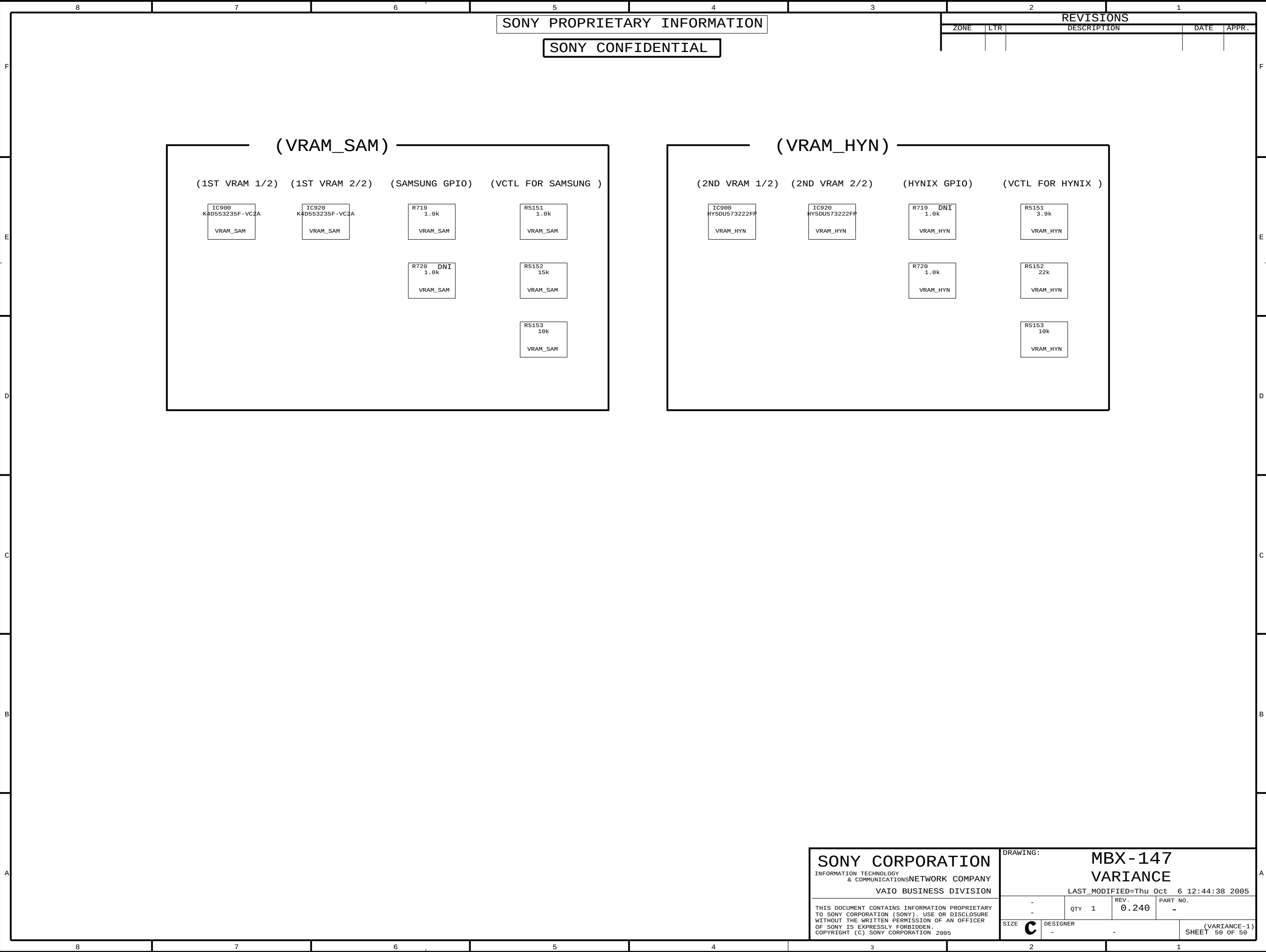
(POWER CHECK CONNECTOR)



SONY CORPORATION
INFORMATION TECHNOLOGY
& COMMUNICATIONS NETWORK COMPANY
VAIO BUSINESS DIVISION

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DRAWING:		MBX-147	
DEB		G	
LAST_MODIFIED=Mon Jan 16 20:46:45 2006		REV. 1.100	
QTY 1		PART NO.	
SIZE C		DESIGNER	
		SHEET (DEBUG-1) 49 OF 50	



8		7		6		5		4		3		2		1	
Title: Basenet Report Design: mbx147 Date: Jan 16 20:49:06 2006 Base nets and synonyms for pwb_lib.MBX147(@pwb_lib.mbx147(sch_1)) Base Signal Synonyms Location([Zone][dir]) +1.2VSUS +1.2VSUS - @pwb_lib.MBX147 26E4 41C3 43C4 +1.05VSUS +1.05VSUS - @pwb_lib.MBX147 12F5 12F8 23F4 39B3 39E1 43C1 48E2 +1.5VSUS +1.5VSUS - @pwb_lib.MBX147 7D6 11B4 11C5 11D4 11D5 11D8 11F5 11F6 12E3 13D5 13E4 21F7 23C7 23F8 32F3 33F5 39D7 43D1 48E5 +1.8VSUS +1.8VSUS - @pwb_lib.MBX147 98B6 9E1 12F5 19F8 26E4 40B5 40B6 40E3 43C5 48E4 11D6 11E7 11E7 11F4 11F4 26E4 41E4 43C2 43C6 +3VALW +3VALW - @pwb_lib.MBX147 21D2 22B4 22E4 23C3 23D3 27B4 27B5 27B6 27C6 27E3 27E3 27E4 27E6 27E6 27F5 28B2 29D5 30D6 30D7 32F7 34D7 34D7 34E7 36B7 36E5 36F3 36F3 40E4 40E6 41D5 41D6 42D1 43C7 43D5 43E1 44B6 44C2 44E3 44E3 44E6 44E7 45B5 45B6 45C4 48C6 48E4 49C6 49C7 49E5 +3VSUS +3VSUS - @pwb_lib.MBX147 6F5 8E5 8E5 9E5 9E5 11C4 14B3 14B8 14C2 14F2 16F2 19B1 19B4 19B8 21D3 22A4 22A5 22A7 22B2 22B2 22B2 22B2 22C1 22D5 23C3 23C3 23E4 24D2 24D3 24E7 25B6 25C7 25D6 25F3 25F5 26D7 26E5 27B4 27E3 27E3 27E6 28E3 29D4 30E7 31B3 31C3 31C4 31D4 32F3 32F3 33E7 33F2 33F4 35C2 35C7 35E3 35E7 36B7 36E4 36E7 36F2 37C7 38C6 41E5 43D1 43D2 43D4 44B7 44C3 45C2 49E4 6B6 23F6 27B2 27B2 27F5 29E4 30C1 30E2 31E5 32F7 35E7 36F3 38F5 39E4 40C5 42E6 42F1 43D7 43E2 48C7 48E3 +5VALW +5VALW - @pwb_lib.MBX147 7C4 23F6 27E3 28E3 29E7 30C1 31D3 31D7 31E7 35F2 36C7 36F2 37B7 37D3 37D5 37F3 37F6 43D2 43D6 49E5 45F7 46E2 6B8 6D4 6D5 6D5 6E5 7D3 7D6 9A7 9B7 9B7 9B8 9E6 11F1 22C4 22C5 23E4 37C7 37F7 39B2 49C6 49C7 46C3 48E3 7C6 7E6 7E8 7F3 37D1 48E6 34E4 45F7 46D6 47B7 47C7 47E4 47F6 20E4 40B4 27F4 29D8 42D5 48B6 18B5 18C2 18C6 17D3 26E5 29E4 30D7 35C7 39B3 43C7 43D7 43E6 17F4 17F6 38F8 48E6 15B8 15C1 15E3 15F1 15F5 18B5 18E3 18E3 18E7 18E7 38F1 14E4 17D5 15B4 17B7 17F6 17F8 43C4 17C6 43C5 17B5 17C5 17C6 17D6 43C5 14C5 14D4 14F7 14F8 16D2 16D4 16D7 16D8 16E3 16E3 16E4 16E7 16F3 17B5 17C5 17D4 17F4 43C6 25F3 32E6 42B8 45E6 47C5 48A6 45E6 48D6 35F2 37D4 37F4 38F4 38F5 38F6 39E5 42B8 42D4 42E7 42F4 43F7 45E5 46D4 46F5 45E6 46E3 46F7 47E1 22A7 22C5 23B3 48B5 9E1 19E4 19E7 40B4 48A5 48C4 35C6 32E2 32E2 32D4 32E2 32E6 +VBATT1 +VBATT1 - @pwb_lib.MBX147 45F7 46E2 +VCCP +VCCP - @pwb_lib.MBX147 6B8 6D4 6D5 6D5 6E5 7D3 7D6 9A7 9B7 9B7 9B8 9E6 11F1 22C4 22C5 23E4 37C7 37F7 39B2 49C6 49C7 46C3 48E3 7C6 7E6 7E8 7F3 37D1 48E6 34E4 45F7 46D6 47B7 47C7 47E4 47F6 20E4 40B4 27F4 29D8 42D5 48B6 18B5 18C2 18C6 17D3 26E5 29E4 30D7 35C7 39B3 43C7 43D7 43E6 17F4 17F6 38F8 48E6 15B8 15C1 15E3 15F1 15F5 18B5 18E3 18E3 18E7 18E7 38F1 14E4 17D5 15B4 17B7 17F6 17F8 43C4 17C6 43C5 17B5 17C5 17C6 17D6 43C5 14C5 14D4 14F7 14F8 16D2 16D4 16D7 16D8 16E3 16E3 16E4 16E7 16F3 17B5 17C5 17D4 17F4 43C6 25F3 32E6 42B8 45E6 47C5 48A6 45E6 48D6 35F2 37D4 37F4 38F4 38F5 38F6 39E5 42B8 42D4 42E7 42F4 43F7 45E5 46D4 46F5 45E6 46E3 46F7 47E1 22A7 22C5 23B3 48B5 9E1 19E4 19E7 40B4 48A5 48C4 35C6 32E2 32E2 32D4 32E2 32E6 +VCHG +VCHG - @pwb_lib.MBX147 45F7 46E2 +VCPUCORE +VCPUCORE - @pwb_lib.MBX147 6B8 6D4 6D5 6D5 6E5 7D3 +VDC_IN +VDC_IN - @pwb_lib.MBX147 7D6 9A7 9B7 9B7 9B8 9E6 11F1 22C4 22C5 23E4 37C7 37F7 39B2 49C6 49C7 46C3 48E3 7C6 7E6 7E8 7F3 37D1 48E6 34E4 45F7 46D6 47B7 47C7 47E4 47F6 20E4 40B4 27F4 29D8 42D5 48B6 18B5 18C2 18C6 17D3 26E5 29E4 30D7 35C7 39B3 43C7 43D7 43E6 17F4 17F6 38F8 48E6 15B8 15C1 15E3 15F1 15F5 18B5 18E3 18E3 18E7 18E7 38F1 14E4 17D5 15B4 17B7 17F6 17F8 43C4 17C6 43C5 17B5 17C5 17C6 17D6 43C5 14C5 14D4 14F7 14F8 16D2 16D4 16D7 16D8 16E3 16E3 16E4 16E7 16F3 17B5 17C5 17D4 17F4 43C6 25F3 32E6 42B8 45E6 47C5 48A6 45E6 48D6 35F2 37D4 37F4 38F4 38F5 38F6 39E5 42B8 42D4 42E7 42F4 43F7 45E5 46D4 46F5 45E6 46E3 46F7 47E1 22A7 22C5 23B3 48B5 9E1 19E4 19E7 40B4 48A5 48C4 35C6 32E2 32E2 32D4 32E2 32E6 +VDDRVT +VDDRVT - @pwb_lib.MBX147 45F7 46E2 +VEC_3.3 +VEC_3.3 - @pwb_lib.MBX147 6B8 6D4 6D5 6D5 6E5 7D3 +VFBAVREF +VFBAVREF - @pwb_lib.MBX147 7D6 9A7 9B7 9B7 9B8 9E6 +VFETDRV +VFETDRV - @pwb_lib.MBX147 11F1 22C4 22C5 23E4 37C7 37F7 39B2 49C6 49C7 46C3 48E3 7C6 7E6 7E8 7F3 37D1 48E6 34E4 45F7 46D6 47B7 47C7 47E4 47F6 20E4 40B4 27F4 29D8 42D5 48B6 18B5 18C2 18C6 17D3 26E5 29E4 30D7 35C7 39B3 43C7 43D7 43E6 17F4 17F6 38F8 48E6 15B8 15C1 15E3 15F1 15F5 18B5 18E3 18E3 18E7 18E7 38F1 14E4 17D5 15B4 17B7 17F6 17F8 43C4 17C6 43C5 17B5 17C5 17C6 17D6 43C5 14C5 14D4 14F7 14F8 16D2 16D4 16D7 16D8 16E3 16E3 16E4 16E7 16F3 17B5 17C5 17D4 17F4 43C6 25F3 32E6 42B8 45E6 47C5 48A6 45E6 48D6 35F2 37D4 37F4 38F4 38F5 38F6 39E5 42B8 42D4 42E7 42F4 43F7 45E5 46D4 46F5 45E6 46E3 46F7 47E1 22A7 22C5 23B3 48B5 9E1 19E4 19E7 40B4 48A5 48C4 35C6 32E2 32E2 32D4 32E2 32E6 +VGFXCORE +VGFXCORE - @pwb_lib.MBX147 45F7 46E2 +VGFXMEMVCC +VGFXMEMVCC - @pwb_lib.MBX147 6B8 6D4 6D5 6D5 6E5 7D3 7D6 9A7 9B7 9B7 9B8 9E6 11F1 22C4 22C5 23E4 37C7 37F7 39B2 49C6 49C7 46C3 48E3 7C6 7E6 7E8 7F3 37D1 48E6 34E4 45F7 46D6 47B7 47C7 47E4 47F6 20E4 40B4 27F4 29D8 42D5 48B6 18B5 18C2 18C6 17D3 26E5 29E4 30D7 35C7 39B3 43C7 43D7 43E6 17F4 17F6 38F8 48E6 15B8 15C1 15E3 15F1 15F5 18B5 18E3 18E3 18E7 18E7 38F1 14E4 17D5 15B4 17B7 17F6 17F8 43C4 17C6 43C5 17B5 17C5 17C6 17D6 43C5 14C5 14D4 14F7 14F8 16D2 16D4 16D7 16D8 16E3 16E3 16E4 16E7 16F3 17B5 17C5 17D4 17F4 43C6 25F3 32E6 42B8 45E6 47C5 48A6 45E6 48D6 35F2 37D4 37F4 38F4 38F5 38F6 39E5 42B8 42D4 42E7 42F4 43F7 45E5 46D4 46F5 45E6 46E3 46F7 47E1 22A7 22C5 23B3 48B5 9E1 19E4 19E7 40B4 48A5 48C4 35C6 32E2 32E2 32D4 32E2 32E6 +VGFXTMDS_3.3 +VGFXTMDS_3.3 - @pwb_lib.MBX147 45F7 46E2 +VGFX_1.2 +VGFX_1.2 - @pwb_lib.MBX147 6B8 6D4 6D5 6D5 6E5 7D3 +VGFX_1.8 +VGFX_1.8 - @pwb_lib.MBX147 7D6 9A7 9B7 9B7 9B8 9E6 +VGFX_2.5 +VGFX_2.5 - @pwb_lib.MBX147 11F1 22C4 22C5 23E4 37C7 +VGFX_3.3 +VGFX_3.3 - @pwb_lib.MBX147 37F7 39B2 49C6 49C7 46C3 48E3 7C6 7E6 7E8 7F3 37D1 48E6 34E4 45F7 46D6 47B7 47C7 47E4 47F6 20E4 40B4 27F4 29D8 42D5 48B6 18B5 18C2 18C6 17D3 26E5 29E4 30D7 35C7 39B3 43C7 43D7 43E6 17F4 17F6 38F8 48E6 15B8 15C1 15E3 15F1 15F5 18B5 18E3 18E3 18E7 18E7 38F1 14E4 17D5 15B4 17B7 17F6 17F8 43C4 17C6 43C5 17B5 17C5 17C6 17D6 43C5 14C5 14D4 14F7 14F8 16D2 16D4 16D7 16D8 16E3 16E3 16E4 16E7 16F3 17B5 17C5 17D4 17F4 43C6 25F3 32E6 42B8 45E6 47C5 48A6 45E6 48D6 35F2 37D4 37F4 38F4 38F5 38F6 39E5 42B8 42D4 42E7 42F4 43F7 45E5 46D4 46F5 45E6 46E3 46F7 47E1 22A7 22C5 23B3 48B5 9E1 19E4 19E7 40B4 48A5 48C4 35C6 32E2 32E2 32D4 32E2 32E6 +VPCCDVCC +VPCCDVCC - @pwb_lib.MBX147 45F7 46E2 +VPRTCT_5 +VPRTCT_5 - @pwb_lib.MBX147 6B8 6D4 6D5 6D5 6E5 7D3 +VPRTCT_SRC +VPRTCT_SRC - @pwb_lib.MBX147 7D6 9A7 9B7 9B7 9B8 9E6 +VPWRSRC +VPWRSRC - @pwb_lib.MBX147 11F1 22C4 22C5 23E4 37C7 37F7 39B2 49C6 49C7 46C3 48E3 7C6 7E6 7E8 7F3 37D1 48E6 34E4 45F7 46D6 47B7 47C7 47E4 47F6 20E4 40B4 27F4 29D8 42D5 48B6 18B5 18C2 18C6 17D3 26E5 29E4 30D7 35C7 39B3 43C7 43D7 43E6 17F4 17F6 38F8 48E6 15B8 15C1 15E3 15F1 15F5 18B5 18E3 18E3 18E7 18E7 38F1 14E4 17D5 15B4 17B7 17F6 17F8 43C4 17C6 43C5 17B5 17C5 17C6 17D6 43C5 14C5 14D4 14F7 14F8 16D2 16D4 16D7 16D8 16E3 16E3 16E4 16E7 16F3 17B5 17C5 17D4 17F4 43C6 25F3 32E6 42B8 45E6 47C5 48A6 45E6 48D6 35F2 37D4 37F4 38F4 38F5 38F6 39E5 42B8 42D4 42E7 42F4 43F7 45E5 46D4 46F5 45E6 46E3 46F7 47E1 22A7 22C5 23B3 48B5 9E1 19E4 19E7 40B4 48A5 48C4 35C6 32E2 32E2 32D4 32E2 32E6 +VPWR_COMM +VPWR_COMM - @pwb_lib.MBX147 45F7 46E2 +VRTC +VRTC - @pwb_lib.MBX147 6B8 6D4 6D5 6D5 6E5 7D3 +VSMVREF +VSMVREF - @pwb_lib.MBX147 7D6 9A7 9B7 9B7 9B8 9E6 +V_UL +V_UL - @pwb_lib.MBX147 11F1 22C4 22C5 23E4 37C7 =+VCAM =+VCAM - @pwb_lib.MBX147 37F7 39B2 49C6 49C7 =+VEXCD15 =+VEXCD15 - @pwb_lib.MBX147 46C3 48E3 =+VEXCD33 =+VEXCD33 - @pwb_lib.MBX147 7C6 7E6 7E8 7F3 37D1 48E6 =+VEXCDAUX =+VEXCDAUX - @pwb_lib.MBX147 34E4 45F7 46D6 47B7 47C7 =+VPCCDVPP =+VPCCDVPP - @pwb_lib.MBX147 47E4 47F6 20E4 40B4 27F4 29D8 42D5 48B6 18B5 18C2 18C6 17D3 26E5 29E4 30D7 35C7 39B3 43C7 43D7 43E6 17F4 17F6 38F8 48E6 15B8 15C1 15E3 15F1 15F5 18B5 18E3 18E3 18E7 18E7 38F1 14E4 17D5 15B4 17B7 17F6 17F8 43C4 17C6 43C5 17B5 17C5 17C6 17D6 43C5 14C5 14D4 14F7 14F8 16D2 16D4 16D7 16D8 16E3 16E3 16E4 16E7 16F3 17B5 17C5 17D4 17F4 43C6 25F3 32E6 42B8 45E6 47C5 48A6 45E6 48D6 35F2 37D4 37F4 38F4 38F5 38F6 39E5 42B8 42D4 42E7 42F4 43F7 45E5 46D4 46F5 45E6 46E3 46F7 47E1 22A7 22C5 23B3 48B5 9E1 19E4 19E7 40B4 48A5 48C4 35C6 32E2 32E2 32D4 32E2 32E6															
												=A_RCVENIN# =A_RCVENIN# - @pwb_lib.MBX147 10C5 =A_RCVENOUT# =A_RCVENOUT# - @pwb_lib.MBX147 10C5 =B_RCVENIN# =B_RCVENIN# - @pwb_lib.MBX147 10C1 =B_RCVENOUT# =B_RCVENOUT# - @pwb_lib.MBX147 10C1 =CARD_SCL =CARD_SCL - @pwb_lib.MBX147 25C6 =CARD_SDA =CARD_SDA - @pwb_lib.MBX147 25C6 =CFG3 =CFG3 - @pwb_lib.MBX147 9E4 =CFG4 =CFG4 - @pwb_lib.MBX147 9E4 =CFG5 =CFG5 - @pwb_lib.MBX147 9E4 =CFG8 =CFG8 - @pwb_lib.MBX147 9E4 =CFG9 =CFG9 - @pwb_lib.MBX147 9E4 =CFG10 =CFG10 - @pwb_lib.MBX147 9E4 =CFG11 =CFG11 - @pwb_lib.MBX147 9E4 =CFG12 =CFG12 - @pwb_lib.MBX147 9E4 =CFG13 =CFG13 - @pwb_lib.MBX147 9E4 =CFG14 =CFG14 - @pwb_lib.MBX147 9E4 =CFG15 =CFG15 - @pwb_lib.MBX147 9E4 =CFG16 =CFG16 - @pwb_lib.MBX147 9E4 =CFG17 =CFG17 - @pwb_lib.MBX147 9E4 =CFG18 =CFG18 - @pwb_lib.MBX147 9E4 =CFG19 =CFG19 - @pwb_lib.MBX147 9E4 =CLK_27M =CLK_27M - @pwb_lib.MBX147 14A4 14A7 =CLK_27M_SS =CLK_27M_SS - @pwb_lib.MBX147 14A4 14A7 =DRIVE_RST# =DRIVE_RST# - @pwb_lib.MBX147 29C4 =DRIVE_RSTF# =DRIVE_RSTF# - @pwb_lib.MBX147 29C3 =D_SUS_ON =D_SUS_ON - @pwb_lib.MBX147 34C6 =EXT_GFX_VGA_BLU =EXT_GFX_VGA_BLU - @pwb_lib.MBX147 14C4 =EXT_GFX_VGA_DDCCLK =EXT_GFX_VGA_DDCCLK - @pwb_lib.MBX147 14B4 47 =EXT_GFX_VGA_DDCDATA =EXT_GFX_VGA_DDCDATA - @pwb_lib.MBX147 14A4 147 =EXT_GFX_VGA_GRN =EXT_GFX_VGA_GRN - @pwb_lib.MBX147 14C4 =EXT_GFX_VGA_HSYNC =EXT_GFX_VGA_HSYNC - @pwb_lib.MBX147 14B4 7 =EXT_GFX_VGA_RED =EXT_GFX_VGA_RED - @pwb_lib.MBX147 14C4 =EXT_GFX_VGA_VSYNC =EXT_GFX_VGA_VSYNC - @pwb_lib.MBX147 14B4 7 =EXT_LCD_BKLCTR =EXT_LCD_BKLCTR - @pwb_lib.MBX147 16E3 =EXT_LCD_BKLEN =EXT_LCD_BKLEN - @pwb_lib.MBX147 16E3 =EXT_LCD_PNLEN =EXT_LCD_PNLEN - @pwb_lib.MBX147 16E3 =EXT_LVDS_ACLK+ =EXT_LVDS_ACLK+ - @pwb_lib.MBX147 14E4 =EXT_LVDS_ACLK- =EXT_LVDS_ACLK- - @pwb_lib.MBX147 14E4 =EXT_LVDS_ADATA0+ =EXT_LVDS_ADATA0+ - @pwb_lib.MBX147 14E4 =EXT_LVDS_ADATA0- =EXT_LVDS_ADATA0- - @pwb_lib.MBX147 14F4 =EXT			

		8		7		6		5		4		3		2		1	
														REVISONS			
														ZONE		LTR	
														DESCRIPTION		DATE	
																APPR.	
F	=VCD31	=VCD31 - @pwb_lib.MBX147	30D2	CAM_PRS#		CAM_PRS# - @pwb_lib.MBX147	27B3 35D6										
	=VCD32	=VCD32 - @pwb_lib.MBX147	30D2	CHGLMT_CHK		CHGLMT_CHK - @pwb_lib.MBX147	46E7 49B3										
	=VGA_CRTDET#	=VGA_CRTDET# - @pwb_lib.MBX147	31C4	CHG_OFF#		CHG_OFF# - @pwb_lib.MBX147	27D1 46C8										
	=VGA_VSYNC	=VGA_VSYNC - @pwb_lib.MBX147	31E4	CHG_TST		CHG_TST - @pwb_lib.MBX147	27D8 46B7										
	=VGT_LREF0	=VGT_LREF0 - @pwb_lib.MBX147	6C4	CHK_+1.2VSUS		CHK_+1.2VSUS - @pwb_lib.MBX147	41A3 49B3										
	=WL_CH_CLK_PCIE_F	=WL_CH_CLK_PCIE_F - @pwb_lib.MBX147	33D7	CHK_+1.05VSUS		CHK_+1.05VSUS - @pwb_lib.MBX147	39C1 49B3										
	A20GATE	A20GATE - @pwb_lib.MBX147	21B2 22D4 27D8	CHK_+1.5VSUS		CHK_+1.5VSUS - @pwb_lib.MBX147	39C8 49B3										
	ACC_X	ACC_X - @pwb_lib.MBX147	27D8 29C8	CHK_+3VALW		CHK_+3VALW - @pwb_lib.MBX147	42C1 49B3										
	ACC_Y	ACC_Y - @pwb_lib.MBX147	27D8 29C5	CHK_+5VALW		CHK_+5VALW - @pwb_lib.MBX147	42E1 49B3										
	ACC_Z	ACC_Z - @pwb_lib.MBX147	27D8 29C5	CHK_+VCPUCORE		CHK_+VCPUCORE - @pwb_lib.MBX147	37E1 49B3										
E	AC_OFF	AC_OFF - @pwb_lib.MBX147	27D1 47A7	CHK_+VDDRVT		CHK_+VDDRVT - @pwb_lib.MBX147	40B3 49B3										
	AC_STS	AC_STS - @pwb_lib.MBX147	27A3 47B3	CHK_+VGFXCORE		CHK_+VGFXCORE - @pwb_lib.MBX147	38D7 49B3										
	ALW_DETECT	ALW_DETECT - @pwb_lib.MBX147	37B4 42A6 44D4	CHK_+VGFXMEMVCC		CHK_+VGFXMEMVCC - @pwb_lib.MBX147	38D1 49B3										
	ALW_RST_C#	ALW_RST_C# - @pwb_lib.MBX147	39B5 42A6 45C5 47B6 47C5	CLKREQ_EXCD#		CLKREQ_EXCD# - @pwb_lib.MBX147	8C2 32C5										
	ANL66_ID	ANL66_ID - @pwb_lib.MBX147	27B3 36C4	CLKREQ_MCH#		CLKREQ_MCH# - @pwb_lib.MBX147	8C2 9D4										
	AVSS_EC	AVSS_EC - @pwb_lib.MBX147	27C5 27E4 27F5 29C6	CLKREQ_MNCD#		CLKREQ_MNCD# - @pwb_lib.MBX147	8C2 33D6										
	AZ_BCLK_AUD	AZ_BCLK_AUD - @pwb_lib.MBX147	22E5 36E4	CLKREQ_PEG#		CLKREQ_PEG# - @pwb_lib.MBX147	8C2 14A8 27A3										
	AZ_BCLK_MDM	AZ_BCLK_MDM - @pwb_lib.MBX147	22E5 35E6	CLKREQ_SATA#		CLKREQ_SATA# - @pwb_lib.MBX147	8C2 22C6										
	AZ_RST_AUD#	AZ_RST_AUD# - @pwb_lib.MBX147	22E5 36E4	CLK_27MHZ		CLK_27MHZ - @pwb_lib.MBX147	8C1 14A8										
	AZ_RST_MDM#	AZ_RST_MDM# - @pwb_lib.MBX147	22E5 35E6	CLK_27MHZ_SS		CLK_27MHZ_SS - @pwb_lib.MBX147	8C1 14A8										
D	AZ_SDIN0	AZ_SDIN0 - @pwb_lib.MBX147	22E5 36E4	CLK_BSEL0		CLK_BSEL0 - @pwb_lib.MBX147	6C4 8C2 9E4										
	AZ_SDIN1	AZ_SDIN1 - @pwb_lib.MBX147	22D5 35E6	CLK_BSEL1		CLK_BSEL1 - @pwb_lib.MBX147	6C4 8B2 9E4										
	AZ_SDOUT_AUD	AZ_SDOUT_AUD - @pwb_lib.MBX147	22D5 36E4	CLK_BSEL2		CLK_BSEL2 - @pwb_lib.MBX147	6C4 9E4										
	AZ_SDOUT_MDM	AZ_SDOUT_MDM - @pwb_lib.MBX147	22D5 35E6	CLK_CARD		CLK_CARD - @pwb_lib.MBX147	8C2 25D6										
	AZ_SYNC_AUD	AZ_SYNC_AUD - @pwb_lib.MBX147	22D5 36E4	CLK_CPU_BCLK+		CLK_CPU_BCLK+ - @pwb_lib.MBX147	6C5 8D2										
	AZ_SYNC_MDM	AZ_SYNC_MDM - @pwb_lib.MBX147	22D5 35E6	CLK_CPU_BCLK-		CLK_CPU_BCLK- - @pwb_lib.MBX147	6C5 8D2										
	A_A<0>	A_A<0> - @pwb_lib.MBX147	25E4 32B5	CLK_DEBUG_1		CLK_DEBUG_1 - @pwb_lib.MBX147	8C2 49E3										
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	A_A<2>	A_A<2> - @pwb_lib.MBX147	25E4 32B5	CLK_DREFCLK+		CLK_DREFCLK+ - @pwb_lib.MBX147	8C2 9D1										
	A_A<3>	A_A<3> - @pwb_lib.MBX147	25E4 32B7	CLK_DREFCLK-		CLK_DREFCLK- - @pwb_lib.MBX147	8C2 9D1										
C	A_A<4>	A_A<4> - @pwb_lib.MBX147	25E4 32B7	CLK_DREF_SS+		CLK_DREF_SS+ - @pwb_lib.MBX147	8C2 9D1										
	A_A<5>	A_A<5> - @pwb_lib.MBX147	25E4 32C7	CLK_DREF_SS-		CLK_DREF_SS- - @pwb_lib.MBX147	8C2 9D1										
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	A_A<8>	A_A<8> - @pwb_lib.MBX147	25E4 32C7	CLK_ICH14		CLK_ICH14 - @pwb_lib.MBX147	8C2 22D5										
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	A_A<13>	A_A<13> - @pwb_lib.MBX147	25E4 32C5	CLK_MCH_GPLL+		CLK_MCH_GPLL+ - @pwb_lib.MBX147	8D2 9E1										
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	A_A<15>	A_A<15> - @pwb_lib.MBX147	25E4 32C5	CLK_MS48		CLK_MS48 - @pwb_lib.MBX147	8C2 24C4										
	A_A<16>	A_A<16> - @pwb_lib.MBX147	25E3 32C7	CLK_PCIE_DOCK+		CLK_PCIE_DOCK+ - @pwb_lib.MBX147	8D2 34D6										
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	A_A<18>	A_A<18> - @pwb_lib.MBX147	25E4 32C7	CLK_PCIE_EXCD+		CLK_PCIE_EXCD+ - @pwb_lib.MBX147	8D2 32C1										
	A_A<19>	A_A<19> - @pwb_lib.MBX147	25F4 32C7	CLK_PCIE_EXCD-		CLK_PCIE_EXCD- - @pwb_lib.MBX147	8D2 32C1										
	A_A<20>	A_A<20> - @pwb_lib.MBX147	25F4 32C7	CLK_PCIE_ICH+		CLK_PCIE_ICH+ - @pwb_lib.MBX147	8D2 21E8										
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	A_BVD2	A_BVD2 - @pwb_lib.MBX147	25D3 32B7	CLK_PCIE_PEG-		CLK_PCIE_PEG- - @pwb_lib.MBX147	8D2 14D8										
	A_CD1#	A_CD1# - @pwb_lib.MBX147	25D4 32D5	CLK_PCIE_SATA+		CLK_PCIE_SATA+ - @pwb_lib.MBX147	8D2 22D6										
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	A_CE2#	A_CE2# - @pwb_lib.MBX147	25D4 32C7	CLK_TPM		CLK_TPM - @pwb_lib.MBX147	8C2 35B4										
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	A_D<1>	A_D<1> - @pwb_lib.MBX147	25D4 32B7	CPU_VCCSENSE		CPU_VCCSENSE - @pwb_lib.MBX147	7C5 37D8										

[illegible]

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		REVISIONS									
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		H_DSTB2#+	H_DSTB2#+	-	@pwb_lib.MBX147	6D2	9D6				
		H_DSTB2#-	H_DSTB2#-	-	@pwb_lib.MBX147	6D2	9D6				
		H_DSTB3#+	H_DSTB3#+	-	@pwb_lib.MBX147	6D2	9D6				
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		ICH_PWROK_EC	ICH_PWROK_EC	-	@pwb_lib.MBX147	9D5	22B3 22D1 27D1				
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		ICH_SPI_MISO	ICH_SPI_MISO	-	@pwb_lib.MBX147	22B6	22E5				
		ICH_SPI_MOSI	ICH_SPI_MOSI	-	@pwb_lib.MBX147	22B3	22E5				
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		IDSELCB	IDSELCB	-	@pwb_lib.MBX147	25D6	25D6				
		IMVP_OTP_1	IMVP_OTP_1	-	@pwb_lib.MBX147	37B2	37C8 37E2				
		IMVP_OTP_2	IMVP_OTP_2	-	@pwb_lib.MBX147	37B2	37C8 37D2				
		IMVP_PWRGD	IMVP_PWRGD	-	@pwb_lib.MBX147	22D1	27A3 37A6 37C6				
		INTVRMEN	INTVRMEN	-	@pwb_lib.MBX147	22A8	22C6				
		INT_GFX_VGA_BLU	INT_GFX_VGA_BLU	-	@pwb_lib.MBX147	13C7	14C4				
		INT_GFX_VGA_DDCCLK	INT_GFX_VGA_DDCCLK	-	@pwb_lib.MBX14	13C6	14A4				
						7					
		INT_GFX_VGA_DDCDATA	INT_GFX_VGA_DDCDATA	-	@pwb_lib.MBX1	13C6	14A4				
						47					
		INT_GFX_VGA_GRN	INT_GFX_VGA_GRN	-	@pwb_lib.MBX147	13C7	14C4				
		INT_GFX_VGA_HSYNC	INT_GFX_VGA_HSYNC	-	@pwb_lib.MBX147	13C6	14B4				
		INT_GFX_VGA_RED	INT_GFX_VGA_RED	-	@pwb_lib.MBX147	13C7	14B4				
		INT_GFX_VGA_VSYNC	INT_GFX_VGA_VSYNC	-	@pwb_lib.MBX147	13C6	14B4				
		INT_IDEIRQ	INT_IDEIRQ	-	@pwb_lib.MBX147	21B2	22D8 29C4				
		INT_LCD_BKLCTR	INT_LCD_BKLCTR	-	@pwb_lib.MBX147	13E7	16E3				
		INT_LCD_BKLEN	INT_LCD_BKLEN	-	@pwb_lib.MBX147	13E7	16E3				
		INT_LCD_PNLEN	INT_LCD_PNLEN	-	@pwb_lib.MBX147	13E7	16E3				
		INT_LVDS_ACLK+	INT_LVDS_ACLK+	-	@pwb_lib.MBX147	13D6	14E4				
		INT_LVDS_ACLK-	INT_LVDS_ACLK-	-	@pwb_lib.MBX147	13D6	14E4				
		INT_LVDS_ADATA0+	INT_LVDS_ADATA0+	-	@pwb_lib.MBX147	13D6	14F4				
		INT_LVDS_ADATA0-	INT_LVDS_ADATA0-	-	@pwb_lib.MBX147	13D6	14F4				
		INT_LVDS_ADATA1+	INT_LVDS_ADATA1+	-	@pwb_lib.MBX147	13D6	14E4				
		INT_LVDS_ADATA1-	INT_LVDS_ADATA1-	-	@pwb_lib.MBX147	13D6	14E4				
		INT_LVDS_ADATA2+	INT_LVDS_ADATA2+	-	@pwb_lib.MBX147	13D6	14E4				
		INT_LVDS_ADATA2-	INT_LVDS_ADATA2-	-	@pwb_lib.MBX147	13D6	14E4				
		INT_PIRQA#	INT_PIRQA#	-	@pwb_lib.MBX147	21D2	21D3				
		INT_PIRQB#	INT_PIRQB#	-	@pwb_lib.MBX147	21D2	21D3				
		INT_PIRQC#	INT_PIRQC#	-	@pwb_lib.MBX147	21D2	21D3				

8		7		6		5		4		3		2		1		
F	KSI<3>	KSI<3> - @pwb_lib.MBX147	27E1	28D5		M_A_DM<7>	M_A_DM<7> - @pwb_lib.MBX147	10D5	19B7			REVISIONS				
	KSI<4>	KSI<4> - @pwb_lib.MBX147	27E1	28D5		M_A_DM<0>	M_A_DM<0> - @pwb_lib.MBX147	10E7	19E7			ZONE	LTR	DESCRIPTION	DATE	APPR.
	KSI<5>	KSI<5> - @pwb_lib.MBX147	27E1	28D5		M_A_DQ<1>	M_A_DQ<1> - @pwb_lib.MBX147	10E7	19E7							
	KSI<6>	KSI<6> - @pwb_lib.MBX147	27E1	28D5		M_A_DQ<2>	M_A_DQ<2> - @pwb_lib.MBX147	10D7	19E7							
	KSI<7>	KSI<7> - @pwb_lib.MBX147	27E1	28D5		M_A_DQ<3>	M_A_DQ<3> - @pwb_lib.MBX147	10D7	19E7							
	KSO<0>	KSO<0> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<4>	M_A_DQ<4> - @pwb_lib.MBX147	10D7	19E5							
	KSO<1>	KSO<1> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<5>	M_A_DQ<5> - @pwb_lib.MBX147	10D7	19E5							
	KSO<2>	KSO<2> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<6>	M_A_DQ<6> - @pwb_lib.MBX147	10D7	19E5							
	KSO<3>	KSO<3> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<7>	M_A_DQ<7> - @pwb_lib.MBX147	10D7	19E5							
	KSO<4>	KSO<4> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<8>	M_A_DQ<8> - @pwb_lib.MBX147	10D7	19E7							
	KSO<5>	KSO<5> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<9>	M_A_DQ<9> - @pwb_lib.MBX147	10D7	19E7							
	KSO<6>	KSO<6> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<10>	M_A_DQ<10> - @pwb_lib.MBX147	10D7	19E7							
	KSO<7>	KSO<7> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<11>	M_A_DQ<11> - @pwb_lib.MBX147	10D7	19E7							
	KSO<8>	KSO<8> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<12>	M_A_DQ<12> - @pwb_lib.MBX147	10D7	19E5							
E	KSO<9>	KSO<9> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<13>	M_A_DQ<13> - @pwb_lib.MBX147	10D7	19E5							
	KSO<10>	KSO<10> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<14>	M_A_DQ<14> - @pwb_lib.MBX147	10D7	19E5							
	KSO<11>	KSO<11> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<15>	M_A_DQ<15> - @pwb_lib.MBX147	10D7	19E5							
	KSO<12>	KSO<12> - @pwb_lib.MBX147	27A6	28D5		M_A_DQ<16>	M_A_DQ<16> - @pwb_lib.MBX147	10D7	19D7							
	KSO<13>	KSO<13> - @pwb_lib.MBX147	27A6	28D5		M_A_DQ<17>	M_A_DQ<17> - @pwb_lib.MBX147	10D7	19D7							
	KSO<14>	KSO<14> - @pwb_lib.MBX147	27A6	28D5		M_A_DQ<18>	M_A_DQ<18> - @pwb_lib.MBX147	10D7	19D7							
	KSO<15>	KSO<15> - @pwb_lib.MBX147	27A6	28D5		M_A_DQ<19>	M_A_DQ<19> - @pwb_lib.MBX147	10D7	19D7							
	LCD_BKLCCTR	LCD_BKLCCTR - @pwb_lib.MBX147	16E1	30B3	30E3	M_A_DQ<20>	M_A_DQ<20> - @pwb_lib.MBX147	10D7	19D5							
	LCD_BKLEN	LCD_BKLEN - @pwb_lib.MBX147	16E1	30E8		M_A_DQ<21>	M_A_DQ<21> - @pwb_lib.MBX147	10D7	19D5							
	LCD_PNLEN	LCD_PNLEN - @pwb_lib.MBX147	16E1	30C8		M_A_DQ<22>	M_A_DQ<22> - @pwb_lib.MBX147	10D7	19D5							
	LED_BATT1#	LED_BATT1# - @pwb_lib.MBX147	27D8	36D8		M_A_DQ<23>	M_A_DQ<23> - @pwb_lib.MBX147	10D7	19D5							
	LED_BRIGHTNESS	LED_BRIGHTNESS - @pwb_lib.MBX147	27E1	36B3		M_A_DQ<24>	M_A_DQ<24> - @pwb_lib.MBX147	10D7	19D7							
	LED_CAPS#	LED_CAPS# - @pwb_lib.MBX147	27D8	36E8		M_A_DQ<25>	M_A_DQ<25> - @pwb_lib.MBX147	10D7	19D7							
	LED_DOCK_IN_USE#	LED_DOCK_IN_USE# - @pwb_lib.MBX147	27B3	34C7		M_A_DQ<26>	M_A_DQ<26> - @pwb_lib.MBX147	10D7	19D7							
D	LED_GFX_EXT#	LED_GFX_EXT# - @pwb_lib.MBX147	27B3	36B8		M_A_DQ<27>	M_A_DQ<27> - @pwb_lib.MBX147	10D7	19D7							
	LED_GFX_INT#	LED_GFX_INT# - @pwb_lib.MBX147	27B3	36B8		M_A_DQ<28>	M_A_DQ<28> - @pwb_lib.MBX147	10D7	19D5							
	LED_MS	LED_MS - @pwb_lib.MBX147	24D1	28D4		M_A_DQ<29>	M_A_DQ<29> - @pwb_lib.MBX147	10C7	19D5							
	LED_NUM#	LED_NUM# - @pwb_lib.MBX147	27D8	36E8		M_A_DQ<30>	M_A_DQ<30> - @pwb_lib.MBX147	10C7	19D5							
	LED_PWR#	LED_PWR# - @pwb_lib.MBX147	27D8	36D8		M_A_DQ<31>	M_A_DQ<31> - @pwb_lib.MBX147	10C7	19D5							
	LED_SATA#	LED_SATA# - @pwb_lib.MBX147	22C6	36D8		M_A_DQ<32>	M_A_DQ<32> - @pwb_lib.MBX147	10C7	19C7							
	LED_SCRL#	LED_SCRL# - @pwb_lib.MBX147	27D8	36E8		M_A_DQ<33>	M_A_DQ<33> - @pwb_lib.MBX147	10C7	19C7							
	LED_STNDBY#	LED_STNDBY# - @pwb_lib.MBX147	27D8	36D8		M_A_DQ<34>	M_A_DQ<34> - @pwb_lib.MBX147	10C7	19C7							
	LED_WLAN#	LED_WLAN# - @pwb_lib.MBX147	33C3	36D8		M_A_DQ<35>	M_A_DQ<35> - @pwb_lib.MBX147	10C7	19C7							
	LINKALERT#	LINKALERT# - @pwb_lib.MBX147	21D1	22D6		M_A_DQ<36>	M_A_DQ<36> - @pwb_lib.MBX147	10C7	19C5							
	LPC_AD<0>	LPC_AD<0> - @pwb_lib.MBX147	22E5	27E8	35B4 49E6	M_A_DQ<37>	M_A_DQ<37> - @pwb_lib.MBX147	10C7	19C5							
	LPC_AD<1>	LPC_AD<1> - @pwb_lib.MBX147	22E5	27E8	35B4 49E6	M_A_DQ<38>	M_A_DQ<38> - @pwb_lib.MBX147	10C7	19C5							
	LPC_AD<2>	LPC_AD<2> - @pwb_lib.MBX147	22E5	27E8	35B4 49E6	M_A_DQ<39>	M_A_DQ<39> - @pwb_lib.MBX147	10C7	19C5							
	LPC_AD<3>	LPC_AD<3> - @pwb_lib.MBX147	22E5	27E8	35B4 49E6	M_A_DQ<40>	M_A_DQ<40> - @pwb_lib.MBX147	10C7	19C7							
C	LPC_DRQ0#	LPC_DRQ0# - @pwb_lib.MBX147	22E5	49E6		M_A_DQ<41>	M_A_DQ<41> - @pwb_lib.MBX147	10C7	19C7							
	LPC_FRAME#	LPC_FRAME# - @pwb_lib.MBX147	22E5	27E8	35B4 49E6	M_A_DQ<42>	M_A_DQ<42> - @pwb_lib.MBX147	10C7	19B7							
	LVDS_ACLK+	LVDS_ACLK+ - @pwb_lib.MBX147	14E1	30E5		M_A_DQ<43>	M_A_DQ<43> - @pwb_lib.MBX147	10C7	19B7							
	LVDS_ACLK-	LVDS_ACLK- - @pwb_lib.MBX147	14E1	30E5		M_A_DQ<44>	M_A_DQ<44> - @pwb_lib.MBX147	10C7	19C5							
	LVDS_ADATA0+	LVDS_ADATA0+ - @pwb_lib.MBX147	14F1	30D5		M_A_DQ<45>	M_A_DQ<45> - @pwb_lib.MBX147	10C7	19C5							
	LVDS_ADATA0-	LVDS_ADATA0- - @pwb_lib.MBX147	14F1	30D5		M_A_DQ<46>	M_A_DQ<46> - @pwb_lib.MBX147	10C7	19B5							
	LVDS_ADATA1+	LVDS_ADATA1+ - @pwb_lib.MBX147	14E1	30D5		M_A_DQ<47>	M_A_DQ<47> - @pwb_lib.MBX147	10C7	19B5							
	LVDS_ADATA1-	LVDS_ADATA1- - @pwb_lib.MBX147	14E1	30D5		M_A_DQ<48>	M_A_DQ<48> - @pwb_lib.MBX147	10C7	19B7							
	LVDS_ADATA2+	LVDS_ADATA2+ - @pwb_lib.MBX147	14E1	30D5		M_A_DQ<49>	M_A_DQ<49> - @pwb_lib.MBX147	10C7	19B7							
	LVDS_ADATA2-	LVDS_ADATA2- - @pwb_lib.MBX147	14E1	30D5		M_A_DQ<50>	M_A_DQ<50> - @pwb_lib.MBX147	10C7	19B7							
	MCH_ICH_SYNC#	MCH_ICH_SYNC# - @pwb_lib.MBX147	9D4	22D1		M_A_DQ<51>	M_A_DQ<51> - @pwb_lib.MBX147	10C7	19B7							
	MDL_ID<0>	MDL_ID<0> - @pwb_lib.MBX147	22E1	22E1		M_A_DQ<52>	M_A_DQ<52> - @pwb_lib.MBX147	10C7	19B5							
	MDL_ID<1>	MDL_ID<1> - @pwb_lib.MBX147	22E1	22E1		M_A_DQ<53>	M_A_DQ<53> - @pwb_lib.MBX147	10C7	19B5							
	MNCD_RF_OFF#	MNCD_RF_OFF# - @pwb_lib.MBX147	27A6	33D3		M_A_DQ<54>	M_A_DQ<54> - @pwb_lib.MBX147	10C7	19B5							
B	MODEL_ID_EC<0>	MODEL_ID_EC<0> - @pwb_lib.MBX147	27B3	27D1		M_A_DQ<55>	M_A_D									

[illegible]

8		7		6		5		4		3		2		1		
												REVISONS				
												ZONE		LTR		
												DESCRIPTION		DATE		
														APPR.		
F	PCI_LOCK#	PCI_LOCK# - @pwb_lib.MBX147	21C2 21E3	PM_EXTTS#	PM_EXTTS# - @pwb_lib.MBX147	9D5 19D1 19D5										
	PCI_PAR	PCI_PAR - @pwb_lib.MBX147	21E3 25D6	PM_GFX_MGMT0#	PM_GFX_MGMT0# - @pwb_lib.MBX147	16D3 27A3										
	PCI_PERR#	PCI_PERR# - @pwb_lib.MBX147	21C2 21E3 25D6	PM_PSI#	PM_PSI# - @pwb_lib.MBX147	6C2 37F8										
	PCI_PME#	PCI_PME# - @pwb_lib.MBX147	21E3 22B7	PM_RI#	PM_RI# - @pwb_lib.MBX147	21C1 22D1										
	PCI_REQ0#	PCI_REQ0# - @pwb_lib.MBX147	21C2 21E3 25D6	PM_RSMRST#	PM_RSMRST# - @pwb_lib.MBX147	22C6 27C8										
	PCI_REQ1#	PCI_REQ1# - @pwb_lib.MBX147	21B2 21E3	PM_SLP_S3#	PM_SLP_S3# - @pwb_lib.MBX147	22D1 27D8 40B6 49E6										
	PCI_REQ2#	PCI_REQ2# - @pwb_lib.MBX147	21B2 21E3	PM_SLP_S4#	PM_SLP_S4# - @pwb_lib.MBX147	22D1 27D8 49E6										
	PCI_REQ3#	PCI_REQ3# - @pwb_lib.MBX147	21B2 21E3	PM_SLP_S5#	PM_SLP_S5# - @pwb_lib.MBX147	22D1 27D8										
	PCI_REQ4#	PCI_REQ4# - @pwb_lib.MBX147	21B2 21E3	PM_STP_CPU#	PM_STP_CPU# - @pwb_lib.MBX147	8D6 22E1										
	PCI_REQ5#	PCI_REQ5# - @pwb_lib.MBX147	21C2 21D3	PM_STP_PCI#	PM_STP_PCI# - @pwb_lib.MBX147	8D6 22E1										
E	PCI_RST#	PCI_RST# - @pwb_lib.MBX147	21E3 25D6	PM_SUSSTAT#	PM_SUSSTAT# - @pwb_lib.MBX147	22D1 27D8 35B4 49E6										
	PCI_SERR#	PCI_SERR# - @pwb_lib.MBX147	21C2 21E3 25D6	PM_THRMTRIP#	PM_THRMTRIP# - @pwb_lib.MBX147	6B7 45B3										
	PCI_STOP#	PCI_STOP# - @pwb_lib.MBX147	21B2 21E3 25D6	PM_THRMTRP_F#	PM_THRMTRP_F# - @pwb_lib.MBX147	6B8 6C6 9D5										
	PCI_TRDY#	PCI_TRDY# - @pwb_lib.MBX147	21C2 21E3 25D6	PM_THRM_GFX#	PM_THRM_GFX# - @pwb_lib.MBX147	16E2 27D1										
	PEG_BUS_OFF#	PEG_BUS_OFF# - @pwb_lib.MBX147	14A2 14B1 14E1 16E1 27D1	PM_THRM_ICH#	PM_THRM_ICH# - @pwb_lib.MBX147	6E3 21B2 22C1 22D1										
	PEG_PWR_OFF	PEG_PWR_OFF - @pwb_lib.MBX147	17C3 27D1 38B7 43B8	POP_MUTE	POP_MUTE - @pwb_lib.MBX147	27D1 36D4										
	PEG_PWR_ON	PEG_PWR_ON - @pwb_lib.MBX147	14E8 16F3 38B5	PS_ERR#	PS_ERR# - @pwb_lib.MBX147	45D7 47B7 48C1										
	PEG_RST_EC#	PEG_RST_EC# - @pwb_lib.MBX147	14E8 27B3	PWRGDALW	PWRGDALW - @pwb_lib.MBX147	27C1 44E1										
	PEG_RX0+	PEG_RX0+ - @pwb_lib.MBX147	13D3 14B8	PWRGDBGFX	PWRGDBGFX - @pwb_lib.MBX147	27B3 44B5										
	PEG_RX0-	PEG_RX0- - @pwb_lib.MBX147	13E3 14B8	PWRGDSUS	PWRGDSUS - @pwb_lib.MBX147	22B8 22D1 25D6 27A6 32E8										
D	PEG_RX1+	PEG_RX1+ - @pwb_lib.MBX147	13D3 14B8	PWRGD_+1.2VSUS	PWRGD_+1.2VSUS - @pwb_lib.MBX147	44C1 49E3										
	PEG_RX1-	PEG_RX1- - @pwb_lib.MBX147	13E3 14B8	PWRGD_+1.05VSUS	PWRGD_+1.05VSUS - @pwb_lib.MBX147	41C7 44B4										
	PEG_RX2+	PEG_RX2+ - @pwb_lib.MBX147	13D3 14B8	PWRGD_+1.5VSUS	PWRGD_+1.5VSUS - @pwb_lib.MBX147	39C3 44B4										
	PEG_RX2-	PEG_RX2- - @pwb_lib.MBX147	13E3 14B8	PWRGD_+1.8VSUS	PWRGD_+1.8VSUS - @pwb_lib.MBX147	39B3 44D7										
	PEG_RX3+	PEG_RX3+ - @pwb_lib.MBX147	13D3 14B8	PWRGD_IC4400	PWRGD_IC4400 - @pwb_lib.MBX147	40D6 44B4										
	PEG_RX3-	PEG_RX3- - @pwb_lib.MBX147	13E3 14B8	PWRGD_IC5100	PWRGD_IC5100 - @pwb_lib.MBX147	42D5 44D4										
	PEG_RX4+	PEG_RX4+ - @pwb_lib.MBX147	13D3 14B8	PWRLIMIT#	PWRLIMIT# - @pwb_lib.MBX147	38C1 44B7										
	PEG_RX4-	PEG_RX4- - @pwb_lib.MBX147	13E3 14B8	PWRLMT_CHK	PWRLMT_CHK - @pwb_lib.MBX147	27C8 45A4										
	PEG_RX5+	PEG_RX5+ - @pwb_lib.MBX147	13D3 14C8	PWR_B00ST	PWR_B00ST - @pwb_lib.MBX147	45E2 49B3										
	PEG_RX5-	PEG_RX5- - @pwb_lib.MBX147	13E3 14B8	REF4100	REF4100 - @pwb_lib.MBX147	42D3 43F8										
C	PEG_RX6+	PEG_RX6+ - @pwb_lib.MBX147	13D3 14C8	REF_IC5100	REF_IC5100 - @pwb_lib.MBX147	39C3 39D6										
	PEG_RX6-	PEG_RX6- - @pwb_lib.MBX147	13E3 14B8	RTC_RST#	RTC_RST# - @pwb_lib.MBX147	38C4 38D7										
	PEG_RX7+	PEG_RX7+ - @pwb_lib.MBX147	13D3 14C8	SATA_RX0+	SATA_RX0+ - @pwb_lib.MBX147	22C6 48B5										
	PEG_RX7-	PEG_RX7- - @pwb_lib.MBX147	13E3 14B8	SATA_RX0-	SATA_RX0- - @pwb_lib.MBX147	22E6 29C5										
	PEG_RX8+	PEG_RX8+ - @pwb_lib.MBX147	13D3 14C8	SATA_TX0+	SATA_TX0+ - @pwb_lib.MBX147	22E6 29C5										
	PEG_RX8-	PEG_RX8- - @pwb_lib.MBX147	13E3 14B8	SATA_TX0-	SATA_TX0- - @pwb_lib.MBX147	22E6 29C5										
	PEG_RX9+	PEG_RX9+ - @pwb_lib.MBX147	13D3 14C8	SCIREQ	SCIREQ - @pwb_lib.MBX147	22E1 27E8										
	PEG_RX9-	PEG_RX9- - @pwb_lib.MBX147	13D3 14B8	SMBALERT#	SMBALERT# - @pwb_lib.MBX147	21C1 22E1										
	PEG_RX10+	PEG_RX10+ - @pwb_lib.MBX147	13D3 14C8	SMB_0CLK	SMB_0CLK - @pwb_lib.MBX147	27D1 46B4										
	PEG_RX10-	PEG_RX10- - @pwb_lib.MBX147	13D3 14B8	SMB_0DATA	SMB_0DATA - @pwb_lib.MBX147	27C1 46B4										
B	PEG_RX11+	PEG_RX11+ - @pwb_lib.MBX147	13D3 14C8	SMB_1CLK	SMB_1CLK - @pwb_lib.MBX147	6E6 16D2 27C1										
	PEG_RX11-	PEG_RX11- - @pwb_lib.MBX147	13D3 14B8	SMB_1DATA	SMB_1DATA - @pwb_lib.MBX147	6E6 16D2 27C1										
	PEG_RX12+	PEG_RX12+ - @pwb_lib.MBX147	13D3 14C8	SMB_2CLK	SMB_2CLK - @pwb_lib.MBX147	8C6 19B4 19B7 21A2 22A4										
	PEG_RX12-	PEG_RX12- - @pwb_lib.MBX147	13D3 14B8	SMB_2DATA	SMB_2DATA - @pwb_lib.MBX147	22D6										
	PEG_RX13+	PEG_RX13+ - @pwb_lib.MBX147	13D3 14C8	SMLINK0	SMLINK0 - @pwb_lib.MBX147	8C6 19B4 19B7 21A2 22A4										
	PEG_RX13-	PEG_RX13- - @pwb_lib.MBX147	13D3 14B8	SMLINK1	SMLINK1 - @pwb_lib.MBX147	22D6										
	PEG_RX14+	PEG_RX14+ - @pwb_lib.MBX147	13D3 14C8	SOURCE_COM	SOURCE_COM - @pwb_lib.MBX147	21D1 22D6										
	PEG_RX14-	PEG_RX14- - @pwb_lib.MBX147	13D3 14B8	SPKRCB0	SPKRCB0 - @pwb_lib.MBX147	21D1 22D6										
	PEG_RX15+	PEG_RX15+ - @pwb_lib.MBX147	13D3 14B8	SPKR_ICH	SPKR_ICH - @pwb_lib.MBX147	46D3 47C3 47F4										
	PEG_RX15-	PEG_RX15- - @pwb_lib.MBX147	13D3 14B8	SPKR_STBY	SPKR_STBY - @pwb_lib.MBX147	25D6 36E5										
A	PEG_TX0+	PEG_TX0+ - @pwb_lib.MBX147	13C3 14D7	SUSCLK	SUSCLK - @pwb_lib.MBX147	22A8 22C6 36E5										
	PEG_TX0-	PEG_TX0- - @pwb_lib.MBX147	13D3 14C7	SUS_ON	SUS_ON - @pwb_lib.MBX147	27D1 36D4										
	PEG_TX1+	PEG_TX1+ - @pwb_lib.MBX147	13C3 14D7	SUS_ON#	SUS_ON# - @pwb_lib.MBX147	22D1 27C8										
	PEG_TX1-	PEG_TX1- - @pwb_lib.MBX147	13D3 14C7	SUS_ON_EC	SUS_ON_EC - @pwb_lib.MBX147	29D5 34D7 35D8 39B5 40B6										
	PEG_TX2+	PEG_TX2+ - @pwb_lib.MBX147	13C3 14D7	SUS_ON_EC	SUS_ON_EC - @pwb_lib.MBX147	40D6 41C7 43E8 44D5										
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	PEG_TX3-	PEG_TX3- - @pwb_lib.MBX147	13C3 14C7	SW_LID#	SW_LID# - @pwb_lib.MBX147	39C5 44E5										
	PEG_TX4+	PEG_TX4+ - @pwb_lib.MBX147	13C3 14D7	SW_PWR#	SW_PWR# - @pwb_lib.MBX147	27D1 36D6										
	PEG_TX4-	PEG_TX4- - @pwb_lib.MBX147	13C3 14C7	SW_RADIO_DIS	SW_RADIO_DIS - @pwb_lib.MBX147	27D1 36D6										
	PEG_TX5+	PEG_TX5+ - @pwb_lib.MBX147	13C3 14D7	SW_SP0#	SW_SP0# - @pwb_lib.MBX147	27D1 36D6										
	PEG_TX5-	PEG_TX5- - @pwb_lib.MBX147	13C3 14D7	SW_SP1#	SW_SP1# - @pwb_lib.MBX147	27D1 36D6										
	PEG_TX6+	PEG_TX6+ - @pwb_lib.MBX147	13C3 14D7	SYS_ID<13>	SYS_ID<13> - @pwb_lib.MBX147	22C2 22E1										
	PEG_TX6-	PEG_TX6- - @pwb_lib.MBX147	13C3 14D7	SYS_PRS_VR	SYS_PRS_VR - @pwb_lib.MBX147	46B4 48C1										
	PEG_TX7+	PEG_TX7+ - @pwb_lib.MBX147	13C3 14D7	THERMDA	THERMDA - @pwb_lib.MBX147	6C6 6E4										
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	PEG_TX8+	PEG_TX8+ - @pwb_lib.MBX147	13C3 14C7	TPA0+	TPA0+ - @pwb_lib.MBX147	24D4 31F3										
	PEG_TX8-	PEG_TX8- - @pwb_lib.MBX147	13C3 14D7	TPA0-	TPA0- - @pwb_lib.MBX147	24D4 31F3										
	PEG_TX9+	PEG_TX9+ - @pwb_lib.MBX147	13C3 14C7	TPB0+	TPB0+ - @pwb_lib.MBX147	24D4 31E3										
	PEG_TX9-	PEG_TX9- - @pwb_lib.MBX147	13C3 14D7	TPB0-	TPB0- - @pwb_lib.MBX147	24D4 31E3										
	PEG_TX10+	PEG_TX10+ - @pwb_lib.MBX147	13C3 14D7	TPM_PRS#	TPM_PRS# - @pwb_lib.MBX147	21C2 22E1 35B4										
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	PEG_TX11-	PEG_TX11- - @pwb_lib.MBX147	13C3 14C7	UL_BKL	UL_BKL - @pwb_lib.MBX147	30B1 48E8										
	PEG_TX12+	PEG_TX12+ - @pwb_lib.MBX147	13B3 14D7	UL_CHK	UL_CHK - @pwb_lib.MBX147	48E7 49B3										
	PEG_TX12-	PEG_TX12- - @pwb_lib.MBX147	13C3 14C7	UL_IN#	UL_IN# - @pwb_lib.MBX147	48C4 48D5										
	PEG_TX13+	PEG_TX13+ - @pwb_lib.MBX147	13C3 14C7	UNDOCK_REQ#	UNDOCK_REQ# - @pwb_lib.MBX147	27D8 34C6										
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	PEG_TX14+	PEG_TX14+ - @pwb_lib.MBX147	13C3 14D7	USB_OC1#	USB_OC1# - @pwb_lib.MBX147	21C1 21E5										
	PEG_TX14-	PEG_TX14- - @pwb_lib.MBX147	13C3 14C7	USB_OC2#	USB_OC2# - @pwb_lib.MBX147	21C1 21E5 35D8										
	PEG_TX15+	PEG_TX15+ - @pwb_lib.MBX147	13B3 14D7	USB_OC3#	USB_OC3# - @pwb_lib.MBX147	21C1 21E5										
	PEG_TX15-	PEG_TX15- - @pwb_lib.MBX147	13C3 14D7	USB_OC4#	USB_OC4# - @pwb_lib.MBX147	21C1 21E5										
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			26C8 27E8 32E4 33D3 34E7	USB_OC6#	USB_OC6# - @pwb_lib.MBX147	21C1 21E5										
			35B4 45B3 49E6	USB_OC7#	USB_OC7# - @pwb_lib.MBX147	21C1 21E5										
			22A1 29C5	USB_P0+	USB_P0+ - @pwb_lib.MBX147	21E5 35D6										
			22B8 25D6	USB_P0-	USB_P0- - @pwb_lib.MBX147	21E5 35D6										
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			9D5 22E1 25D6 27D8 35B4	USB_P1-	USB_P1- - @pwb_lib.MBX147	21E5 34C6										
			49E6													
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PM_DPRSLPVR		PM_DPRSLPVR - @pwb_lib.MBX147														

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SONY CORPORATION NETWORK COMPANY																		-									
																		-									
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																		SHEET 60 OF 60				<TOTAL DESIGN SHEETS>					

8			7			6			5			4			3			2			1		
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C1630	C_1608	mbx147[	23C7	]	C3977	C_0603	mbx147[	36E2	]	C4000	C_1608	mbx147[	37E6	]									
C1631	C_1005	mbx147[	23C7	]	C4001	C_1005	mbx147[	37D8	]	C4002	C_1005	mbx147[	37E8	]									
C1632	C_1005	mbx147[	23C6	]	C4003	C_1005	mbx147[	37D7	]	C4004	C_1005	mbx147[	37C7	]									
C1633	C_1005	mbx147[	23C6	]	C4005	C_1608	mbx147[	37A6	]	C4006	C_1005	mbx147[	37D6	]									
C1640	C_1005	mbx147[	23E4	]	C4007	C_1005	mbx147[	37D6	]	C4008	C_1005	mbx147[	37D6	]									
C1641	C_1608	mbx147[	23E4	]	C4009	C_1005	mbx147[	37D6	]	C4050	C_1005	mbx147[	37D6	]									
C1642	CE_3528	mbx147[	23E4	]	C4051	C_1608	mbx147[	37E5	]	C4052	C_1608	mbx147[	37E6	]									
C1643	C_1005	mbx147[	23E4	]	C4060	C_3626	mbx147[	37E4	]	C4061	C_1608	mbx147[	37E4	]									
C1644	C_1005	mbx147[	23E4	]	C4070	C_1608	mbx147[	37D5	]	C4071	C_1005	mbx147[	37D5	]									
C1645	C_1608	mbx147[	23E4	]	C4072	C_1608	mbx147[	37C4	]	C4076	C_1005	mbx147[	37C5	]									
C1650	C_1005	mbx147[	23D4	]	C4080	C_3626	mbx147[	37C4	]	C4081	C_1608	mbx147[	37C4	]									
C1651	C_1005	mbx147[	23D4	]	C4090	CE_7343	mbx147[	37D2	]	C4091	CE_7343	mbx147[	37D1	]									
C1652	C_1005	mbx147[	23D4	]																			
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C1656	C_1005	mbx147[	23D4	]																			
C1660	C_1005	mbx147[	23C4	]																			
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C1662	C_1005	mbx147[	23C4	]																			
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C1664	C_1005	mbx147[	23C4	]																			
C1665	C_1005	mbx147[	23C4	]																			
C1666	C_1005	mbx147[	23C4	]																			
C1668	C_1005	mbx147[	23B4	]																			
C1670	C_1005	mbx147[	23B4	]																			
C1671	C_1005	mbx147[	23B4	]																			
C1800	C_1005	mbx147[	24C8	]																			
C1801	C_1005	mbx147[	24C7	]																			
C1805	C_1005	mbx147[	24D6	]																			
C1810	C_1608	mbx147[	24E8	]																			
C1811	C_1005	mbx147[	24E7	]																			
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C1813	C_1005	mbx147[	24E6	]																			
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C1821	C_1608	mbx147[	24D6	]																			
C1822	C_1005	mbx147[	24D5	]																			
C1823	C_1005	mbx147[	24D5	]																			
C1850	C_1005	mbx147[	24C2	]																			
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C1901	C_1005	mbx147[	25A6	]																			
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C1911	C_1005	mbx147[	25F7	]																			
C1912	C_1005	mbx147[	25F7	]																			
C1913	C_1005	mbx147[	25F6	]																			
C1914	C_1005	mbx147[	25F6	]																			
C1915	C_1005	mbx147[	25F6	]																			
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C1917	C_1005	mbx147[	25F6	]																			
C1918	C_1005	mbx147[	25F5	]																			
C1921	C_1005	mbx147[	25F4	]																			
C1922	C_1005	mbx147[	25F4	]																			
C1925	C_1005	mbx147[	25F3	]																			
C1926	C_1005	mbx147[	25F3	]																			

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		QTY 1	REV.
		PART NO. -	
		SHEET 61 OF 61 <TOTAL DESIGN SHEETS>	

8			7			6			5			4			3			2			1		
F	C4092	CE_7343	mbx147[	37D1	]				C4722	C_1005	mbx147[	45E5	]				REVISIONS						
	C4093	CE_7343	mbx147[	37D1	]				C4730	C_1005	mbx147[	45C6	]				ZONE	LTR	DESCRIPTION		DATE	APPR.	
	C4099	C_1005	mbx147[	37E7	]				C4731	C_1005	mbx147[	45B5	]										
	C4100	C_1608	mbx147[	39E6	]				C4740	C_1005	mbx147[	45B6	]										
	C4101	C_3626	mbx147[	39E5	]				C4760	C_1608	mbx147[	45B4	]										
	C4102	C_3626	mbx147[	39E3	]				C4800	C_1005	mbx147[	46D6	]										
	C4103	C_1608	mbx147[	39E3	]				C4801	C_1608	mbx147[	46D6	]										
	C4104	C_1608	mbx147[	39E4	]				C4802	C_1608	mbx147[	46D6	]										
	C4105	C_1608	mbx147[	39E4	]				C4810	C_1608	mbx147[	46D6	]										
	C4106	C_0603	mbx147[	39D4	]				C4820	C_1608	mbx147[	46C7	]										
E	C4107	C_1005	mbx147[	39D5	]				C4821	C_1005	mbx147[	46C7	]										
	C4108	C_1005	mbx147[	39D4	]				C4822	C_1005	mbx147[	46C7	]										
	C4120	C_1005	mbx147[	39D6	]				C4823	C_1005	mbx147[	46C6	]										
	C4121	C_1005	mbx147[	39D6	]				C4824	C_1608	mbx147[	46C6	]										
	C4122	C_1005	mbx147[	39C6	]				C4830	C_1608	mbx147[	46D5	]										
	C4123	C_0603	mbx147[	39C5	]				C4831	C_1608	mbx147[	46D5	]										
	C4124	C_0603	mbx147[	39C5	]				C4832	C_1005	mbx147[	46C5	]										
	C4130	C_0603	mbx147[	39D7	]				C4833	C_3216	mbx147[	46D4	]										
	C4131	CE_6032	mbx147[	39D7	]				C4834	C_3225	mbx147[	46C3	]										
	C4150	C_1005	mbx147[	39D2	]				C4835	C_1005	mbx147[	46C5	]										
D	C4151	C_1005	mbx147[	39D3	]				C4836	C_1608	mbx147[	46C5	]										
	C4160	C_0603	mbx147[	39D1	]				C4850	C_1608	mbx147[	46C2	]										
	C4161	CE_6032	mbx147[	39D2	]				C4860	C_0603	mbx147[	46B3	]										
	C4190	C_1005	mbx147[	39B7	]				C4900	C_1608	mbx147[	47E7	]										
	C4199	C_1005	mbx147[	39B3	]				C4901	C_1608	mbx147[	47E7	]										
	C4200	C_1005	mbx147[	40D5	]				C4910	C_3216	mbx147[	47E6	]										
	C4201	C_1608	mbx147[	40E5	]				C4911	C_1005	mbx147[	47E5	]										
	C4202	C_1608	mbx147[	40E6	]				C4912	C_1608	mbx147[	47D6	]										
	C4203	C_0603	mbx147[	40E6	]				C4960	C_1608	mbx147[	47C7	]										
	C4204	C_1005	mbx147[	40E5	]				C4961	C_1608	mbx147[	47A7	]										
C	C4205	C_1005	mbx147[	40D4	]				C5000	C_1005	mbx147[	48D6	]										
	C4206	C_0603	mbx147[	40E4	]				C5001	C_1005	mbx147[	48D6	]										
	C4207	C_2012	mbx147[	40E4	]				C5010	C_2012	mbx147[	48D3	]										
	C4208	C_1005	mbx147[	40D5	]				C5030	C_1608	mbx147[	48B5	]										
	C4209	CE_6032	mbx147[	40D3	]				C5031	C_1608	mbx147[	48B5	]										
	C4250	C_1608	mbx147[	40B5	]				C5050	C_1005	mbx147[	48C4	]										
	C4251	C_2012	mbx147[	40B5	]				C5051	C_1608	mbx147[	48B3	]										
	C4252	C_1005	mbx147[	40B6	]				C5052	C_1608	mbx147[	48B3	]										
	C4253	C_1005	mbx147[	40B5	]				C5100	C_1608	mbx147[	38E5	]										
	C4254	C_2012	mbx147[	40B4	]				C5101	C_1608	mbx147[	38E5	]										
B	C4255	C_2012	mbx147[	40B4	]				C5102	C_0603	mbx147[	38E4	]										
	C4270	C_0603	mbx147[	40D3	]				C5110	C_3626	mbx147[	38E6	]										
	C4300	C_1608	mbx147[	41E5	]				C5111	C_1608	mbx147[	38E6	]										
	C4301	C_0603	mbx147[	41E4	]				C5120	C_1005	mbx147[	38D7	]										
	C4302	C_2012	mbx147[	41E4	]				C5121	C_0603	mbx147[	38D6	]										
	C4303	C_0603	mbx147[	41E4	]				C5122	C_0603	mbx147[	38C5	]										
	C4350	CE_3528	mbx147[	41B3	]				C5123	C_0603	mbx147[	38C5	]										
	C4351	C_0603	mbx147[	41B3	]				C5124	C_1005	mbx147[	38C5	]										
	C4352	C_0603	mbx147[	41C5	]				C5130	CE_6032	mbx147[	38E8	]										
	C4353	C_2012	mbx147[	41C4	]				C5131	CE_6032	mbx147[	38D8	]										
A	C4354	C_1005	mbx147[	41C4	]				C5132	C_1608	mbx147[	38E8	]										
	C4355	C_1005	mbx147[	41B5	]				C5140	C_3216	mbx147[	38E4	]										
	C4356	C_1005	mbx147[	41B5	]				C5141	C_1608	mbx147[	38E3	]										
	C4357	C_1608	mbx147[	41C5	]				C5151	C_1005	mbx147[	38D3	]										
	C4358	C_1608	mbx147[	41C6	]				C5152	C_1608	mbx147[	38D4	]										
	C4359	C_0603	mbx147[	41C6	]				C5160	CE_6032	mbx147[	38E2	]										
	C4360	C_1005	mbx147[	41C5	]				C5161	C_1608	mbx147[	38E1	]										
	C4410	C_3216	mbx147[	42E4	]				C5162	C_1005	mbx147[	38E1	]										
	C4411	C_1608	mbx147[	42E3	]				C5163	C_1005	mbx147[	38E2	]										
	C4412	C_1005	mbx147[	42E4	]				C5233	C_0603	mbx147[	38E8	]										
A	C4415	C_1005	mbx147[	42D8	]				C5520	C_0603	mbx147[	49C7	]										
	C4416	C_0603	mbx147[	42D8	]				C5521	C_0603	mbx147[	49C6	]										
	C4419	C_1005	mbx147[	42D7	]				CN1	ICS-UFCPGA479P_SMD-D	mbx147[	6D3 6D7	]										
	C4420	C_3216	mbx147[	42C4	]				CN1	ICS-UFCPGA479P_SMD-D	mbx147[	7D7 7C4	]										
	C4421	C_1608	mbx147[	42C3	]				CN60	CN2P_SMD+2	mbx147[	6B5	]										
	C4422	C_1005	mbx147[	42C4	]				CN1000	DDR2_SO_DIMM_200P_SM	mbx147[	19C6	]										
	C4425	C_1005	mbx147[	42D8	]					D+2													
	C4426	C_0603	mbx147[	42D8	]				CN1020	DDR2_SO_DIMM_200P_SM	mbx147[	19C3	]										
	C4428	C_1005	mbx147[	42D7	]					D+2													
	C4429	C_1005	mbx147[	42D7	]				CN2000	CN28P_SMD+2	mbx147[	26D3	]										
A	C4431	CE_6032	mbx147[	42C1	]				CN3000	CN24P_SMD+2	mbx147[	28D7	]										
	C4433	C_0603	mbx147[	42C2	]				CN3050	CN24P_SMD+2	mbx147[	28D2	]										
	C4440	C_1608	mbx147[	42E7	]				CN3060	CN2P_SMD+2	mbx147[	28B2	]										
	C4443	C_2012	mbx147[	42D6	]				CN3100	CN20P_SMD	mbx147[	29C6	]										
	C4444	C_2012	mbx147[	42C5	]				CN3150	CN50P_SMD+2+B2	mbx147[	29C2	]										
	C4451	CE_7343	mbx147[	42E1	]				CN3200	CN44P_SMD+11+B2	mbx147[	30D2	]										
	C4453	C_0603	mbx147[	42E2	]				CN3201	CN20P_SMD	mbx147[	30B2	]										
	C4470	C_1005	mbx147[	42A7	]				CN3260	CN18P_SMD+2	mbx147[	31E2	]										
	C4471	C_1005	mbx147[	42A7	]				CN3300	CN80P_SMD	mbx147[	32C6	]										
	C4500	C_1005	mbx147[	43D7	]				CN3350	CN40P_SMD	mbx147[	32C3	]										
A	C4570	C_1005	mbx147[	43E7	]				CN3400	MINI_PCI_E_CN_SMD+5	mbx147[	33D4	]										
	C4590	C_1005	mbx147[	43B7	]				CN3600	CN80P_SMD	mbx147[	34D4	]										
	C4630	C_1608	mbx147[	44E2	]				CN3800	CN34P_SMD+2	mbx147[	35D5	]										
	C4650	C_1608	mbx147[	44C2	]				CN3830	CN24P_SMD+2	mbx147[	35D1	]										
	C4660	C_1608	mbx147[	44B6	]				CN3850	CN13P_SMD	mbx147[	35B1	]										
	C4702	C_1608	mbx147[	45D5	]				CN3900	CN50P_SMD+2	mbx147[	36D1	]										
	C4703	C_1608	mbx147[	45D5	]				CN4800	CN6P_TH	mbx147[	46B1	]										
	C4710	C_1608	mbx147[	45D7	]				CN4900														

8			7			6			5			4			3			2			1					
															REVISIONS											
															ZONE	LTR	DESCRIPTION		DATE	APPR.						
F	D60	SH0TTKY_SMD	mbx147[	6B6	]				FB881	FB_1005	mbx147[	17C4	]													
	D110	D_SMD	mbx147[	7B3	]				FB890	FB_1005	mbx147[	17C5	]													
	D3000	ZD7_TSS0P8	mbx147[	28C7	]				FB891	FB_1005	mbx147[	17B5	]													
	D3001	ZD7_TSS0P8	mbx147[	28C6	]				FB892	FB_1005	mbx147[	17B5	]													
	D3002	ZD4_SC88A	mbx147[	28C6	]				FB1610	FB_1608	mbx147[	23F7	]													
	D3003	ZD4_SC88A	mbx147[	28C6	]				FB1620	FB_1608	mbx147[	23D7	]													
	D3004	ZD4_SC88A	mbx147[	28C5	]				FB1800	FB_1005	mbx147[	24E7	]													
	D3260	SH0TTKY_SMD	mbx147[	31D3	]				FB1801	CONDUCTOR_1608	mbx147[	24E7	]													
	D3900	D_SMD	mbx147[	36C8	]				FB3050	CONDUCTOR_1005	mbx147[	28D3	]													
	D4000	D2_SSSM3AAK	mbx147[	37C8	]				FB3800	CONDUCTOR_1005	mbx147[	35E6	]													
E	D4050	SH0TTKY_SMD4525	mbx147[	37E3	]				FB3850	CONDUCTOR_1005	mbx147[	35B2	]													
	D4051	SH0TTKY_SMD	mbx147[	37E6	]				FB4830	FB_2012	mbx147[	46C2	]													
	D4070	SH0TTKY_SMD4525	mbx147[	37C3	]				FB4850	FB_4516	mbx147[	46C2	]													
	D4071	SH0TTKY_SMD	mbx147[	37D4	]				FB4860	CONDUCTOR_1005	mbx147[	46B2	]													
	D4091	D2_SSSM3AAK	mbx147[	37B2	]				FB4861	CONDUCTOR_1005	mbx147[	46B2	]													
	D4100	SH0TTKY_SMD	mbx147[	39E5	]				FB4862	CONDUCTOR_1005	mbx147[	46B2	]													
	D4101	SH0TTKY_SMD	mbx147[	39E4	]				FB4863	CONDUCTOR_1005	mbx147[	46B2	]													
	D4110	SH0TTKY_SMD	mbx147[	39D7	]				FB4900	FB_4516	mbx147[	47E7	]													
	D4140	SH0TTKY_SMD	mbx147[	39D2	]				FL651	RB4_2010	mbx147[	14D3	]													
	D4170	D2_SSSM3AAK	mbx147[	39B5	]				FL652	RB4_2010	mbx147[	14D3	]													
D	D4410	D2_SSSM3AAK	mbx147[	42D4	]				FL2000	LF2_SMD-D	mbx147[	26D6	]													
	D4411	SH0TTKY_SC79AK	mbx147[	42E4	]				FL3220	LF4_2010	mbx147[	30D4	]													
	D4421	SH0TTKY_SC79AK	mbx147[	42C4	]				FL3221	LF4_2010	mbx147[	30D4	]													
	D4471	D2_SSSM3AAK	mbx147[	42C7	]				FL3260	RB4_2010	mbx147[	31E3	]													
	D4472	D_SMD	mbx147[	42A6	]				FL3350	LF2_SMD-D	mbx147[	32C3	]													
	D4473	D_SMD	mbx147[	42B8	]				FL3351	LF2_SMD-D	mbx147[	32C3	]													
	D4550	SH0TTKY_SMD	mbx147[	43E2	]				FL3352	LF2_SMD-D	mbx147[	32C1	]													
	D4552	SH0TTKY_SMD	mbx147[	43D2	]				FL3400	LF2_SMD-D	mbx147[	33C7	]													
	D4553	SH0TTKY_SMD	mbx147[	43D2	]				FL3600	LF2_SMD-D	mbx147[	34D5	]													
	D4554	SH0TTKY_SMD	mbx147[	43C2	]				FL3830	LF2_SMD-D	mbx147[	35D2	]													
C	D4570	D_SMD	mbx147[	43F7	]				IC50	LM86_TSS0P8	mbx147[	6E5	]													
	D4571	ZD_SC76AK	mbx147[	43E7	]				IC200	ICS9LPR335_QFN73	mbx147[	8C5	]													
	D4631	SH0TTKY_SMD	mbx147[	44D2	]				IC300	GMCH_1466_CSP1466	mbx147[	9D7 9E3	]													
	D4650	SH0TTKY_SMD	mbx147[	44C2	]				IC300	GMCH_1466_CSP1466	mbx147[	10C3 10C6	]													
	D4660	SH0TTKY_SMD	mbx147[	44B6	]				IC300	GMCH_1466_CSP1466	mbx147[	11D3	]													
	D4710	D2_SSSM3AAK	mbx147[	45E7	]				IC300	GMCH_1466_CSP1466	mbx147[	12C7 12D1 12D3 12D4	]													
	D4712	ZD_S0D323AK	mbx147[	45D7	]				IC300	GMCH_1466_CSP1466	mbx147[	13D4	]													
	D4720	D2_SSSM3AAK	mbx147[	45C3	]				IC600	G7XM_CSP820	mbx147[	14C6	]													
	D4730	SH0TTKY_SMD	mbx147[	45C4	]				IC600	G7XM_CSP820	mbx147[	15D6 15D2	]													
	D4760	D_SMD	mbx147[	45B4	]				IC600	G7XM_CSP820	mbx147[	16D5	]													
B	D4810	D_SMD	mbx147[	46D6	]				IC600	G7XM_CSP820	mbx147[	17A5 17E5	]													
	D4811	ZD_SC90	mbx147[	46D7	]				IC611	SN74CBTLV3257_SS0P16	mbx147[	14C2	]													
	D4830	SH0TTKY_SC79AK	mbx147[	46D5	]				IC612	SN74CBTLV3257_SS0P16	mbx147[	14B3	]													
	D4850	ZD_SC90	mbx147[	46E2	]				IC620	7408_SC88A	mbx147[	14E7	]													
	D4851	ZD_SMD	mbx147[	46B2	]				IC631	PI3DBV40_TSS0P48	mbx147[	14E2	]													
	D4852	ZD_SMD	mbx147[	46B2	]				IC640	74125_TSS0P8	mbx147[	14A7	]													
	D4853	ZD_SMD	mbx147[	46B1	]				IC750	LM89_TSS0P8	mbx147[	16D2	]													
	D4860	ZD4_SC88A	mbx147[	46B2	]				IC751	7408_SC88A	mbx147[	16F3	]													
	D4900	ZD_SC76	mbx147[	47E6	]				IC760	SN74CBTLV3257_SS0P16	mbx147[	16E2	]													
	D4901	SH0TTKY_SMD4525	mbx147[	47E6	]				IC900	DDR_SDRAM256MX32_CSP	mbx147[	18D7	]													
A	D4910	ZD_SC90	mbx147[	47E5	]				144																	
	D4943	D2_SSSM3AAK	mbx147[	47E4	]				IC920	DDR_SDRAM256MX32_CSP	mbx147[	18D3	]													
	D4960	D2_SSSM3AAK	mbx147[	47B7	]				144																	
	D4961	ZD_SC90	mbx147[	47B7	]				IC1400	ICH7_M_CSP652	mbx147[	21E2 21D6	]													
	D5000	D2_SSSM3AAK	mbx147[	48E6	]				IC1400	ICH7_M_CSP652	mbx147[	22D7 22D3	]													
	D5001	ZD_S0D323AK	mbx147[	48D7	]				IC1400	ICH7_M_CSP652	mbx147[	23D5 23C2	]													
	D5010	D2_SSSM3AAK	mbx147[	48E5	]				IC1560	7402_SC88A	mbx147[	22B2	]													
	D5011	D2_SSSM3AAK	mbx147[	48E4	]				IC1561	7432_SC88A	mbx147[	22A2	]													
	D5012	ZD_SC80AK	mbx147[	48E3	]				IC1562	7414_SC88A	mbx147[	22B2	]													
	D5013	D2_SSSM3AAK	mbx147[	48E3	]				IC1580	M25P80_S0N9	mbx147[	22B5	]													
D5014	D_SMD	mbx147[	48E2	]				IC1590	NM24C02M8_TSS0P8	mbx147[	22A5	]														
D5020	D_SMD	mbx147[	48E7	]				IC1800	PCI8412_CSP216	mbx147[	24D7 24C3	]														
D5030	D2_SSSM3AAK	mbx147[	48B6	]				IC1800	PCI8412_CSP216	mbx147[	25C5	]														
D5031	D2_SSSM3AAK	mbx147[	48A6	]				IC1901	NM24C02M8_TSS0P8	mbx147[	25C7	]														
D5050	ZD_S0D323AK	mbx147[	48C3	]				IC2000	7408_SC88A	mbx147[	26C7	]														
D5052	SH0TTKY_SMD	mbx147[	48B3	]				IC2500	H8S2111_FQFP144	mbx147[	27D5 27A5	]														
D5100	SH0TTKY_SMD	mbx147[	38E5	]				IC3150	7400_SC88A	mbx147[	29D5	]														
D5101	SH0TTKY_SMD	mbx147[	38E4	]				IC3151	7408_SC88A	mbx147[	29C4	]														
D5110	SH0TTKY_SMD4525	mbx147[	38E7	]				IC3200	7408_SC88A	mbx147[	30E7	]														
F3050	FUSE_1608	mbx147[	28E3	]				IC3260	7408_SC88A	mbx147[	31E7	]														
F3810	FUSE_1608	mbx147[	35C6	]				IC3261	7408_SC88A	mbx147[	31D7	]														
F3811	FUSE_1608	mbx147[	35C6	]				IC3262	74157_TSS0P16A	mbx147[	31E5	]														
F3830	FUSE_1608	mbx147[	35E2	]				IC3263	SN74CBT3306_TSS0P8	mbx147[	31C4	]														
F3831	FUSE_1608	mbx147[	35E2	]				IC3281	7486_SC88A	mbx147[	31B3	]														
F3970	FUSE_1608	mbx147[	36F3	]				IC3300	TPS2220_SS0P24A	mbx147[	32E7	]														
F3972	FUSE_1608	mbx147[	36F2	]				IC3350	BD4153_SS0P24	mbx147[	32E3	]														
F3976	FUSE_1608	mbx147[	36F2	]				IC3400	7408_SC88A	mbx147[	33D7	]														
F4900	FUSE_SMD	mbx147[	47E6	]				IC3610	7408_SC88A	mbx147[	34D7	]														
FB200	FB_1005	mbx147[	8E5	]				IC3620	7408_SC88A	mbx147[	34E7	]														
FB202	FB_1005	mbx147[	8E5	]				IC3800	BD651X_S0P8	mbx147[	35D7	]														
FB400	FB_2012	mbx147[	11F6	]				IC3970	7486_SC88A	mbx147[	36E4	]														
FB410	FB_1005	mbx147[	11E7	]				IC4000	MAX8736_QFN41	mbx147[	37D7	]														
FB420	FB_2012	mbx147[	11F5	]				IC4070	MAX8552_S0N11	mbx147[	37C5	]														
FB433	FB_1005	mbx147[	11C5	]				IC4100	MAX1540_QFN33	mbx147[	39D4	]														
FB455	FB_1005	mbx147[	11C7	]				IC4200	MAX1515_QFN25	mbx147[	40D5	]														
FB456	FB_1005	mbx147[	11C7	]				IC4250	TPS51100_TSS0P11	mbx147[	40B5	]														
FB682	FB_1005	mbx147[	15B4	]				IC4300	MM156X_S0P7	mbx147[	41E5	]														
FB820	FB_1005	mbx147[	17F6	]				IC4350	MAX1515_QFN25	mbx147[	41C5	]														
FB860	FB_1005	mbx147[	17B7	]				IC4400	LTC3728_QFN33	mbx147[	42D6	]														
FB861	FB_1005	mbx147[	17B6	]				IC4470	RN5VD_S0T23	mbx147[	42A7	]														
FB870	FB_1005	mbx147[	17C6	]				IC4610	7414_TSS0P8	mbx147[	44E3 44C5 44D5	]														
FB880	FB_1005	mbx147[	17D6	]				IC4611	7408_SC88A	mbx147[	44D6	]														
															SONY CORPORATION				-							
															NETWORK COMPANY				-							
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															C		-		1		-					
															SHEET 63 OF <TOTAL DESIGN SHEETS>											

8		7		6		5		4		3		2		1		
												REVISIONS				
												ZONE	LTR	DESCRIPTION	DATE	APPR.
F	IC4630	7414_TSSOP8	mbx147[	44C1	44E2	44C3]										
	IC4660	7414_TSSOP8	mbx147[	44A6	44B6	44B5]										
	IC4700	MAX4211_QFN17	mbx147[	45C5]												
	IC4710	R3112_SC82	mbx147[	45B5]												
	IC4740	7414_SC88A	mbx147[	45B5]												
	IC4800	MAX1908_QFN29	mbx147[	46C6]												
	IC5000	R3112_SOT23-5	mbx147[	48D6]												
	IC5050	7414_TSSOP8	mbx147[	48B4	48B2	48B2]										
	IC5100	MAX1541_QFN41	mbx147[	38D5]												
	JR303	CONDUCTOR_1005	mbx147[	9D5]												
E	JR340	CONDUCTOR_1005	mbx147[	9E1]												
	JR401	FB_1005	mbx147[	11D4]												
	JR620	CONDUCTOR_0603	mbx147[	14F7]												
	JR621	CONDUCTOR_0603	mbx147[	14E7]												
	JR622	CONDUCTOR_0603	mbx147[	14E7]												
	JR640	CONDUCTOR_0603	mbx147[	14A7]												
	JR641	CONDUCTOR_0603	mbx147[	14A7]												
	JR705	CONDUCTOR_1005	mbx147[	16D4]												
	JR707	CONDUCTOR_1005	mbx147[	16D4]												
	JR1561	CONDUCTOR_1005	mbx147[	22D2]												
D	JR2001	CONDUCTOR_1005	mbx147[	26E4]												
	JR2002	CONDUCTOR_1005	mbx147[	26E4]												
	JR4070	CONDUCTOR_1005	mbx147[	37C6]												
	JR4071	CONDUCTOR_1005	mbx147[	37C6]												
	JR4170	CONDUCTOR_0603	mbx147[	39C4]												
	JR4255	CONDUCTOR_0603	mbx147[	40B4]												
	JR4610	CONDUCTOR_0603	mbx147[	44E6]												
	L60	LP_50H28	mbx147[	6B6]												
	L450	FB_1608	mbx147[	11D7]												
	L453	FB_1608	mbx147[	11D7]												
C	L4050	LP_10H40	mbx147[	37E2]												
	L4070	LP_10H40	mbx147[	37C2]												
	L4110	LP_10H43	mbx147[	39D6]												
	L4140	LP_10H45	mbx147[	39D2]												
	L4201	LP_60H35	mbx147[	40D4]												
	L4350	LP_60H35	mbx147[	41C4]												
	L4430	LP_10H45	mbx147[	42C2]												
	L4450	LP_10H43	mbx147[	42E2]												
	L4830	LP_60H35	mbx147[	46C3]												
	L5110	LP_10H40	mbx147[	38E7]												
B	L5140	LP_10H43	mbx147[	38E2]												
	Q41	TR-NPN_SC89BEC	mbx147[	6B7]												
	Q50	DTR-PNP-RBE_FSM3BEC	mbx147[	6F4]												
	Q51	M_FET_NCH_E_3P_VMT3G	mbx147[	6F3]												
		SD														
	Q60	M_FET_NCH_E_3P_SC59G	mbx147[	6A6]												
		SD														
	Q610	2M_FET_NCH_E_6P_SC88	mbx147[	14C4	14C4]											
		SGDSGD														
	Q880	M_FET_NCH_E_3P_SC70G	mbx147[	17D4]												
A		SD														
	Q881	M_FET_NCH_E_3P_VMT3G	mbx147[	17C4]												
		SD														
	Q1580	DTR2-NPN-RBE_SC88	mbx147[	22B7	22B7]											
	Q3100	DTR-NPN-PNP-RBE_SC88	mbx147[	29D8	29D8]											
	Q3150	M_FET_NCH_E_8P_SON8	mbx147[	29E3]												
	Q3151	2M_FET_NCH_E_6P_SC88	mbx147[	29D3	29D4]											
		SGDSGD														
	Q3210	M_FET_NCH_E_3P_VMT3G	mbx147[	30C7]												
		SD														
Q3211	2M_FET_NCH_E_6P_ES6	mbx147[	30C7	30C6]												
Q3212	M_FET_NCH_E_8P_SON8	mbx147[	30D6]													
Q3260	2M_FET_NCH_E_6P_SC88	mbx147[	31E7]													
	SGDSGD															
Q3261	2M_FET_NCH_E_6P_SC88	mbx147[	31D7]													
	SGDSGD															
Q3280	DTR-NPN-PNP-RBE_SC88	mbx147[	31B4]													
Q3610	DTR-PNP-RBE_FSM3BEC	mbx147[	34C7]													
Q3810	M_FET_NCH_E_8P_SON8	mbx147[	35C6]													
Q3811	2M_FET_NCH_E_6P_SC88	mbx147[	35B8	35B6]												
	SGDSGD															
Q3900	DTR2-PNP-RBE_SC88EBC	mbx147[	36E6	36E5]												
	EBC															
Q3901	DTR2-PNP-RBE_SC88EBC	mbx147[	36E6	36D6]												
	EBC															
Q3902	DTR2-PNP-RBE_SC88EBC	mbx147[	36D6	36D7]												
	EBC															
Q3903	DTR2-PNP-RBE_SC88EBC	mbx147[	36D7	36D7]												
	EBC															
Q3904	DTR-NPN-PNP-RBE_SC88	mbx147[	36C7	36B7]												
Q3905	DTR2-PNP-RBE_SC88EBC	mbx147[	36B7	36B7]												
	EBC															
Q3906	DTR-NPN-PNP-RBE_SC88	mbx147[	36A7	36A7]												
Q3950	M_FET_NCH_E_3P_VMT3G	mbx147[	36B2]													
	SD															
Q4000	2M_FET_NCH_E_6P_SC88	mbx147[	37A6	37B5]												
	SGDSGD															
Q4010	M_FET_NCH_E_3P_VMT3G	mbx147[	37B7]													
	SD															
Q4050	M_FET_NCH_E_5P_SON5	mbx147[	37E3]													
Q4051	M_FET_NCH_E_5P_SON5	mbx147[	37E3]													
Q4052	M_FET_NCH_E_5P_SON5	mbx147[	37E3]													

8			7			6			5			4			3			2			1		
F	R2530	R_0603	mbx147[	27B2	]				R4071	R_1005	mbx147[	37C5	]				REVISIONS						
	R2531	R_0603	mbx147[	27B2	]				R4072	R_1005	mbx147[	37D3	]				ZONE	LTR	DESCRIPTION	DATE	APPR.		
	R2550	R_0603	mbx147[	27B6	]				R4077	CONDUCTOR_1005	mbx147[	37C4	]										
	R2580	CONDUCTOR_1005	mbx147[	27C3	]				R4078	CONDUCTOR_1005	mbx147[	37C4	]										
	R2581	R_1005	mbx147[	27C4	]				R4079	CONDUCTOR_1005	mbx147[	37C3	]										
	R3051	R_0603	mbx147[	28E3	]				R4083	R_0603	mbx147[	37C5	]										
	R3052	R_0603	mbx147[	28D3	]				R4090	R_0603	mbx147[	37E1	]										
	R3060	R_0603	mbx147[	28B2	]				R4101	R_0603	mbx147[	39E4	]										
	R3061	R_0603	mbx147[	28B3	]				R4102	R_1005	mbx147[	39D4	]										
	R3150	R_0603	mbx147[	29E4	]				R4103	R_1005	mbx147[	39D5	]										
E	R3151	R_0603	mbx147[	29E3	]				R4120	R_1005	mbx147[	39D6	]										
	R3152	R_0603	mbx147[	29D3	]				R4122	R_1005	mbx147[	39D5	]										
	R3153	R_0603	mbx147[	29D2	]				R4123	R_1005	mbx147[	39C6	]										
	R3154	R_1005	mbx147[	29C4	]				R4124	R_1005	mbx147[	39C6	]										
	R3200	R_0603	mbx147[	30E6	]				R4125	R_1005	mbx147[	39C6	]										
	R3210	R_0603	mbx147[	30C7	]				R4126	R_1005	mbx147[	39C6	]										
	R3211	R_0603	mbx147[	30D7	]				R4127	R_1005	mbx147[	39C6	]										
	R3212	R_0603	mbx147[	30C6	]				R4128	R_1005	mbx147[	39C5	]										
	R3260	CONDUCTOR_1005	mbx147[	31E7	]				R4129	R_1005	mbx147[	39C5	]										
	R3261	CONDUCTOR_1005	mbx147[	31D7	]				R4131	R_0603	mbx147[	39C7	]										
D	R3265	R_0603	mbx147[	31D3	]				R4150	R_1005	mbx147[	39D3	]										
	R3266	R_0603	mbx147[	31D3	]				R4152	R_1005	mbx147[	39D4	]										
	R3280	R_0603	mbx147[	31C4	]				R4153	R_1005	mbx147[	39C3	]										
	R3281	R_0603	mbx147[	31C3	]				R4154	R_1005	mbx147[	39C3	]										
	R3282	R_0603	mbx147[	31B3	]				R4155	R_1005	mbx147[	39C3	]										
	R3350	R_0603	mbx147[	32E3	]				R4161	R_0603	mbx147[	39C2	]										
	R3351	R_0603	mbx147[	32E3	]				R4170	R_0603	mbx147[	39C5	]										
	R3361	R_1005	mbx147[	32E1	]				R4171	R_0603	mbx147[	39B5	]										
	R3362	R_0603	mbx147[	32E1	]				R4172	R_0603	mbx147[	39B5	]										
	R3370	R_1005	mbx147[	32C4	]				R4173	R_0603	mbx147[	39B4	]										
C	R3371	R_1005	mbx147[	32C4	]				R4190	R_1005	mbx147[	39B7	]										
	R3400	R_0603	mbx147[	33D7	]				R4191	R_1005	mbx147[	39B7	]										
	R3610	R_0603	mbx147[	34D7	]				R4192	R_0603	mbx147[	39B7	]										
	R3611	R_0603	mbx147[	34C7	]				R4199	R_0603	mbx147[	39B3	]										
	R3801	R_1005	mbx147[	35E7	]				R4201	R_0603	mbx147[	40E6	]										
	R3802	CONDUCTOR_1005	mbx147[	35E6	]				R4202	R_1005	mbx147[	40D5	]										
	R3803	CONDUCTOR_1005	mbx147[	35E6	]				R4203	R_1005	mbx147[	40D4	]										
	R3812	R_0603	mbx147[	35C7	]				R4205	R_1005	mbx147[	40D3	]										
	R3813	R_0603	mbx147[	35C6	]				R4206	R_1005	mbx147[	40D4	]										
	R3832	R_1005	mbx147[	35E3	]				R4207	R_1005	mbx147[	40D4	]										
B	R3850	R_1005	mbx147[	35C2	]				R4208	R_1005	mbx147[	40D4	]										
	R3901	R_0603	mbx147[	36C7	]				R4250	R_0603	mbx147[	40B4	]										
	R3902	R_0603	mbx147[	36B8	]				R4255	R_1005	mbx147[	41B5	]										
	R3910	R_0603	mbx147[	36D4	]				R4320	R_1005	mbx147[	41E4	]										
	R3911	R_0603	mbx147[	36D4	]				R4350	R_0603	mbx147[	41C6	]										
	R3912	R_0603	mbx147[	36D4	]				R4354	R_1005	mbx147[	41B6	]										
	R3913	R_0603	mbx147[	36D4	]				R4356	R_1005	mbx147[	41B4	]										
	R3914	R_0603	mbx147[	36D4	]				R4357	R_1005	mbx147[	41B4	]										
	R3915	R_0603	mbx147[	36D4	]				R4358	R_1005	mbx147[	41C4	]										
	R3916	R_0603	mbx147[	36D4	]				R4359	R_0603	mbx147[	41A3	]										
A	R3918	R_1005	mbx147[	36D4	]				R4370	R_1005	mbx147[	41B3	]										
	R3919	R_0603	mbx147[	36D4	]				R4411	R_1005	mbx147[	42E4	]										
	R3921	R_0603	mbx147[	36C4	]				R4412	R_1005	mbx147[	42E4	]										
	R3922	R_0603	mbx147[	36C4	]				R4413	R_1005	mbx147[	42D4	]										
	R3923	R_1005	mbx147[	36C4	]				R4414	R_1005	mbx147[	42E4	]										
	R3930	R_0603	mbx147[	36D4	]				R4418	R_0603	mbx147[	42D8	]										
	R3931	R_0603	mbx147[	36D4	]				R4421	R_1005	mbx147[	42C4	]										
	R3932	R_0603	mbx147[	36D4	]				R4422	R_1005	mbx147[	42B4	]										
	R3933	R_0603	mbx147[	36D4	]				R4423	R_1005	mbx147[	42B4	]										
	R3940	R_0603	mbx147[	36E3	]				R4424	R_1005	mbx147[	42C4	]										
R3941	R_0603	mbx147[	36E3	]				R4428	R_0603	mbx147[	42D8	]											
R3990	R_0603	mbx147[	36B4	]				R4430	R4P_SMD	mbx147[	42C2	]											
R4000	R_0603	mbx147[	37E6	]				R4431	R_0603	mbx147[	42C1	]											
R4001	R_1005	mbx147[	37D7	]				R4441	R_0603	mbx147[	42D7	]											
R4002	R_1005	mbx147[	37D7	]				R4442	R_0603	mbx147[	42D7	]											
R4003	R_1005	mbx147[	37D7	]				R4444	R_0603	mbx147[	42E7	]											
R4006	R_1005	mbx147[	37D7	]				R4450	R4P_SMD	mbx147[	42E2	]											
R4007	R_1005	mbx147[	37D8	]				R4451	R_0603	mbx147[	42E1	]											
R4008	R_1005	mbx147[	37D8	]				R4470	R_1005	mbx147[	42B8	]											
R4009	R_1005	mbx147[	37B7	]				R4471	R_1005	mbx147[	42B8	]											
R4010	R_1005	mbx147[	37D8	]				R4472	R_1005	mbx147[	42A8	]											
R4011	R_1005	mbx147[	37D8	]				R4500	R_0603	mbx147[	43D7	]											
R4012	R_1005	mbx147[	37D7	]				R4510	R_1005	mbx147[	43D6	]											
R4013	R_1005	mbx147[	37D7	]				R4520	R_1005	mbx147[	43D4	]											
R4014	R_0603	mbx147[	37D8	]				R4550	R_1005	mbx147[	43E2	]											
R4015	R_0603	mbx147[	37D8	]				R4552	R_1005	mbx147[	43D2	]											
R4018	R_1005	mbx147[	37C7	]				R4553	CONDUCTOR_1005	mbx147[	43D2	]											
R4019	R_0603	mbx147[	37C7	]				R4554	CONDUCTOR_1005	mbx147[	43C2	]											
R4020	R_0603	mbx147[	37A6	]				R4570	R_0603	mbx147[	43E7	]											
R4021	R_0603	mbx147[	37D6	]				R4571	R_0603	mbx147[	43E7	]											
R4022	R_0603	mbx147[	37D5	]				R4572	R_0603	mbx147[	43E7	]											
R4030	R_0603	mbx147[	37B7	]				R4590	R_0603	mbx147[	43B7	]											
R4031	CONDUCTOR_1005	mbx147[	37C7	]				R4591	R_1005	mbx147[	43B6	]											
R4032	R_1005	mbx147[	37C7	]				R4592	R_1005	mbx147[	43B5	]											
R4050	R_5025	mbx147[	37E2	]				R4593	R_1005	mbx147[	43B5	]											
R4051	R_1005	mbx147[	37E6	]				R4594	R_1005	mbx147[	43B4	]											
R4052	R_1005	mbx147[	37E3	]				R4610	R_0603	mbx147[	44E7	]											
R4053	R_0603	mbx147[	37F7	]				R4630	R_0603	mbx147[	44E3	]											
R4059	CONDUCTOR_1005	mbx147[	37E4	]				R4631	R_0603	mbx147[	44E2	]											

8			7			6			5			4			3			2			1		
F	R4651	R_0603	mbx147[	44C2	]				R5001	R_0603	mbx147[	48E7	]				REVISIONS						
	R4652	R_0603	mbx147[	44C2	]				R5002	R_0603	mbx147[	48D6	]				ZONE	LTR	DESCRIPTION	DATE	APPR.		
	R4660	R_0603	mbx147[	44B7	]				R5003	R_0603	mbx147[	48D6	]										
	R4661	R_0603	mbx147[	44B6	]				R5010	R_0603	mbx147[	48E4	]										
	R4662	R_0603	mbx147[	44B6	]				R5011	R_0603	mbx147[	48D4	]										
	R4701	R_1005	mbx147[	45E6	]				R5012	R_0603	mbx147[	48E4	]										
	R4702	R_1005	mbx147[	45E6	]				R5013	R_0603	mbx147[	48D4	]										
	R4703	CONDUCTOR_1005	mbx147[	45E6	]				R5014	R_0603	mbx147[	48E3	]										
	R4710	R_0603	mbx147[	45E7	]				R5015	R_1005	mbx147[	48D3	]										
	R4712	R_0603	mbx147[	45D7	]				R5016	R_0603	mbx147[	48E3	]										
E	R4713	R_0603	mbx147[	45D6	]				R5017	R_1005	mbx147[	48D3	]										
	R4714	R_0603	mbx147[	45D6	]				R5020	R_0603	mbx147[	48E1	]										
	R4715	R_0603	mbx147[	45C6	]				R5021	R_0603	mbx147[	48D1	]										
	R4722	R_1005	mbx147[	45C3	]				R5030	R_0603	mbx147[	48B7	]										
	R4723	R_1005	mbx147[	45C3	]				R5031	R_0603	mbx147[	48B6	]										
	R4724	R_1005	mbx147[	45C5	]				R5032	R_0603	mbx147[	48B6	]										
	R4725	R_0603	mbx147[	45D4	]				R5035	R_0603	mbx147[	48B6	]										
	R4726	R_1005	mbx147[	45D4	]				R5036	R_0603	mbx147[	48B5	]										
	R4727	R_1005	mbx147[	45D4	]				R5050	R_0603	mbx147[	48C3	]										
	R4728	R_1005	mbx147[	45C4	]				R5051	R_0603	mbx147[	48B3	]										
D	R4729	R_1005	mbx147[	45D5	]				R5052	R_0603	mbx147[	48B3	]										
	R4730	R_1005	mbx147[	45C5	]				R5083	R_1005	mbx147[	48B6	]										
	R4731	R_1005	mbx147[	45E5	]				R5084	R_0603	mbx147[	48A7	]										
	R4732	R_1005	mbx147[	45E5	]				R5100	R_0603	mbx147[	38E5	]										
	R4733	R_1005	mbx147[	45C5	]				R5101	CONDUCTOR_1005	mbx147[	38E5	]										
	R4734	R_1005	mbx147[	45C5	]				R5102	R_1005	mbx147[	38E4	]										
	R4735	R_1005	mbx147[	45C5	]				R5110	R_5025	mbx147[	38E7	]										
	R4740	R_0603	mbx147[	45B6	]				R5120	R_0603	mbx147[	38D8	]										
	R4741	R_0603	mbx147[	45B6	]				R5121	R_1005	mbx147[	38D6	]										
	R4750	R_0603	mbx147[	45B2	]				R5122	R_1005	mbx147[	38D6	]										
C	R4751	R_0603	mbx147[	45B2	]				R5123	R_1005	mbx147[	38D6	]										
	R4755	R_1005	mbx147[	45E3	]				R5124	R_1005	mbx147[	38C6	]										
	R4756	R_1005	mbx147[	45D3	]				R5125	R_1005	mbx147[	38C6	]										
	R4757	R_1005	mbx147[	45D4	]				R5126	R_1005	mbx147[	38D6	]										
	R4760	R_0603	mbx147[	45C4	]				R5127	R_1005	mbx147[	38C6	]										
	R4761	R_0603	mbx147[	45B4	]				R5128	R_1005	mbx147[	38D6	]										
	R4800	R4P_SMD	mbx147[	46E6	]				R5129	R_1005	mbx147[	38C6	]										
	R4801	R_0603	mbx147[	46F5	]				R5151	R_1005	mbx147[	38D3	]										
	R4802	R_0603	mbx147[	46F5	]				R5152	R_1005	mbx147[	38D3	]										
	R4803	CONDUCTOR_1005	mbx147[	46E6	]				R5153	R_1005	mbx147[	38C3	]										
B	R4804	R_1005	mbx147[	46E6	]				R5160	R_1005	mbx147[	38E2	]										
	R4805	R_1005	mbx147[	46E6	]				R5161	R_1005	mbx147[	38D3	]										
	R4806	R_1005	mbx147[	46E6	]				R5170	R_0603	mbx147[	38C6	]										
	R4807	R_0603	mbx147[	46E6	]				R5174	R_0603	mbx147[	38B5	]										
	R4808	R_0603	mbx147[	46D6	]				R5177	R_0603	mbx147[	38C6	]										
	R4814	R_1005	mbx147[	46D7	]				R5180	R_0603	mbx147[	38C4	]										
	R4815	R_1005	mbx147[	46D7	]				R5181	R_0603	mbx147[	38B4	]										
	R4816	R_1005	mbx147[	46D7	]				R5182	R_0603	mbx147[	38B4	]										
	R4817	R_1005	mbx147[	46C7	]				R5183	R_0603	mbx147[	38C4	]										
	R4818	R_1005	mbx147[	46C7	]				R5184	R_0603	mbx147[	38B4	]										
A	R4819	R_1005	mbx147[	46D6	]				R5190	R_0603	mbx147[	38F5	]										
	R4820	R_0603	mbx147[	46C7	]				R5191	R_0603	mbx147[	38F5	]										
	R4821	R_0603	mbx147[	46B8	]				R5196	R_0603	mbx147[	38D1	]										
	R4822	R_0603	mbx147[	46C7	]				R5520	R_0603	mbx147[	49C7	]										
	R4823	R_1005	mbx147[	46B6	]				R5521	R_1005	mbx147[	49C7	]										
	R4824	R_1005	mbx147[	46B6	]				R5522	R_1005	mbx147[	49B7	]										
	R4825	R_1005	mbx147[	46B6	]				R5523	R_1005	mbx147[	49B7	]										
	R4826	R_1005	mbx147[	46B6	]				R5524	R_0603	mbx147[	49B6	]										
	R4830	R_1608	mbx147[	46C3	]				R5525	R_0603	mbx147[	49B5	]										
	R4831	R_0603	mbx147[	46D5	]				R5527	R_1005	mbx147[	49B5	]										
A	R4832	R_0603	mbx147[	46D5	]				R5528	R_1005	mbx147[	49B5	]										
	R4833	R_1005	mbx147[	46D5	]				RB200	RB4_2010	mbx147[	8D3	]										
	R4834	R_0603	mbx147[	46D4	]				RB202	RB4_2010	mbx147[	8D3	]										
	R4835	CONDUCTOR_1005	mbx147[	46C5	]				RB203	RB4_2010	mbx147[	8D3	]										
	R4836	CONDUCTOR_1005	mbx147[	46C5	]				RB204	RB4_2010	mbx147[	8D3	]										
	R4850	R_0603	mbx147[	46E2	]				RB205	RB4_2010	mbx147[	8D3	]										
	R4851	R_0603	mbx147[	46D3	]				RB207	RB4_2010	mbx147[	8C3	]										
	R4852	R_0603	mbx147[	46D2	]				RB208	RB4_2010	mbx147[	8C3	]										
	R4860	R_0603	mbx147[	46B3	]				RB601	RB4_2010	mbx147[	14D4	]										
	R4861	R_0603	mbx147[	46B3	]				RB602	RB4_2010	mbx147[	14D4	]										
A	R4862	R_0603	mbx147[	46B3	]				RB630	RB4_2010	mbx147[	14F3	]										
	R4863	R_0603	mbx147[	46B3	]				RB632	RB4_2010	mbx147[	14E3	]										
	R4900	R_0603	mbx147[	47F6	]				RB640	RB4_2010	mbx147[	14C3	]										
	R4910	R_1608	mbx147[	47E6	]				RB650	RB4_2010	mbx147[	14B3	]										
	R4911	R_0603	mbx147[	47E5	]				RB700	RB4_2010	mbx147[	16F2	]										
	R4912	R_0603	mbx147[	47E5	]				RB760	RB4_2010	mbx147[	16B3	]										
	R4913	R_0603	mbx147[	47E4	]				RB1050	RB4_2010	mbx147[	20D6	]										
	R4914	R_0603	mbx147[	47E4	]				RB1051	RB4_2010	mbx147[	20D6	]										
	R4915	R_0603	mbx147[	47D6	]				RB1052	RB4_2010	mbx147[	20D6	]										
	R4916	R_0603	mbx147[	47D5	]				RB1053	RB4_2010	mbx147[	20D6	]										
A	R4917	R_0603	mbx147[	47D5	]				RB1054	RB4_2010	mbx147[	20C6	]										
	R4918	R_0603	mbx147[	47D4	]				RB1055	RB4_2010	mbx147[	20C6	]										
	R4919	R_0603	mbx147[	47C5	]				RB1056	RB4_2010	mbx147[	20C6	]										
	R4960	R_1005	mbx147[	47C7	]				RB1070	RB4_2010	mbx147[	20D3	]										
	R4961	R_1005	mbx147[	47C7	]				RB1071	RB4_2010	mbx147[	20D3	]										
	R4962	R_1005	mbx147[	47B7	]				RB1072	RB4_2010	mbx147[	20D3	]										
	R4963	R_0603	mbx147[	47A7	]				RB1073	RB4_2010	mbx147[	20D3	]										
	R4964	R_0603	mbx147[	47A7	]				RB1074	RB4_2010	mbx147[	20C3	]										
	R4965	R_0603	mbx147[	47A7	]				RB1075	RB4_2010	mbx147[	20C3	]										
	R4970	R_0603																					

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F	RB1452	RB4_2010	mbx147[	21C3	]				TP382	TESTPAD_-	mbx147[	10B6	]				REVISIONS						
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	RB1454	RB4_2010	mbx147[	21B3	]				TP391	TESTPAD_-	mbx147[	10C5	]										
	RB1455	RB4_2010	mbx147[	21B3	]				TP392	TESTPAD_-	mbx147[	10C5	]										
	RB1456	RB4_2010	mbx147[	21B3	]				TP393	TESTPAD_-	mbx147[	10C2	]										
	RB1457	RB4_2010	mbx147[	21B3	]				TP394	TESTPAD_-	mbx147[	10C2	]										
	RB1460	RB4_2010	mbx147[	21D2	]				TP503	TESTPAD_-	mbx147[	13E5	]										
	RB1461	RB4_2010	mbx147[	21C2	]				TP600	TESTPAD_-	mbx147[	14D5	]										
	RB1462	RB4_2010	mbx147[	21C2	]				TP601	TESTPAD_-	mbx147[	14D5	]										
	RB1520	RB4_2010	mbx147[	22E4	]				TP610	TESTPAD_-	mbx147[	14C5	]										
E	RB1521	RB4_2010	mbx147[	22E4	]				TP630	TESTPAD_-	mbx147[	14D7	]										
	RB1540	RB4_2010	mbx147[	22E4	]				TP631	TESTPAD_-	mbx147[	14D7	]										
	RB1544	RB4_2010	mbx147[	22D4	]				TP660	TESTPAD_-	mbx147[	15C5	]										
	RB1550	RB4_2010	mbx147[	22E1	]				TP662	TESTPAD_-	mbx147[	15C5	]										
	RB2510	RB4_2010	mbx147[	27E6	]				TP663	TESTPAD_-	mbx147[	15B5	]										
	RB2511	RB4_2010	mbx147[	27E4	]				TP664	TESTPAD_-	mbx147[	15B5	]										
	RB2512	RB4_2010	mbx147[	27E3	]				TP665	TESTPAD_-	mbx147[	15B5	]										
	RB2513	RB4_2010	mbx147[	27E3	]				TP666	TESTPAD_-	mbx147[	15B5	]										
	RB2521	RB4_2010	mbx147[	27E6	]				TP670	TESTPAD_-	mbx147[	15E8	]										
	RB2530	RB4_2010	mbx147[	27B2	]				TP671	TESTPAD_-	mbx147[	15D8	]										
D	RB2540	RB4_2010	mbx147[	27C5	]				TP672	TESTPAD_-	mbx147[	15D8	]										
	RB2550	RB4_2010	mbx147[	27B4	]				TP673	TESTPAD_-	mbx147[	15C8	]										
	RB3150	RB4_2010	mbx147[	29D2	]				TP674	TESTPAD_-	mbx147[	15C8	]										
	RB3630	RB4_2010	mbx147[	34D3	]				TP675	TESTPAD_-	mbx147[	15B8	]										
	RB3930	RB4_2010	mbx147[	36E3	]				TP676	TESTPAD_-	mbx147[	15B8	]										
	SL1	SHORTLAND_SMD	mbx147[	6D6	]				TP677	TESTPAD_-	mbx147[	15B8	]										
	SL50	SHORTLAND_025MM	mbx147[	6F4	]				TP680	TESTPAD_-	mbx147[	15C1	]										
	SL51	SHORTLAND_025MM	mbx147[	6E4	]				TP722	TESTPAD_-	mbx147[	16D6	]										
	SL81	SHORTLAND_SMD	mbx147[	6C4	]				TP723	TESTPAD_-	mbx147[	16D6	]										
	SL82	SHORTLAND_SMD	mbx147[	6C4	]				TP724	TESTPAD_-	mbx147[	16D6	]										
C	SL83	SHORTLAND_SMD	mbx147[	6C4	]				TP725	TESTPAD_-	mbx147[	16C6	]										
	SL202	SHORTLAND_025MM	mbx147[	8C3	]				TP730	TESTPAD_-	mbx147[	16E4	]										
	SL300	SHORTLAND_SMD	mbx147[	9D4	]				TP732	TESTPAD_-	mbx147[	16D4	]										
	SL302	SHORTLAND_025MM	mbx147[	9D5	]				TP734	TESTPAD_-	mbx147[	16D4	]										
	SL1508	SHORTLAND_SMD	mbx147[	22C1	]				TP736	TESTPAD_-	mbx147[	16D4	]										
	SL1510	SHORTLAND_SMD	mbx147[	22B5	]				TP737	TESTPAD_-	mbx147[	16D4	]										
	SL1560	SHORTLAND_025MM	mbx147[	22D2	]				TP738	TESTPAD_-	mbx147[	16D4	]										
	SL1580	SHORTLAND_SMD	mbx147[	22B8	]				TP744	TESTPAD_-	mbx147[	16E6	]										
	SL1581	SHORTLAND_SMD	mbx147[	22B8	]				TP748	TESTPAD_-	mbx147[	16D6	]										
	SL1900	SHORTLAND_SMD	mbx147[	25D5	]				TP749	TESTPAD_-	mbx147[	16C6	]										
B	SL2520	SHORTLAND_SMD	mbx147[	27C5	]				TP750	TESTPAD_-	mbx147[	16D6	]										
	SL4000	SHORTLAND_SMD	mbx147[	37F8	]				TP900	TESTPAD_-	mbx147[	18D5	]										
	SL4001	SHORTLAND_SMD	mbx147[	37E8	]				TP901	TESTPAD_-	mbx147[	18D5	]										
	SL4002	SHORTLAND_SMD	mbx147[	37E8	]				TP902	TESTPAD_-	mbx147[	18D5	]										
	SL4003	SHORTLAND_SMD	mbx147[	37E8	]				TP903	TESTPAD_-	mbx147[	18D5	]										
	SL4004	SHORTLAND_SMD	mbx147[	37E8	]				TP904	TESTPAD_-	mbx147[	18D6	]										
	SL4005	SHORTLAND_SMD	mbx147[	37E8	]				TP905	TESTPAD_-	mbx147[	18D5	]										
	SL4006	SHORTLAND_SMD	mbx147[	37E8	]				TP906	TESTPAD_-	mbx147[	18D5	]										
	SL4016	SHORTLAND_SMD	mbx147[	37B6	]				TP907	TESTPAD_-	mbx147[	18C5	]										
	SL4120	SHORTLAND_01MM	mbx147[	39D7	]				TP930	TESTPAD_-	mbx147[	18D8	]										
A	SL4129	SHORTLAND_01MM	mbx147[	39D5	]				TP931	TESTPAD_-	mbx147[	18D8	]										
	SL4150	SHORTLAND_01MM	mbx147[	39D2	]				TP932	TESTPAD_-	mbx147[	18D6	]										
	SL4151	SHORTLAND_SMD	mbx147[	39C4	]				TP933	TESTPAD_-	mbx147[	18D6	]										
	SL4152	SHORTLAND_SMD	mbx147[	39C4	]				TP950	TESTPAD_-	mbx147[	18D2	]										
	SL4159	SHORTLAND_01MM	mbx147[	39D4	]				TP951	TESTPAD_-	mbx147[	18D1	]										
	SL4200	SHORTLAND_SMD	mbx147[	40E6	]				TP952	TESTPAD_-	mbx147[	18D1	]										
	SL4350	SHORTLAND_SMD	mbx147[	41C6	]				TP953	TESTPAD_-	mbx147[	18D1	]										
	SL4610	SHORTLAND_SMD	mbx147[	44E7	]				TP954	TESTPAD_-	mbx147[	18D2	]										
	SL4611	SHORTLAND_SMD	mbx147[	44D7	]				TP955	TESTPAD_-	mbx147[	18D2	]										
	SL4620	SHORTLAND_SMD	mbx147[	44D7	]				TP956	TESTPAD_-	mbx147[	18D1	]										
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												NETWORK COMPANY			-								
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												C		-		QTY 1		-					
														-		68		SHEET 68 OF <TOTAL DESIGN SHEETS>					