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P. Leader	Appr. by	Check by	Design by
			Alex

Project Code & Schematics Subject:	**MS03 M/B-FUBAI**	**PCB P/N:**	1P-0055100-80SB
Project Code & Schematics Subject:	**MS03 M/B-HANNSTAR**	**PCB P/N:**	1P-0055500-80SB
Project Code & Schematics Subject:	**MS03 M/B-NAN YA**	**PCB P/N:**	1P-0055200-80SB

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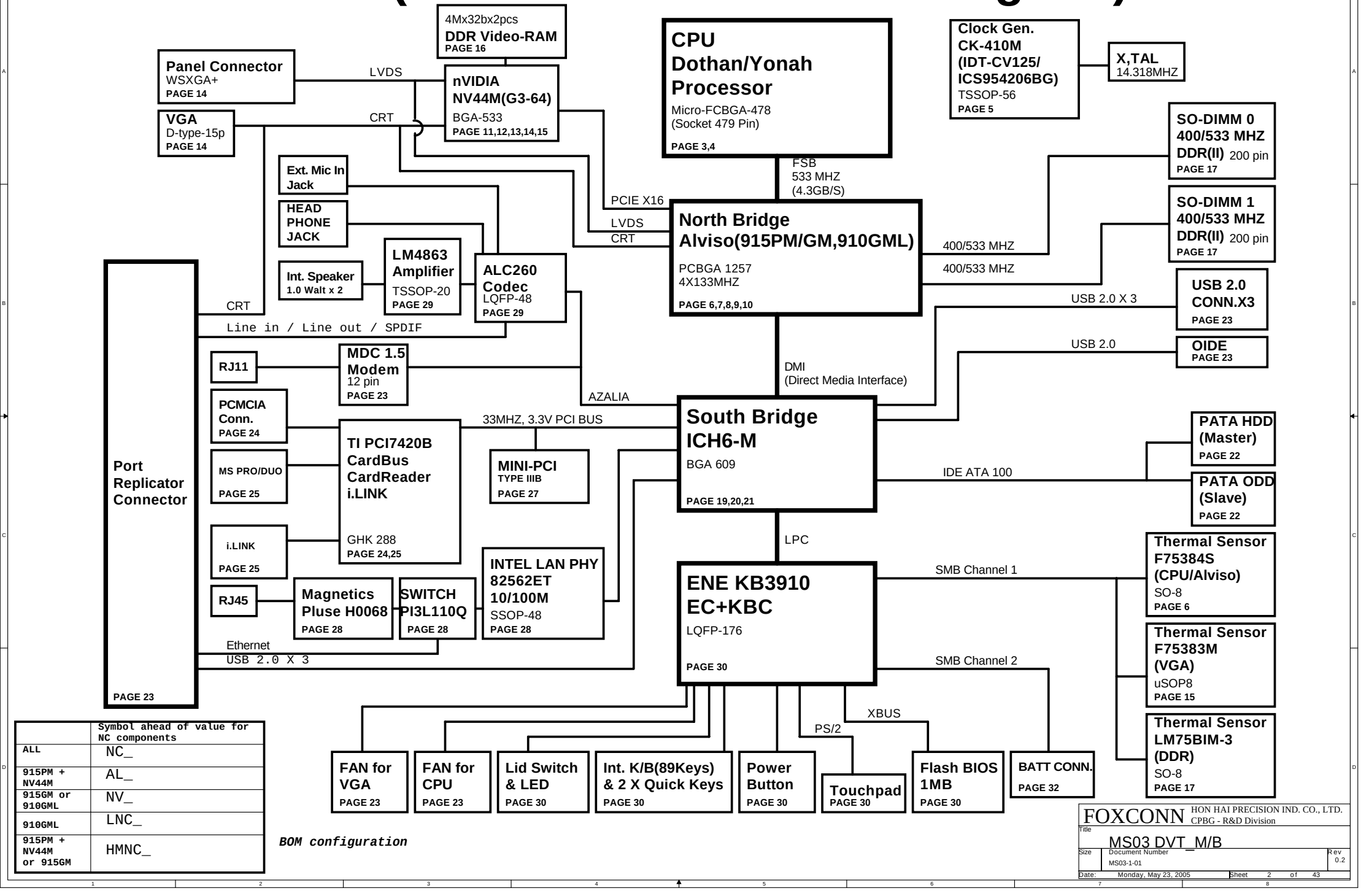
MS03(915PM/GM+Gfx Block Diagram)



	Symbol ahead of value for NC components
ALL	NC_
915PM + NV44M	AL_
915GM or 910GML	NV_
910GML	LNC_
915PM + NV44M or 915GM	HMNC_

BOM configuration

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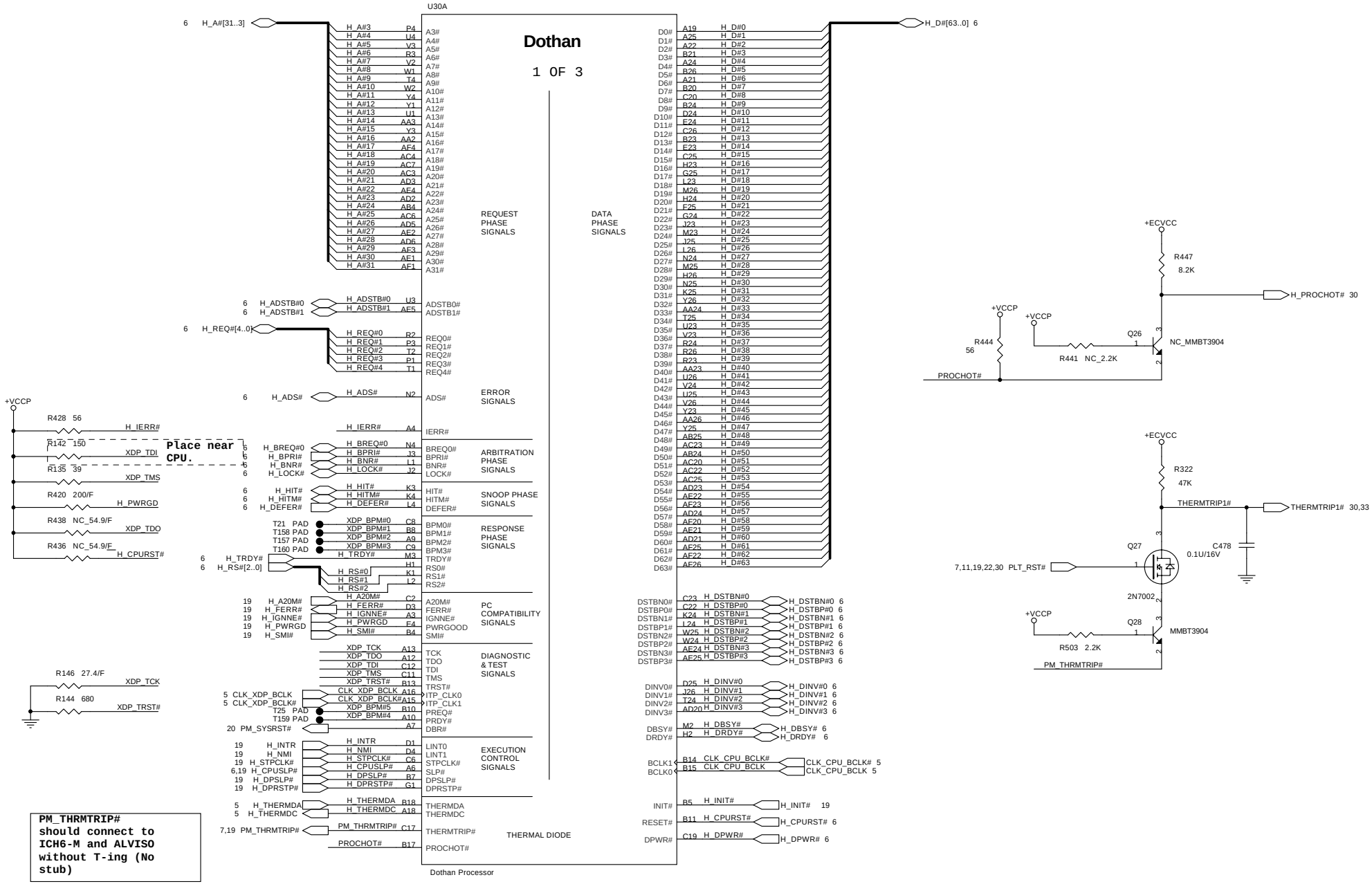

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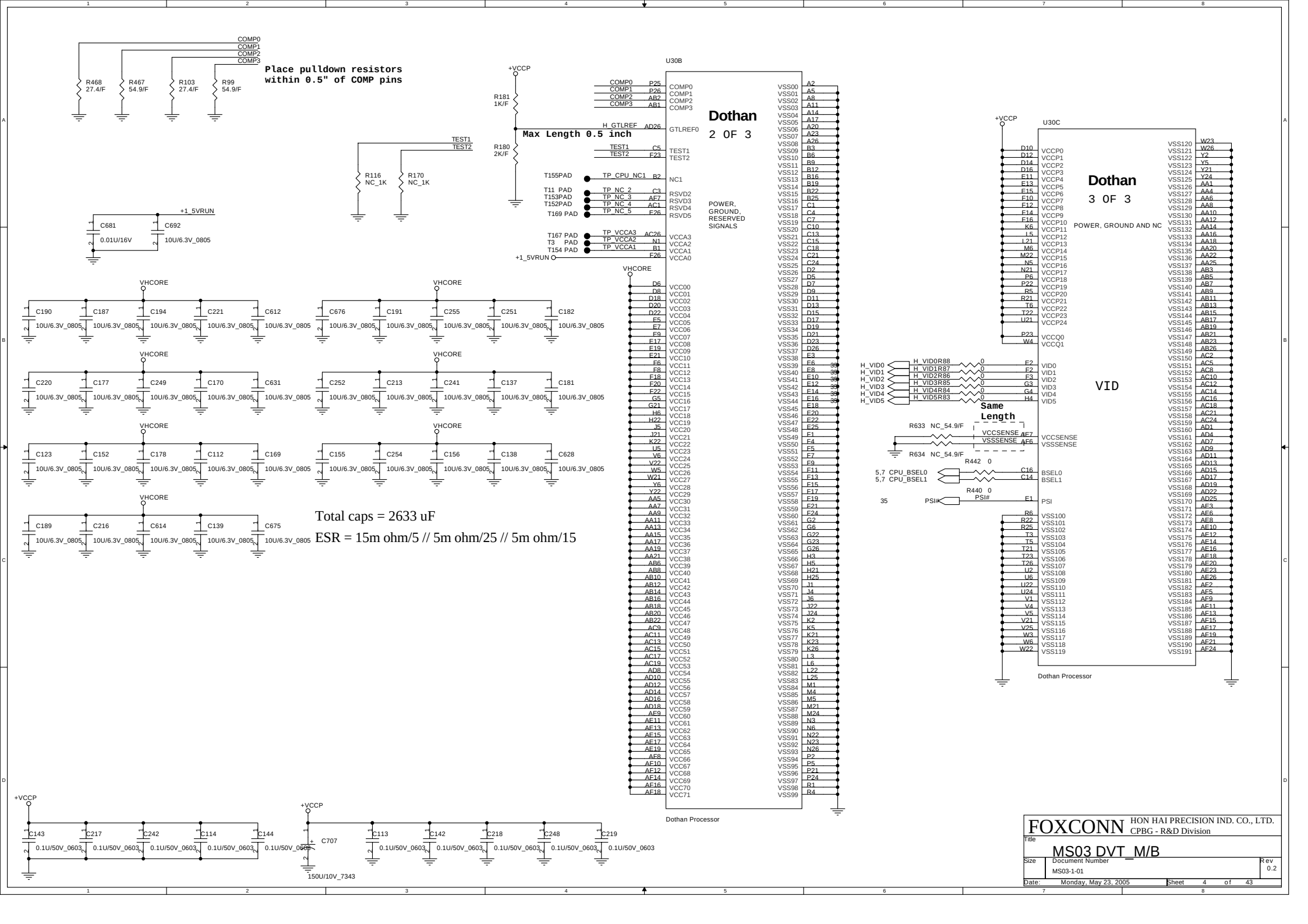

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FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33

Placed within 500mils of Y1

L5030-14.318MHZ/+-30ppm/20pF

Length as short as possible.

IDTCV125 / ICS954206BG

SM bus Address : 1101001 (ICH6) For clock generator

SM bus Address : 1001101(EC) For F75384S

Alviso Chip HOST

CPU

Alviso Chip

For NV44M & Alviso option

ICH6 DMI

Graphic NV44M

ALVISO SSCK

Place Thermal-Sensor near CPU & GMCH.

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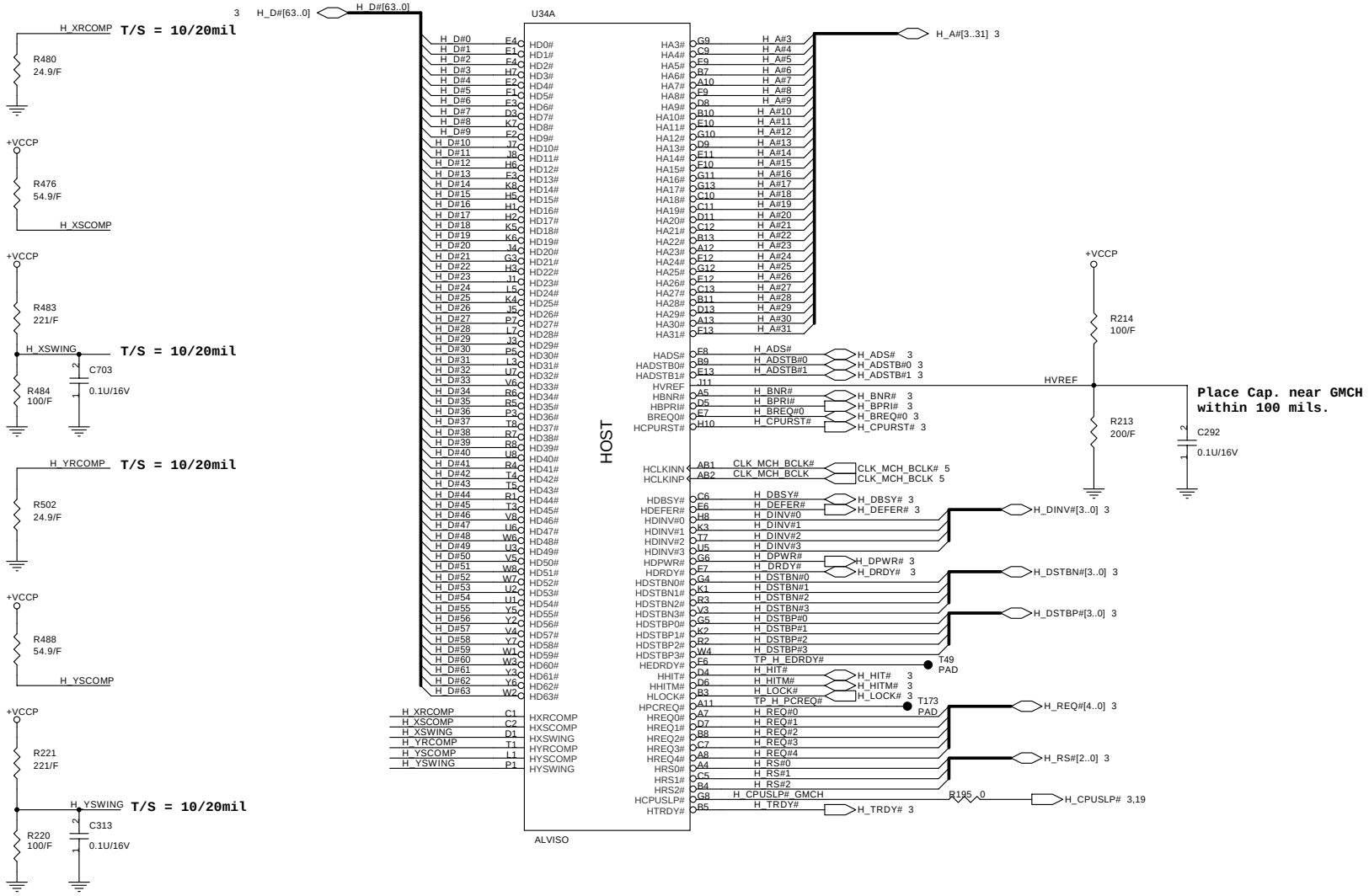

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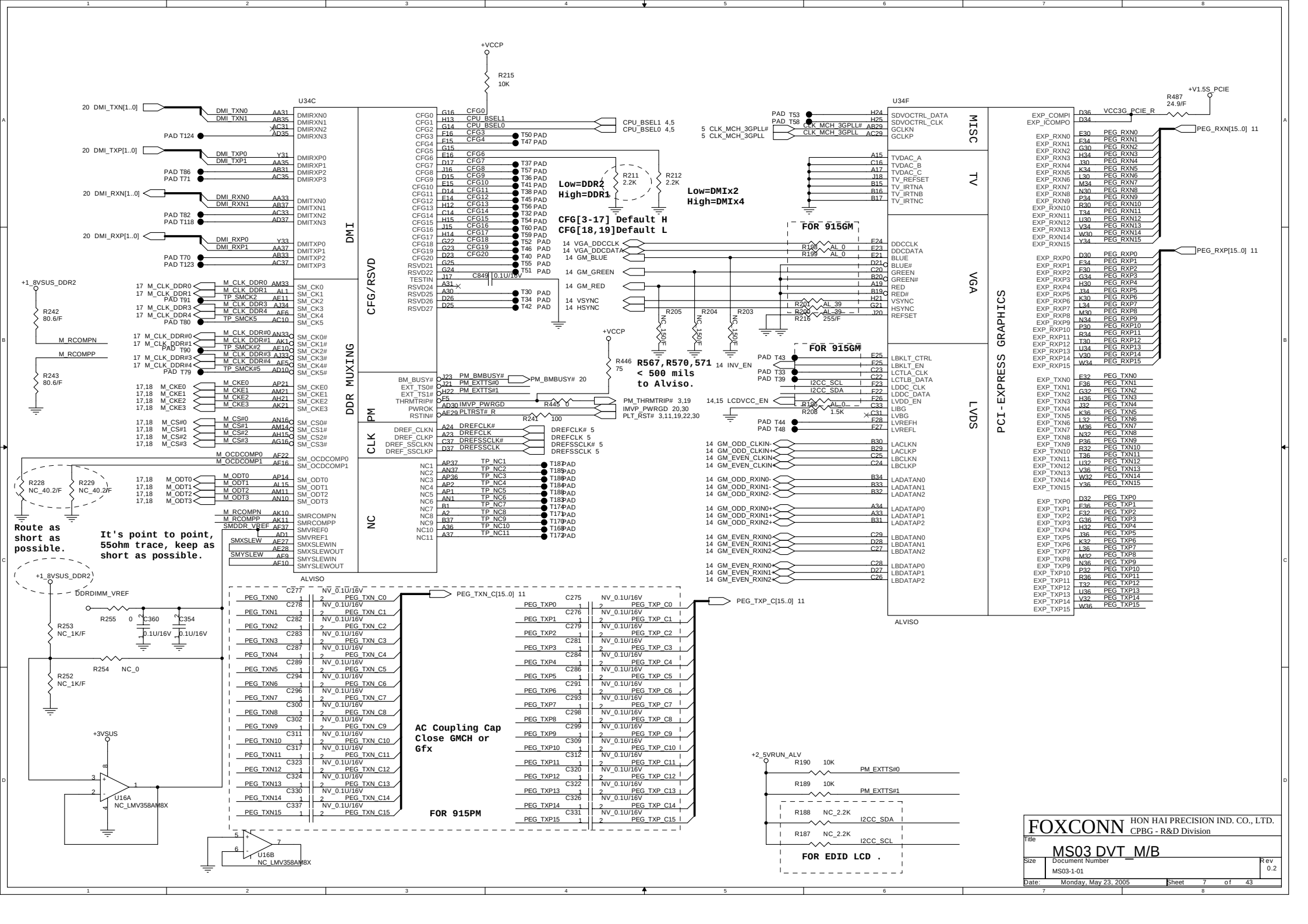
U34C
U34F
DMI
CFG/RSVD
DDR MUXING
PM
CLK
NC
MISC
TV
VGA
LVDS
PCI-EXPRESS GRAPHICS
ALVISO
Low=DDR2
High=DDR1
Low=DMIx2
High=DMIx4
CFG[3-17] Default H
CFG[18,19]Default L
FOR 915GM
FOR 915GM
R567,R570,571
< 500 mils
to Alviso.
Route as
short as
possible.
It's point to point,
55ohm trace, keep as
short as possible.
AC Coupling Cap
Close GMCH or
Gfx
FOR 915PM
FOR EDID LCD .
U16A
NC_LMV358AM8X
U16B
NC_LMV358AM8X
+VCCP
+1_8VSUS_DDR2
+3VSUS
+2_5VRUN_ALV
+V1.5S_PCIE
DDRDIMM_VREF
PEG_TXN_C[15..0] 11
PEG_TXP_C[15..0] 11
PEG_RXN[15..0] 11
PEG_RXP[15..0] 11
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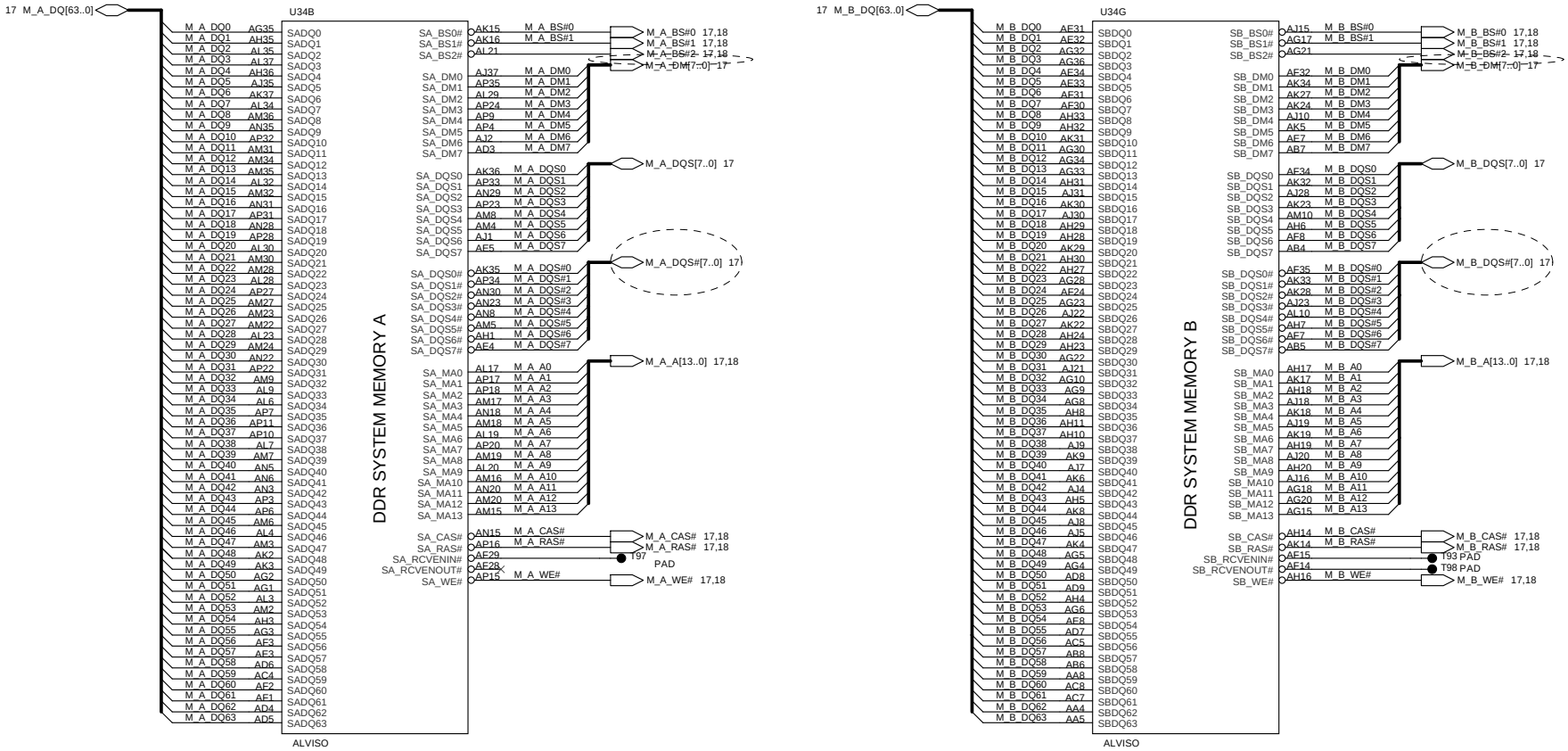

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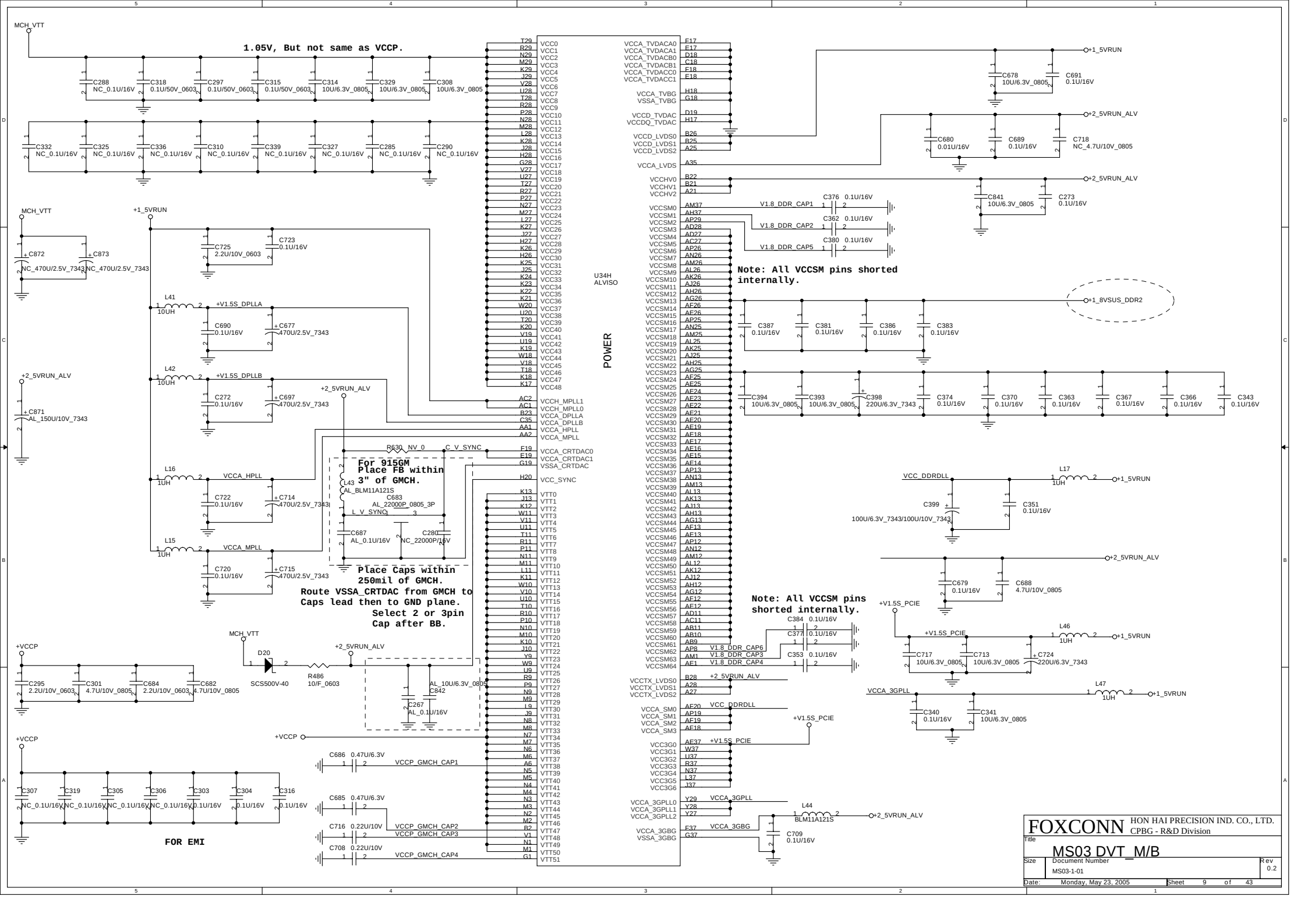

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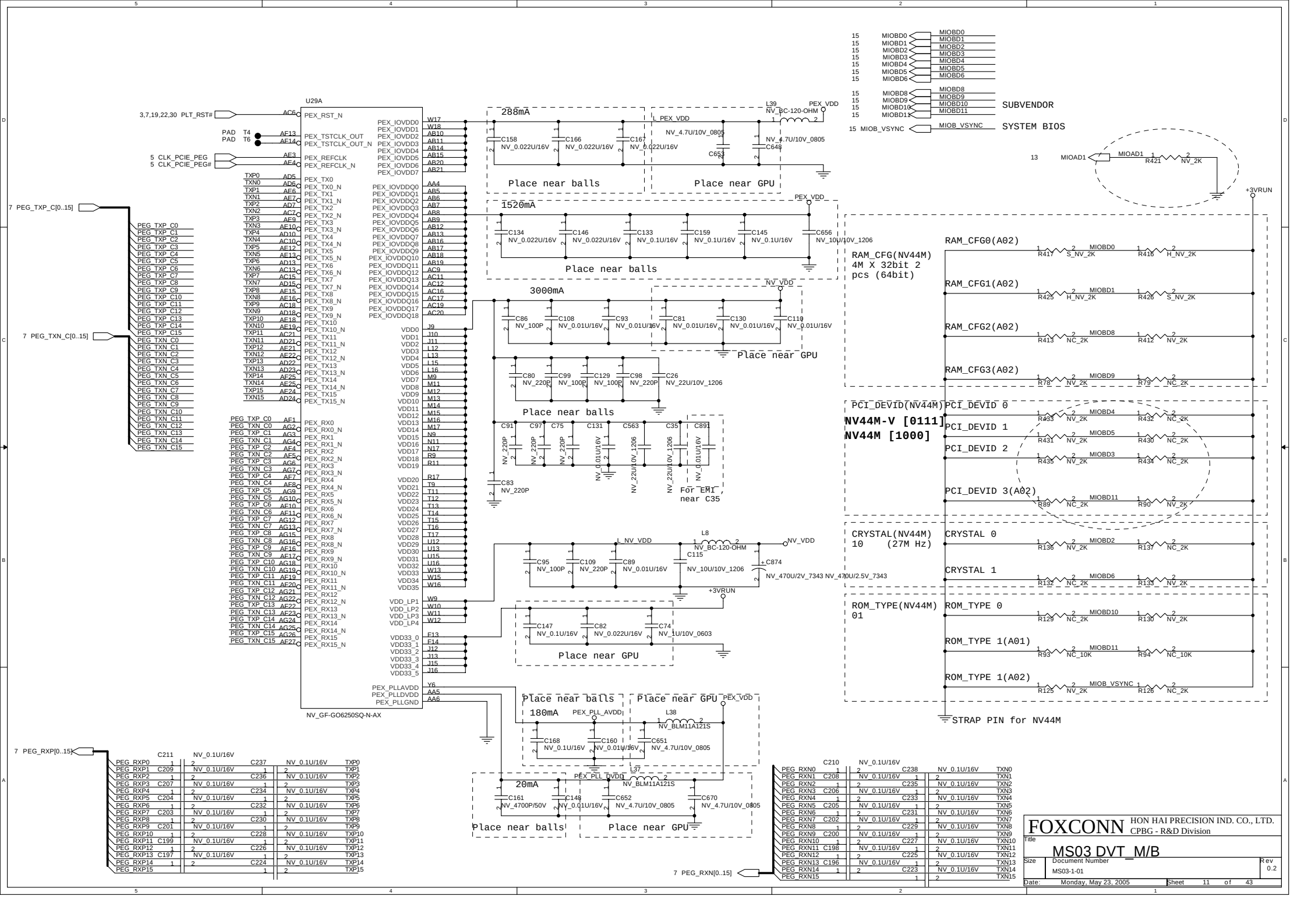
U29A
NV_GF-GO6250SQ-N-AX
3,7,19,22,30 PLT_RST#
PAD T4
PAD T6
5 CLK_PCIE_PEG
5 CLK_PCIE_PEG#
7 PEG_TXP_C[0..15]
7 PEG_TXN_C[0..15]
7 PEG_RXP[0..15]
7 PEG_RXN[0..15]
PEX_RST_N
PEX_TSTCLK_OUT
PEX_TSTCLK_OUT_N
PEX_REFCLK
PEX_REFCLK_N
PEX_TX0
PEX_TX0_N
PEX_TX1
PEX_TX1_N
PEX_TX2
PEX_TX2_N
PEX_TX3
PEX_TX3_N
PEX_TX4
PEX_TX4_N
PEX_TX5
PEX_TX5_N
PEX_TX6
PEX_TX6_N
PEX_TX7
PEX_TX7_N
PEX_TX8
PEX_TX8_N
PEX_TX9
PEX_TX9_N
PEX_TX10
PEX_TX10_N
PEX_TX11
PEX_TX11_N
PEX_TX12
PEX_TX12_N
PEX_TX13
PEX_TX13_N
PEX_TX14
PEX_TX14_N
PEX_TX15
PEX_TX15_N
PEX_RX0
PEX_RX0_N
PEX_RX1
PEX_RX1_N
PEX_RX2
PEX_RX2_N
PEX_RX3
PEX_RX3_N
PEX_RX4
PEX_RX4_N
PEX_RX5
PEX_RX5_N
PEX_RX6
PEX_RX6_N
PEX_RX7
PEX_RX7_N
PEX_RX8
PEX_RX8_N
PEX_RX9
PEX_RX9_N
PEX_RX10
PEX_RX10_N
PEX_RX11
PEX_RX11_N
PEX_RX12
PEX_RX12_N
PEX_RX13
PEX_RX13_N
PEX_RX14
PEX_RX14_N
PEX_RX15
PEX_RX15_N
PEX_IOVDD0
PEX_IOVDD1
PEX_IOVDD2
PEX_IOVDD3
PEX_IOVDD4
PEX_IOVDD5
PEX_IOVDD6
PEX_IOVDD7
PEX_IOVDDQ0
PEX_IOVDDQ1
PEX_IOVDDQ2
PEX_IOVDDQ3
PEX_IOVDDQ4
PEX_IOVDDQ5
PEX_IOVDDQ6
PEX_IOVDDQ7
PEX_IOVDDQ8
PEX_IOVDDQ9
PEX_IOVDDQ10
PEX_IOVDDQ11
PEX_IOVDDQ12
PEX_IOVDDQ13
PEX_IOVDDQ14
PEX_IOVDDQ15
PEX_IOVDDQ16
PEX_IOVDDQ17
PEX_IOVDDQ18
VDD0
VDD1
VDD2
VDD3
VDD4
VDD5
VDD6
VDD7
VDD8
VDD9
VDD10
VDD11
VDD12
VDD13
VDD14
VDD15
VDD16
VDD17
VDD18
VDD19
VDD20
VDD21
VDD22
VDD23
VDD24
VDD25
VDD26
VDD27
VDD28
VDD29
VDD30
VDD31
VDD32
VDD33
VDD34
VDD35
VDD_LP1
VDD_LP2
VDD_LP3
VDD_LP4
VDD33_0
VDD33_1
VDD33_2
VDD33_3
VDD33_4
VDD33_5
PEX_PLLAVDD
PEX_PLLDVDD
PEX_PLLGND
288mA
L_PEX_VDD
L39
NV_BC-120-OHM
PEX_VDD
C158 NV_0.022U/16V
C166 NV_0.022U/16V
C167 NV_0.022U/16V
NV_4.7U/10V_0805
C653
NV_4.7U/10V_0805
C648
Place near balls
Place near GPU
1520mA
C134 NV_0.022U/16V
C146 NV_0.022U/16V
C133 NV_0.1U/16V
C159 NV_0.1U/16V
C145 NV_0.1U/16V
C656 NV_10U/10V_1206
3000mA
NV_VDD
C86 NV_100P
C108 NV_0.01U/16V
C93 NV_0.01U/16V
C81 NV_0.01U/16V
C130 NV_0.01U/16V
C110 NV_0.01U/16V
C80 NV_220P
C99 NV_100P
C129 NV_100P
C98 NV_220P
C26 NV_22U/10V_1206
C91 NV_220P
C97 NV_220P
C75 NV_220P
C131 NV_0.01U/16V
C563 NV_22U/10V_1206
C35 NV_22U/10V_1206
C891 NV_0.01U/16V
C83 NV_220P
For EMI, near C35
L8
NV_BC-120-OHM
C95 NV_100P
C109 NV_220P
C89 NV_0.01U/16V
C115 NV_10U/10V_1206
+C874
NV_470U/2V_7343
NV_470U/2.5V_7343
+3VRUN
C147 NV_0.1U/16V
C82 NV_0.022U/16V
C74 NV_1U/10V_0603
180mA
PEX_PLL_AVDD
L38
NV_BLM11A121S
C168 NV_0.1U/16V
C160 NV_0.01U/16V
C651 NV_4.7U/10V_0805
20mA
PEX_PLL_DVDD
L37
NV_BLM11A121S
C161 NV_4700P/50V
C148 NV_0.01U/16V
C652 NV_4.7U/10V_0805
C670 NV_4.7U/10V_0805
15 MIOBD0
15 MIOBD1
15 MIOBD2
15 MIOBD3
15 MIOBD4
15 MIOBD5
15 MIOBD6
15 MIOBD8
15 MIOBD9
15 MIOBD10
15 MIOBD11
15 MIOB_VSYNC
SUBVENDOR
SYSTEM BIOS
13 MIOAD1
R421 NV_2K
+3VRUN
RAM_CFG(NV44M)
4M X 32bit 2 pcs (64bit)
RAM_CFG0(A02)
R417 S_NV_2K
R416 H_NV_2K
RAM_CFG1(A02)
R425 H_NV_2K
R426 S_NV_2K
RAM_CFG2(A02)
R413 NC_2K
R412 NV_2K
RAM_CFG3(A02)
R78 NV_2K
R79 NC_2K
PCI_DEVID(NV44M)
NV44M-V [0111]
NV44M [1000]
PCI_DEVID 0
R433 NV_2K
R432 NC_2K
PCI_DEVID 1
R431 NV_2K
R430 NC_2K
PCI_DEVID 2
R435 NV_2K
R434 NC_2K
PCI_DEVID 3(A02)
R89 NC_2K
R90 NV_2K
CRYSTAL(NV44M)
10 (27M Hz)
CRYSTAL 0
R135 NV_2K
R137 NC_2K
CRYSTAL 1
R132 NC_2K
R133 NV_2K
ROM_TYPE(NV44M)
01
ROM_TYPE 0
R129 NC_2K
R130 NV_2K
ROM_TYPE 1(A01)
R93 NC_10K
R94 NC_10K
ROM_TYPE 1(A02)
R125 NV_2K
R126 NC_2K
STRAP PIN for NV44M
C211 NV_0.1U/16V
C237 NV_0.1U/16V
C209 NV_0.1U/16V
C236 NV_0.1U/16V
C207 NV_0.1U/16V
C234 NV_0.1U/16V
C204 NV_0.1U/16V
C232 NV_0.1U/16V
C203 NV_0.1U/16V
C230 NV_0.1U/16V
C201 NV_0.1U/16V
C228 NV_0.1U/16V
C199 NV_0.1U/16V
C226 NV_0.1U/16V
C197 NV_0.1U/16V
C224 NV_0.1U/16V
C210 NV_0.1U/16V
C238 NV_0.1U/16V
C208 NV_0.1U/16V
C235 NV_0.1U/16V
C206 NV_0.1U/16V
C233 NV_0.1U/16V
C205 NV_0.1U/16V
C231 NV_0.1U/16V
C202 NV_0.1U/16V
C229 NV_0.1U/16V
C200 NV_0.1U/16V
C227 NV_0.1U/16V
C198 NV_0.1U/16V
C225 NV_0.1U/16V
C196 NV_0.1U/16V
C223 NV_0.1U/16V
Place near balls
Place near GPU
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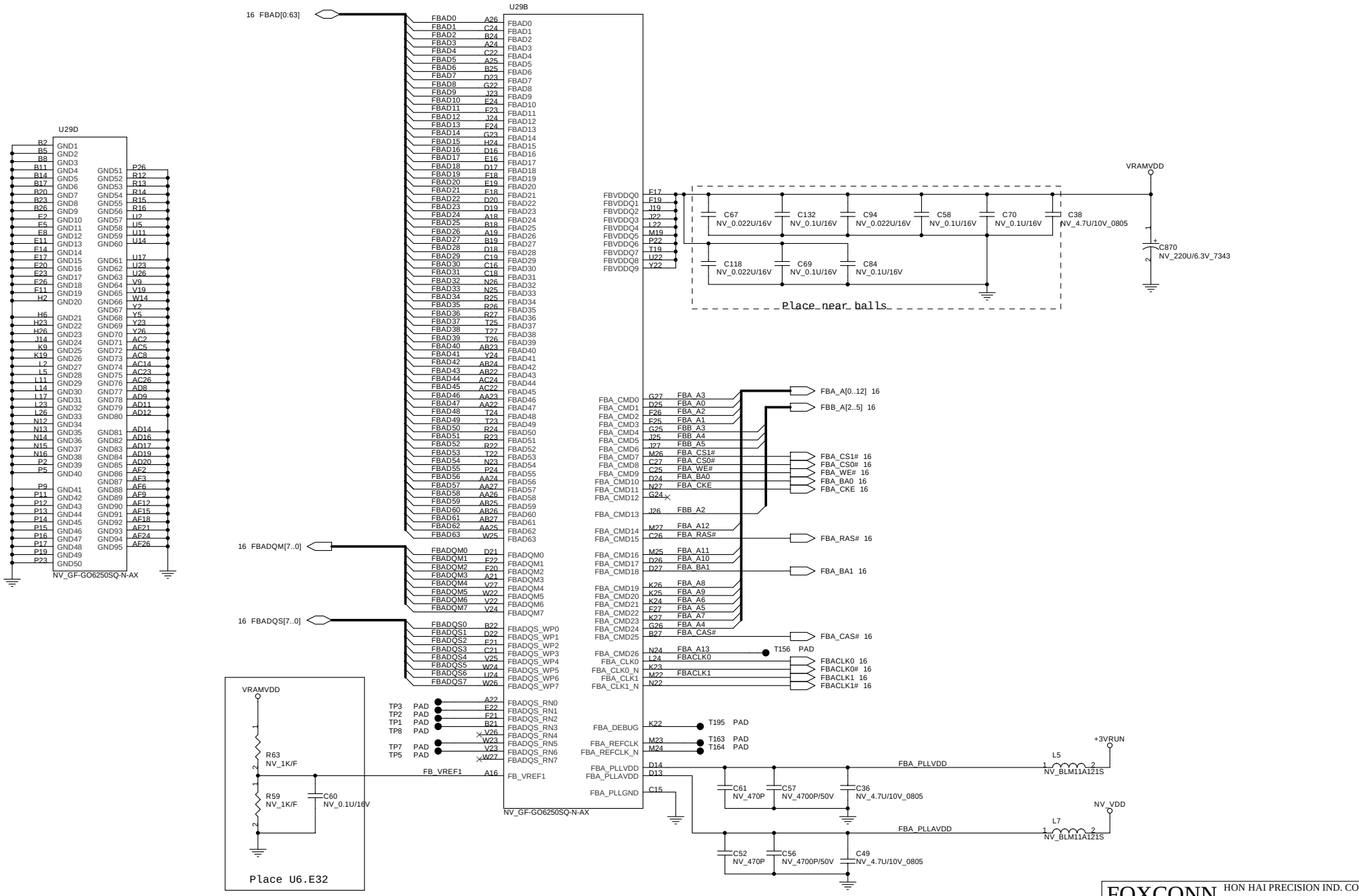

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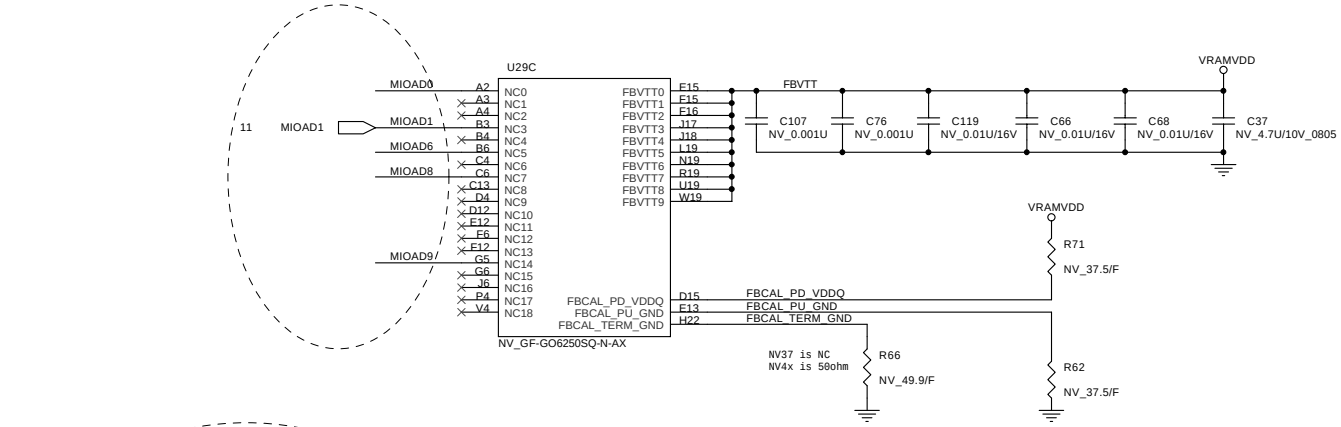

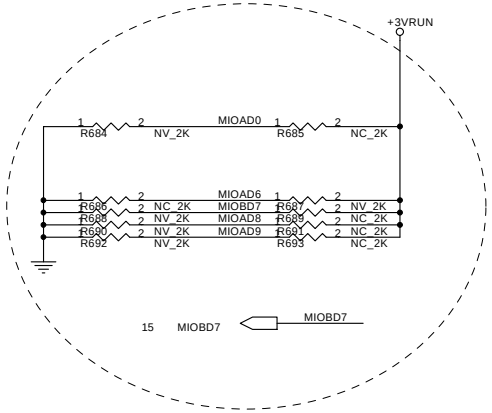

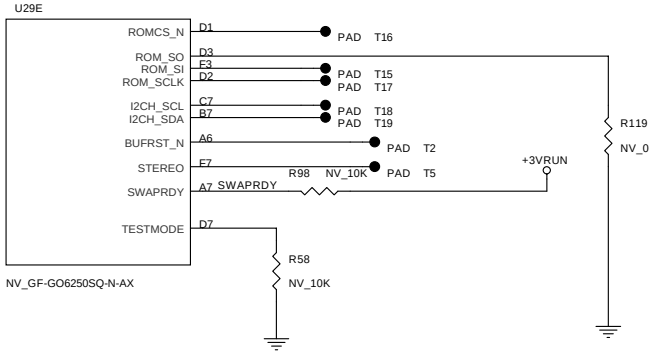

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LVDS CONNECTOR

FOXCONN_GS12401_1011

H : NOTEBOOK
L : PORT REPLICATOR

RGB ANALOG SWITCH

CLOSE

For 915PM

For 915GM

CRT CON

R235,R237,R241 <1000 mils to JVGA1.

L30,L32,L33 <500mils to JVGA1.

Close CRT connector .

Close CRT connector .

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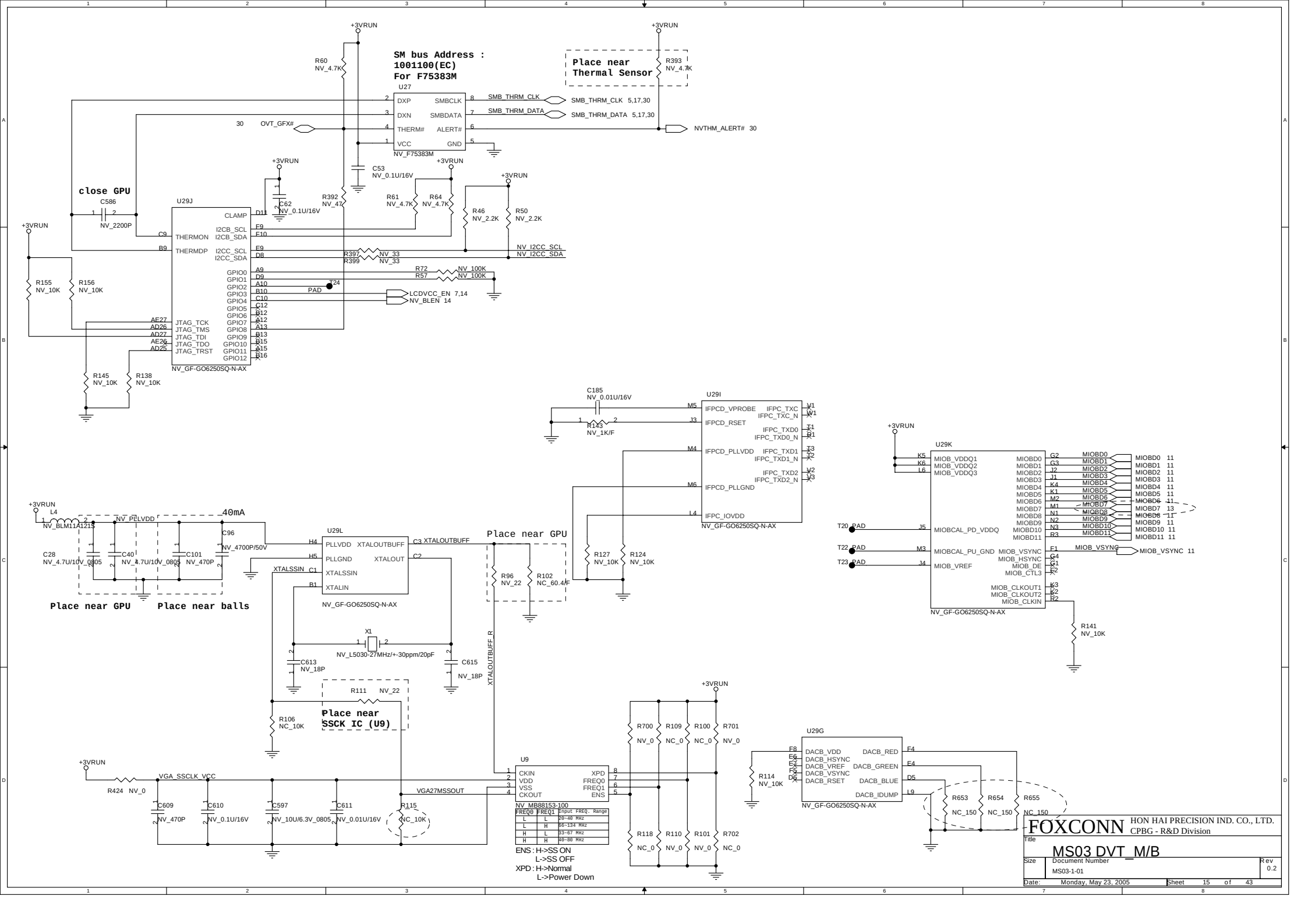
SM bus Address :
1001100(EC)
For F75383M
Place near
Thermal Sensor
U27
NV_F75383M
DXP
DXN
THERM#
VCC
SMBCLK
SMBDATA
ALERT#
GND
SMB_THRM_CLK
SMB_THRM_DATA
SMB_THRM_CLK 5,17,30
SMB_THRM_DATA 5,17,30
OVT_GFX#
NVTHM_ALERT# 30
R60 NV_4.7K
R393 NV_4.7K
C53 NV_0.1U/16V
C62 NV_0.1U/16V
R392 NV_47
R61 NV_4.7K
R64 NV_4.7K
R46 NV_2.2K
R50 NV_2.2K
close GPU
C586 NV_2200P
U29J
NV_GF-GO6250SQ-N-AX
CLAMP
THERMON
THERMDP
I2CB_SCL
I2CB_SDA
I2CC_SCL
I2CC_SDA
GPIO0
GPIO1
GPIO2
GPIO3
GPIO4
GPIO5
GPIO6
GPIO7
GPIO8
GPIO9
GPIO10
GPIO11
GPIO12
JTAG_TCK
JTAG_TMS
JTAG_TDI
JTAG_TDO
JTAG_TRST
R397 NV_33
R399 NV_33
R72 NV_100K
R57 NV_100K
T24 PAD
LCDVCC_EN 7,14
NV_BLEN 14
NV_I2CC_SCL
NV_I2CC_SDA
R155 NV_10K
R156 NV_10K
R145 NV_10K
R138 NV_10K
C185 NV_0.01U/16V
R143 NV_1K/F
U29I
NV_GF-GO6250SQ-N-AX
IFPCD_VPROBE
IFPCD_RSET
IFPCD_PLLVDD
IFPCD_PLLGND
IFPCD_IOVDD
IFPC_TXC
IFPC_TXC_N
IFPC_TXD0
IFPC_TXD0_N
IFPC_TXD1
IFPC_TXD1_N
IFPC_TXD2
IFPC_TXD2_N
R127 NV_10K
R124 NV_10K
U29K
NV_GF-GO6250SQ-N-AX
MIOB_VDDQ1
MIOB_VDDQ2
MIOB_VDDQ3
MIOBCAL_PD_VDDQ
MIOBCAL_PU_GND
MIOB_VREF
MIOB_VSYNC
MIOB_HSYNC
MIOB_DE
MIOB_CTL3
MIOB_CLKOUT1
MIOB_CLKOUT2
MIOB_CLKIN
MIOBD0
MIOBD1
MIOBD2
MIOBD3
MIOBD4
MIOBD5
MIOBD6
MIOBD7
MIOBD8
MIOBD9
MIOBD10
MIOBD11
T20 PAD
T22 PAD
T23 PAD
R141 NV_10K
L4 NV_BLM11A121S
NV_PLLVDD
40mA
C28 NV_4.7U/10V_0805
C40 NV_4.7U/10V_0805
C101 NV_470P
C96 NV_4700P/50V
Place near GPU
Place near balls
U29L
NV_GF-GO6250SQ-N-AX
PLLVDD
PLLGND
XTALSSIN
XTALIN
XTALOUTBUFF
XTALOUT
C3 XTALOUTBUFF
C2
C1
B1
Place near GPU
R96 NV_22
R102 NC_60.4/F
X1
NV_L5030-27MHz/+-30ppm/20pF
C613 NV_18P
C615 NV_18P
XTALOUTBUFF_R
R111 NV_22
Place near
SSCK_IC (U9)
R106 NC_10K
U9
NV_MB88153-100
CKIN
VDD
VSS
CKOUT
XPD
FREQ0
FREQ1
ENS
FREQ0 FREQ1 Input FREQ. Range
L L 20-40 MHz
L H 66-134 MHz
H L 33-67 MHz
H H 40-80 MHz
ENS : H->SS ON
L->SS OFF
XPD : H->Normal
L->Power Down
R700 NV_0
R109 NC_0
R100 NC_0
R701 NV_0
R118 NC_0
R110 NV_0
R101 NV_0
R702 NC_0
VGA_SSCLK_VCC
R424 NV_0
C609 NV_470P
C610 NV_0.1U/16V
C597 NV_10U/6.3V_0805
C611 NV_0.01U/16V
R115 NC_10K
VGA27MSSOUT
U29G
NV_GF-GO6250SQ-N-AX
DACB_VDD
DACB_HSYNC
DACB_VREF
DACB_VSYNC
DACB_RSET
DACB_RED
DACB_GREEN
DACB_BLUE
DACB_IDUMP
R114 NV_10K
R653 NC_150
R654 NC_150
R655 NC_150
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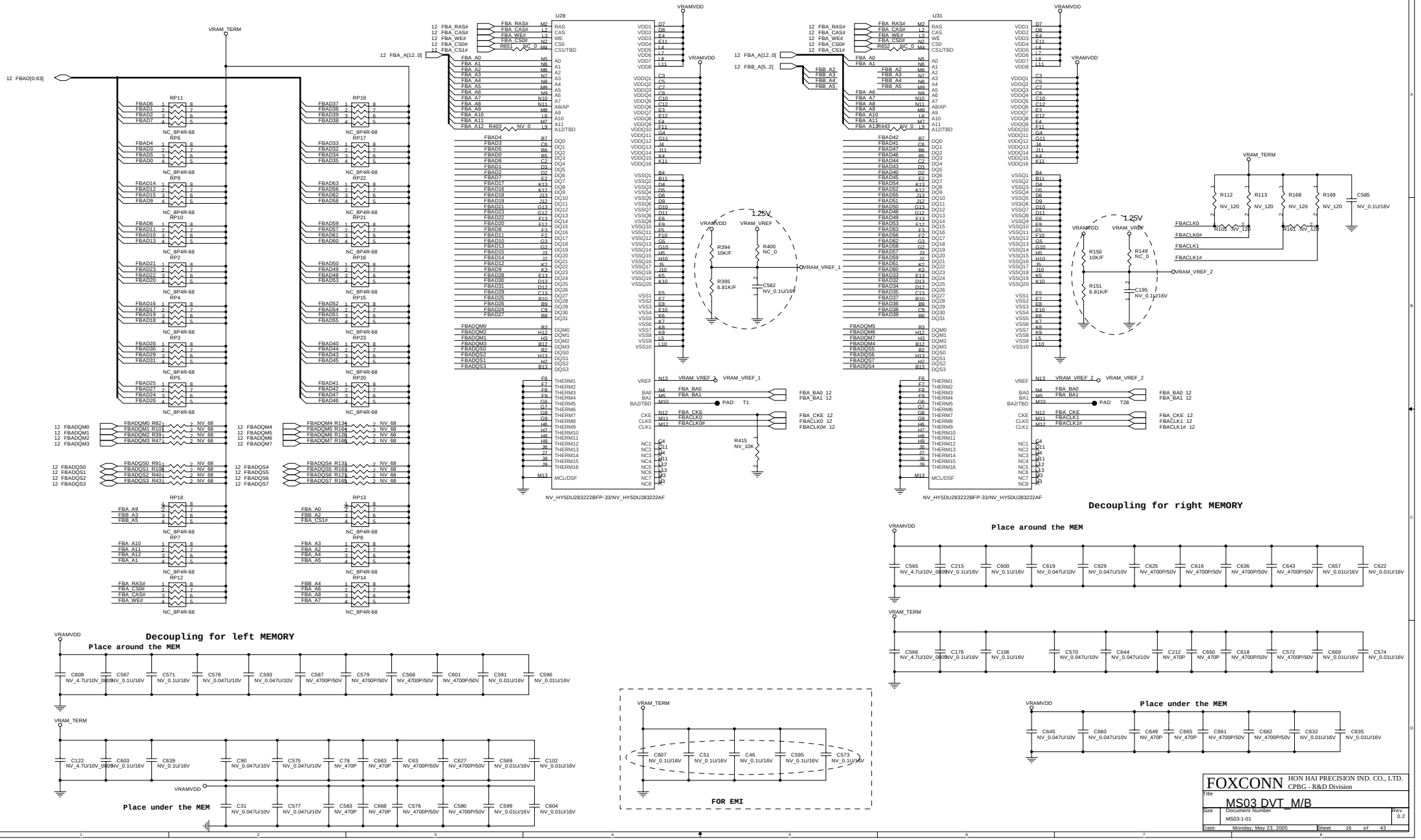
VRAM_TERM
VRAMVDD
12 FBAD[0:63]
RP11
RP6
RP9
RP10
RP2
RP4
RP3
RP5
RP19
RP17
RP22
RP21
RP16
RP15
RP23
RP20
NC_8P4R-68
12 FBADQM0
12 FBADQM1
12 FBADQM2
12 FBADQM3
12 FBADQS0
12 FBADQS1
12 FBADQS2
12 FBADQS3
12 FBADQM4
12 FBADQM5
12 FBADQM6
12 FBADQM7
12 FBADQS4
12 FBADQS5
12 FBADQS6
12 FBADQS7
RP18
RP7
RP12
RP13
RP8
RP14
U28
U31
NV_HY5DU283222BFP-33/NV_HY5DU283222AF
12 FBA_RAS#
12 FBA_CAS#
12 FBA_WE#
12 FBA_CS0#
12 FBA_CS1#
12 FBA_A[12..0]
12 FBA_A[5..2]
R651
R652
R403
R443
NC_0
NV_0
1.25V
R394 10K/F
R400 NC_0
R395 6.81K/F
C582 NV_0.1U/16V
VRAM_VREF_1
VRAM_VREF_2
R150 10K/F
R149 NC_0
R151 6.81K/F
C195 NV_0.1U/16V
R112
R113
R168
R169
C585
NV_120
NV_0.1U/16V
FBACLK0
FBACLK0#
FBACLK1
FBACLK1#
R105
R161
FBA_BA0 12
FBA_BA1 12
FBA_CKE 12
FBACLK0 12
FBACLK0# 12
FBACLK1 12
FBACLK1# 12
R415 NV_10K
PAD T1
PAD T28
Decoupling for right MEMORY
Place around the MEM
C565 NV_4.7U/10V_0805
C215 NV_0.1U/16V
C600 NV_0.1U/16V
C619 NV_0.047U/10V
C629 NV_0.047U/10V
C625 NV_4700P/50V
C616 NV_4700P/50V
C636 NV_4700P/50V
C643 NV_4700P/50V
C657 NV_0.01U/16V
C622 NV_0.01U/16V
C566 NV_4.7U/10V_0805
C176 NV_0.1U/16V
C106 NV_0.1U/16V
C570 NV_0.047U/10V
C644 NV_0.047U/10V
C212 NV_470P
C650 NV_470P
C618 NV_4700P/50V
C572 NV_4700P/50V
C669 NV_0.01U/16V
C574 NV_0.01U/16V
Place under the MEM
C645 NV_0.047U/10V
C660 NV_0.047U/10V
C649 NV_470P
C665 NV_470P
C661 NV_4700P/50V
C662 NV_4700P/50V
C632 NV_0.01U/16V
C635 NV_0.01U/16V
Decoupling for left MEMORY
Place around the MEM
C608 NV_4.7U/10V_0805
C567 NV_0.1U/16V
C571 NV_0.1U/16V
C578 NV_0.047U/10V
C593 NV_0.047U/10V
C587 NV_4700P/50V
C579 NV_4700P/50V
C568 NV_4700P/50V
C601 NV_4700P/50V
C591 NV_0.01U/16V
C596 NV_0.01U/16V
C122 NV_4.7U/10V_0805
C603 NV_0.1U/16V
C639 NV_0.1U/16V
C90 NV_0.047U/10V
C575 NV_0.047U/10V
C78 NV_470P
C663 NV_470P
C63 NV_4700P/50V
C627 NV_4700P/50V
C569 NV_0.01U/16V
C102 NV_0.01U/16V
Place under the MEM
C31 NV_0.047U/10V
C577 NV_0.047U/10V
C583 NV_470P
C668 NV_470P
C576 NV_4700P/50V
C580 NV_4700P/50V
C599 NV_0.01U/16V
C604 NV_0.01U/16V
C607 NV_0.1U/16V
C51 NV_0.1U/16V
C46 NV_0.1U/16V
C595 NV_0.1U/16V
C573 NV_0.1U/16V
FOR EMI
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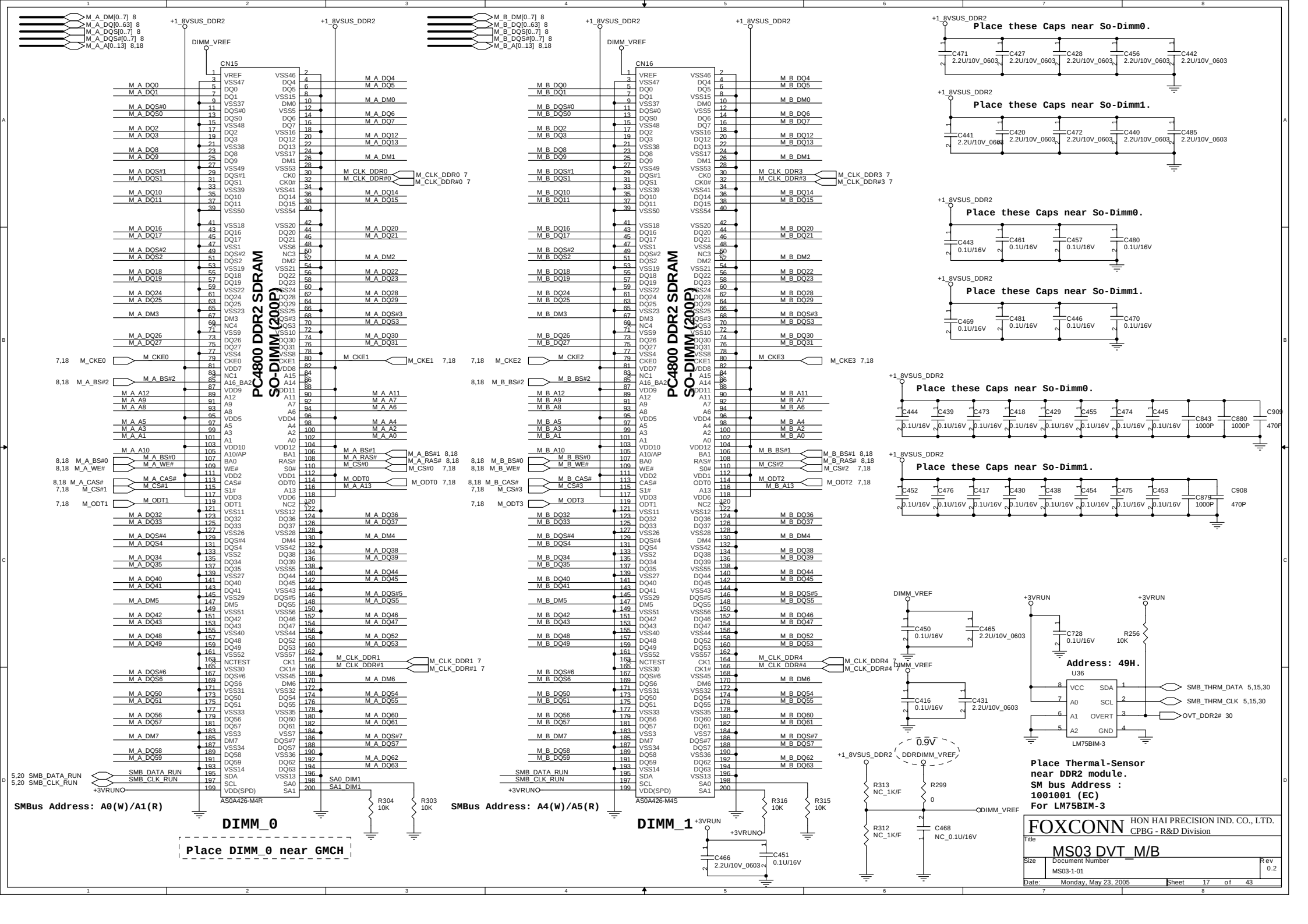
M_A_DM[0..7] 8
M_A_DQ[0..63] 8
M_A_DQS[0..7] 8
M_A_DQS#[0..7] 8
M_A_A[0..13] 8,18
+1_8VSUS_DDR2
DIMM_VREF
CN15
PC4800 DDR2 SDRAM
SO-DIMM (200P)
AS0A426-M4R
M_CLK_DDR0 7
M_CLK_DDR0# 7
M_CLK_DDR1 7
M_CLK_DDR1# 7
M_CKE0
M_CKE1
M_A_BS#2
M_A_BS#1 8,18
M_A_RAS# 8,18
M_CS#0 7,18
M_ODT0 7,18
M_A_CAS# 8,18
M_CS#1 7,18
M_ODT1 7,18
M_A_BS#0
M_A_WE# 8,18
SMB_DATA_RUN 5,20
SMB_CLK_RUN 5,20
+3VRUN
SA0_DIM1
SA1_DIM1
R304 10K
R303 10K
SMBus Address: A0(W)/A1(R)
DIMM_0
Place DIMM_0 near GMCH
M_B_DM[0..7] 8
M_B_DQ[0..63] 8
M_B_DQS[0..7] 8
M_B_DQS#[0..7] 8
M_B_A[0..13] 8,18
CN16
AS0A426-M4S
M_CLK_DDR3 7
M_CLK_DDR3# 7
M_CLK_DDR4 7
M_CLK_DDR4# 7
M_CKE2
M_CKE3 7,18
M_B_BS#2
M_B_BS#1 8,18
M_B_RAS# 8,18
M_CS#2 7,18
M_ODT2 7,18
M_B_BS#0
M_B_WE# 8,18
M_B_CAS# 8,18
M_CS#3 7,18
M_ODT3 7,18
R316 10K
R315 10K
SMBus Address: A4(W)/A5(R)
DIMM_1
C466 2.2U/10V_0603
C451 0.1U/16V
Place these Caps near So-Dimm0.
C471 2.2U/10V_0603
C427 2.2U/10V_0603
C428 2.2U/10V_0603
C456 2.2U/10V_0603
C442 2.2U/10V_0603
Place these Caps near So-Dimm1.
C441 2.2U/10V_0603
C420 2.2U/10V_0603
C472 2.2U/10V_0603
C440 2.2U/10V_0603
C485 2.2U/10V_0603
Place these Caps near So-Dimm0.
C443 0.1U/16V
C461 0.1U/16V
C457 0.1U/16V
C480 0.1U/16V
Place these Caps near So-Dimm1.
C469 0.1U/16V
C481 0.1U/16V
C446 0.1U/16V
C470 0.1U/16V
Place these Caps near So-Dimm0.
C444 0.1U/16V
C439 0.1U/16V
C473 0.1U/16V
C418 0.1U/16V
C429 0.1U/16V
C455 0.1U/16V
C474 0.1U/16V
C445 0.1U/16V
C843 1000P
C880 1000P
C909 470P
Place these Caps near So-Dimm1.
C452 0.1U/16V
C476 0.1U/16V
C417 0.1U/16V
C430 0.1U/16V
C438 0.1U/16V
C454 0.1U/16V
C475 0.1U/16V
C453 0.1U/16V
C879 1000P
C908 470P
DIMM_VREF
C450 0.1U/16V
C465 2.2U/10V_0603
C416 0.1U/16V
C431 2.2U/10V_0603
0.9V
DDRDIMM_VREF
R313 NC_1K/F
R299 0
R312 NC_1K/F
C468 NC_0.1U/16V
+3VRUN
C728 0.1U/16V
R256 10K
Address: 49H.
U36
LM75BIM-3
VCC
SDA
A0
SCL
A1
OVERT
A2
GND
SMB_THRM_DATA 5,15,30
SMB_THRM_CLK 5,15,30
OVT_DDR2# 30
Place Thermal-Sensor near DDR2 module.
SM bus Address : 1001001 (EC)
For LM75BIM-3
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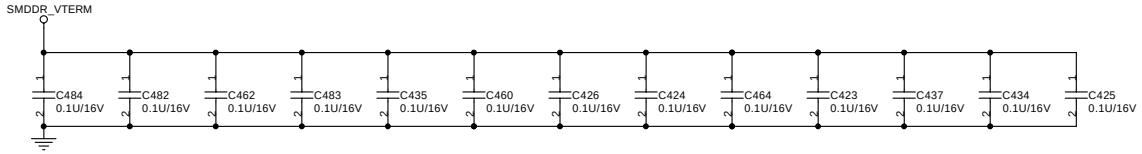
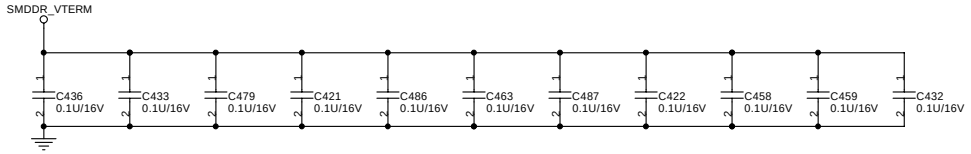
M_A_A[0..13] 8,17

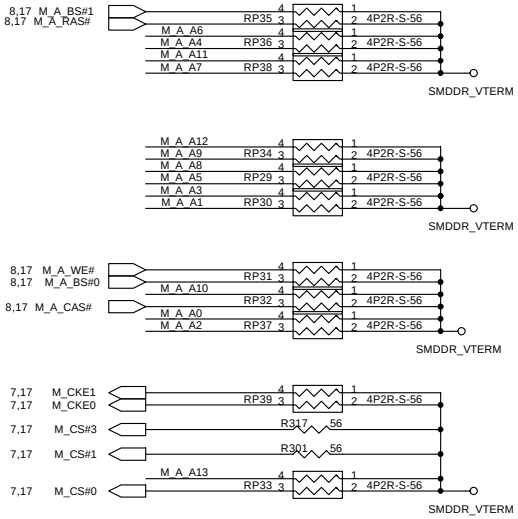
M_B_A[0..13] 8,17

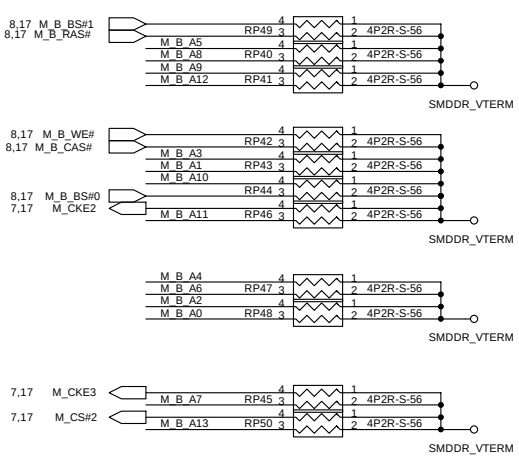


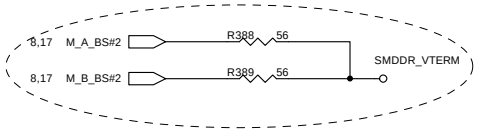
Layout note: Place 1 cap close to every 1 R-pack terminated to SMDDR_VTERM.

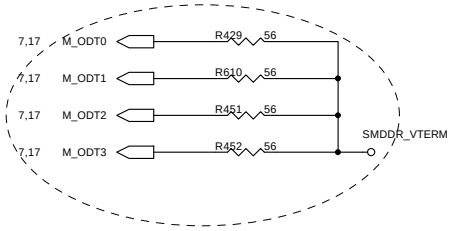


Layout note: Place 1 cap close to every 1 R-pack terminated to SMDDR_VTERM.









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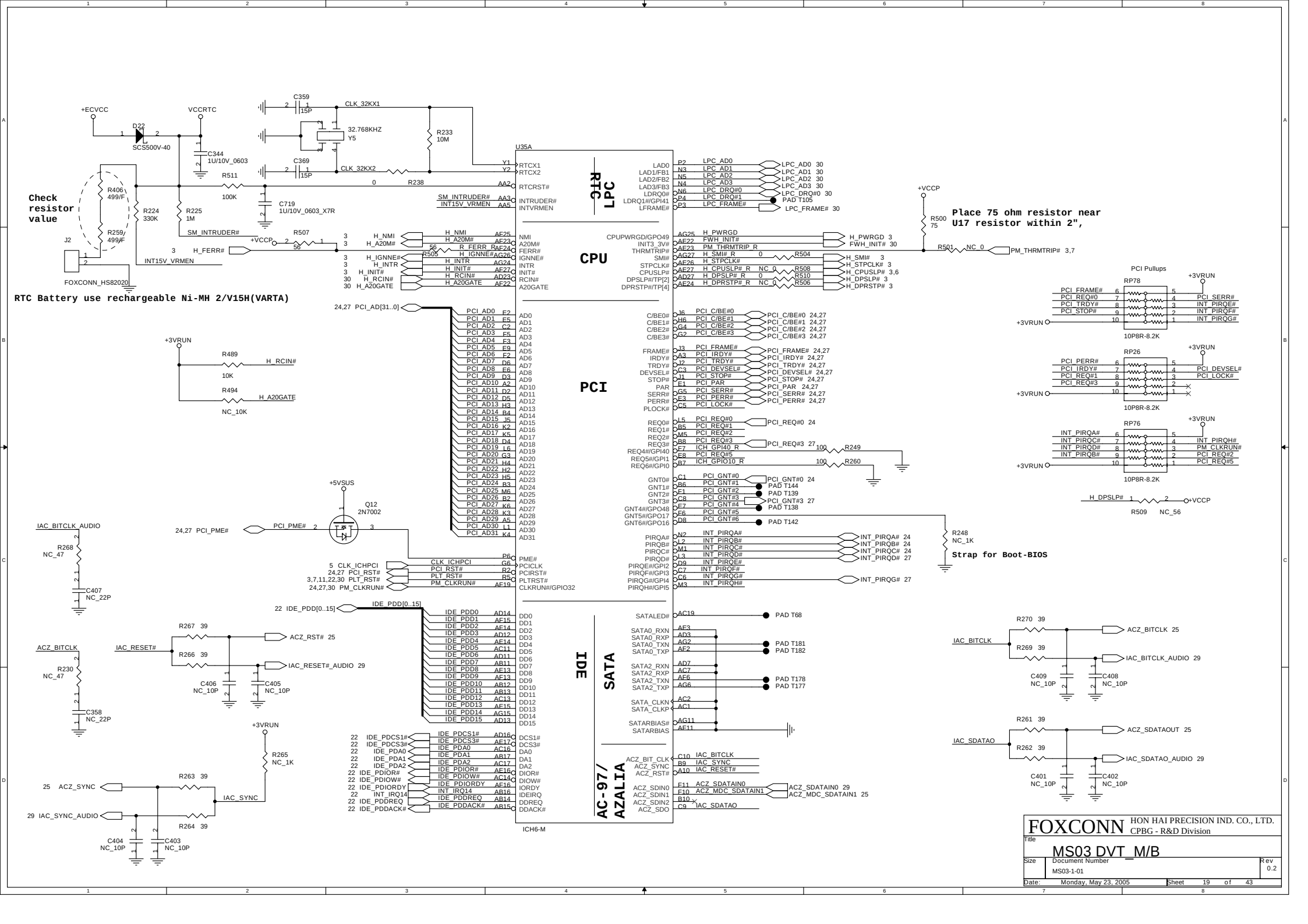
Check resistor value
RTC Battery use rechargeable Ni-MH 2/V15H(VARTA)
Place 75 ohm resistor near U17 resistor within 2",
Strap for Boot-BIOS
U35A
RTC
LPC
CPU
PCI
SATA
IDE
AC-97/ AZALIA
ICH6-M
PCI Pullups
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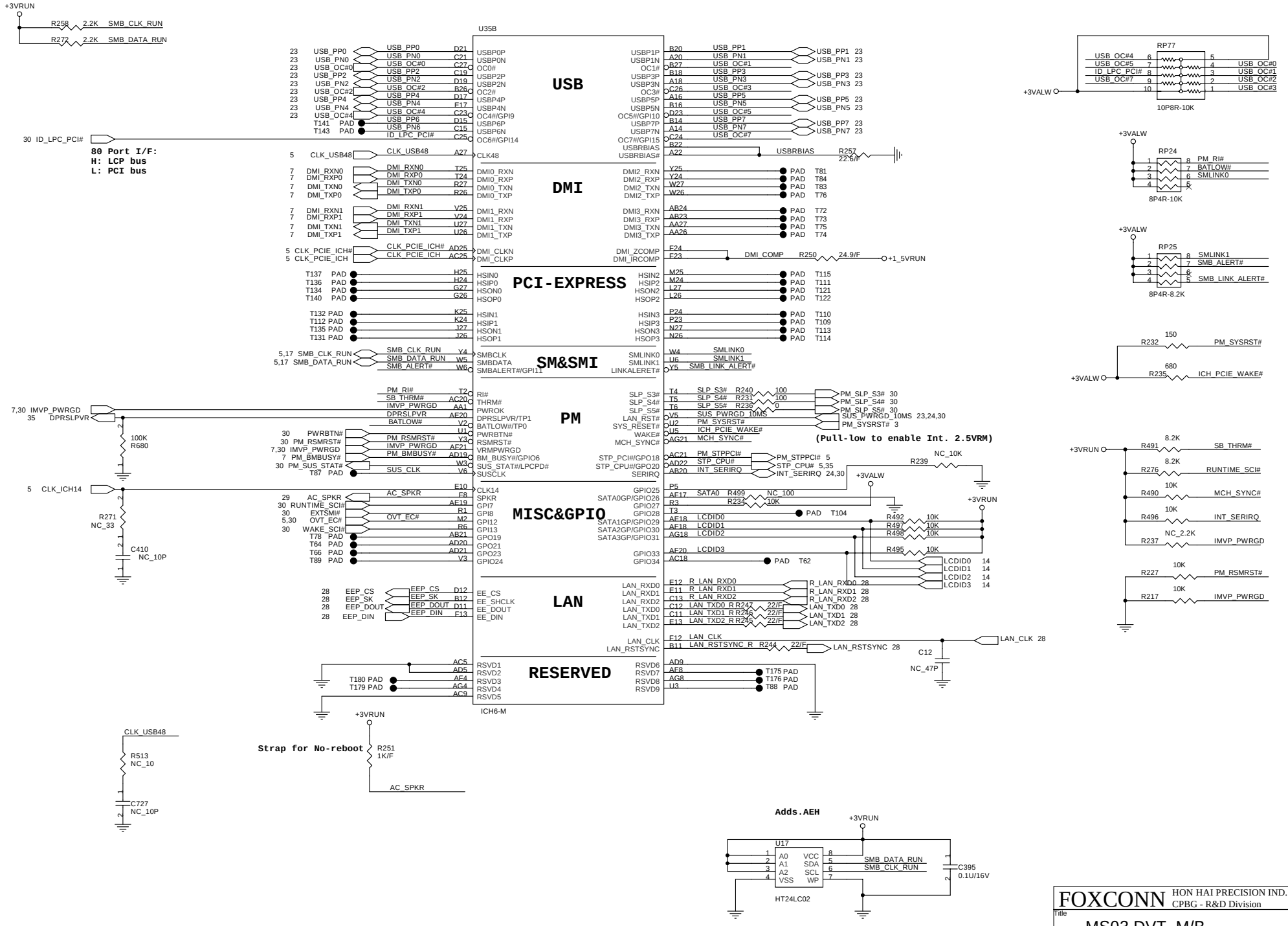
U35B
USB
DMI
PCI-EXPRESS
SM&SMI
PM
MISC&GPIO
LAN
RESERVED
ICH6-M
80 Port I/F:
H: LCP bus
L: PCI bus
Strap for No-reboot
(Pull-low to enable Int. 2.5VRM)
Adds.AEH
HT24LC02
RP77
10P8R-10K
RP24
8P4R-10K
RP25
8P4R-8.2K
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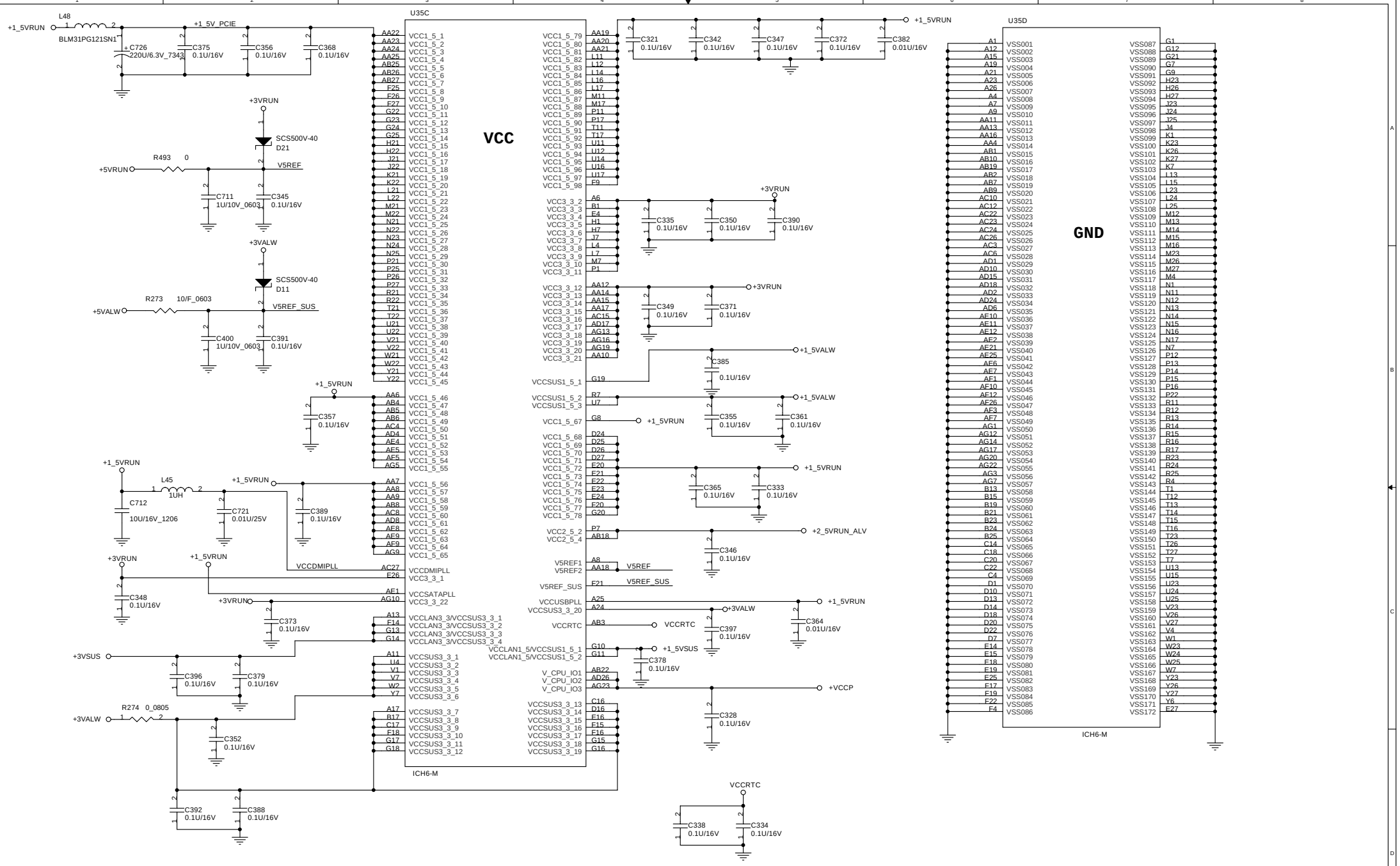

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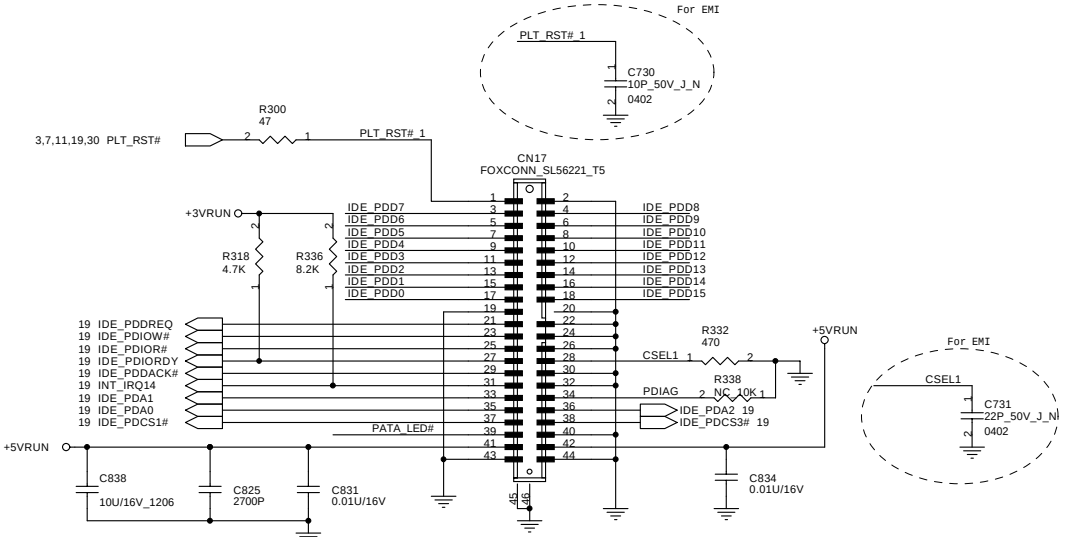
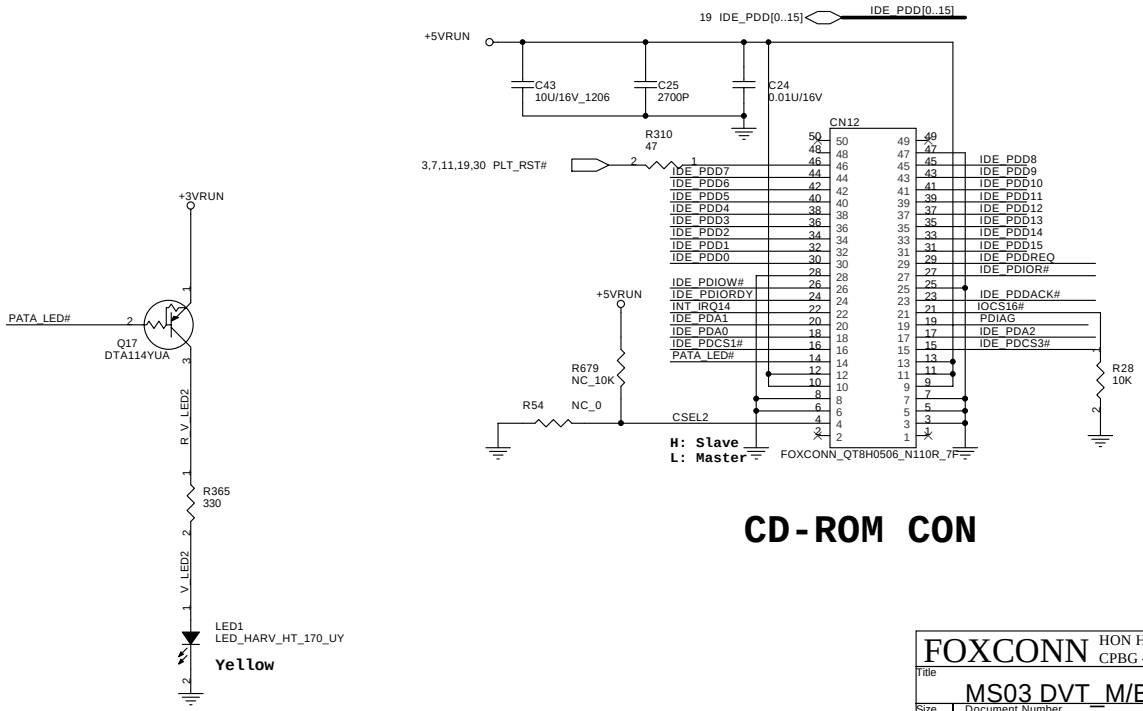


HDD CON



CD-ROM CON

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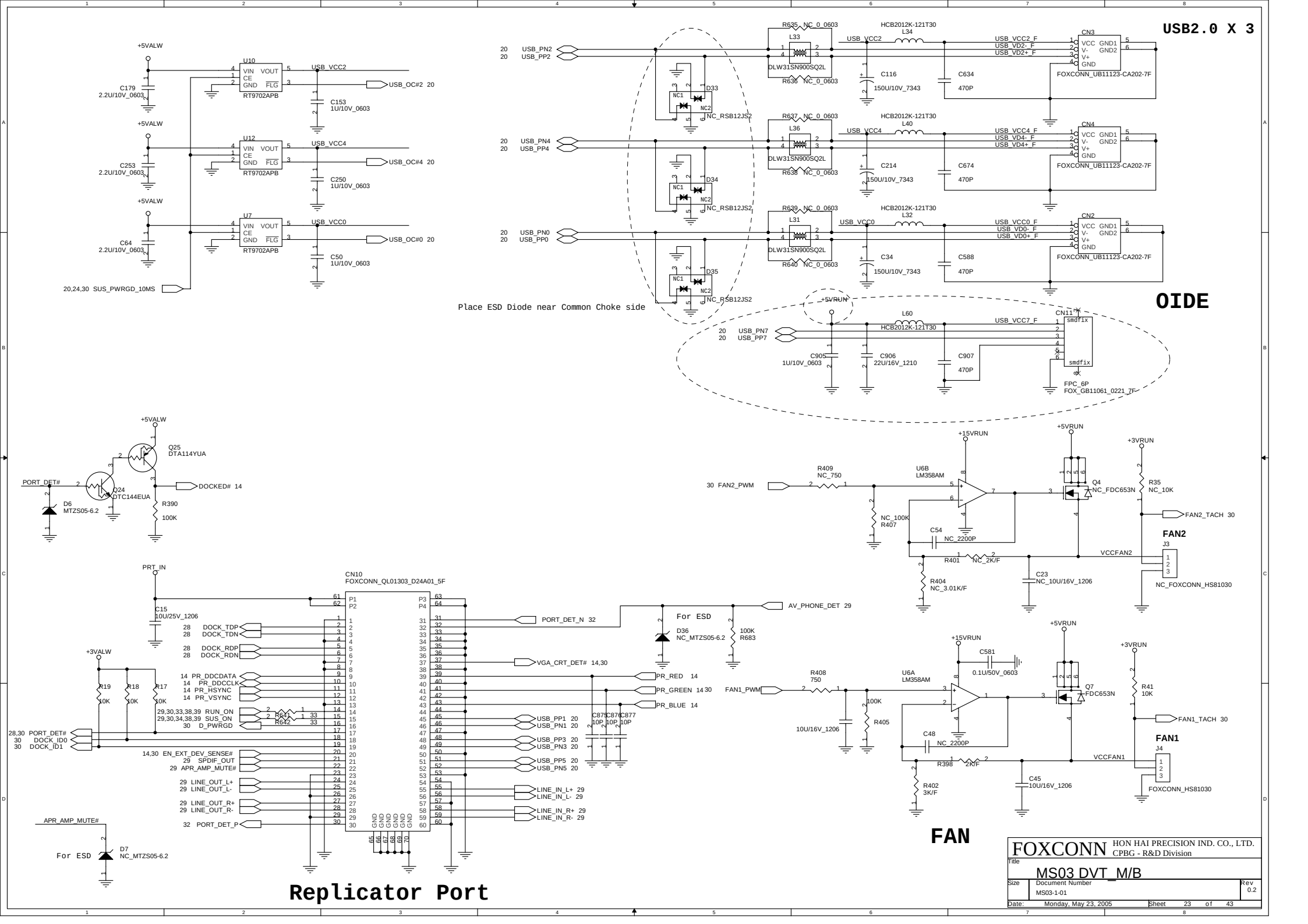
USB2.0 X 3
OIDE
Place ESD Diode near Common Choke side
FAN
Replicator Port
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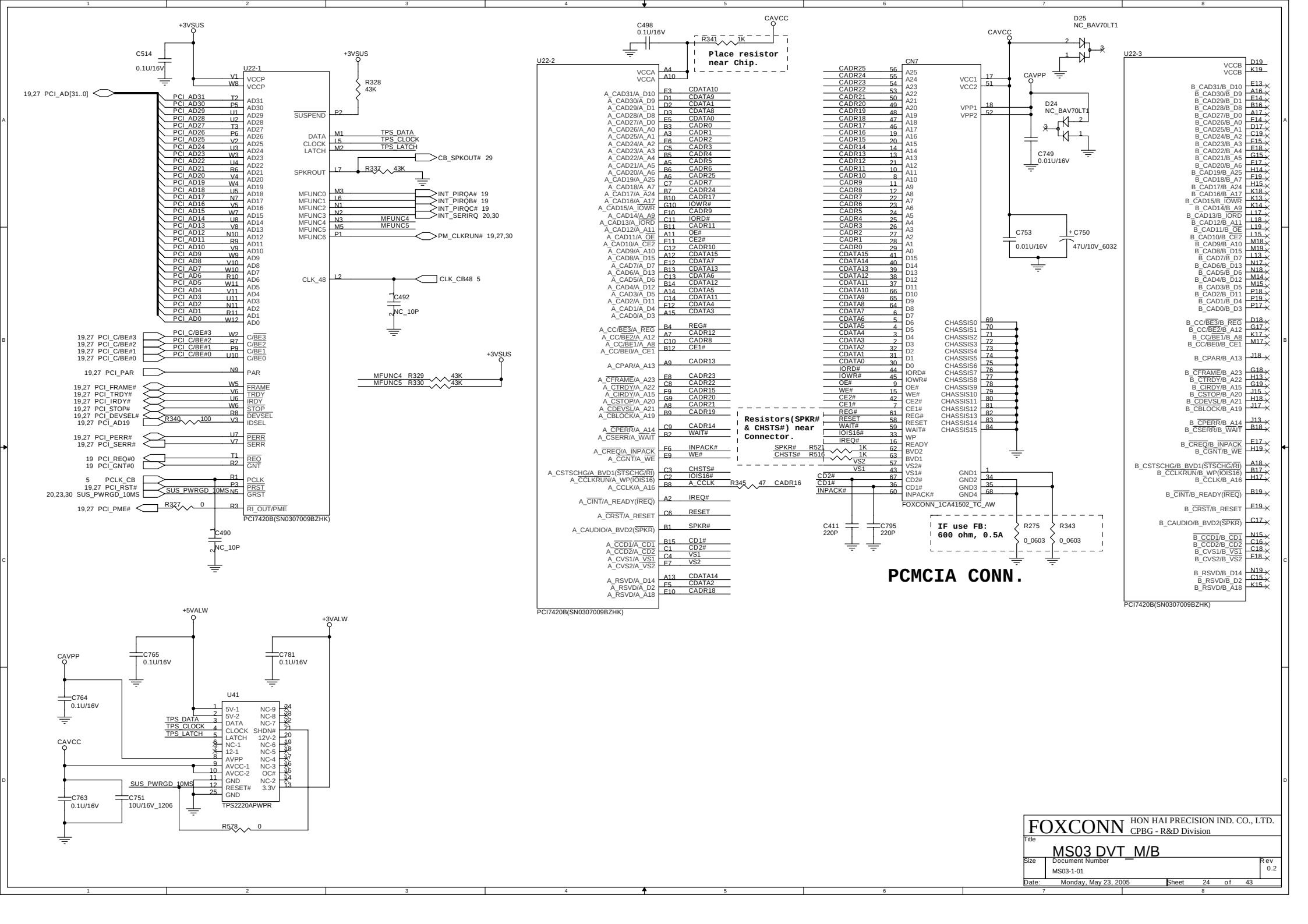

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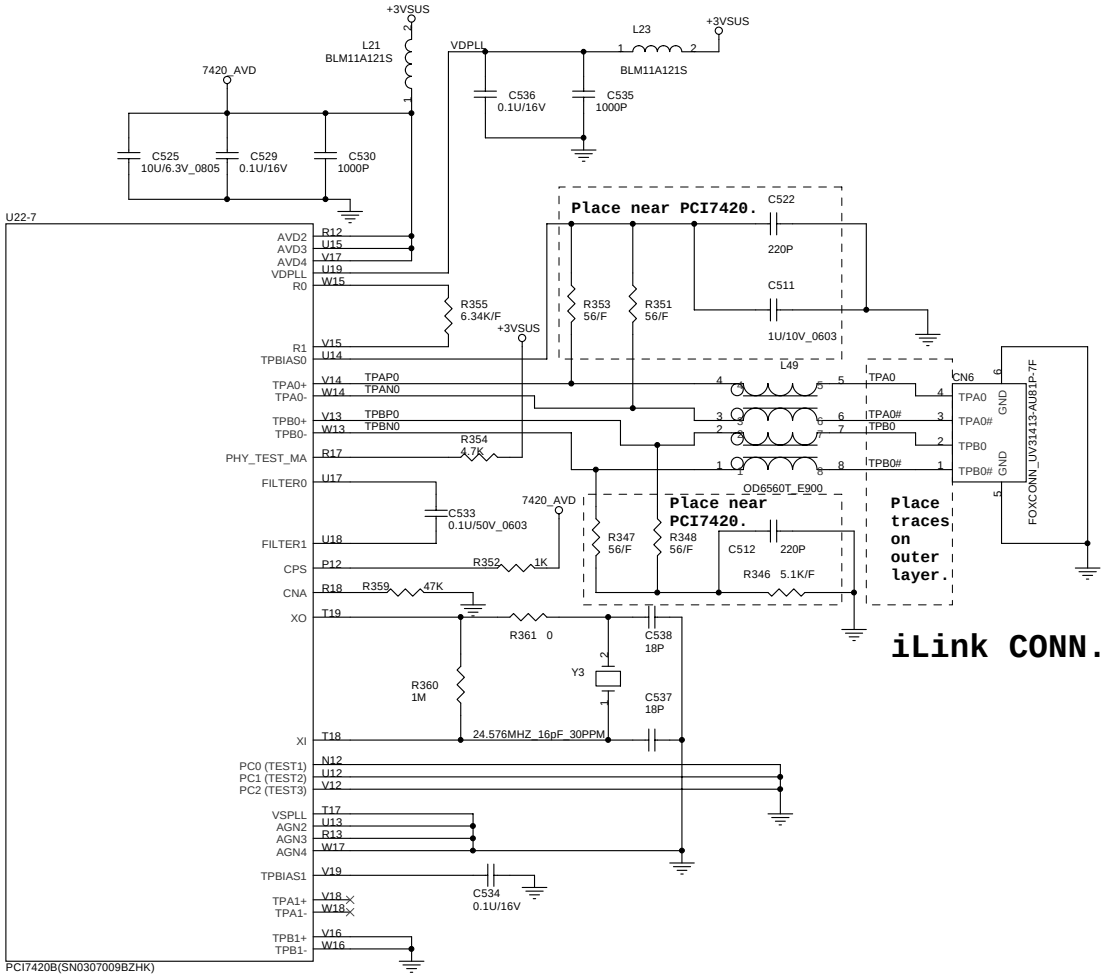
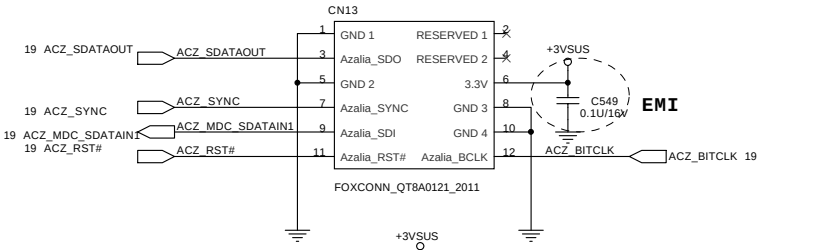
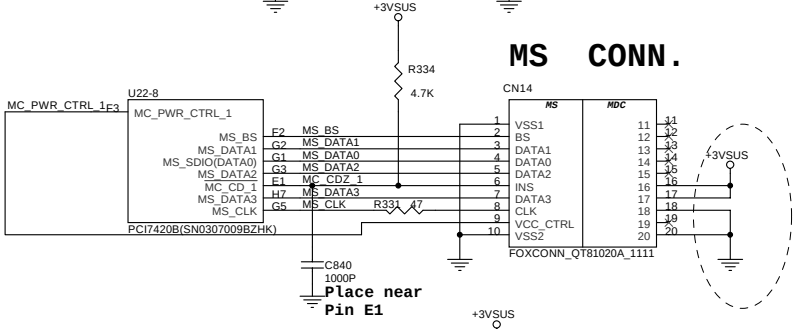
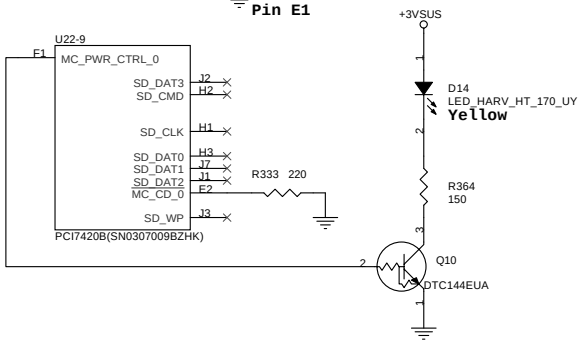
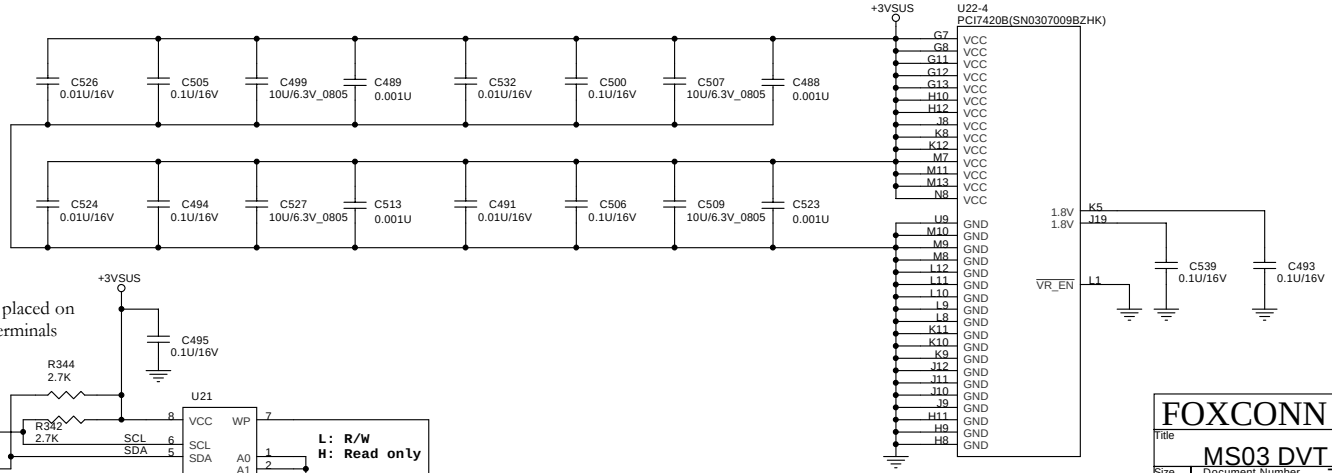




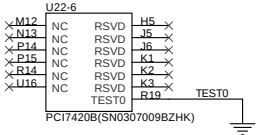


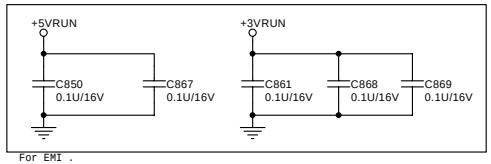


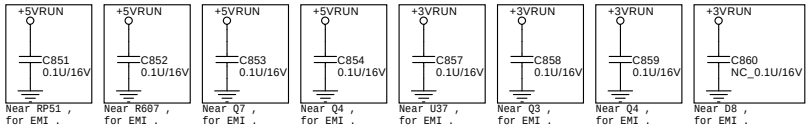
PCI7420 Power Terminals

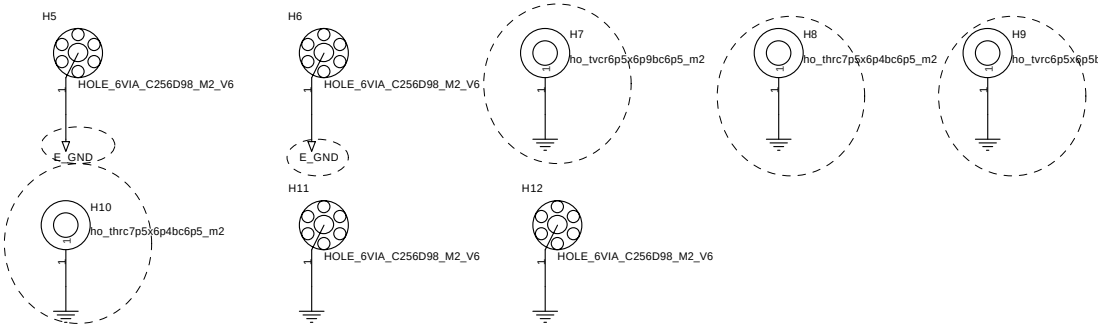


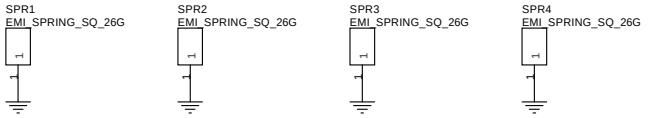
PCI7420 UNUSED TERMINALS

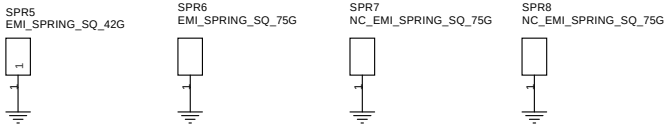


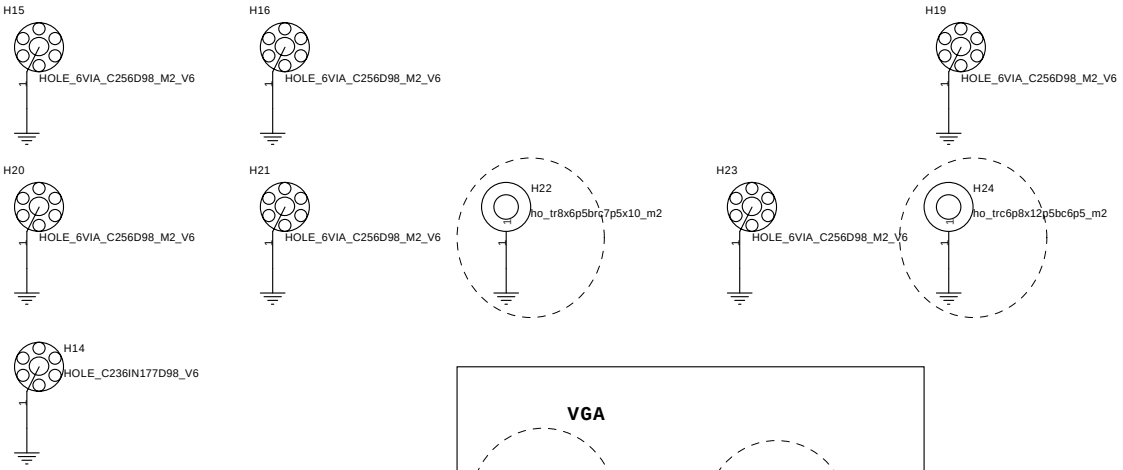


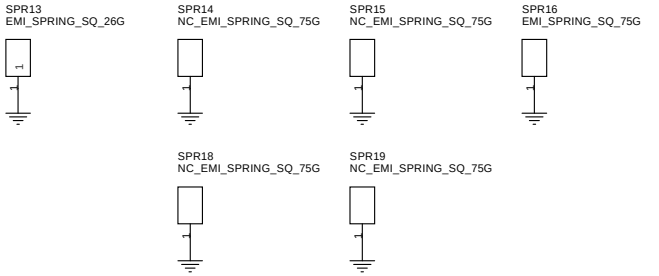


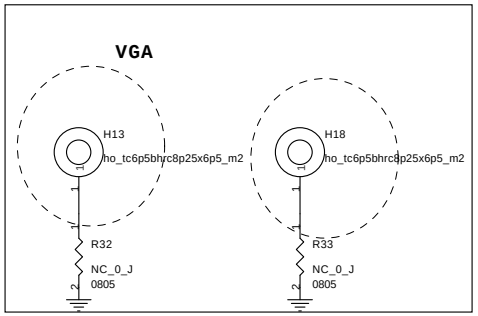


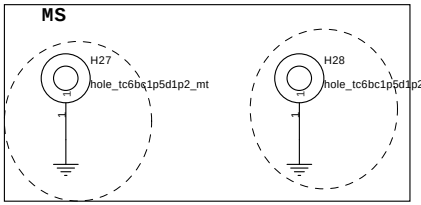


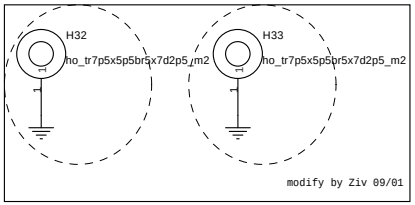


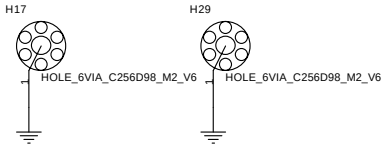


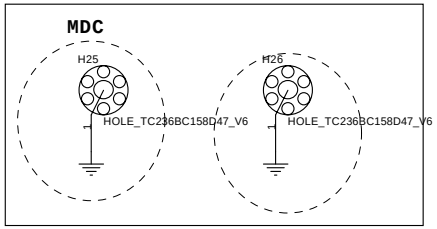












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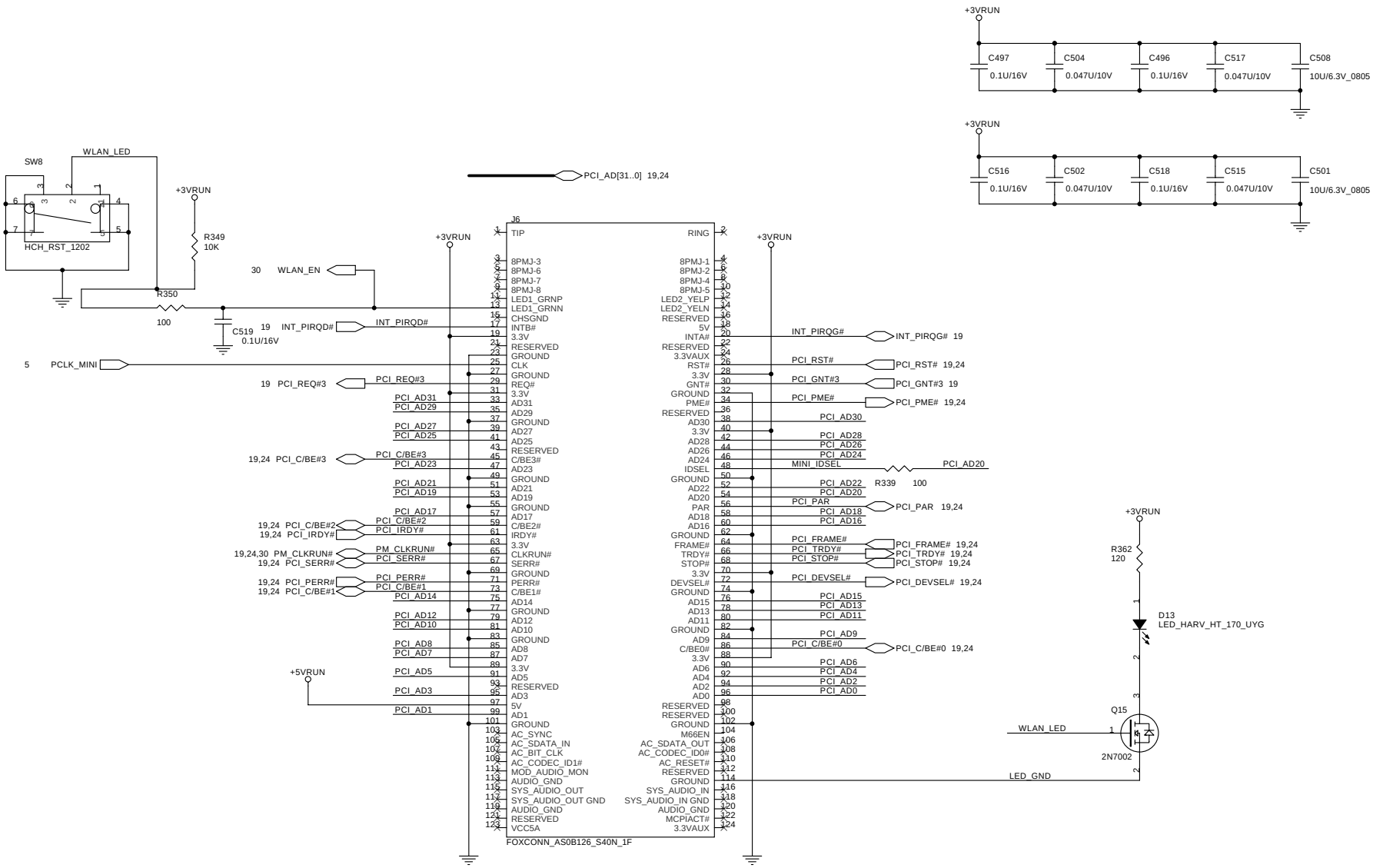

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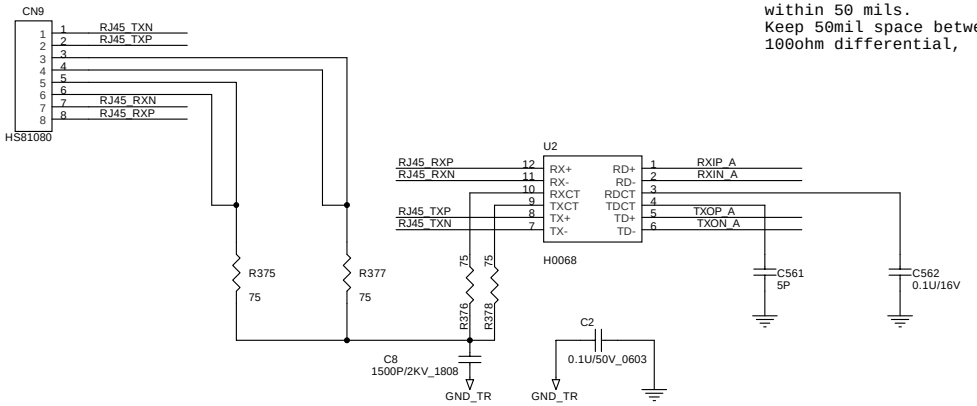
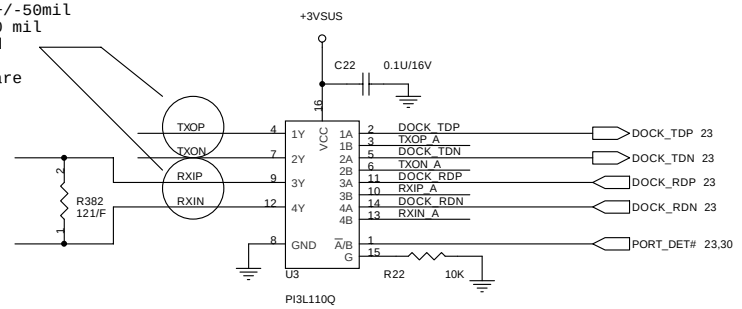
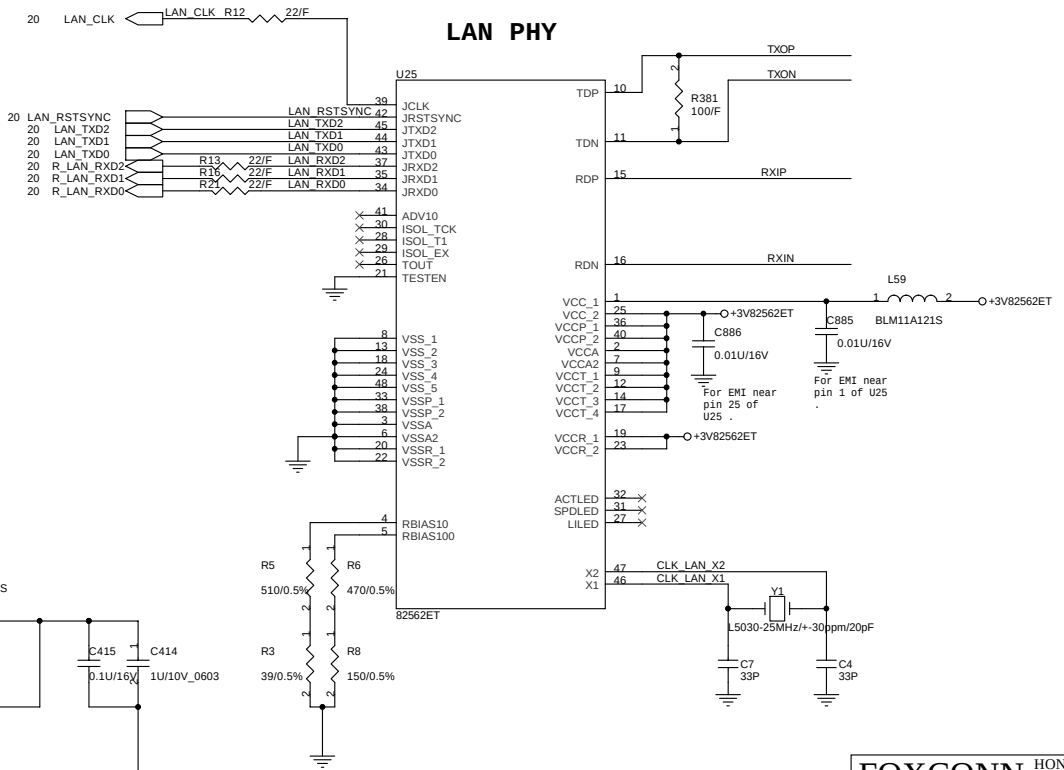
Match trace length

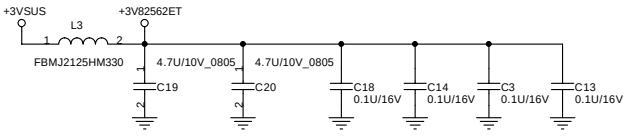
LAYOUT NOTES:
Match total length of chip side Rx and Tx pair traces +/-50mil
Match length of cable side Rx and Tx pair traces +/- 50 mil
Total line TX+ to TX- and RX- and RX+ should be matched within 50 mils.
Keep 50mil space between pairs and other traces.Pairs are 100ohm differential,

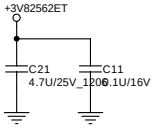


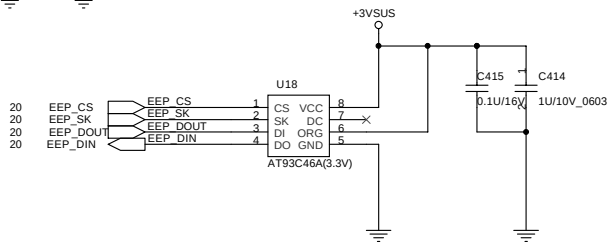


LAN PHY









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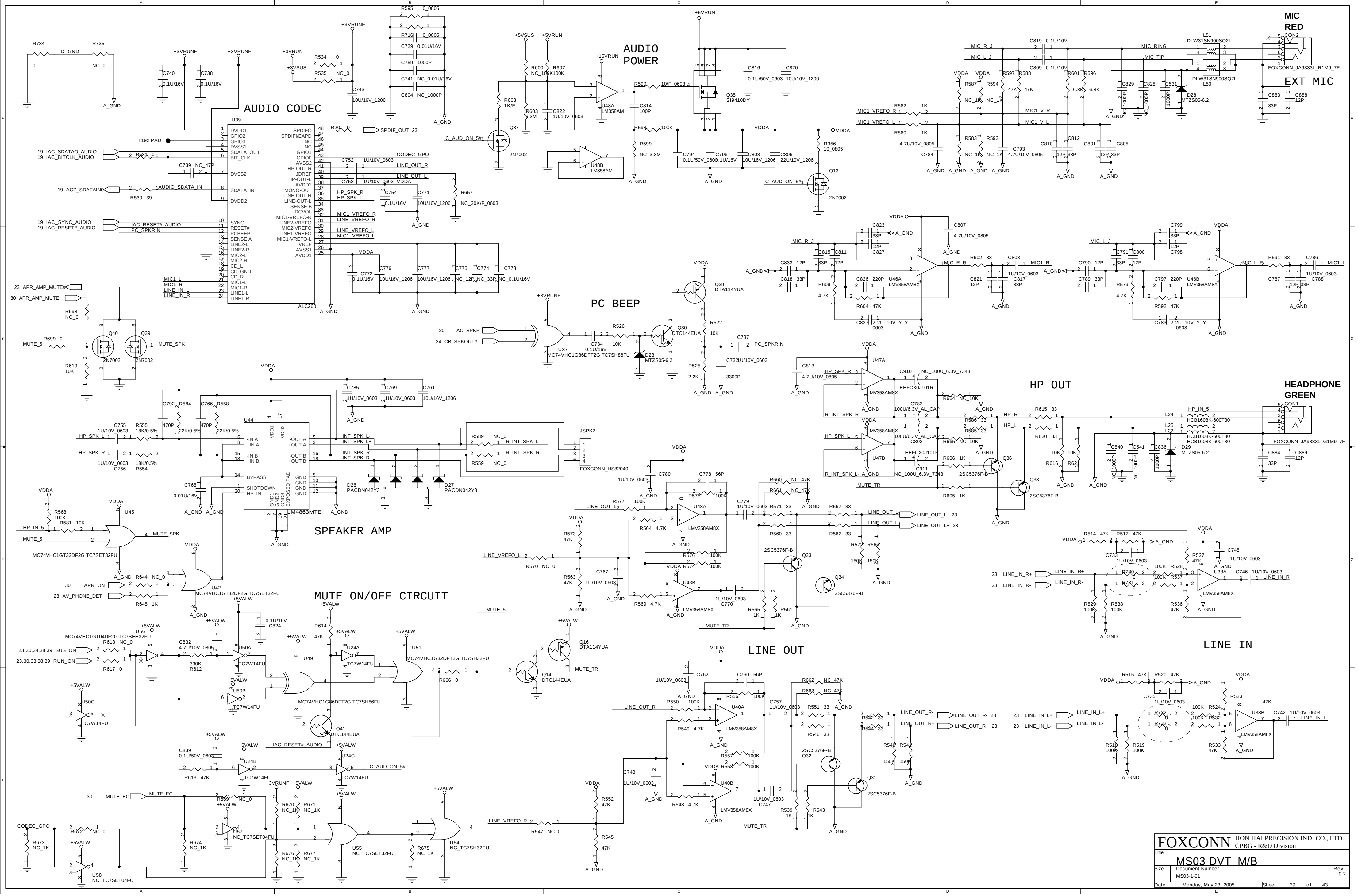

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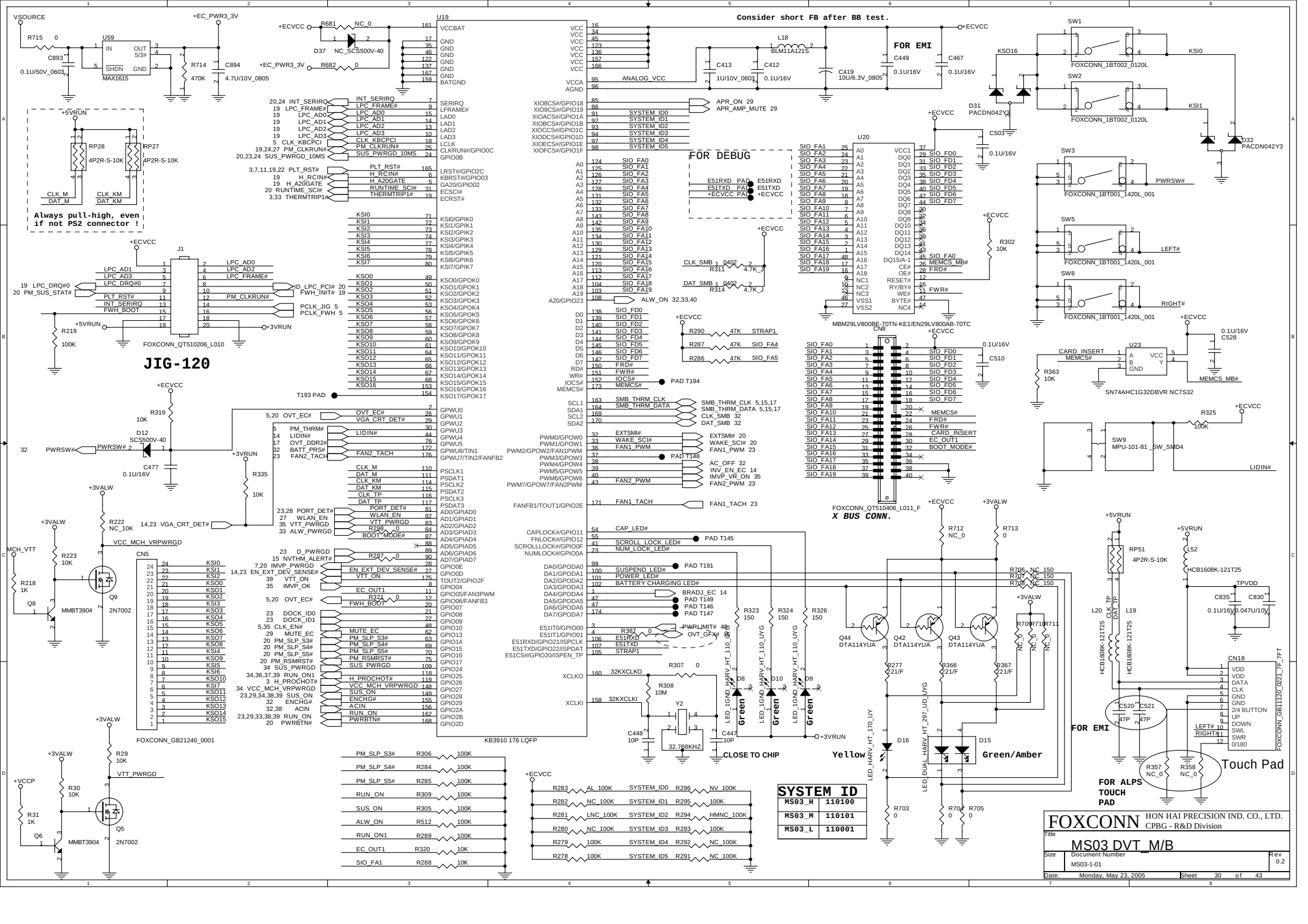
Consider short FB after BB test.
FOR EMI
FOR DEBUG
JIG-120
Always pull-high, even if not PS2 connector !
X BUS CONN.
CLOSE TO CHIP
Yellow
Green/Amber
Touch Pad
FOR ALPS TOUCH PAD
FOR EMI
SYSTEM ID
MS03_H 110100
MS03_M 110101
MS03_L 110001
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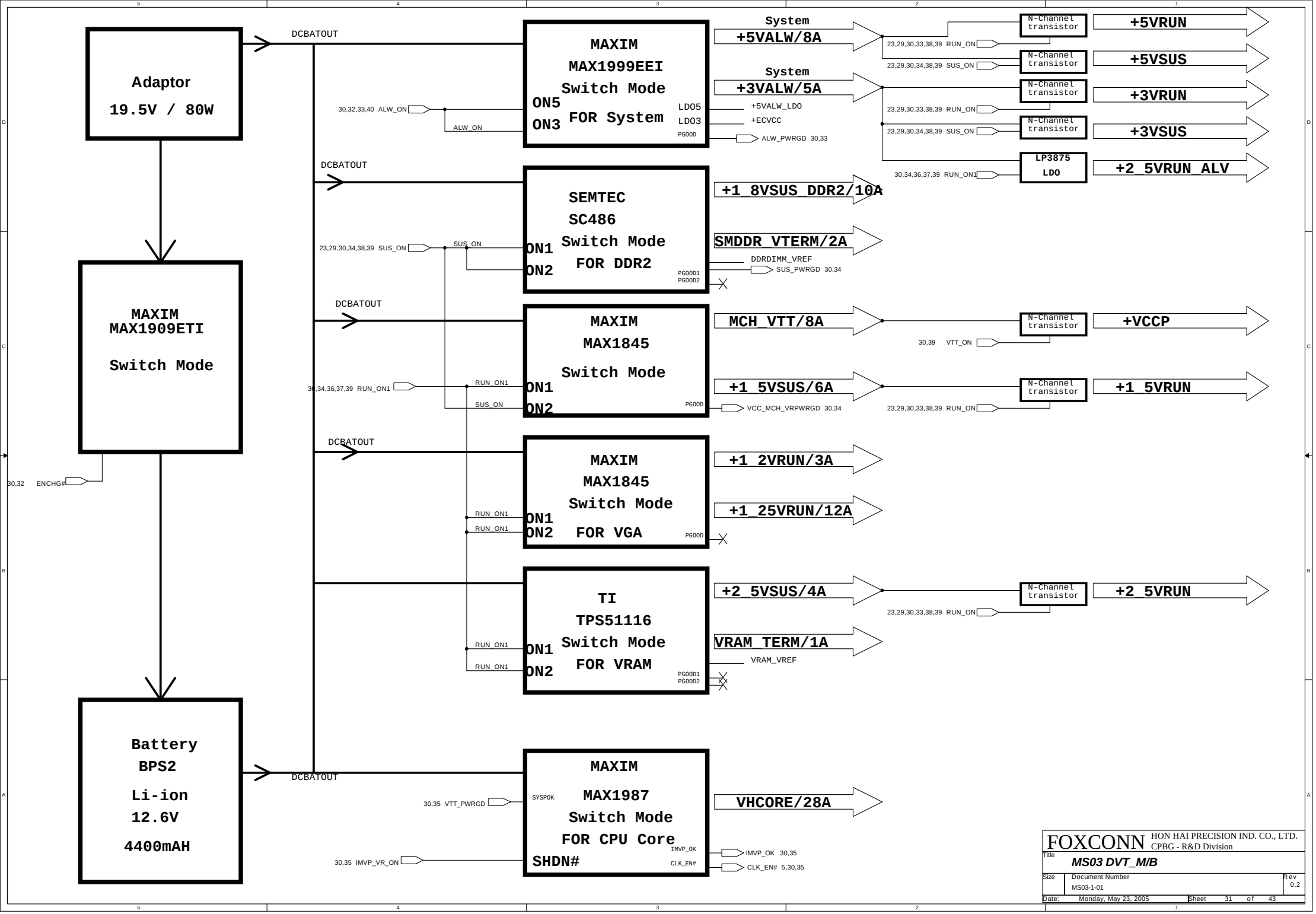
Adaptor
19.5V / 80W
MAXIM
MAX1909ETI
Switch Mode
Battery
BPS2
Li-ion
12.6V
4400mAH
30,32 ENCHG#
DCBATOUT
MAXIM
MAX1999EEI
Switch Mode
FOR System
ON5
ON3
30,32,33,40 ALW_ON
ALW_ON
LDO5
LDO3
PGOOD
+5VALW_LDO
+ECVCC
ALW_PWRGD 30,33
System
+5VALW/8A
System
+3VALW/5A
N-Channel transistor
23,29,30,33,38,39 RUN_ON
23,29,30,34,38,39 SUS_ON
+5VRUN
+5VSUS
+3VRUN
+3VSUS
LP3875
LDO
30,34,36,37,39 RUN_ON1
+2_5VRUN_ALV
SEMTEC
SC486
Switch Mode
FOR DDR2
ON1
ON2
23,29,30,34,38,39 SUS_ON
SUS_ON
+1_8VSUS_DDR2/10A
SMDDR_VTERM/2A
DDRDIMM_VREF
SUS_PWRGD 30,34
PGOOD1
PGOOD2
MAXIM
MAX1845
Switch Mode
ON1
ON2
RUN_ON1
SUS_ON
MCH_VTT/8A
30,39 VTT_ON
+VCCP
+1_5VSUS/6A
VCC_MCH_VRPWRGD 30,34
+1_5VRUN
MAXIM
MAX1845
Switch Mode
FOR VGA
ON1
ON2
RUN_ON1
+1_2VRUN/3A
+1_25VRUN/12A
TI
TPS51116
Switch Mode
FOR VRAM
ON1
ON2
+2_5VSUS/4A
+2_5VRUN
VRAM_TERM/1A
VRAM_VREF
MAXIM
MAX1987
Switch Mode
FOR CPU Core
SHDN#
SYSPOK
30,35 VTT_PWRGD
30,35 IMVP_VR_ON
VHCORE/28A
IMVP_OK
CLK_EN#
IMVP_OK 30,35
CLK_EN# 5,30,35
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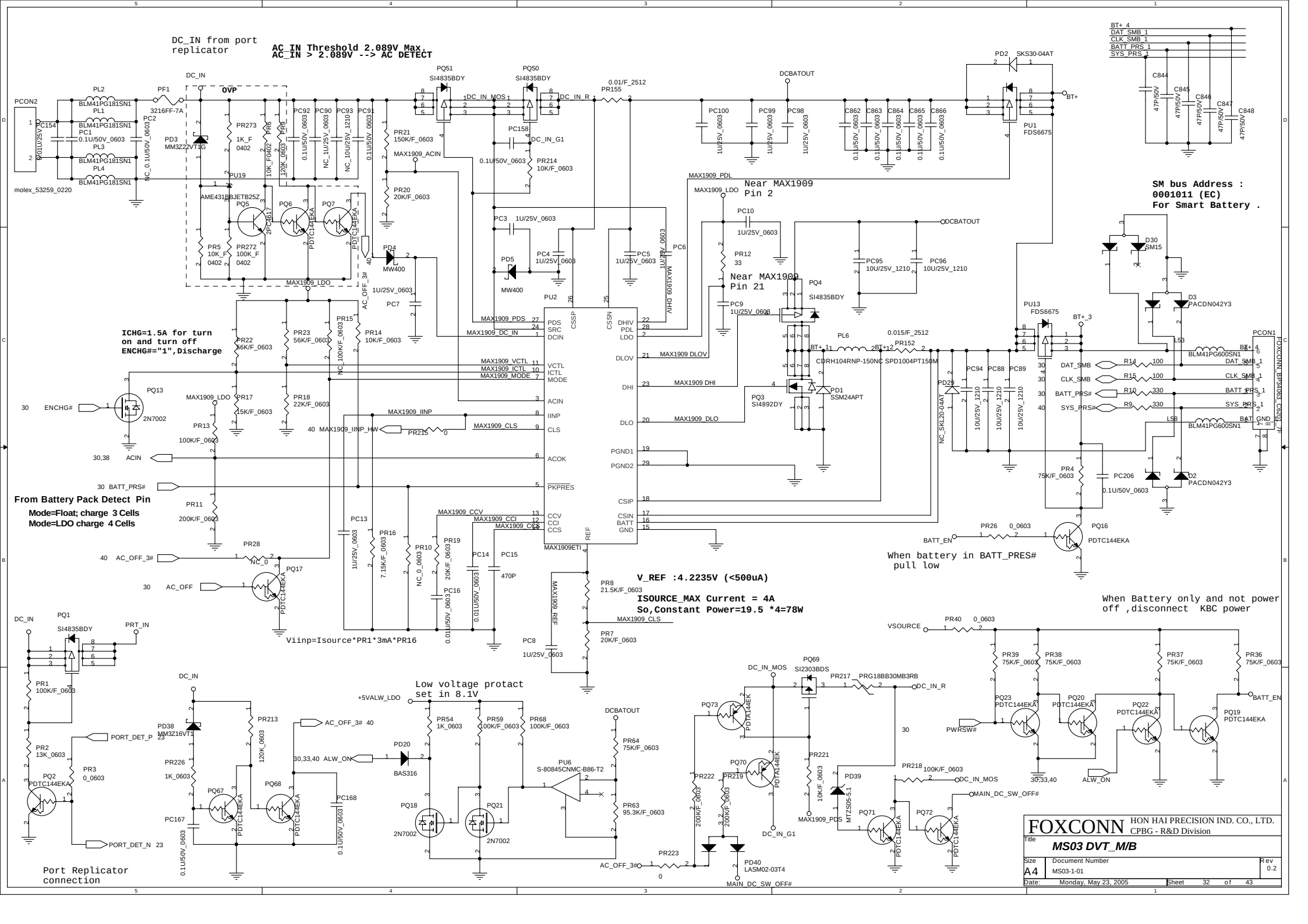
DC_IN from port replicator
AC_IN Threshold 2.089V Max.
AC_IN > 2.089V --> AC DETECT
DC_IN
OVP
PCON2
molex_53259_0220
PL2
PL1
PL3
PL4
BLM41PG181SN1
PF1
3216FF-7A
PC2
PC1
0.1U/50V_0603
0.01U/25V
NC_0.1U/50V_0603
PD3
MM3Z22VT1G
PR273
1K_F 0402
PR6
10K_F0402
PR7
120K_0603
PC92
PC90
PC93
PC91
0.1U/50V_0603
NC_1U/25V_0603
NC_10U/25V_1210
PR21
150K/F_0603
MAX1909_ACIN
PR20
20K/F_0603
PU19
AME431BBJETB25Z
PQ5
2PC4617
PQ6
PDTC144EKA
PQ7
PR5
10K_F 0402
PR272
100K_F 0402
PD4
MW400
AC_OFF_3#
40
1U/25V_0603
PC7
MAX1909_LDO
PQ51
SI4835BDY
PQ50
SI4835BDY
DC_IN_MOS
DC_IN_R
0.01/F_2512
PR155
PC158
DC_IN_G1
0.1U/50V_0603
PR214
10K/F_0603
DCBATOUT
PC100
PC99
PC98
1U/25V_0603
C862
C863
C864
C865
C866
0.1U/50V_0603
PD2
SKS30-04AT
BT+
PU1
FDS6675
BT+ 4
DAT_SMB_1
CLK_SMB_1
BATT_PRS_1
SYS_PRS_1
C844
C845
C846
C847
C848
47P/50V
MAX1909_PDL
Near MAX1909 Pin 2
MAX1909_LDO
PC10
1U/25V_0603
PR12
33
Near MAX1909 Pin 21
PC3
PC4
PC5
PC6
PD5
MW400
PU2
MAX1909 DHIV
PQ4
SI4835BDY
PC95
PC96
10U/25V_1210
PC9
1U/25V_0603
SM bus Address :
0001011 (EC)
For Smart Battery .
D30
SM15
D3
PACDN042Y3
PU13
FDS6675
BT+_3
PL6
0.015/F_2512
PR152
CDRH104RNP-150NC SPD1004PT150M
PCON1
FOXCONN_BP34063_C6201_7F
L53
L58
BLM41PG600SN1
DAT_SMB
CLK_SMB
BATT_PRS#
SYS_PRS#
BAT_GND
R14
R15
R10
R9
100
330
D2
PACDN042Y3
PR4
75K/F_0603
PC206
0.1U/50V_0603
PQ16
PDTC144EKA
PR26
0_0603
BATT_EN
When battery in BATT_PRES# pull low
ICHG=1.5A for turn on and turn off
ENCHG#="1",Discharge
PQ13
2N7002
ENCHG#
30
PR22
56K/F_0603
PR23
56K/F_0603
PR15
NC_100K/F_0603
PR14
10K/F_0603
PR17
15K/F_0603
PR18
22K/F_0603
PR13
100K/F_0603
ACIN
30,38
BATT_PRS#
30
From Battery Pack Detect Pin
Mode=Float; charge 3 Cells
Mode=LDO charge 4 Cells
PR11
200K/F_0603
PR28
NC_0
PQ17
PDTC144EKA
AC_OFF
30
40
AC_OFF_3#
MAX1909_PDS
MAX1909_DC_IN
MAX1909_VCTL
MAX1909_ICTL
MAX1909_MODE
MAX1909_IINP
MAX1909_IINP_HW
MAX1909_CLS
MAX1909_CCV
MAX1909_CCI
MAX1909_CCS
MAX1909ET1
PR215
0
PC13
PR16
7.15K/F_0603
PR10
NC_0_0603
PR19
20K/F_0603
PC14
PC15
470P
PC16
0.01U/50V_0603
PC8
1U/25V_0603
PR8
21.5K/F_0603
MAX1909_REF
PR7
20K/F_0603
MAX1909_CLS
PDS
SRC
DCIN
VCTL
ICTL
MODE
ACIN
IINP
CLS
ACOK
PKPRES
CCV
CCI
CCS
REF
CSSP
CSSN
DHIV
PDL
LDO
DLOV
DHI
DLO
PGND1
PGND2
CSIP
CSIN
BATT
GND
MAX1909 DLOV
MAX1909 DHI
MAX1909_DLO
PQ3
SI4892DY
PD1
SSM24APT
CDRH104RNP-150NC
NC_SKL20-04AT
PD29
PC94
PC88
PC89
10U/25V_1210
V_REF :4.2235V (<500uA)
ISOURCE_MAX Current = 4A
So,Constant Power=19.5 *4=78W
Viinp=Isource*PR1*3mA*PR16
When Battery only and not power off ,disconnect KBC power
VSOURCE
PR40
0_0603
PR39
PR38
PR37
PR36
75K/F_0603
PQ23
PQ20
PQ22
PQ19
PDTC144EKA
PWRSW#
30,33,40
ALW_ON
BATT_EN
PQ1
SI4835BDY
PRT_IN
PR1
100K/F_0603
PR2
13K_0603
PR3
0_0603
PQ2
PDTC144EKA
PORT_DET_P
PORT_DET_N
23
Port Replicator connection
PD38
MM3Z16VT1
PR213
120K_0603
PR226
1K_0603
PC167
0.1U/50V_0603
PQ67
PQ68
PC168
AC_OFF_3# 40
30,33,40 ALW_ON
Low voltage protact set in 8.1V
+5VALW_LDO
PR54
1K_0603
PR59
100K/F_0603
PR68
100K/F_0603
PD20
BAS316
PQ18
PQ21
2N7002
PU6
S-80845CNMC-B86-T2
PR64
75K/F_0603
PR63
95.3K/F_0603
AC_OFF_3#
PR223
0
MAIN_DC_SW_OFF#
PD40
LASM02-03T4
PQ69
SI2303BDS
PR217
PRG18BB30MB3RB
PQ73
PQ70
PR222
PR219
200K/F_0603
PR221
10K/F_0603
MAX1909_PDS
PD39
MTZ5V6-5.1
PR218
100K/F_0603
MAIN_DC_SW_OFF#
PQ71
PQ72
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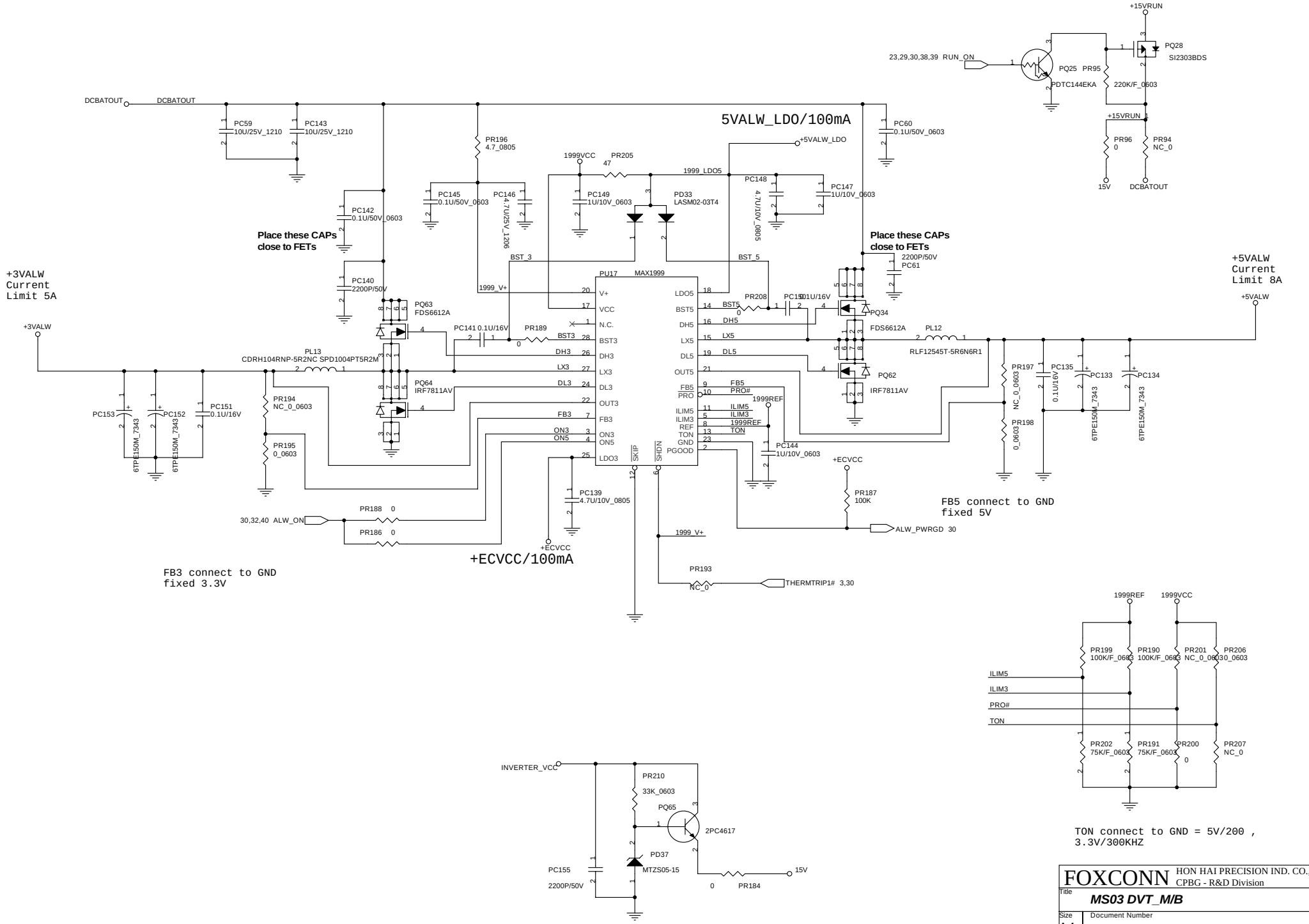

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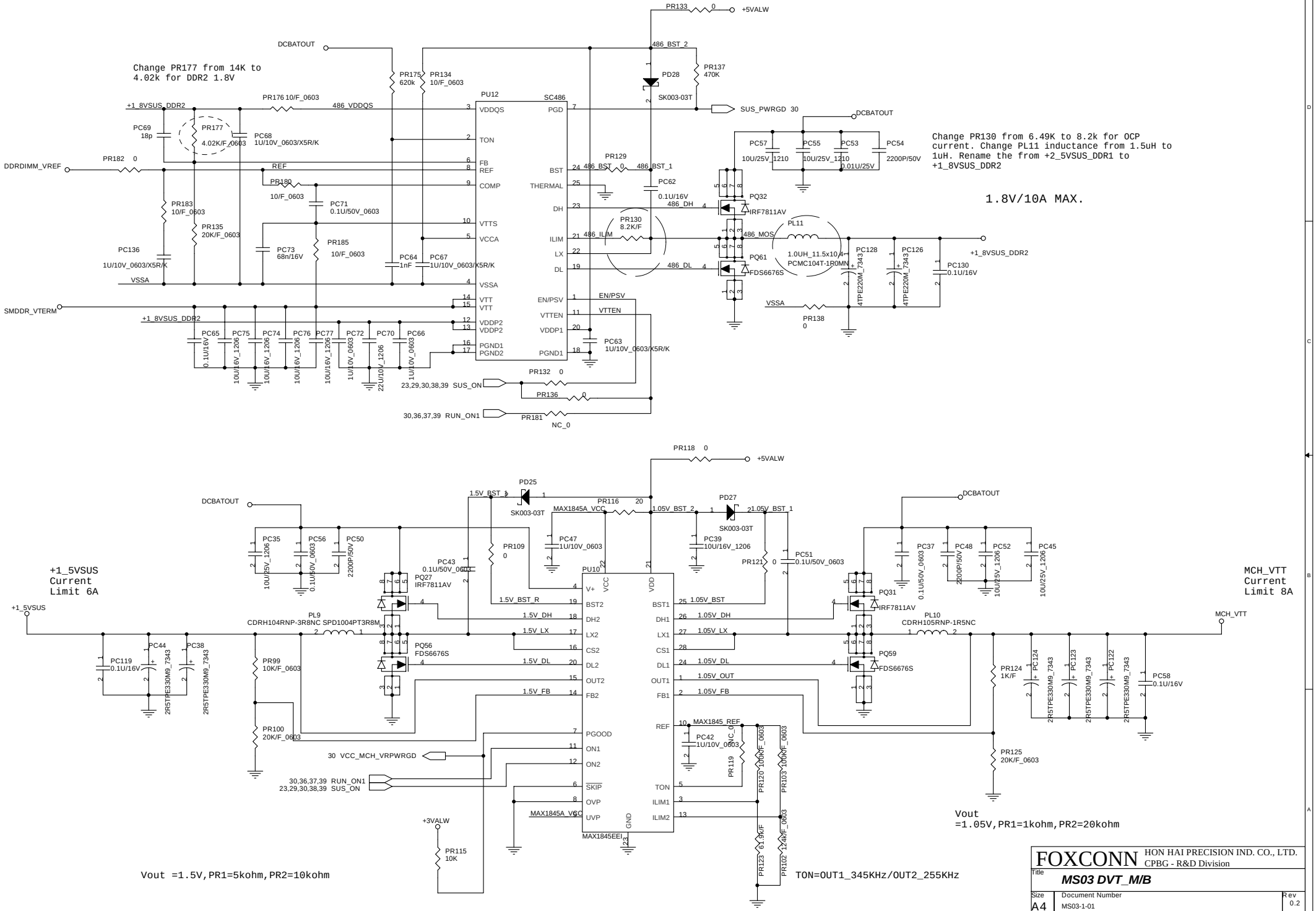
Change PR177 from 14K to 4.02k for DDR2 1.8V
Change PR130 from 6.49K to 8.2k for OCP current. Change PL11 inductance from 1.5uH to 1uH. Rename the from +2_5VSUS_DDR1 to +1_8VSUS_DDR2
1.8V/10A MAX.
+1_5VSUS Current Limit 6A
MCH_VTT Current Limit 8A
Vout =1.5V,PR1=5kohm,PR2=10kohm
Vout =1.05V,PR1=1kohm,PR2=20kohm
TON=OUT1_345KHz/OUT2_255KHz
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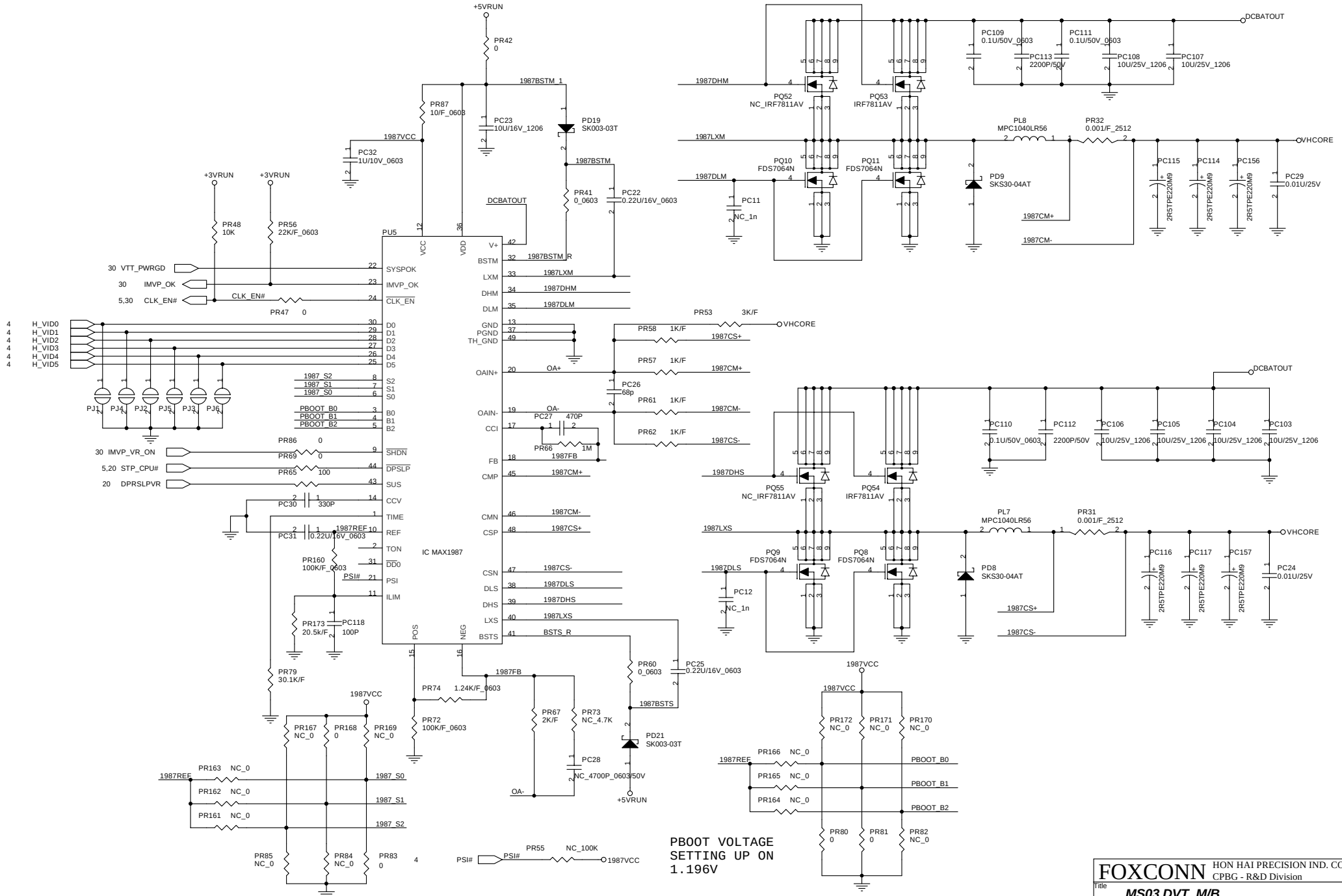

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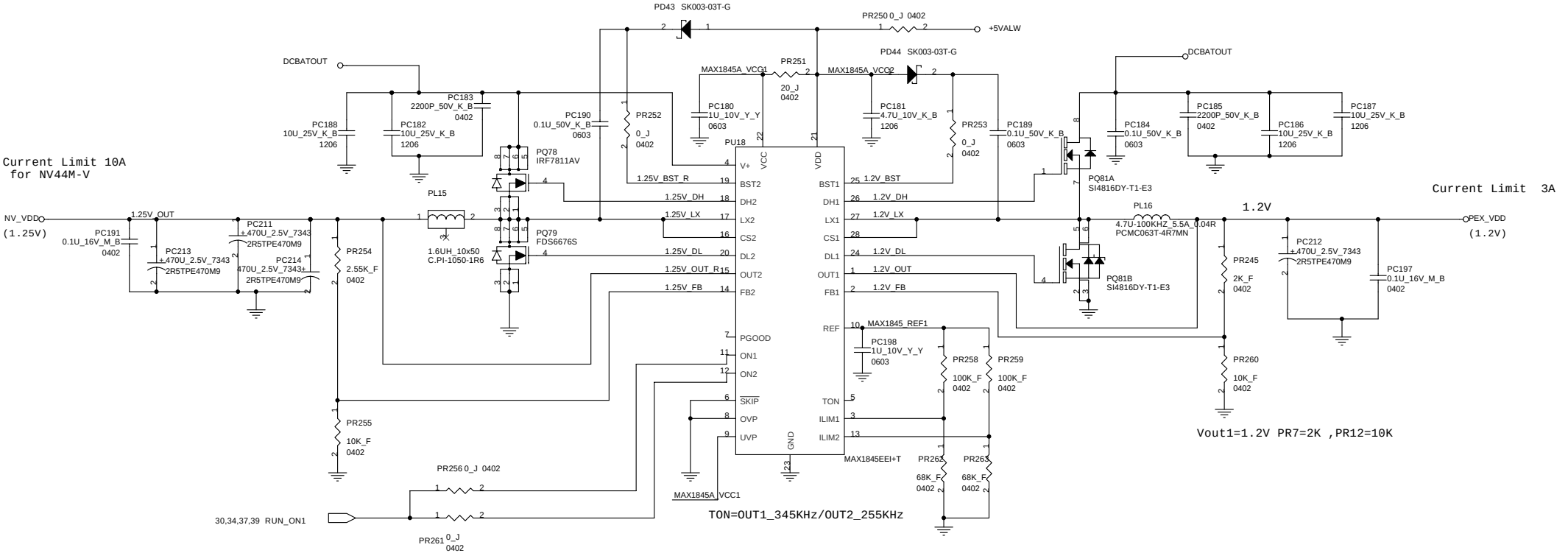

Power daughter board for VGA

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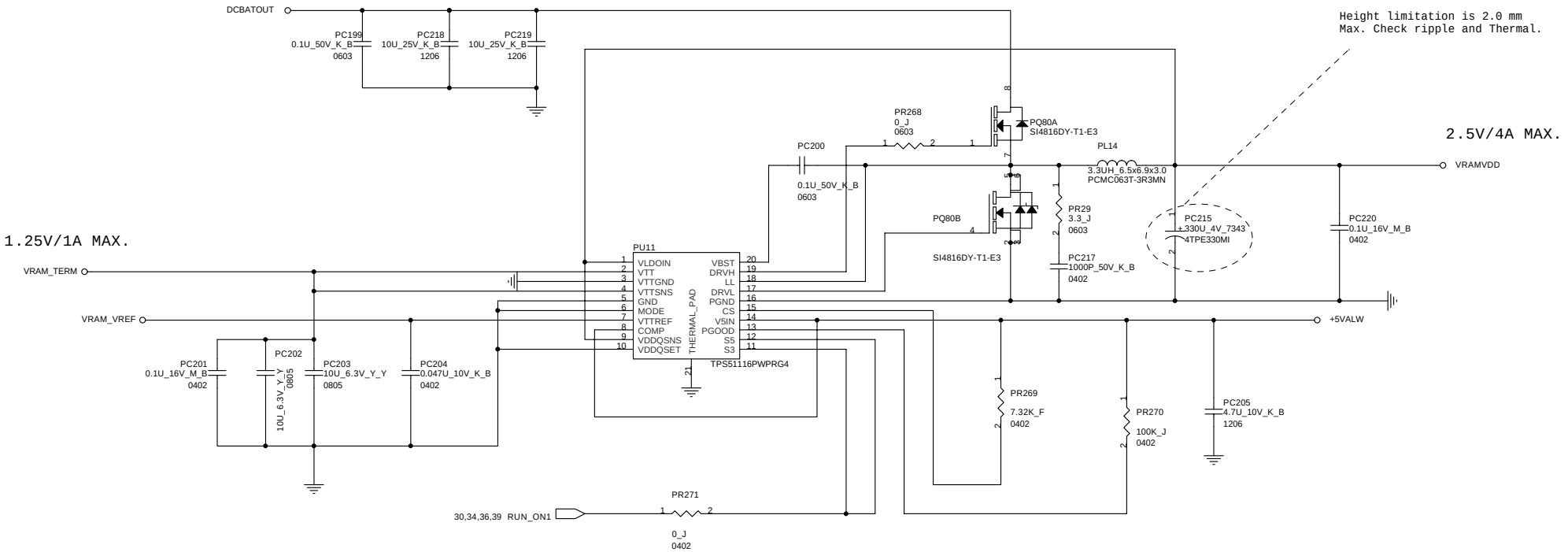

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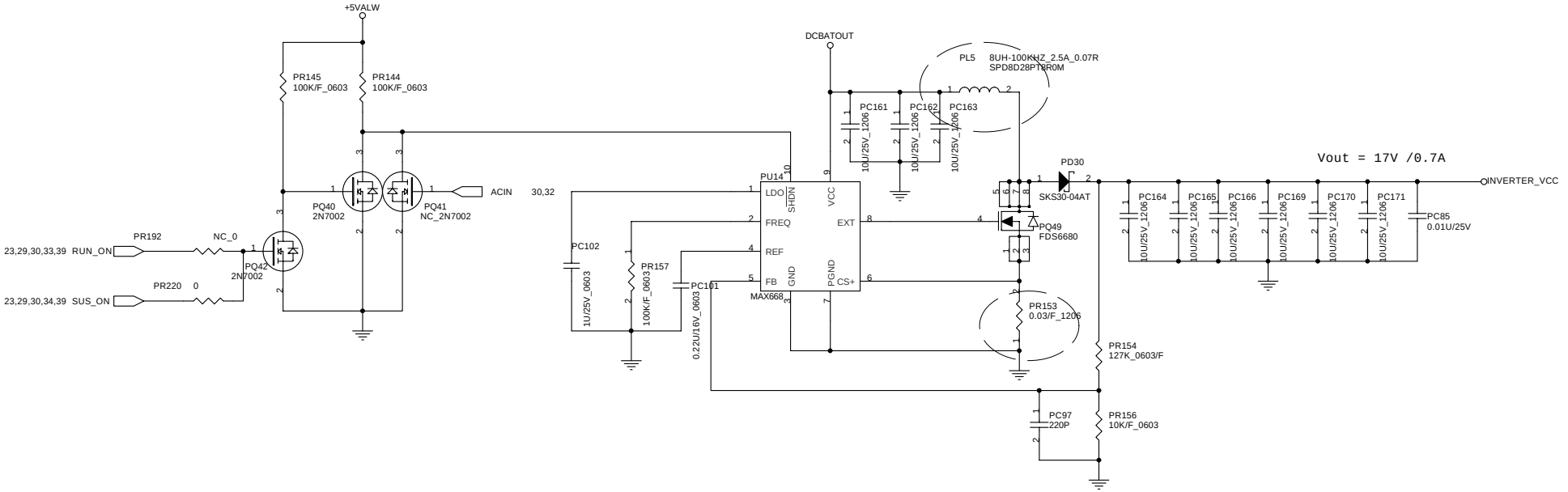
Change PL5 and PR153 for improving the step-up circuit



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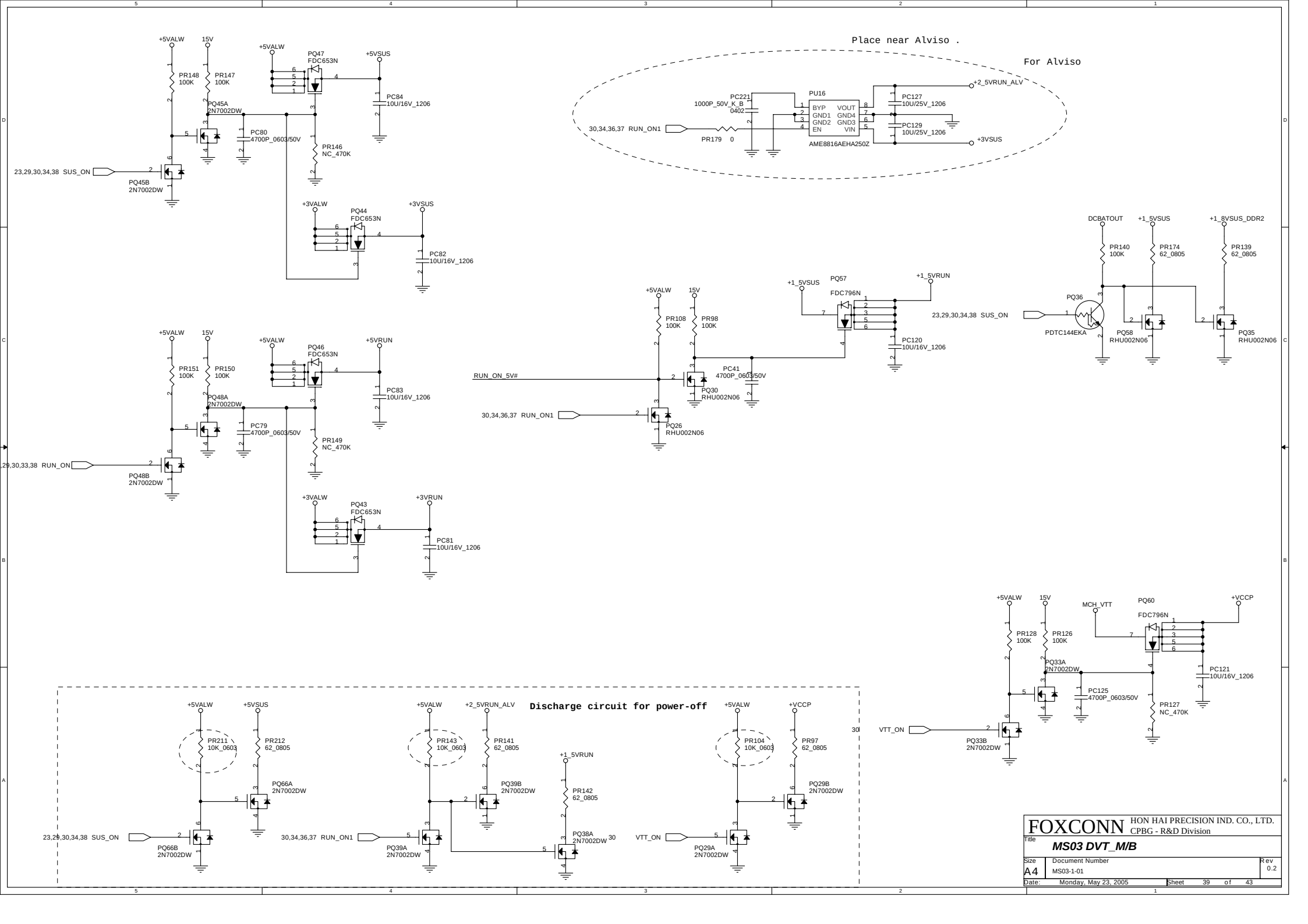

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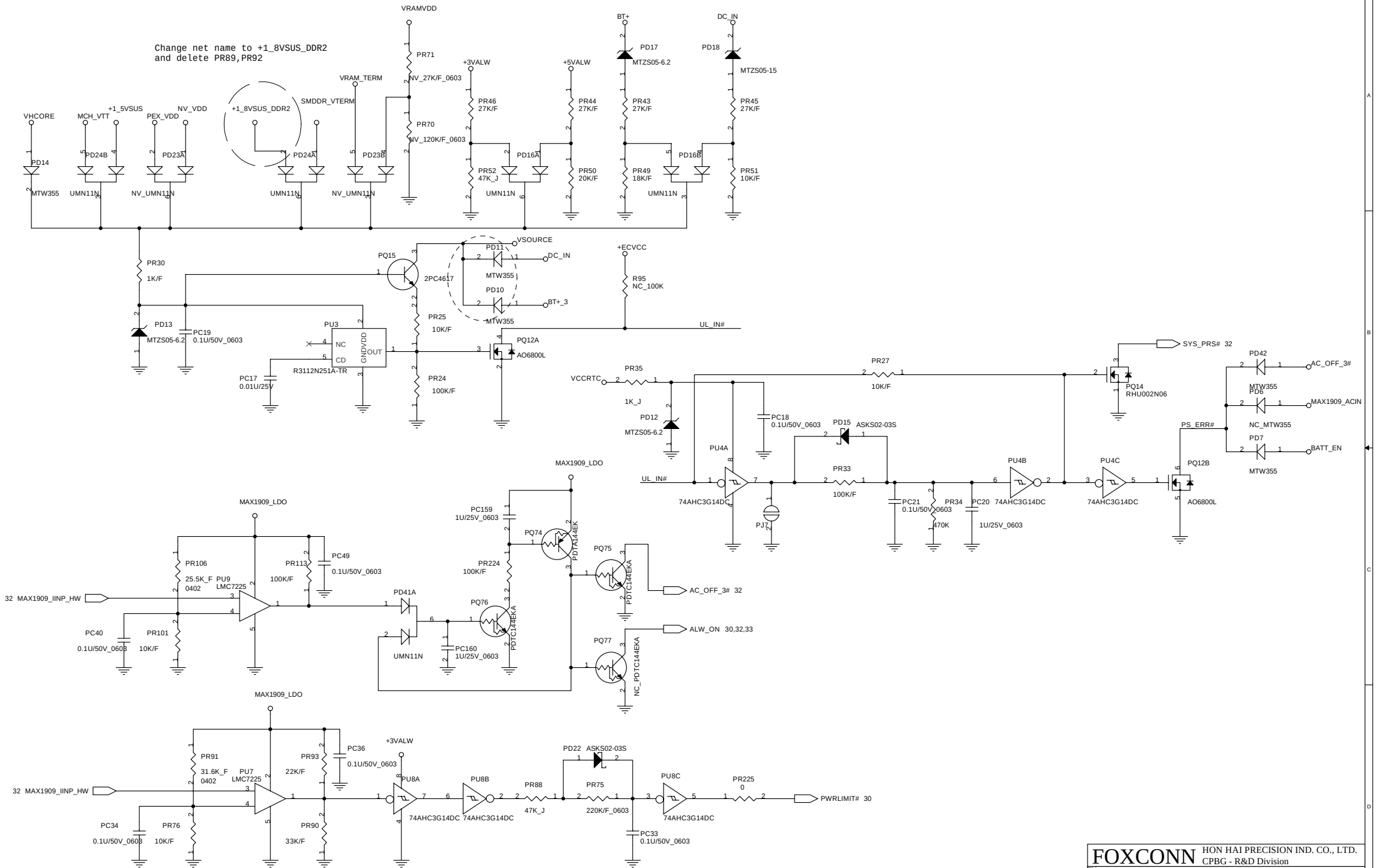

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HISTORY

EVT board start .

(2005/03/31)

P14: Change D1 from NC to mount.

P15: Change R115 from mount to NC.

(2005/04/01)

P40: Rotate PD10, PD11.

(2005/04/12)

P14: Correct LVDS Bus relative of connection (swap Data+/Data-, CLK+/CLK-).

P25: Change CN14 pin16, pin17 from NC to +3VSUS, change CN14 pin18, pin20 from NC to GND.

P30: Delete SW7, Q46, R396, R721.

(2005/04/25)

P12: Add T195 for measure VRAM Timing.

(2005/04/26)

P14: Delete the right side dot of L1.

(2005/04/29)

P14: Change Q22, Q23 from 2N7002 to BSS138.

(2005/05/04)

P14: Change R694, R695, R696, R697 from 2.2K to 0 ohm.

(2005/05/09)

P26: Change H14 part from HOLE_6VIA_C256IN177D98_M2_V6(6.5mm) to HOLE_C236IN177D98_V6(6.0mm).

P15: Change R701 from NC to mount, change R702 from mount to NC.

(2005/05/10)

P32: Change PD3 from P4SSMJ24APT to MM3Z22VT1G.

P32: Change PQ5 from PDTC144EKA to 2PC4617.

P32: Change PR5 from 75K/F_0603 to 10K/F_0402.

P32: Change PR6 from 120K_0603 to 10K/F_0402.

P32: Add PU19 AME, AME431BBJETB25Z.

P32: Add PR272 100K/F_0402.

P32: Add PR273 1K/F_0402.

P36: Change PR262 from 15.8K/F_0402 to 68K/F_0402.

P36: Change PR263 from 40.2K/F_0402 to 68K/F_0402.

P36: Change PL15 footprint from CHOKE_3P_348_409X409_2 to CHOKE_3P_315_394X394_2.

P36: Delete PJ11, PJ12.

P37: Delete PJ13, PJ14.

P39: Delete PJ8, PR178.

P39: Add PC221 1000P_50V_0402.

P39: Change PU16 from LP3875 to AME8816AEHA250Z.

P05: Change R478, R481 from 33ohm to 56ohm for rising edge rate and falling edge rate.

P05: Change C696, C698, C700, C701, C702, C706 from NC to 18pF (EMI solution).

P22: Add C730, C731 (EMI solution).

P22: Connect CN17.20, CN17.32, CN17.44 to GND.

P26: Change SPR7, SPR15 from mount to NC (EMI solution).

(2005/05/11)

P23: Add D33,D34 and D35 for ESD(Not mount).

P36: Change PL15 footprint from CHOKE_3P_348_409X409_2 to L_3P_315_394X394.

(2005/05/13)

P26: Change H25, H26 footprint from ho_tc236bc158d47_v6 to ho_tc236bc158d47_mt.

P26: Change H27, H28 footprint from ho_tc1p5bc6d1p2_m2 to ho_tc1p5bc6d1p2_mb.

(2005/05/16)

P02: Update symbol ahead table, add LNC_ and HMNC_.

P30: Change R280 from mount to NC.

P30: Change R281 from AL_100K to LNC_100K.

P30: Change R293 from NC to mount.

P30: Change R294 from NV_100K to HMNC_100K.

(2005/05/17)

P26: Add R32, R33 NC_0_J (EMI solution).

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HISTORY

(2005/05/18)

P29: Change C783, C837 from 1U/10V_0603 to 2.2U_10V_Y_Y.

P26: Change H27, H28 from ho_tc1p5bc6d1p2_m2(ho_tc1p5bc6d1p2_mb) to hole_tc6bc1p5d1p2_mt(ho_tc6bc1p5d1p2_mt).

P40: Change PR91 from 31.6K to 25.5K, change PR109 from 49.9K to 31.6K.

P29: Delete R611, R622, R628, R629, R647, R648, R649, R650, JSPK1.

P29: Add C910, C911 NC_100U_6.3V_7343.

(2005/05/19)

P29: Add R734,R735 for anti-digital noise interference.

(2005/05/20)

P30: Change R311, R314 from 10K to 4.7K_J.

P29: Change R735 from mount to NC.

P05: Change R481 from 56ohm to 47ohm.

P05: Change R478 from 56ohm to 47ohm.

Revision History

Doc. Rev.	ECN No.:	Sch. Page	Rev.Change From	Rev.Change To	Description of Change

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