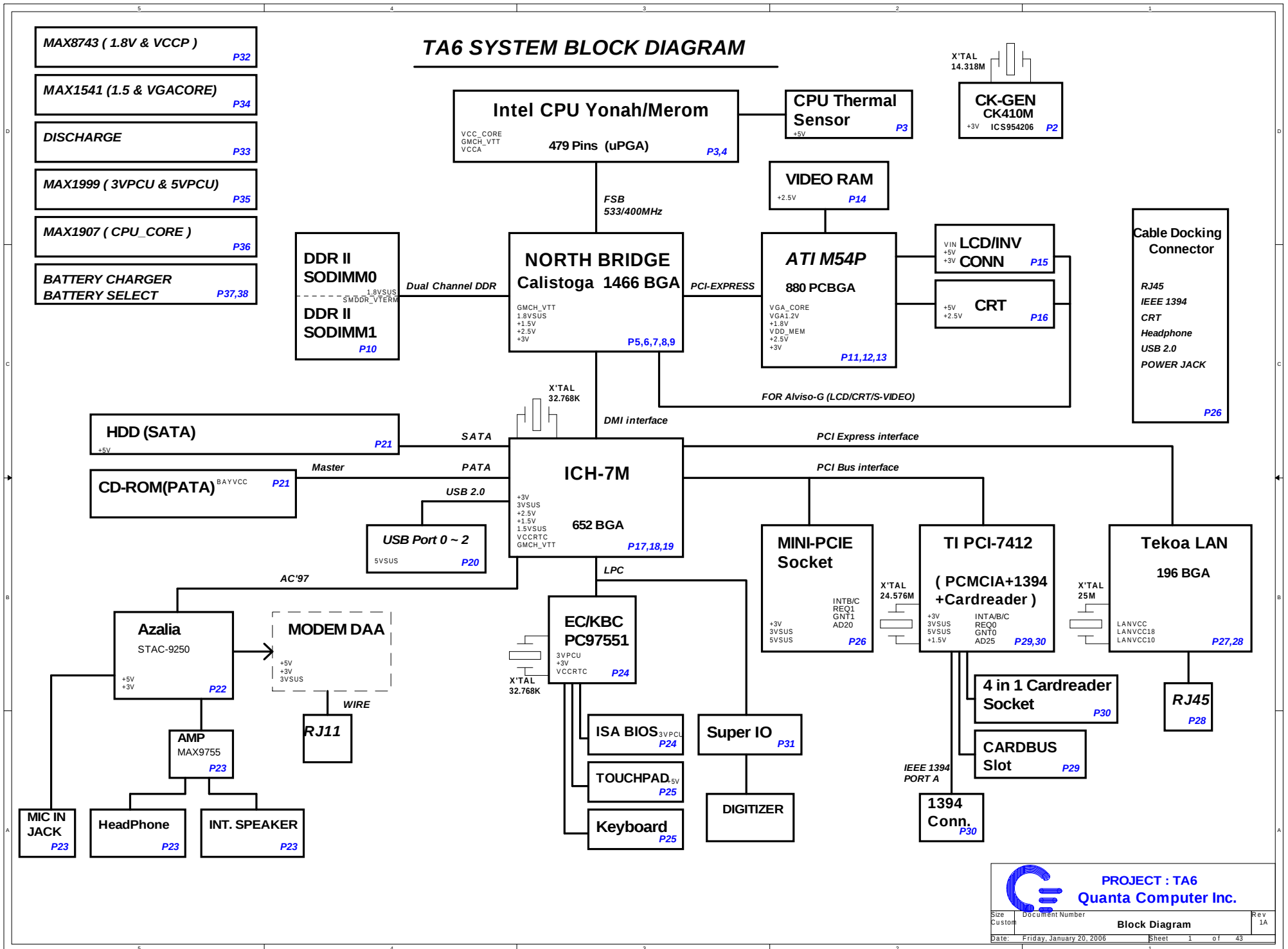
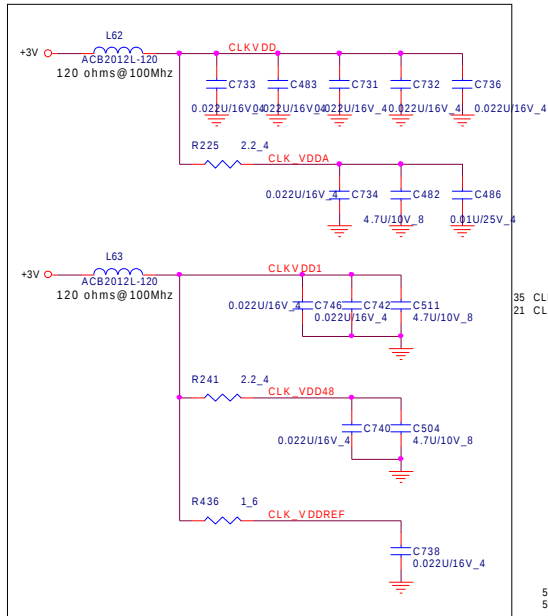


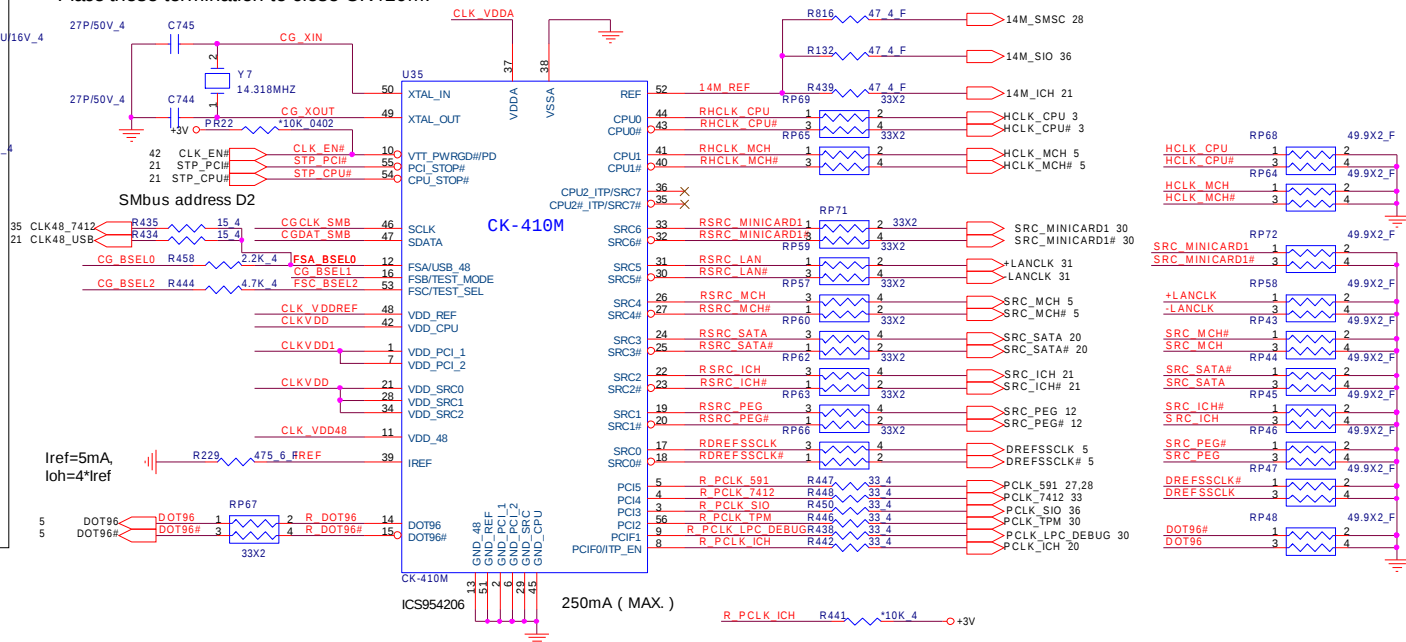
TA6 SYSTEM BLOCK DIAGRAM



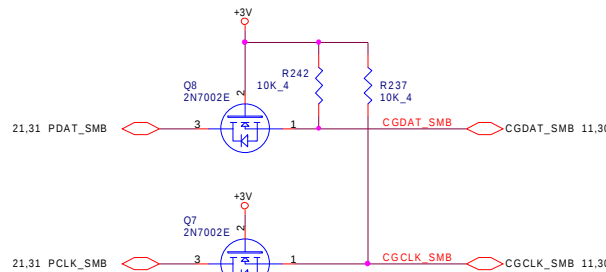
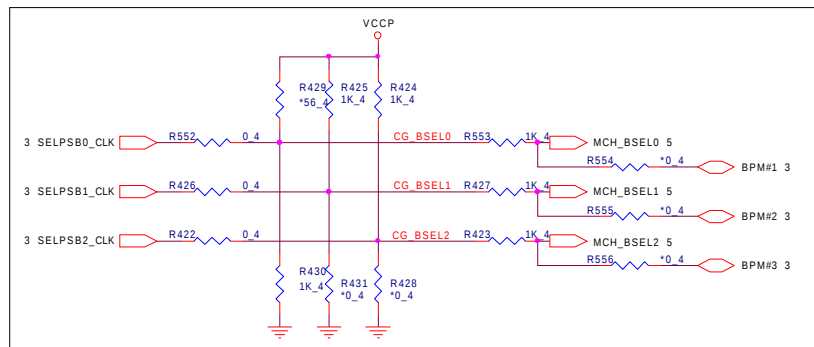
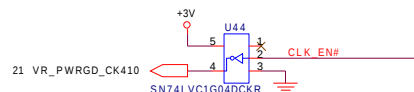


FSC	FSB	FSA	CPU	SRC	PCI	
1	0	1	100	100	33	
0	0	1	133	100	33	Default
0	1	1	166	100	33	
0	1	0	200	100	33	
0	0	0	266	100	33	
1	0	0	333	100	33	
1	1	0	400	100	33	
1	1	1	RESERVED			

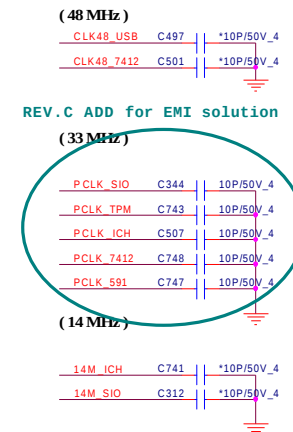
Place these termination to close CK410M.

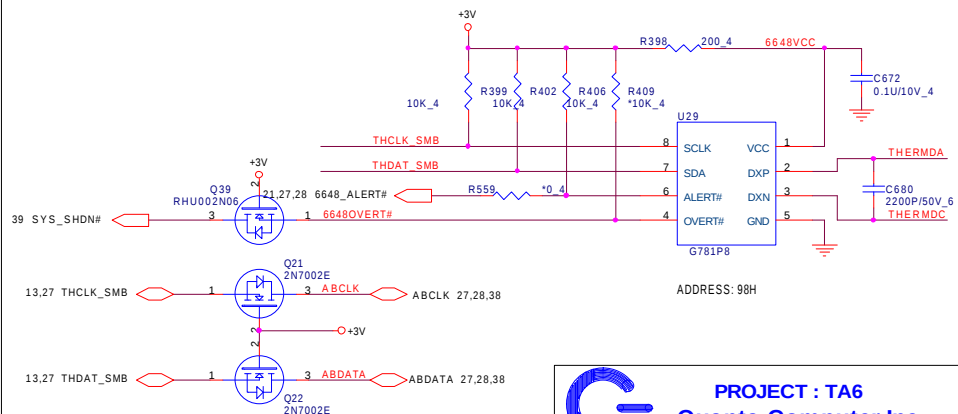
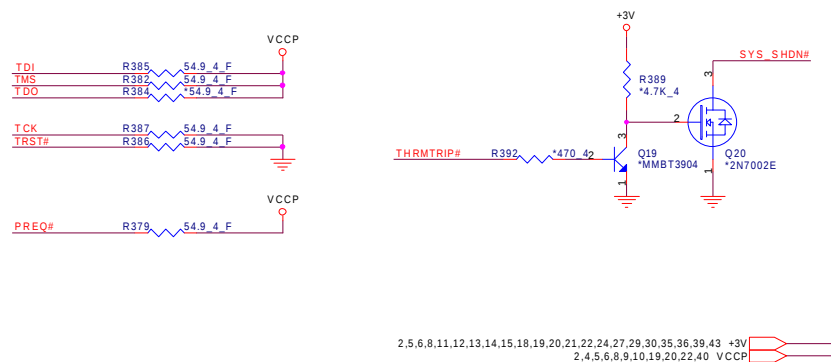
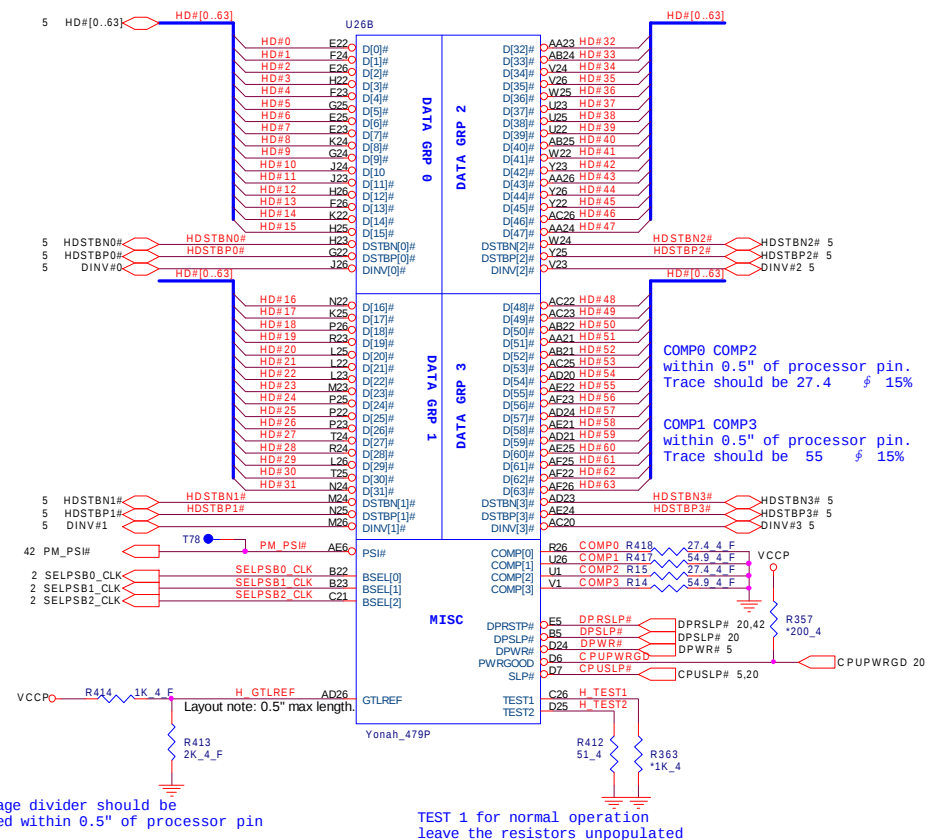


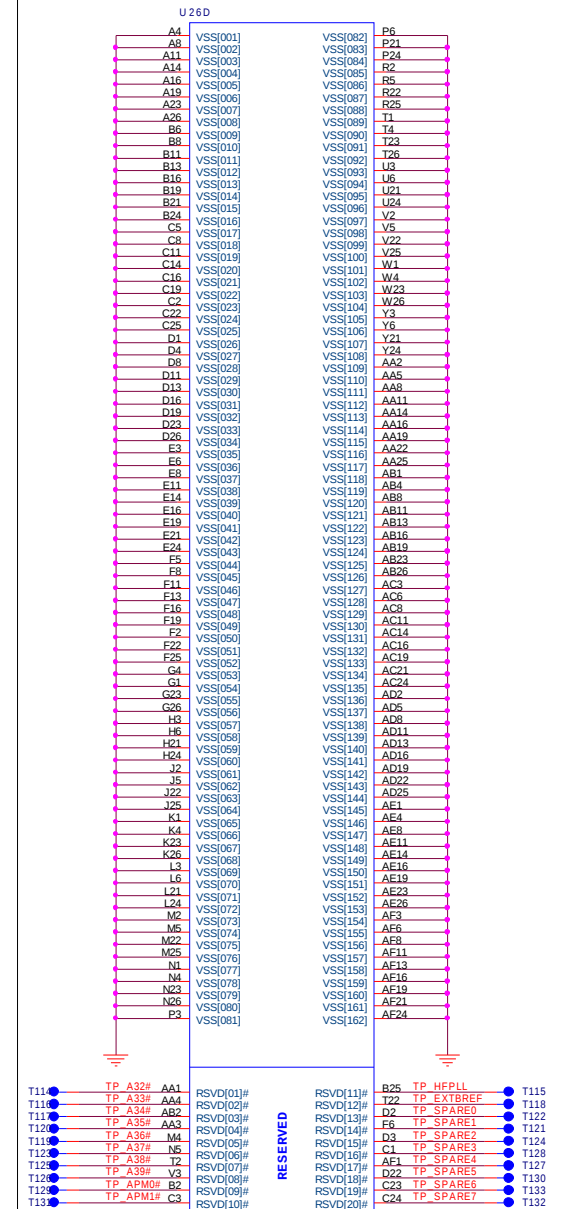
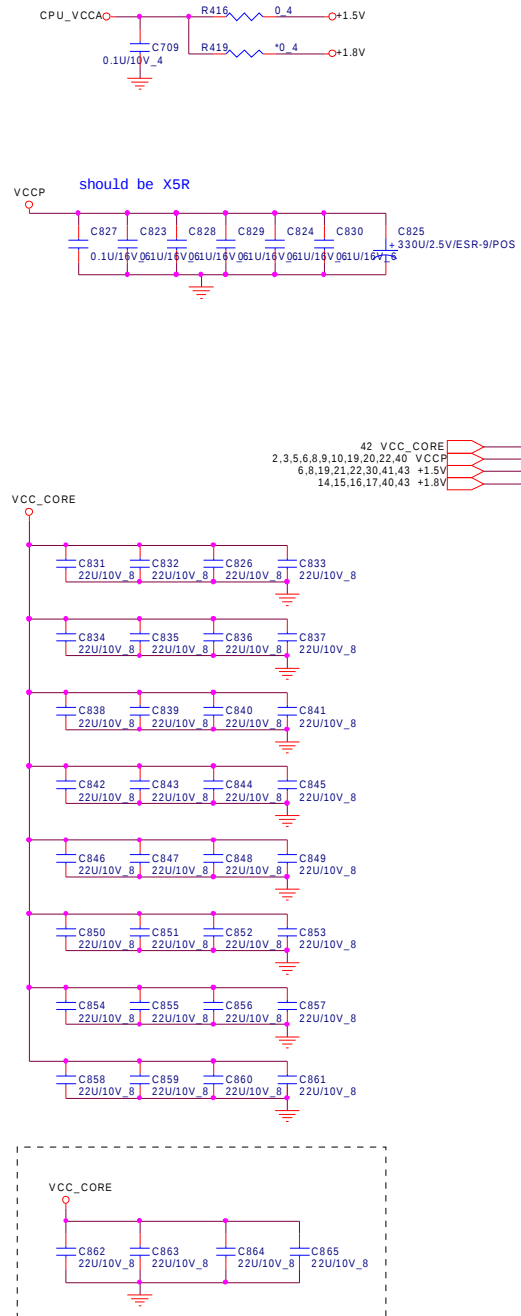
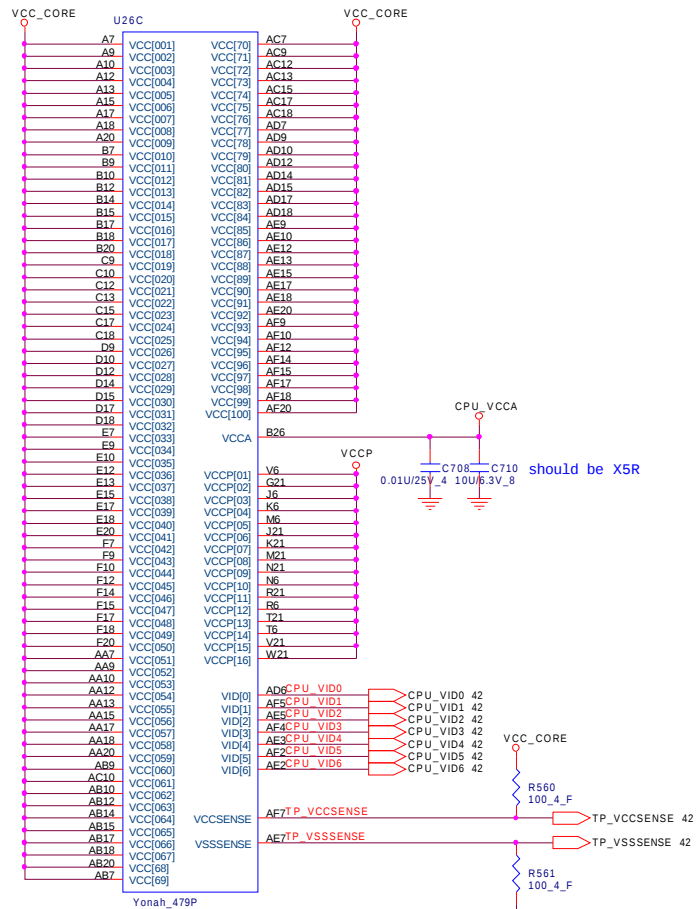
Place these termination to close CK410M.

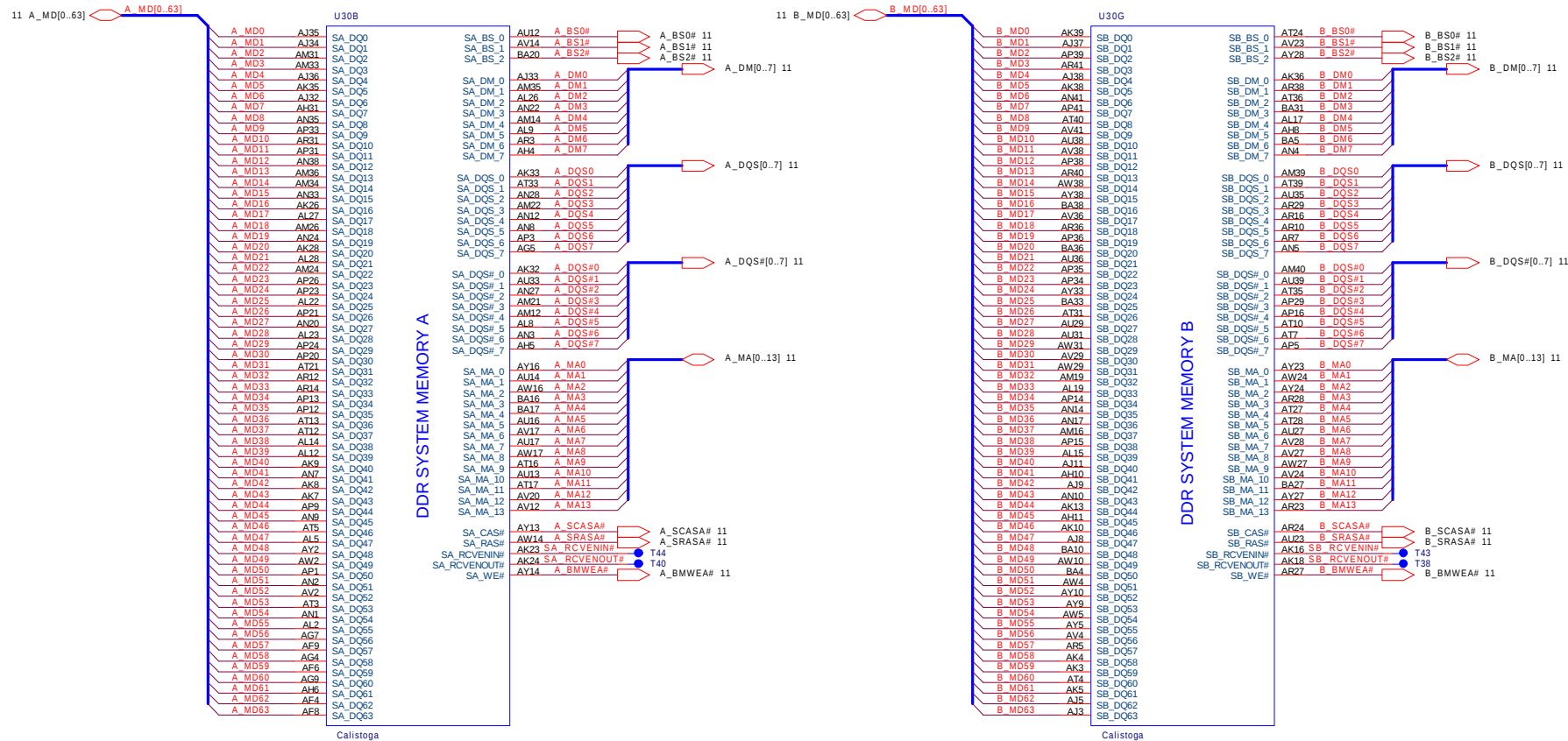


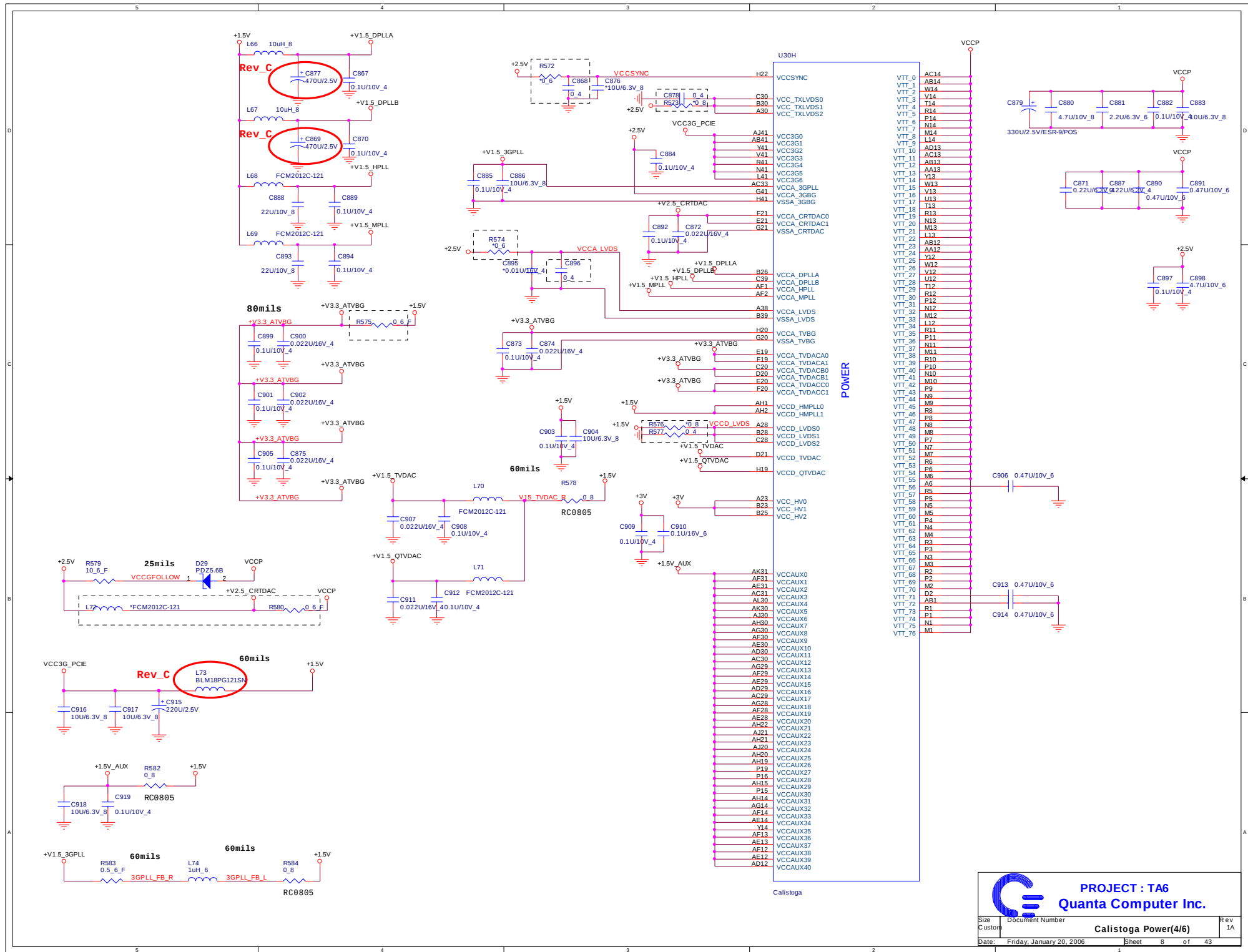
These are for backdrive issue

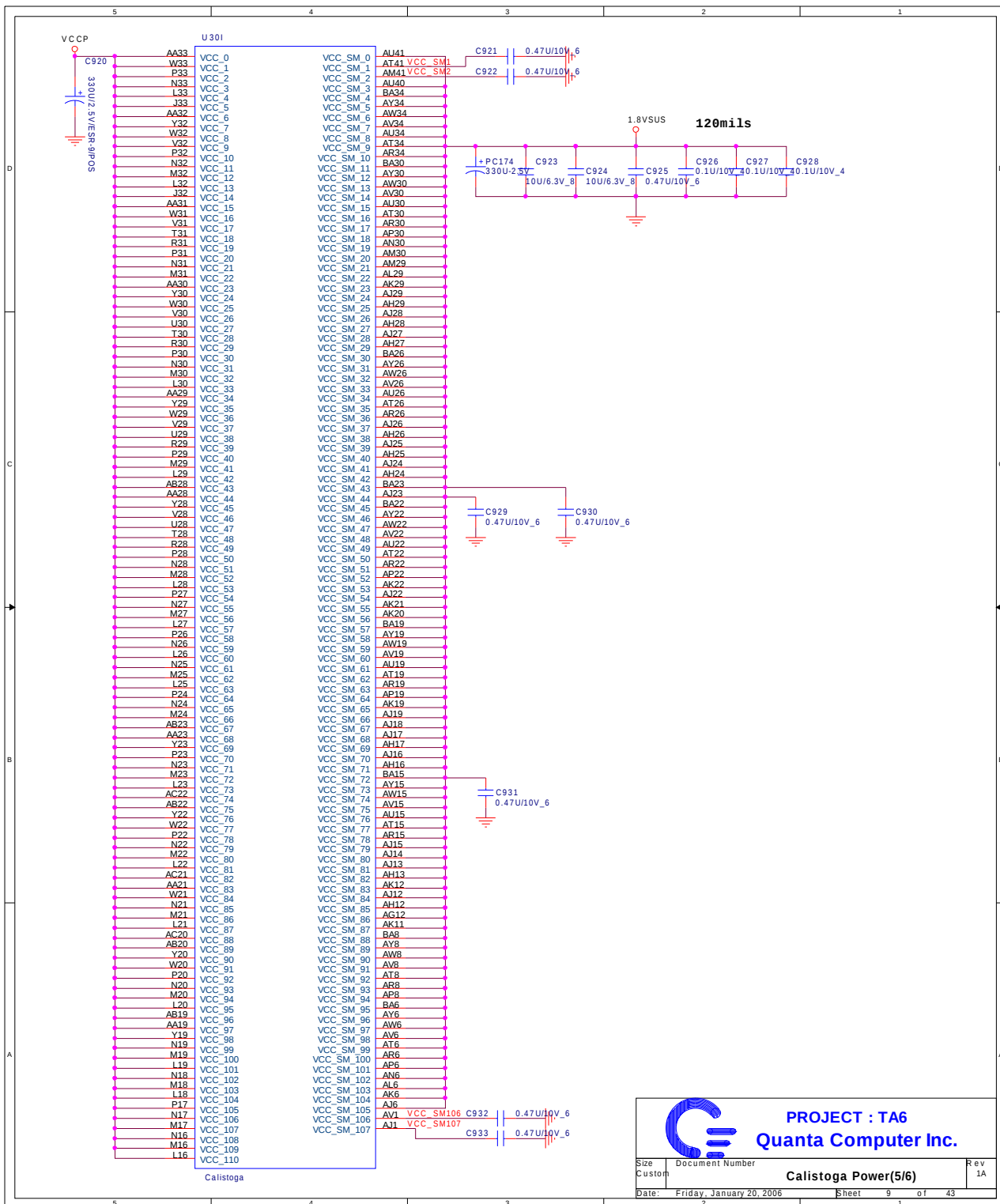


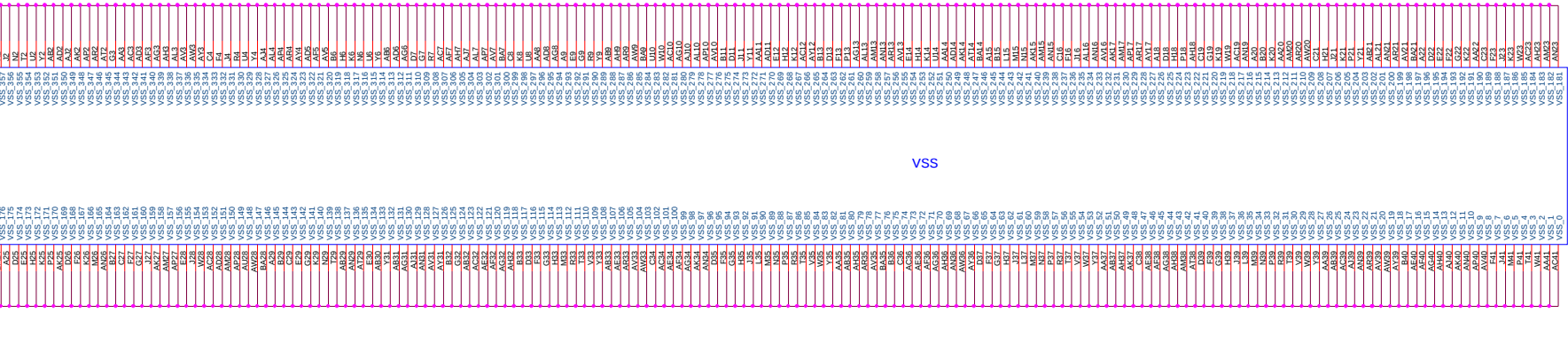




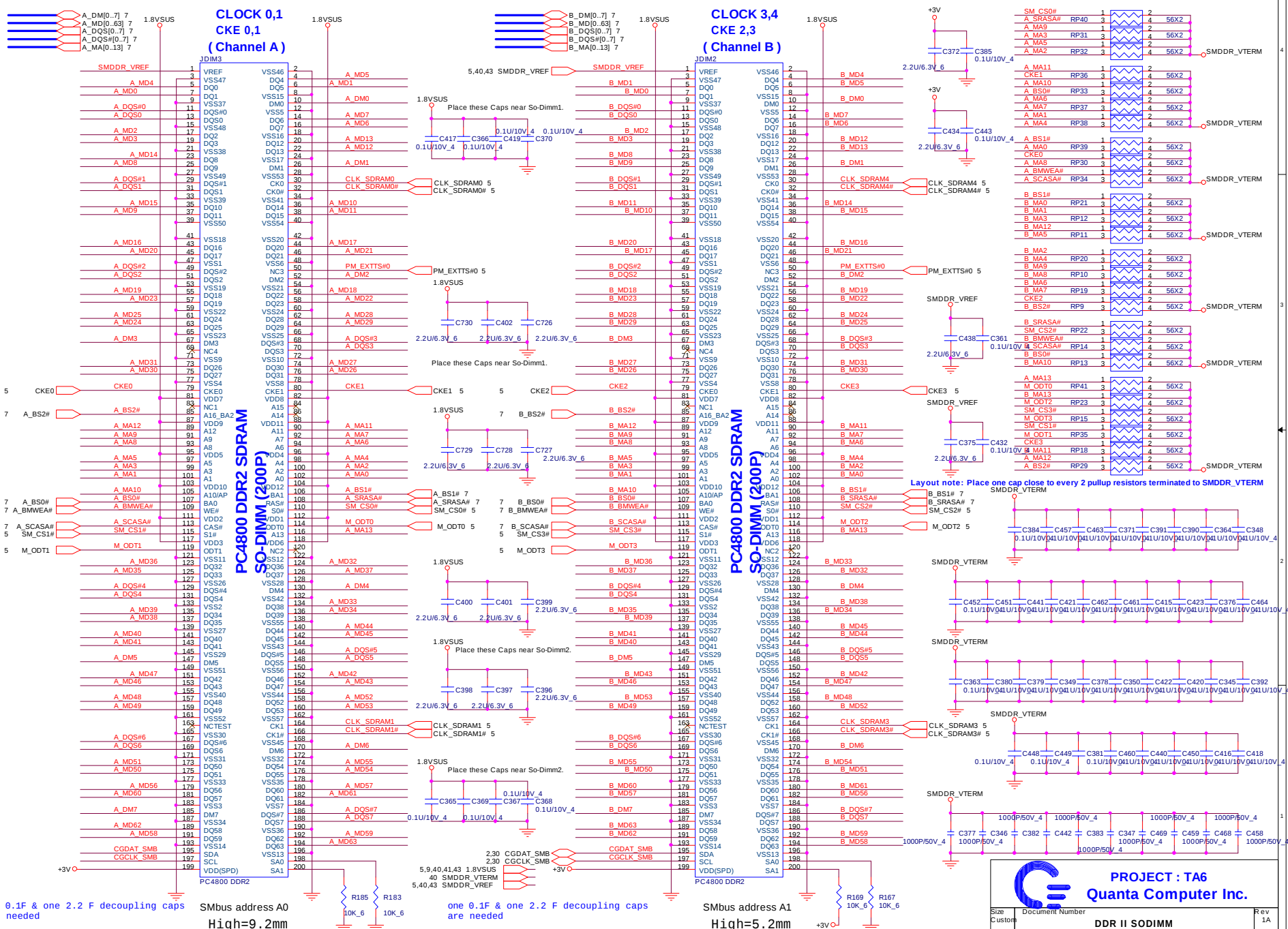






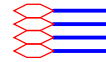


Signal voltage level = 0.9V
Decoupling caps are needed
one 0.1 F & one 2.2 F
placed close to VREF pins of DDR2 SO-DIMM.



NOTE: some of the PCIE testpoints will be available through via on traces.

6 PEG_RXP[15:0]
6 PEG_RXN[15:0]
6 PEG_TXP_C[0..15]
6 PEG_TXN_C[0..15]



U28A

PART 1 OF 7

P
C
I
-
E
X
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I
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T
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R
F
A
C
E

PEG_TXP_C0 AH31
PEG_TXN_C0 AH31C
PEG_TXP_C1 AH30
PEG_TXN_C1 AG30C
PEG_TXP_C2 AG32
PEG_TXN_C2 AF32C
PEG_TXP_C3 AF31
PEG_TXN_C3 AE31C
PEG_TXP_C4 AE30
PEG_TXN_C4 AD30C
PEG_TXP_C5 AD32
PEG_TXN_C5 AC32C
PEG_TXP_C6 AC31
PEG_TXN_C6 AB31C
PEG_TXP_C7 AB30
PEG_TXN_C7 AA30C
PEG_TXP_C8 AA32
PEG_TXN_C8 Y32C
PEG_TXP_C9 Y31
PEG_TXN_C9 W31C
PEG_TXP_C10 W30
PEG_TXN_C10 V30C
PEG_TXP_C11 V32
PEG_TXN_C11 U32C
PEG_TXP_C12 U31
PEG_TXN_C12 T31C
PEG_TXP_C13 T30
PEG_TXN_C13 R30C
PEG_TXP_C14 R32
PEG_TXN_C14 P32C
PEG_TXP_C15 P31
PEG_TXN_C15 N31C

PCIE_RX0P
PCIE_RX0N
PCIE_RX1P
PCIE_RX1N
PCIE_RX2P
PCIE_RX2N
PCIE_RX3P
PCIE_RX3N
PCIE_RX4P
PCIE_RX4N
PCIE_RX5P
PCIE_RX5N
PCIE_RX6P
PCIE_RX6N
PCIE_RX7P
PCIE_RX7N
PCIE_RX8P
PCIE_RX8N
PCIE_RX9P
PCIE_RX9N
PCIE_RX10P
PCIE_RX10N
PCIE_RX11P
PCIE_RX11N
PCIE_RX12P
PCIE_RX12N
PCIE_RX13P
PCIE_RX13N
PCIE_RX14P
PCIE_RX14N
PCIE_RX15P
PCIE_RX15N

AK27 V_GMCHEXP_RXP0 C56 0.1U/10V 4 PEG_RXP0
AJ27 V_GMCHEXP_RXN0 C72 0.1U/10V 4 PEG_RXN0
AJ25 V_GMCHEXP_RXP1 C73 0.1U/10V 4 PEG_RXP1
AH25 V_GMCHEXP_RXN1 C82 0.1U/10V 4 PEG_RXN1
AH28 V_GMCHEXP_RXP2 C88 0.1U/10V 4 PEG_RXP2
AG28 V_GMCHEXP_RXN2 C96 0.1U/10V 4 PEG_RXN2
AG27 V_GMCHEXP_RXP3 C98 0.1U/10V 4 PEG_RXP3
AE27 V_GMCHEXP_RXN3 C114 0.1U/10V 4 PEG_RXN3
AE25 V_GMCHEXP_RXP4 C120 0.1U/10V 4 PEG_RXP4
AE25 V_GMCHEXP_RXN4 C131 0.1U/10V 4 PEG_RXN4
AE28 V_GMCHEXP_RXP5 C134 0.1U/10V 4 PEG_RXP5
AD28 V_GMCHEXP_RXN5 C143 0.1U/10V 4 PEG_RXN5
AD27 V_GMCHEXP_RXP6 C147 0.1U/10V 4 PEG_RXP6
AC27 V_GMCHEXP_RXN6 C157 0.1U/10V 4 PEG_RXN6
AC25 V_GMCHEXP_RXP7 C160 0.1U/10V 4 PEG_RXP7
AB25 V_GMCHEXP_RXN7 C170 0.1U/10V 4 PEG_RXN7
AB28 V_GMCHEXP_RXP8 C179 0.1U/10V 4 PEG_RXP8
AA28 V_GMCHEXP_RXN8 C187 0.1U/10V 4 PEG_RXN8
AA27 V_GMCHEXP_RXP9 C192 0.1U/10V 4 PEG_RXP9
Y27 V_GMCHEXP_RXN9 C201 0.1U/10V 4 PEG_RXN9
Y25 V_GMCHEXP_RXP10 C209 0.1U/10V 4 PEG_RXP10
W25 V_GMCHEXP_RXN10 C222 0.1U/10V 4 PEG_RXN10
W28 V_GMCHEXP_RXP11 C234 0.1U/10V 4 PEG_RXP11
V28 V_GMCHEXP_RXN11 C240 0.1U/10V 4 PEG_RXN11
V27 V_GMCHEXP_RXP12 C226 0.1U/10V 4 PEG_RXP12
U27 V_GMCHEXP_RXN12 C230 0.1U/10V 4 PEG_RXN12
U25 V_GMCHEXP_RXP13 C242 0.1U/10V 4 PEG_RXP13
T25 V_GMCHEXP_RXN13 C248 0.1U/10V 4 PEG_RXN13
T28 V_GMCHEXP_RXP14 C250 0.1U/10V 4 PEG_RXP14
R28 V_GMCHEXP_RXN14 C257 0.1U/10V 4 PEG_RXN14
R27 V_GMCHEXP_RXP15 C260 0.1U/10V 4 PEG_RXP15
P27 V_GMCHEXP_RXN15 C265 0.1U/10V 4 PEG_RXN15

Clock

PCIE_REFCLKP
PCIE_REFCLKN

Calibration

PCIE_CALRN
PCIE_CALRP

PCIE_CALI

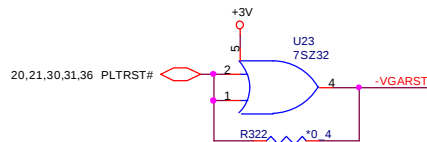
PERSTB

PCIE_TEST

PERSTB_MASK

Tie To VSS

M54-P



2 SRC_PEG SRC_PEG# AL28
2 SRC_PEG# SRC_PEG# AK28C

T159

AE24 R88 2K 4 F
AD24 R87 562 4 F

AB24 R89 1.47K 4 F



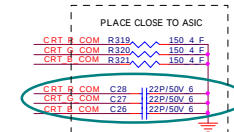
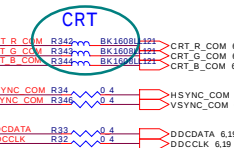
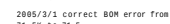
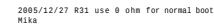
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Quanta Computer Inc.

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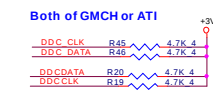


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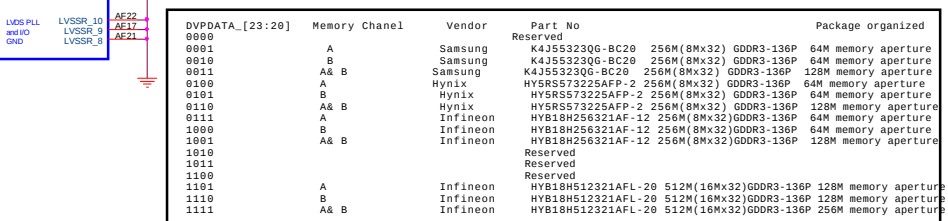
MEMORY CLOCK SPREAD SPECTRUM

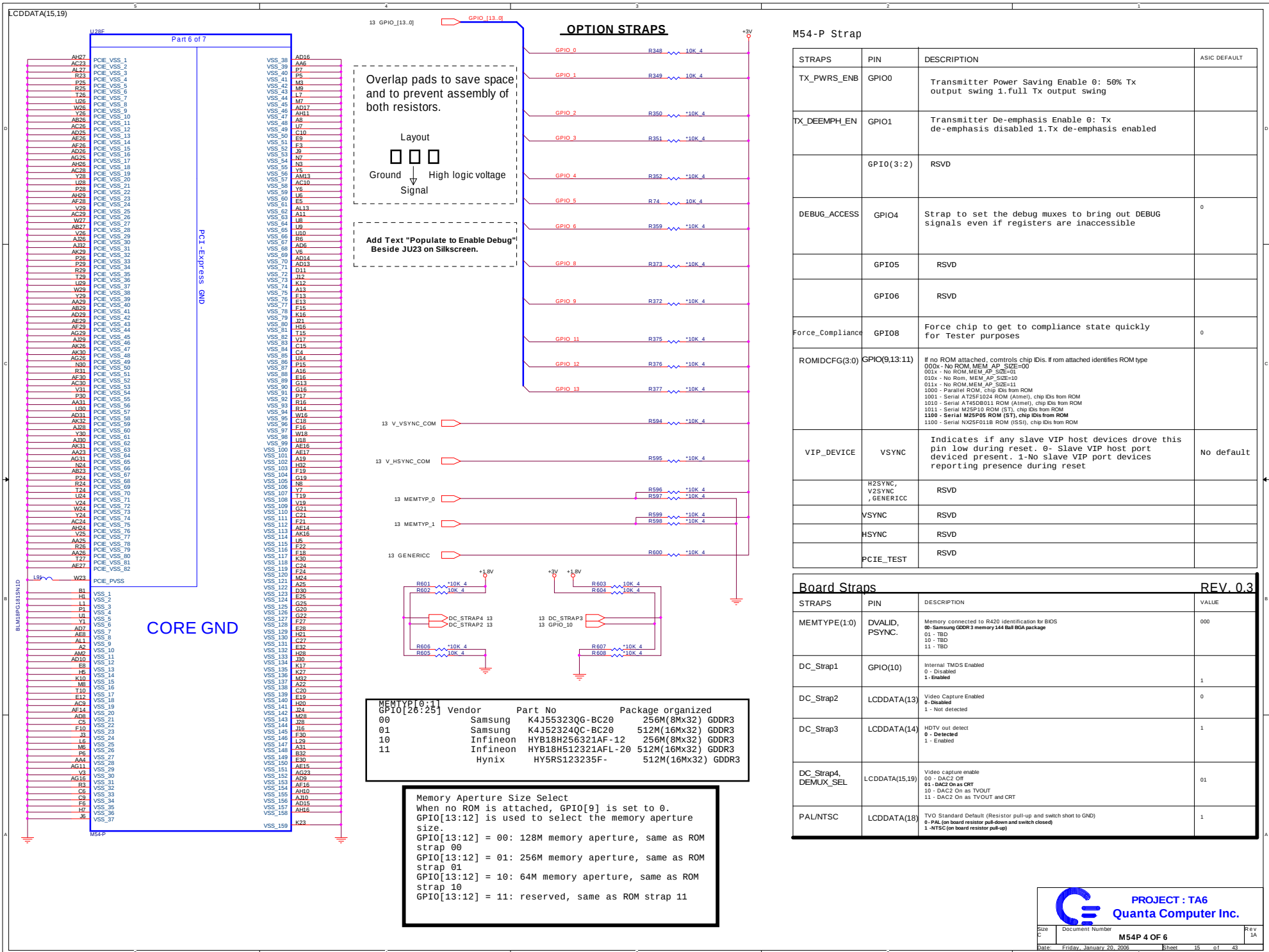


REV.C ADD for EMI solution



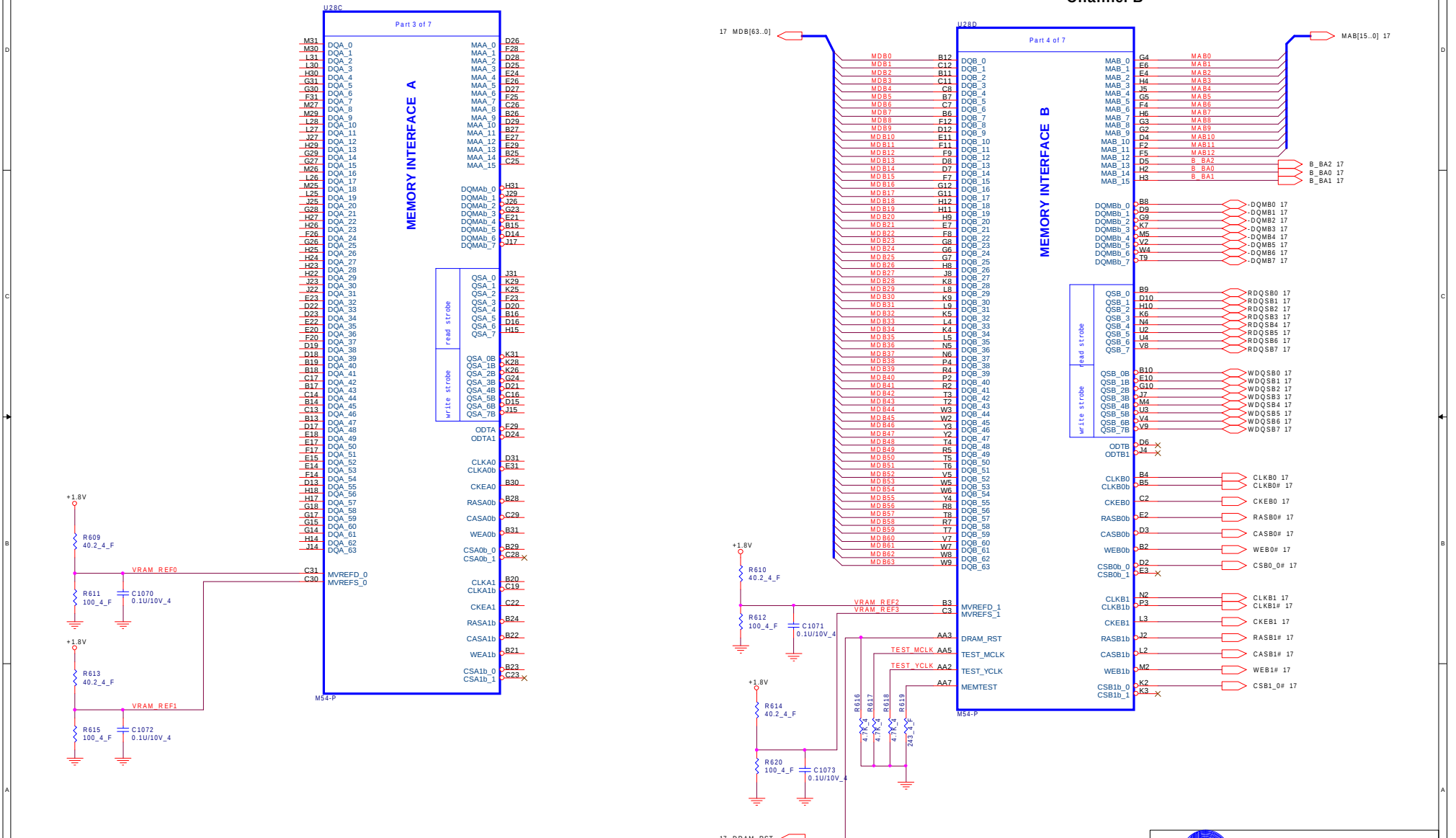
Both of GMCH or ATI





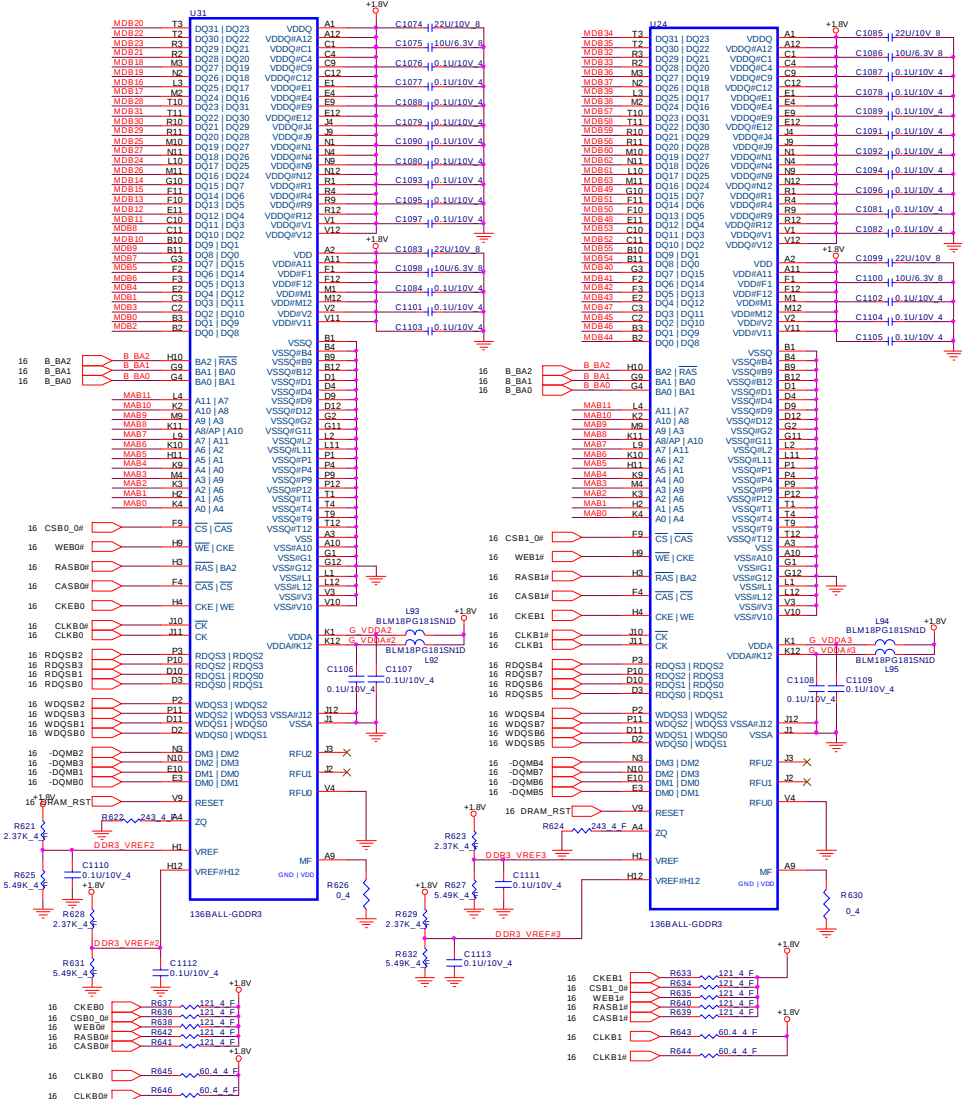
Channel A RV410 MEMORY CHANNELS A and B

Channel B



MAB[15..0]
MDB[63..0]

Channel B

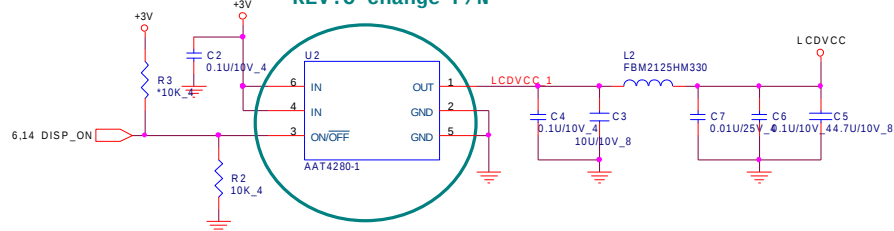


PROJECT : TA6
Quanta Computer Inc.

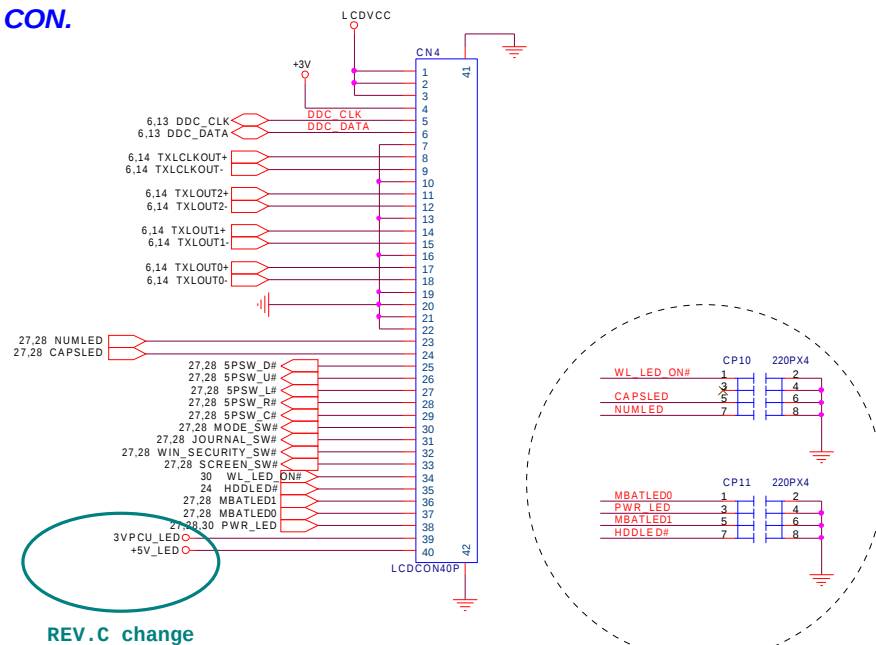
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Document Number: M54P 6 OF 6
Date: Friday, January 29, 2006
Rev: 1A
E:\net 37 01 49

PANEL VCC CONTROL

REV.C change P/N



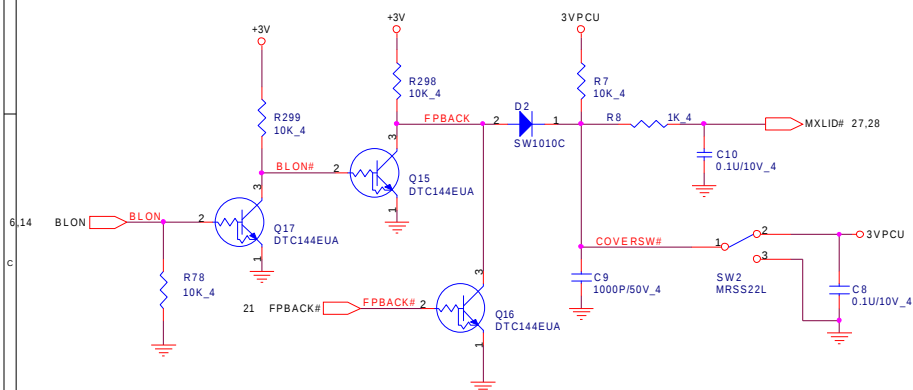
LCD CONNECTOR & SW CON.



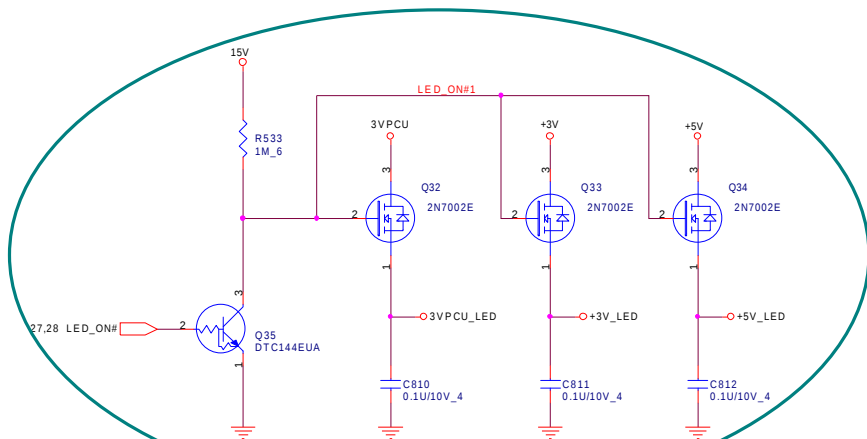
REV.C change

REV.B change

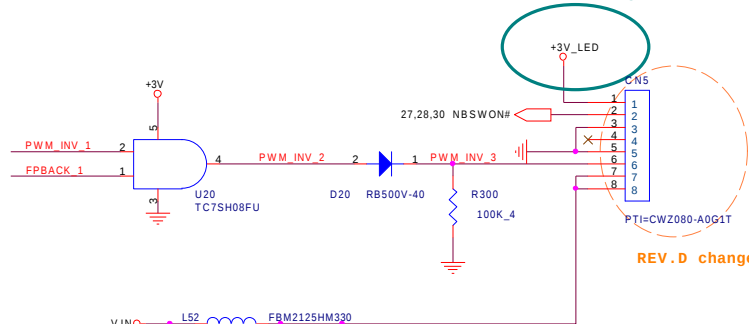
BACKLIGHT CONTROL



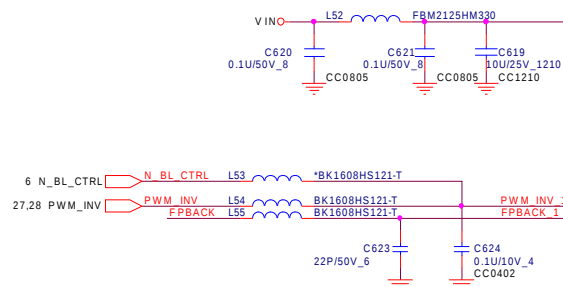
REV.C change



REV.C change

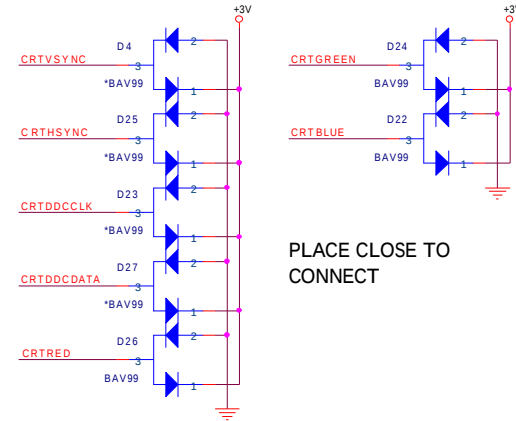
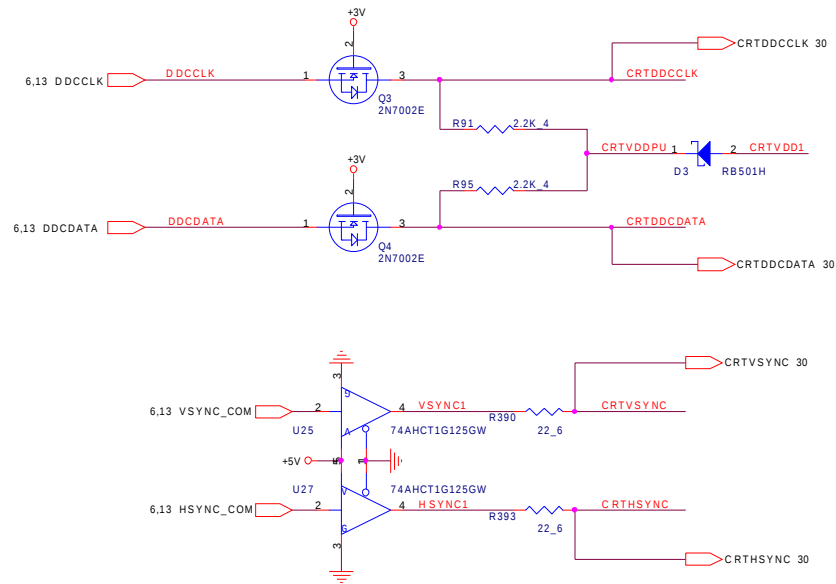


REV.D change



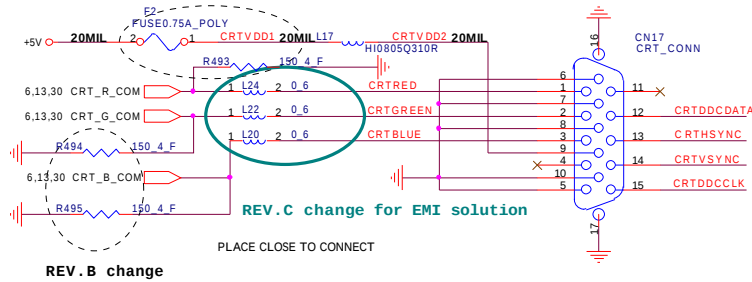
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Quanta Computer Inc.

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CRT PORT

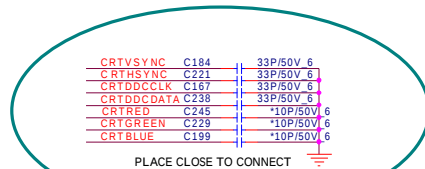
REV. B change



REV. B change

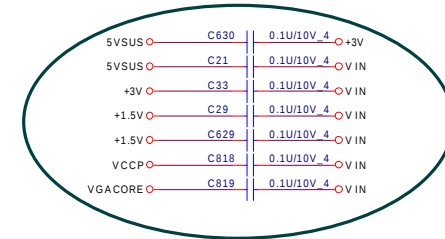
PLACE CLOSE TO CONNECT

REV. C change for EMI solution



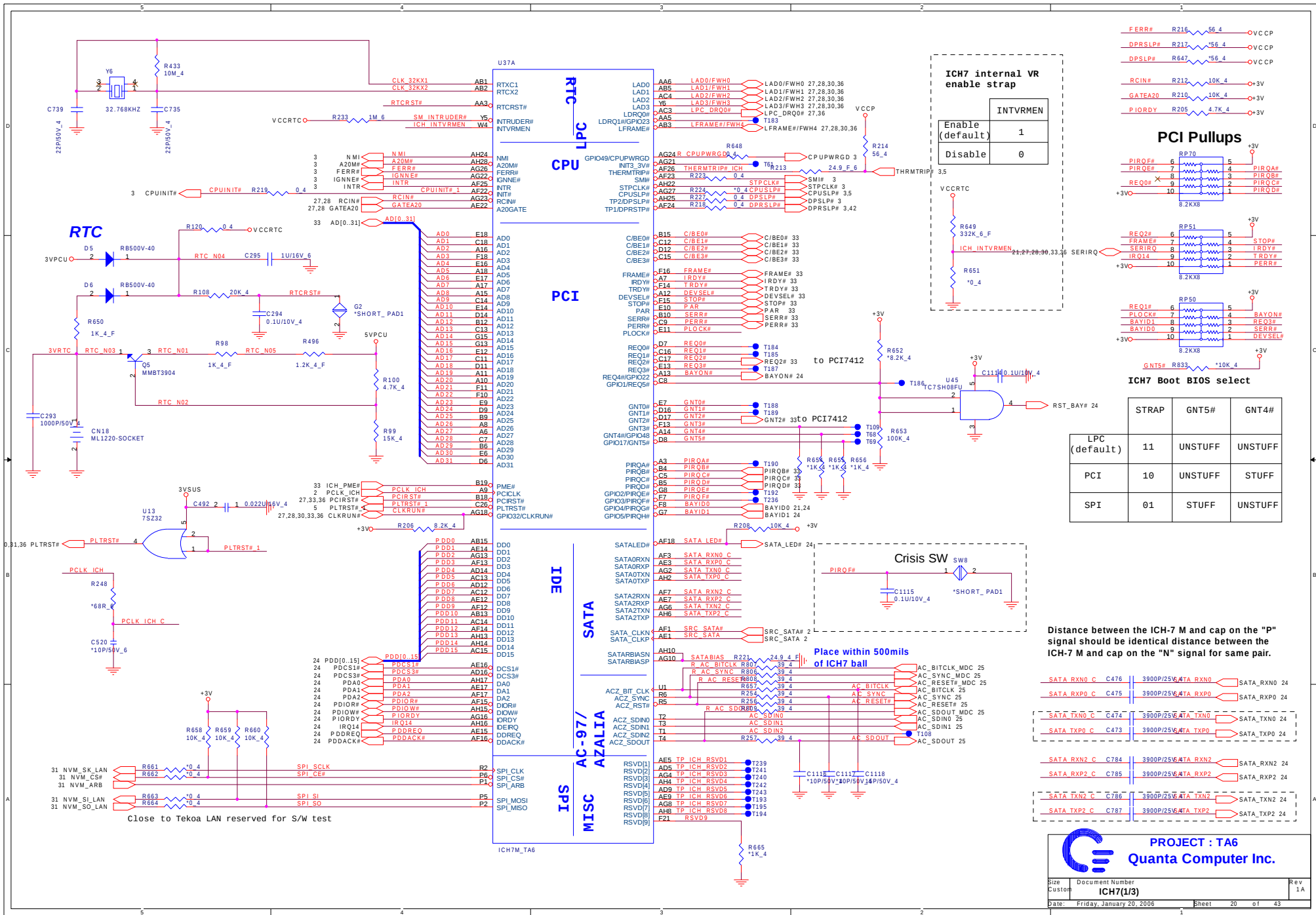
ADD C184, C221, C167, C238
DEL C245, C229, C199

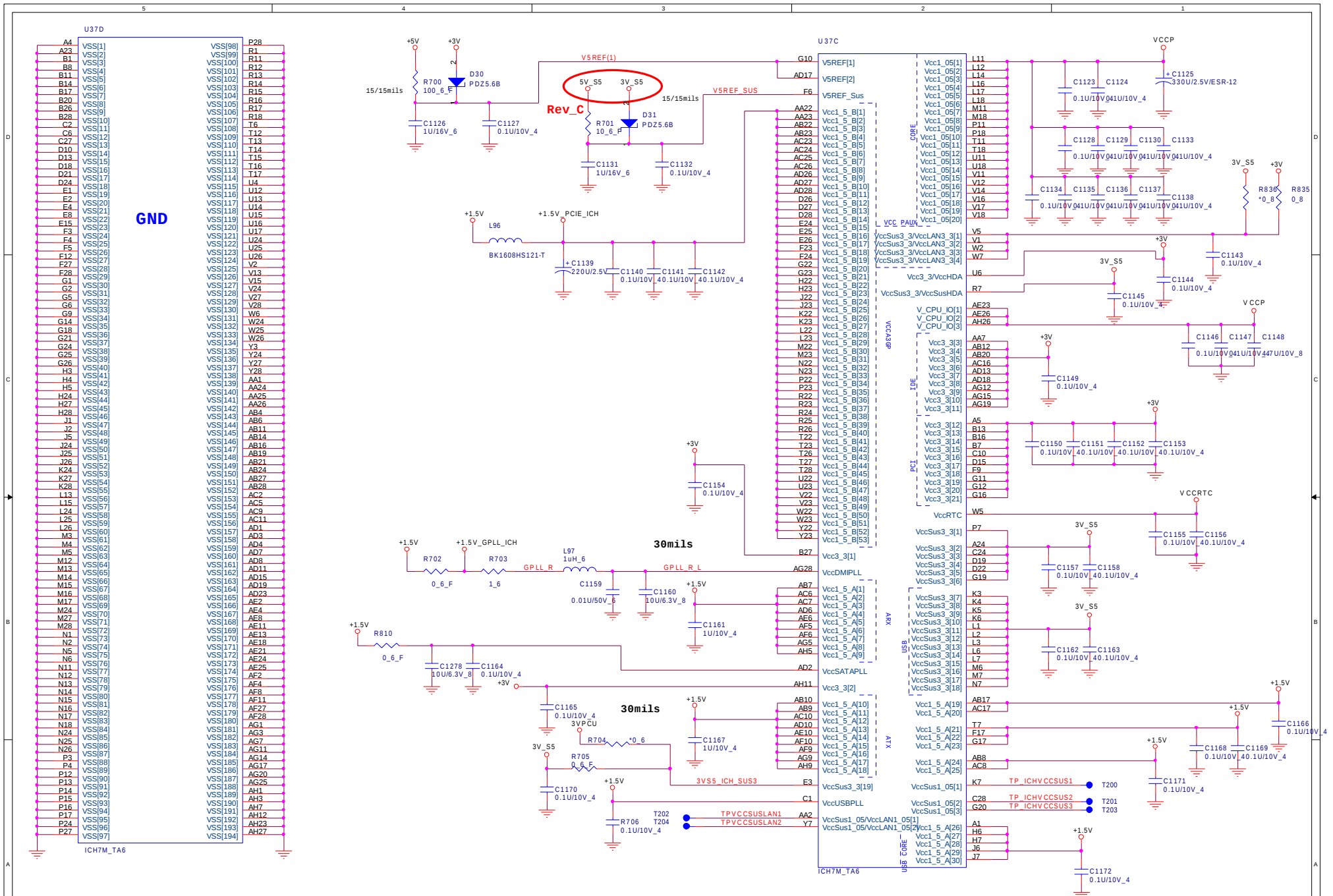
REV. C ADD for EMI solution



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Quanta Computer Inc.

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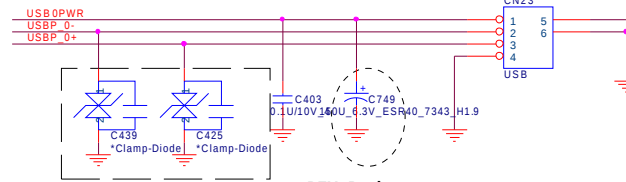
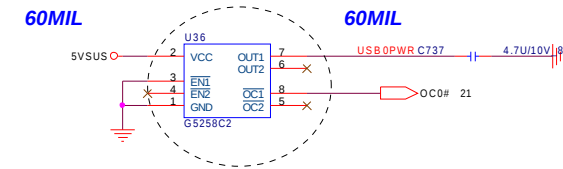
FRONT SIDE

REV. B change

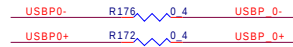
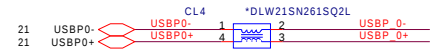
60MIL

60MIL

USB-0



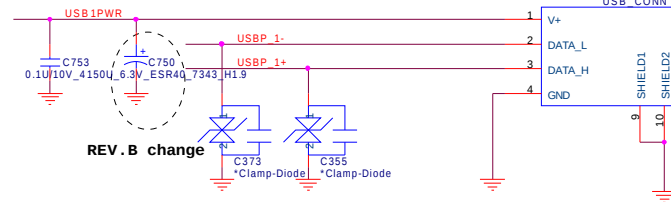
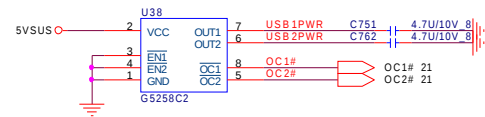
REV. B change



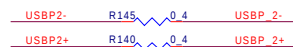
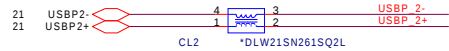
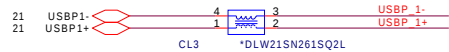
USB-1

60MIL

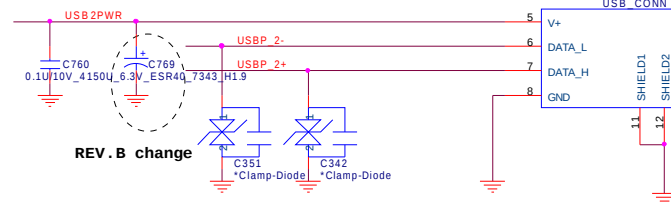
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REV. B change



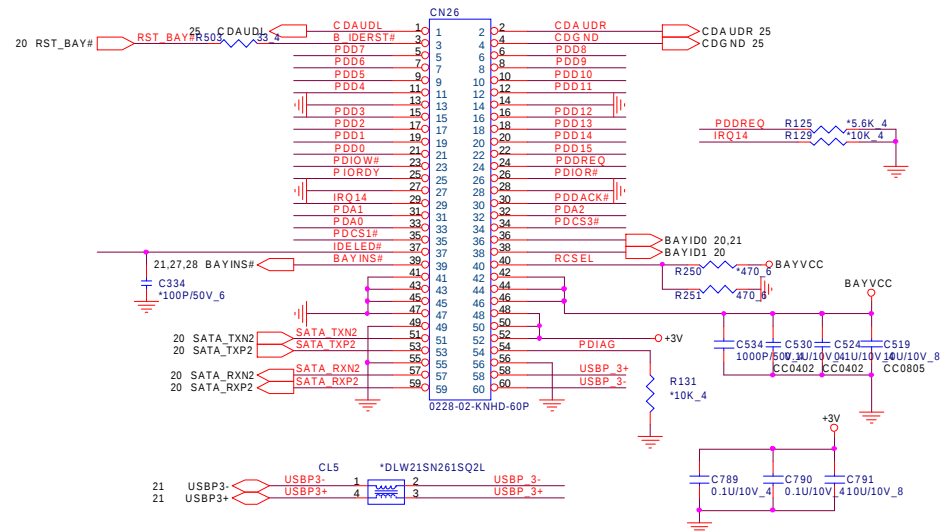
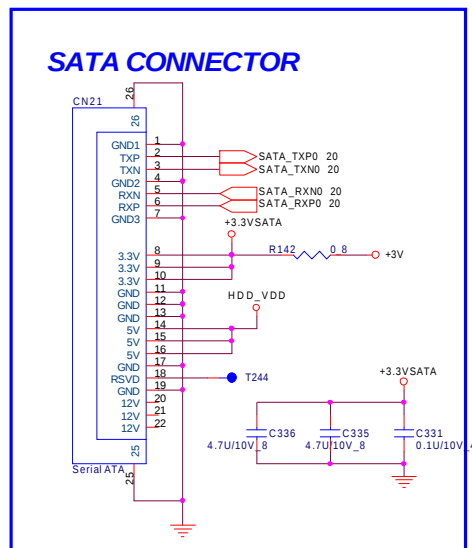
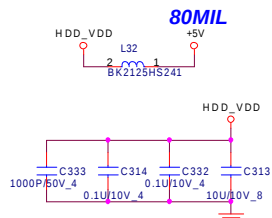
USB-2



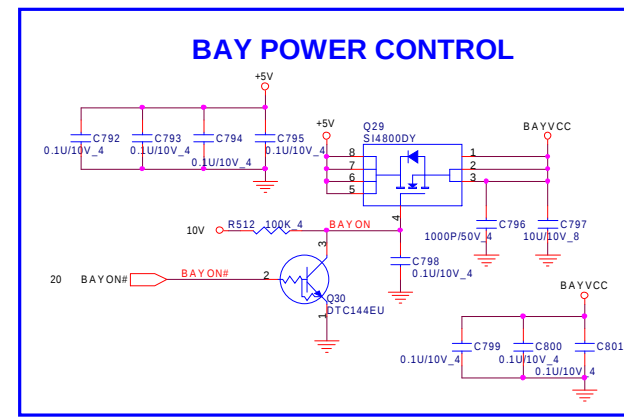
REV. B change

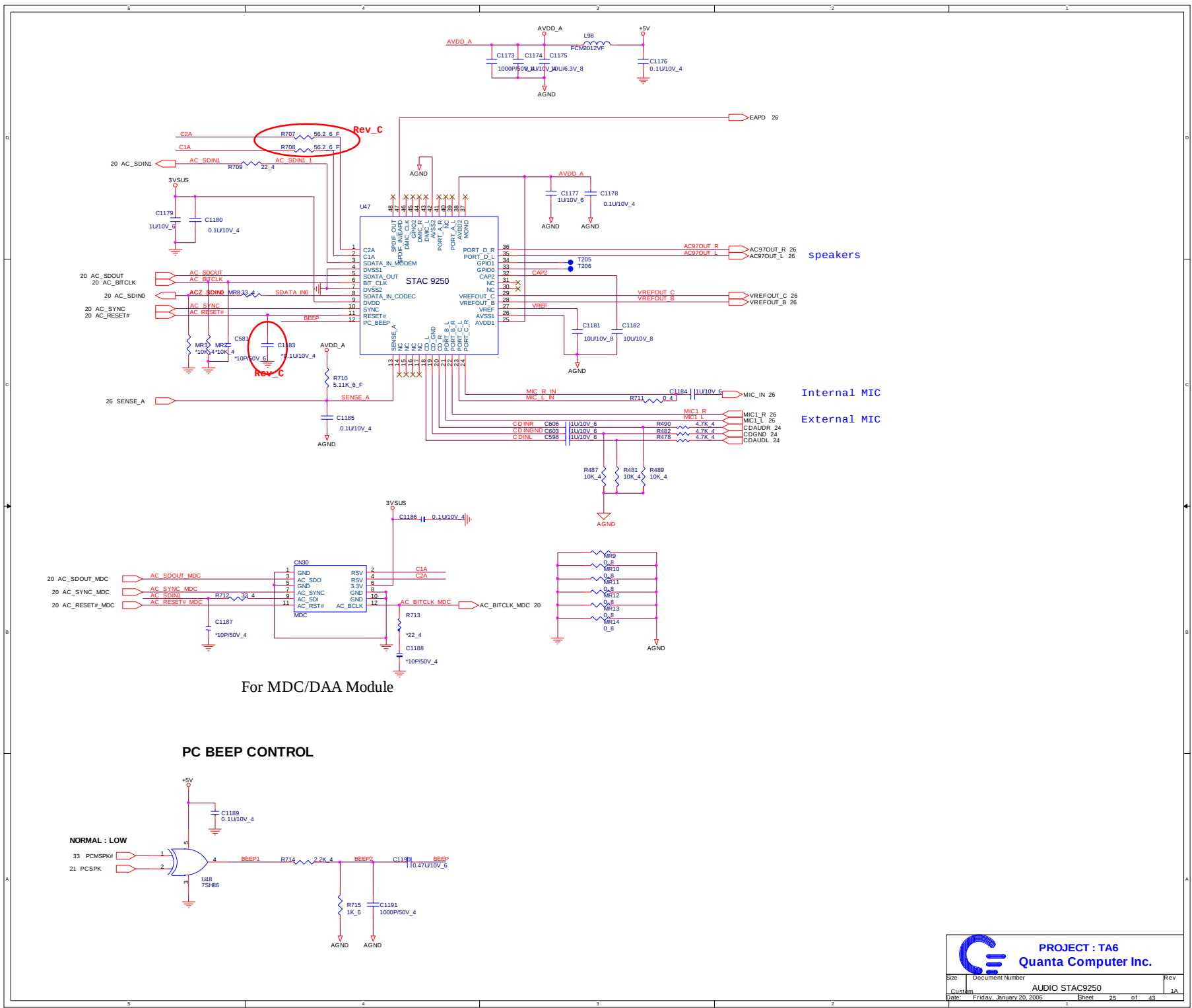
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Quanta Computer Inc.

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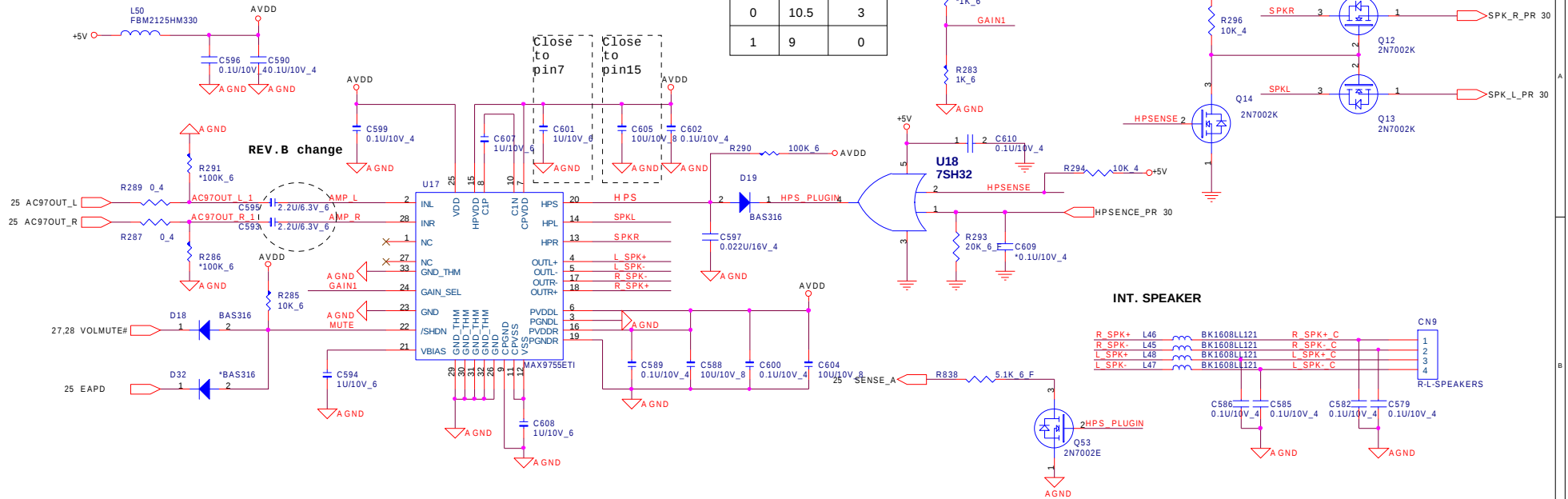
BAY ID STATUS		
LEFT BAYID0	LEFT BAYID1	STATUS
0	0	
0	1	HDD (SATA)
1	0	CD-ROM
1	1	USB-FDD



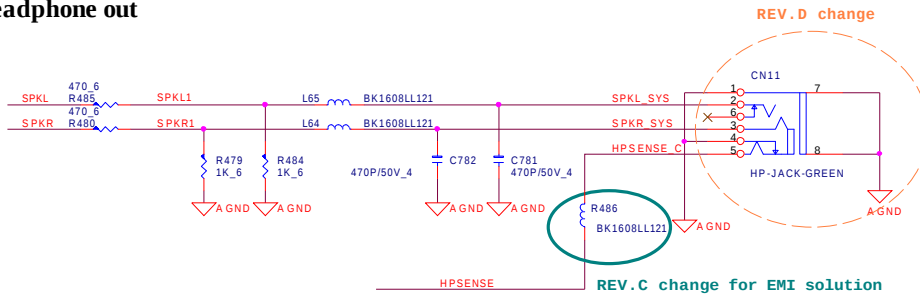


AUDIO AMPLIFIER-MAX9755

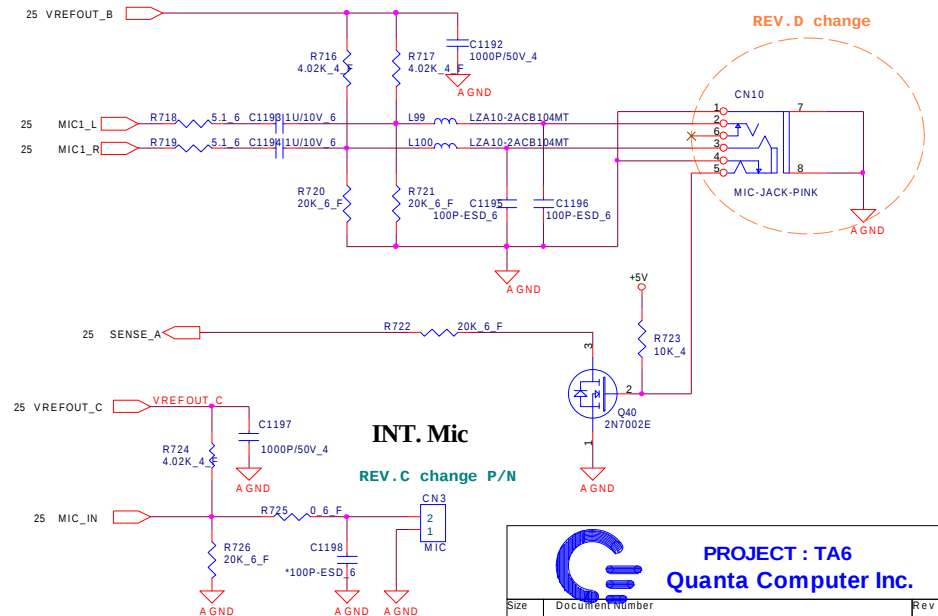
GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0



Headphone out



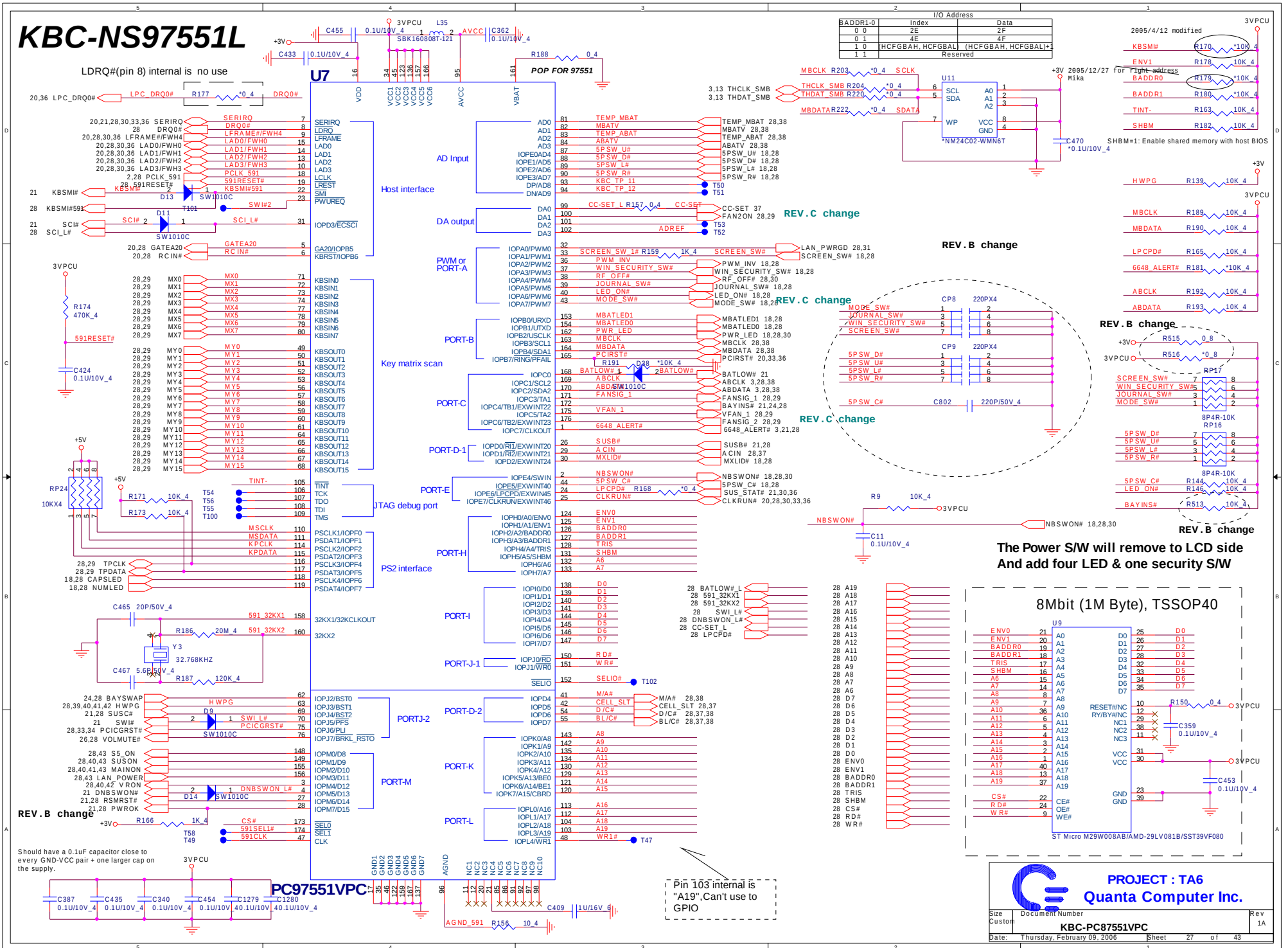
MIC in

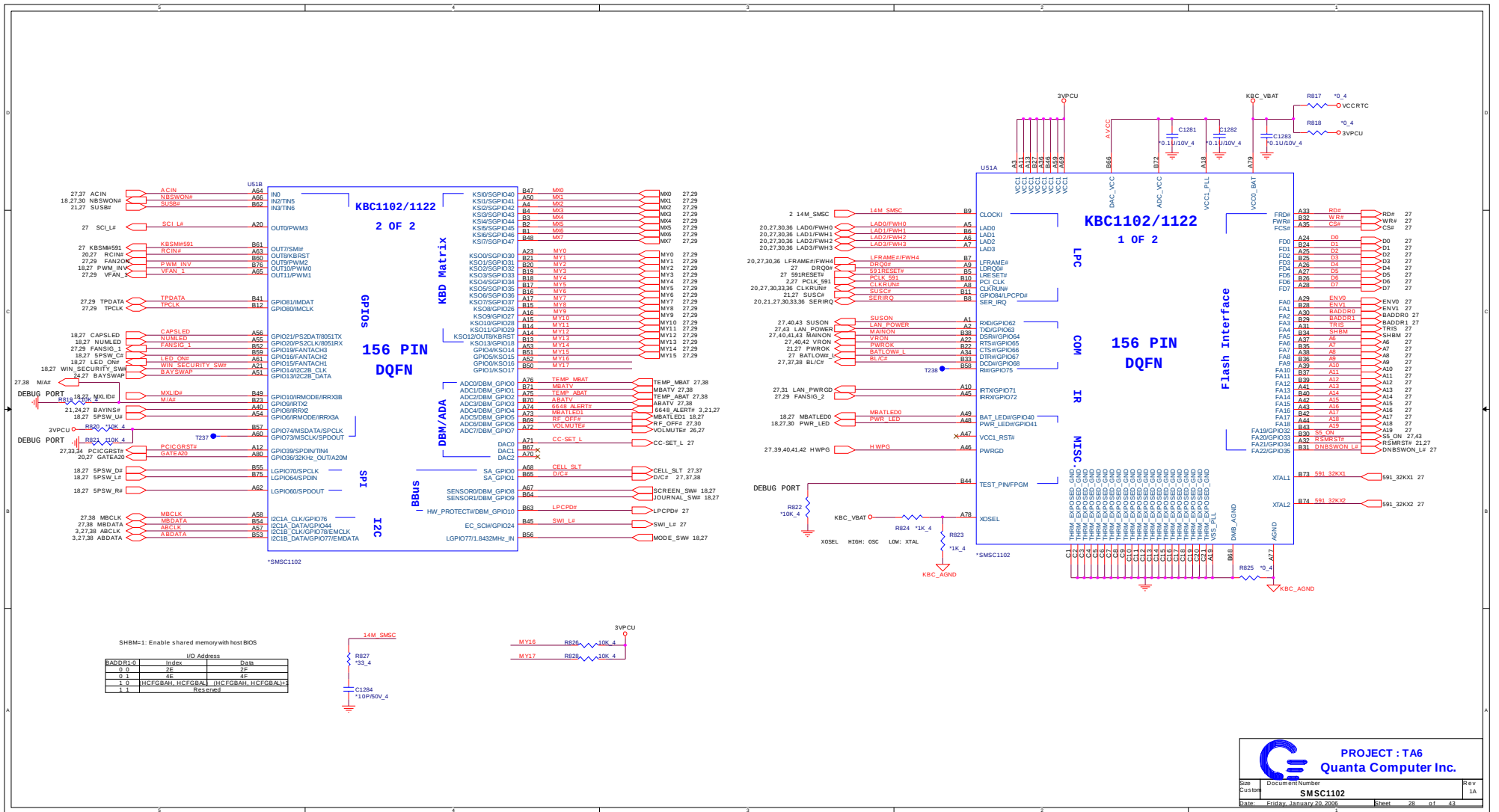


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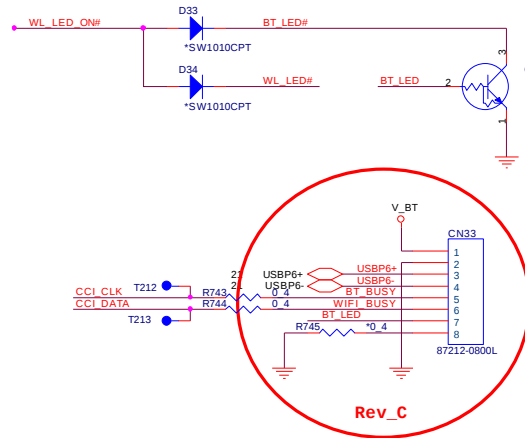
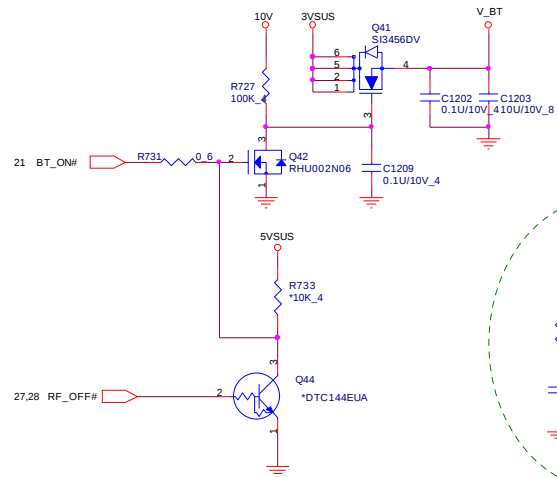
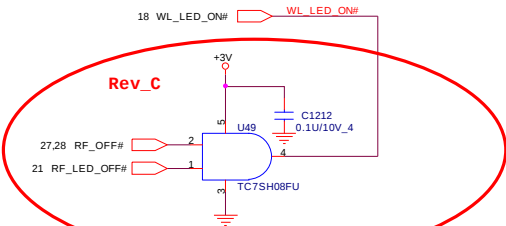
Size	Document Number	Rev
Custom	AUDIO AMP & JACK	1A
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KBC-NS97551L

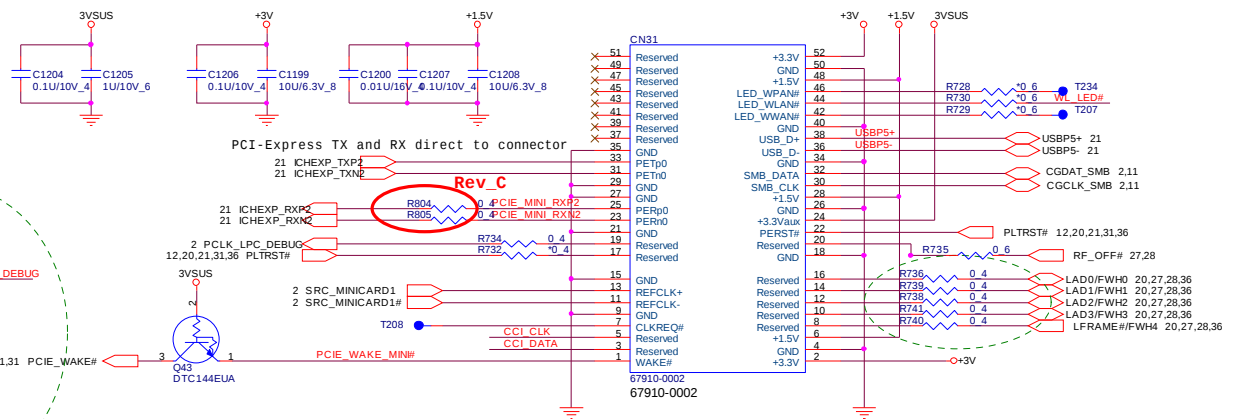




BLUETOOTH CONNECTOR

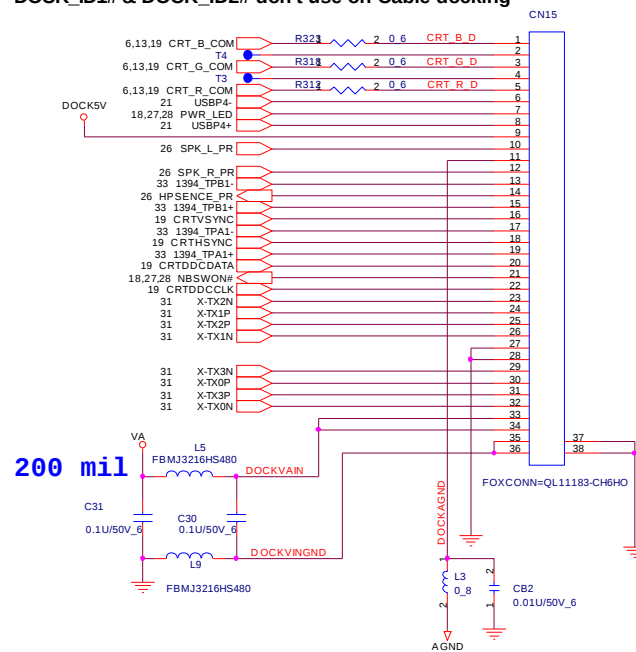
**WIRELESS LED**

Mini PCI-E Card 1

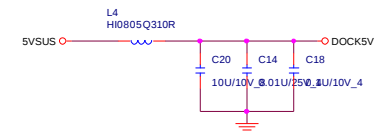
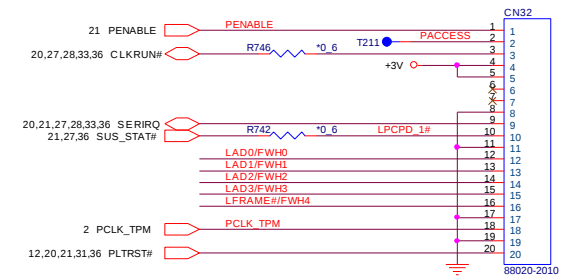


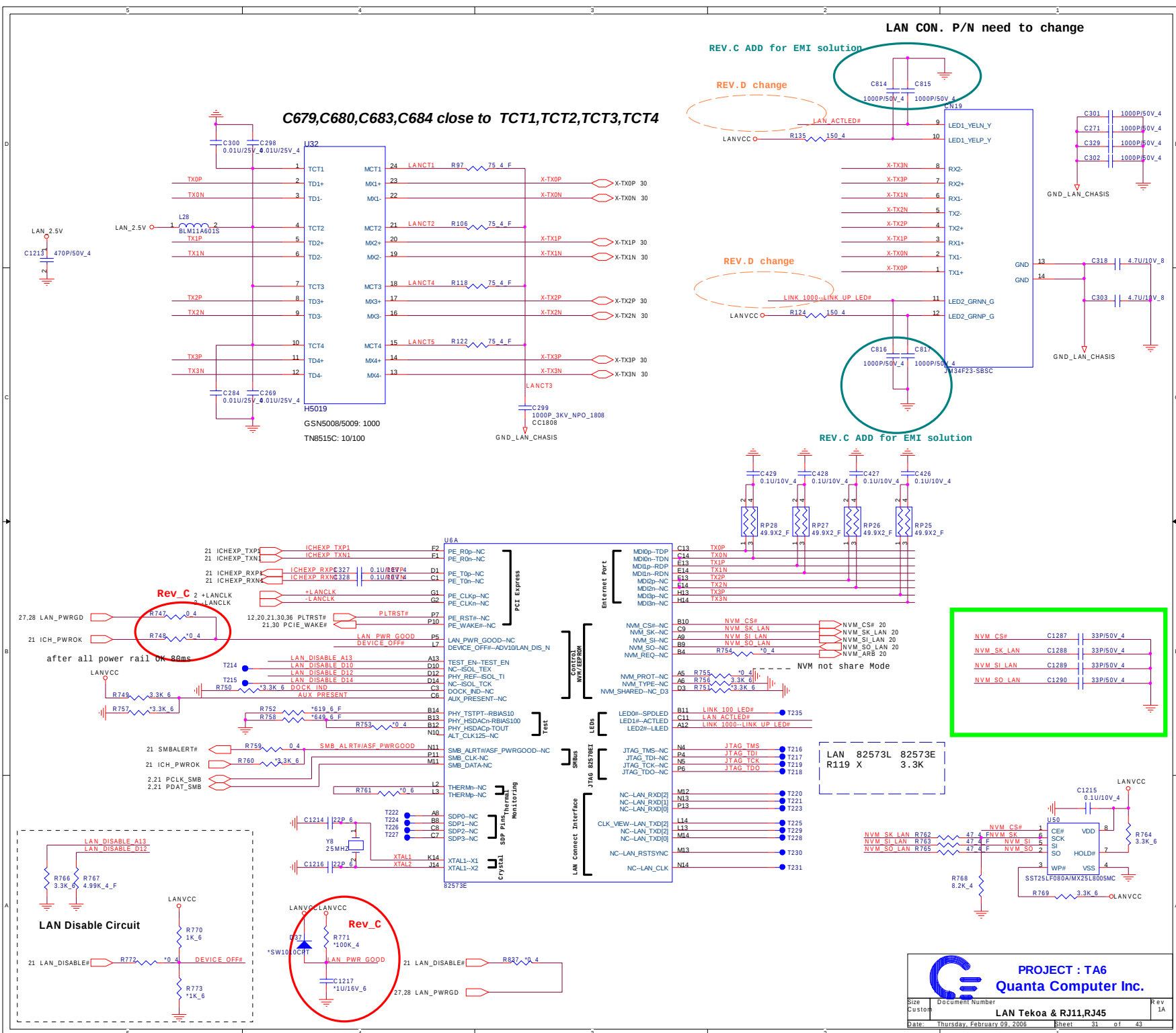
CABLE DOCK

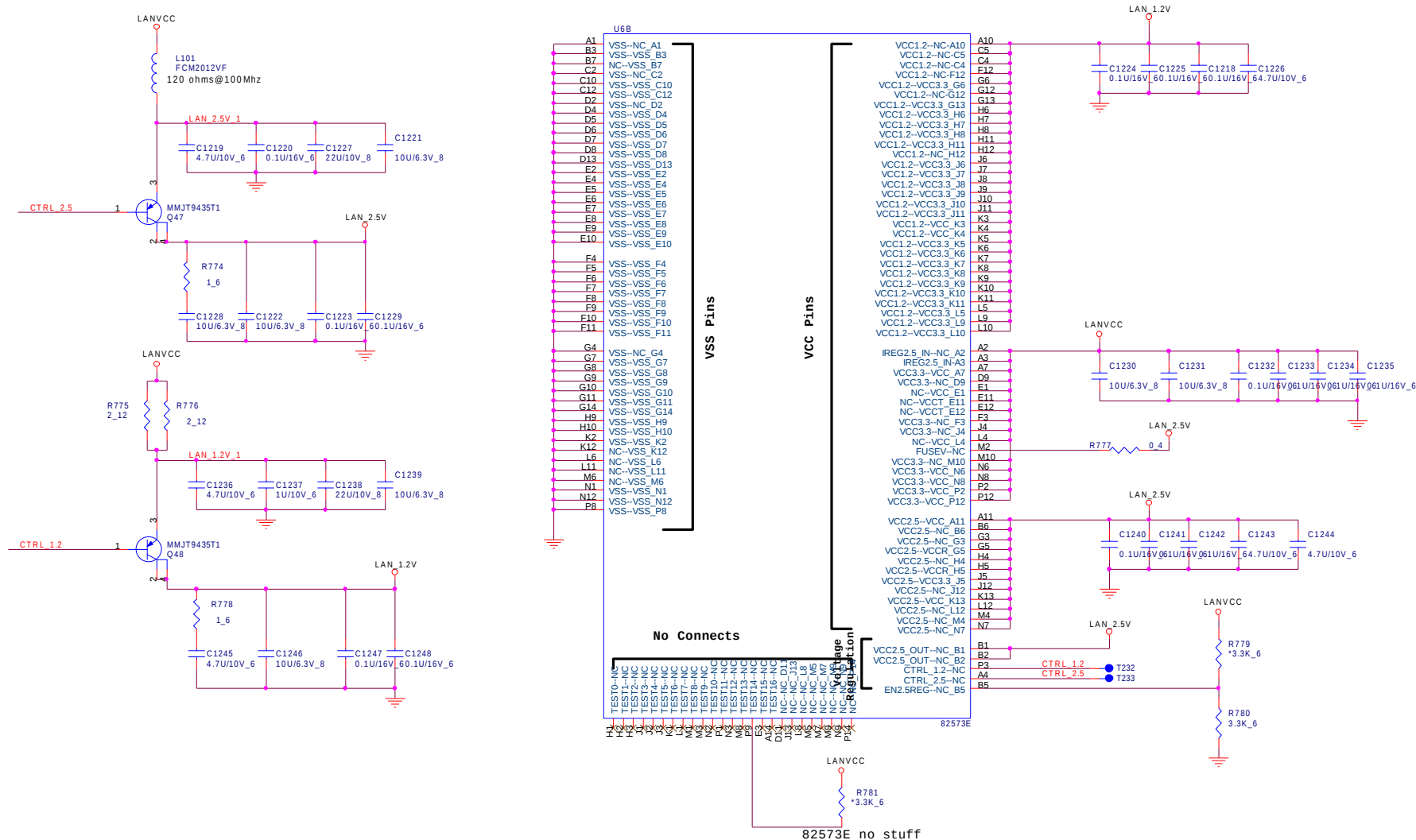
DOCK_ID1# & DOCK_ID2# don't use on Cable docking



TPM Connector



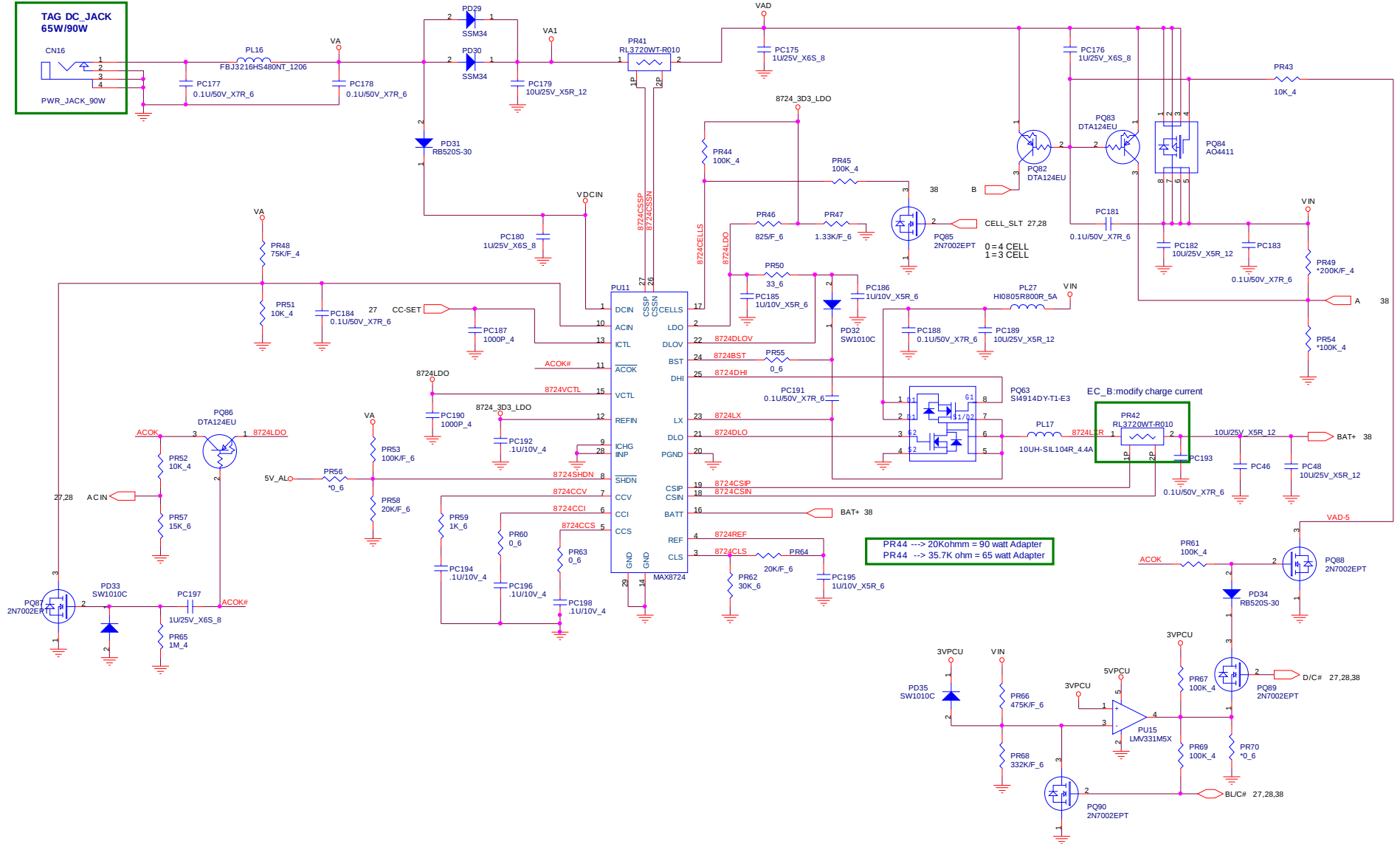


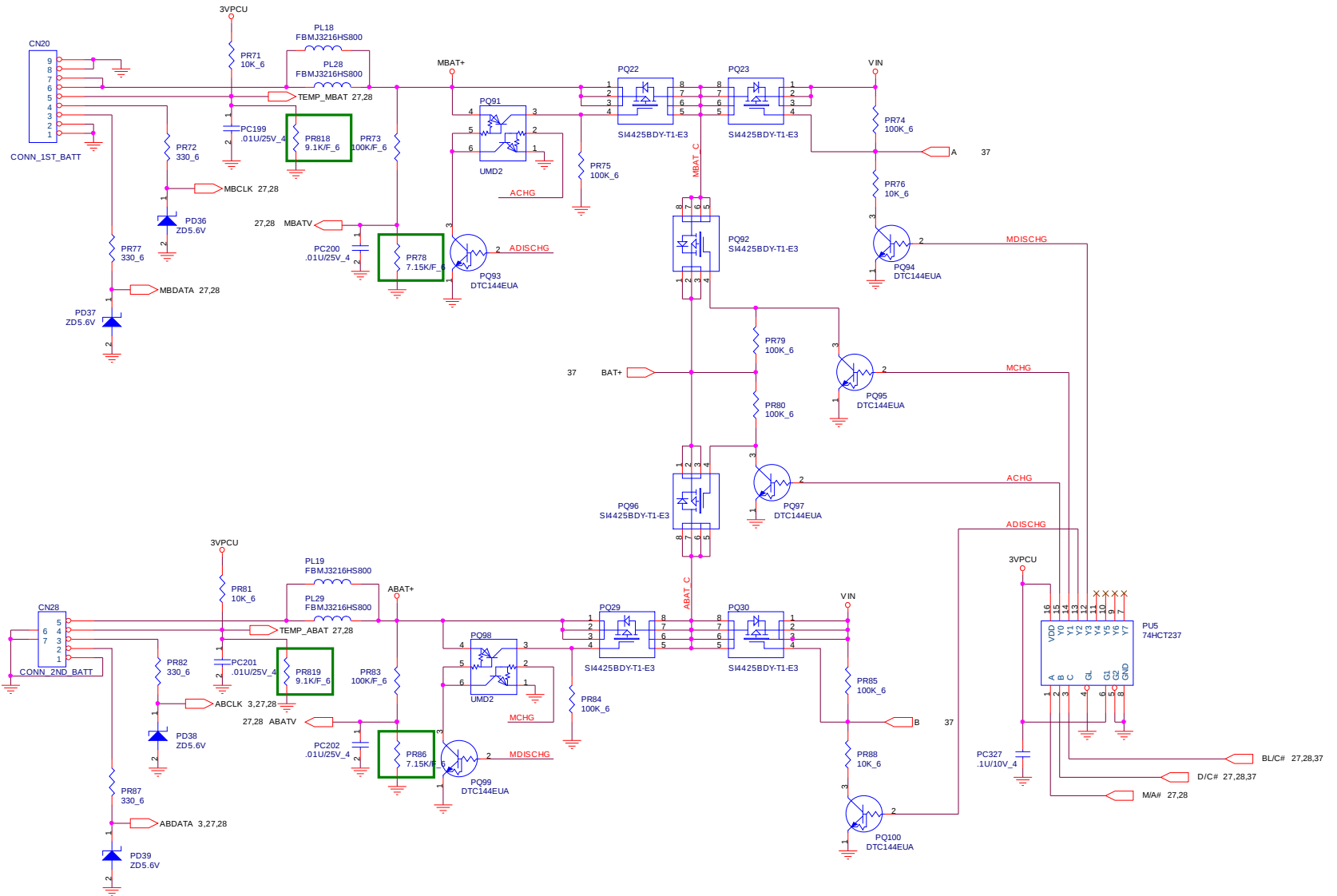


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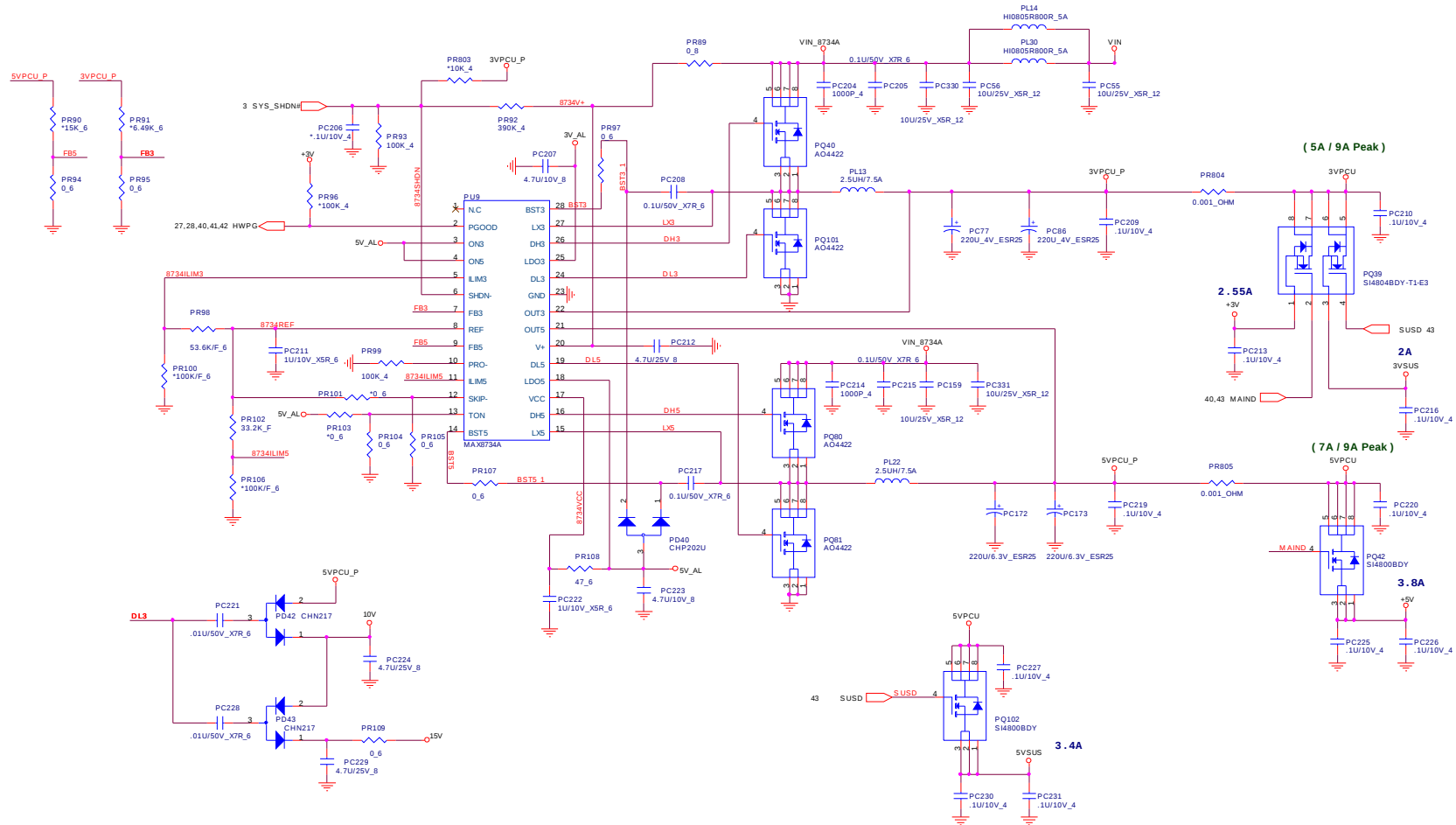


TAG DC JACK 65W/90W





DC-DC (MAX8734A)

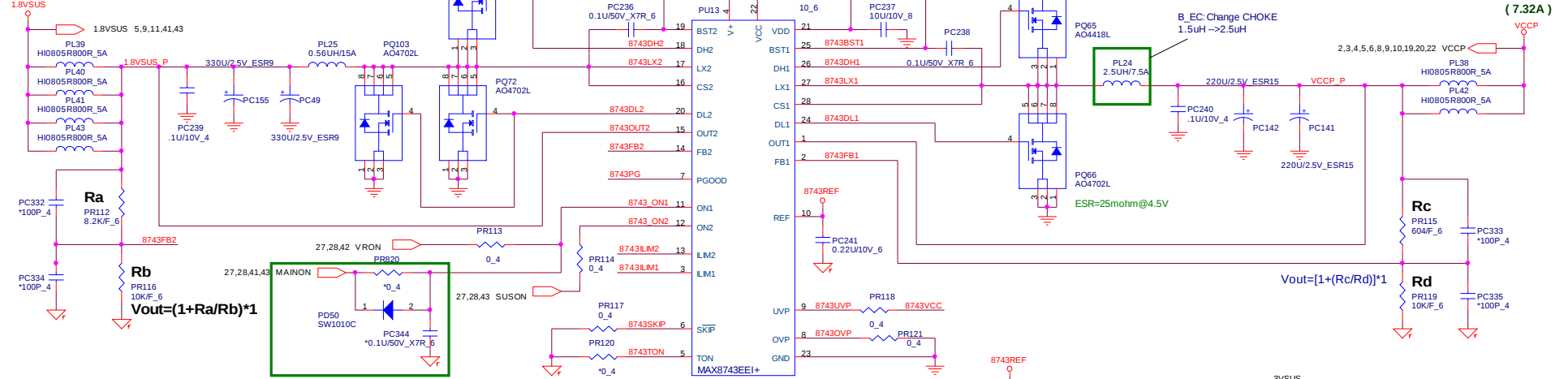


PROJECT : TA6
Quanta Computer Inc.

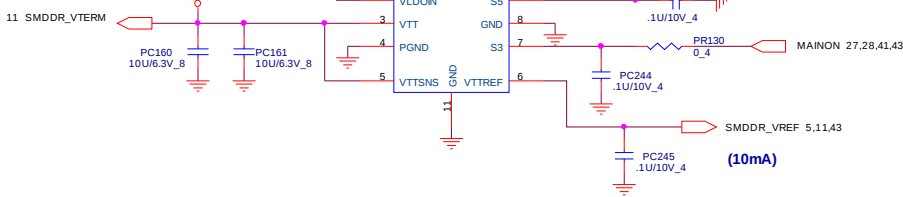
Size	Document Number	Rev
C	SYS PWR 5V/3.3V(MAX8734A)	2A
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MAX8743

(15A/Peak)



(2A / Peak 3A)



(10mA)

(2.5A)

