

Compal Confidential

STORM M/B Schematic

LA-C581P

Rev: 1.0_D

2015.08.13

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	COVER PAGE
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	1 of 102
				Rev	0.5

STORM Block-Diagram

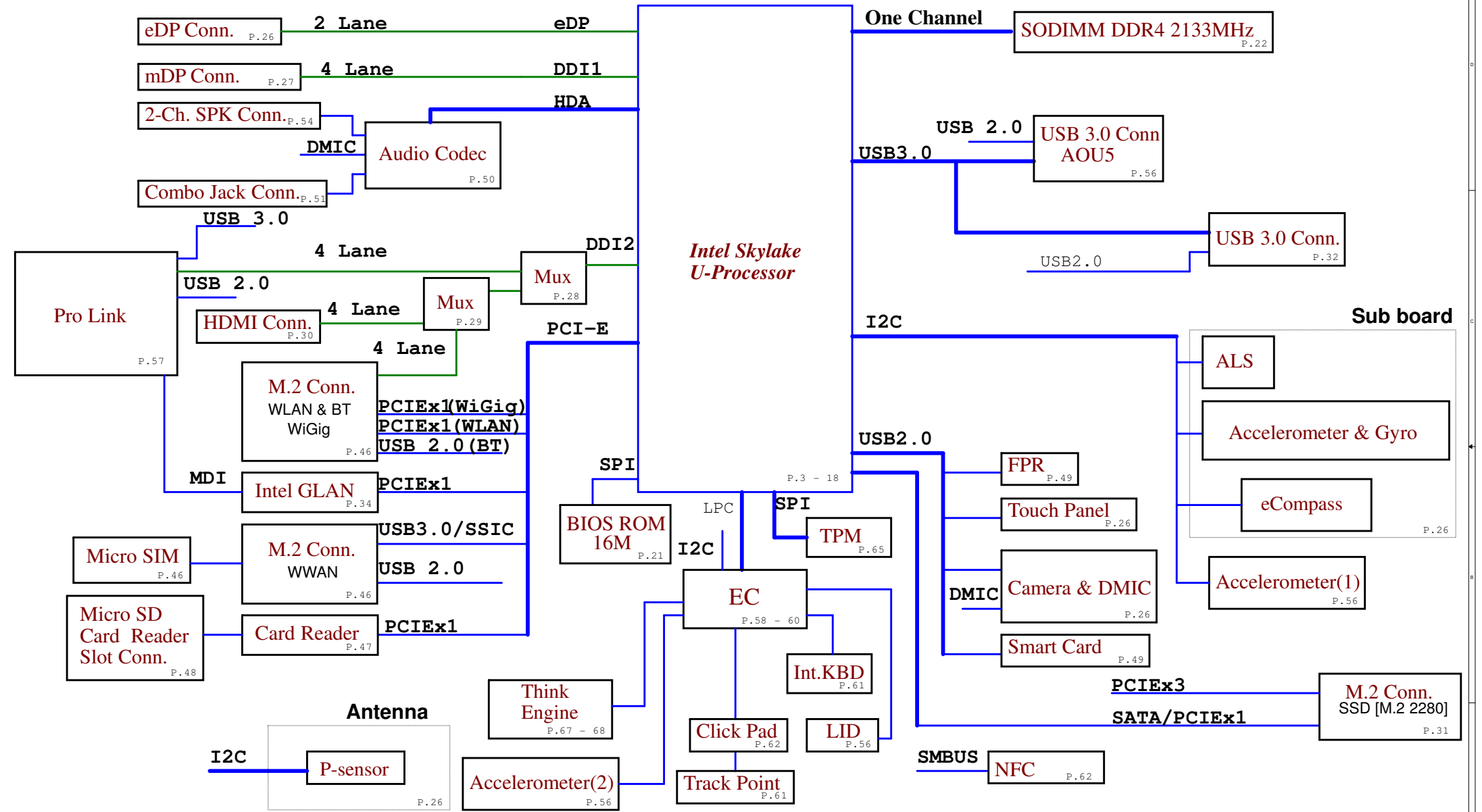


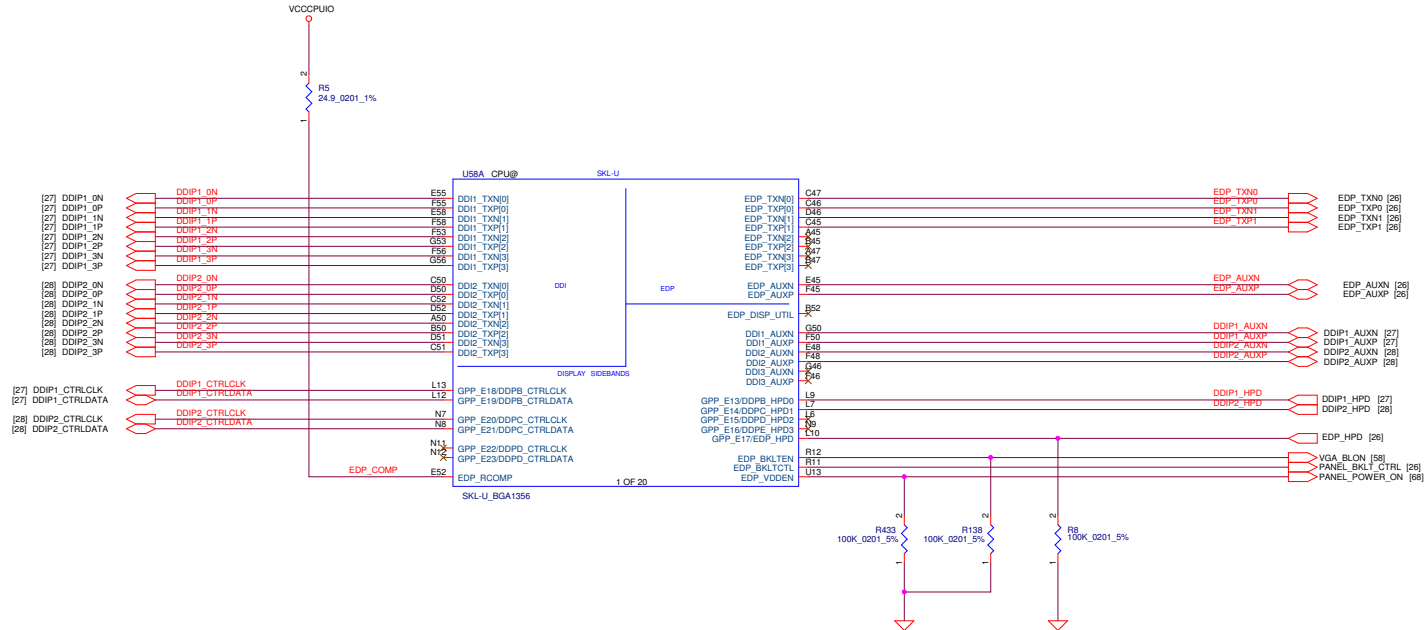
TABLE : Function Strap

DDPB_CTRLDATA

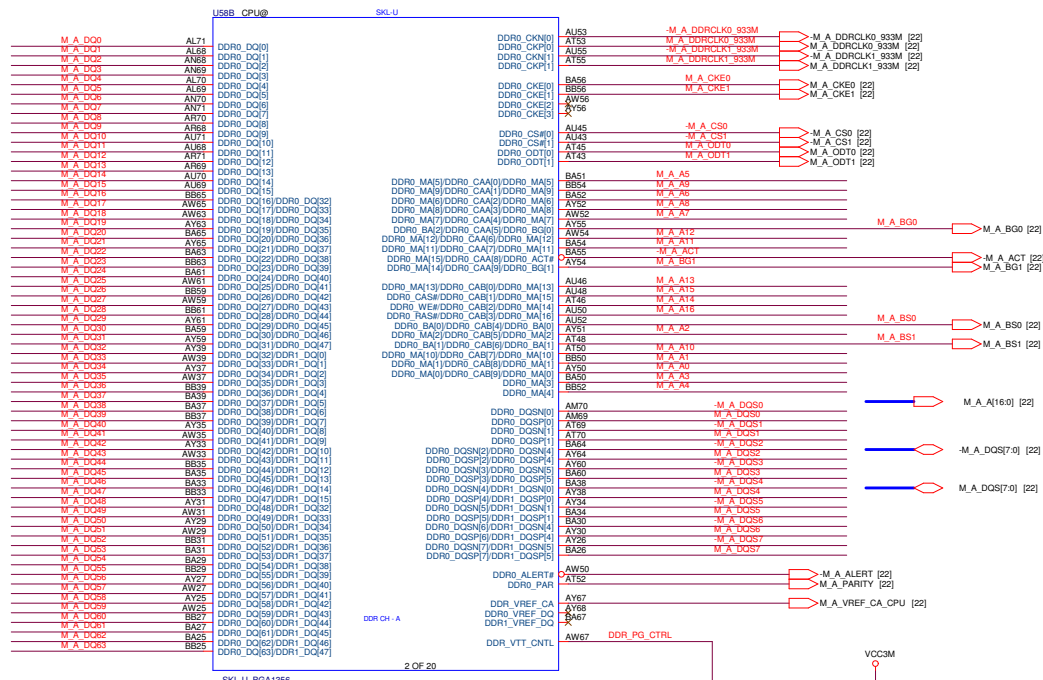
HIGH Port B is detected.
LOW Port B is not detected.

DDPC_CTRLDATA

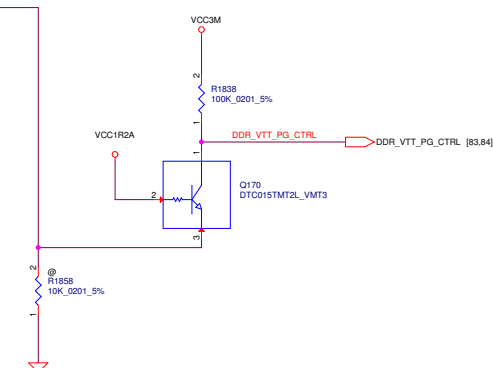
HIGH Port C is detected.
LOW Port C is not detected.



	Pin	Interleave	Non-Interleave
Block 0	AL71	DDRO_DQ[0]	DDRO_DQ[0]
	AL68	DDRO_DQ[1]	DDRO_DQ[1]
	AN68	DDRO_DQ[2]	DDRO_DQ[2]
	AN69	DDRO_DQ[3]	DDRO_DQ[3]
	AL70	DDRO_DQ[4]	DDRO_DQ[4]
	AL69	DDRO_DQ[5]	DDRO_DQ[5]
	AN70	DDRO_DQ[6]	DDRO_DQ[6]
	AN71	DDRO_DQ[7]	DDRO_DQ[7]
	AR68	DDRO_DQ[8]	DDRO_DQ[8]
	AR69	DDRO_DQ[9]	DDRO_DQ[9]
	AU71	DDRO_DQ[10]	DDRO_DQ[10]
	AU68	DDRO_DQ[11]	DDRO_DQ[11]
Block 2	AR71	DDRO_DQ[12]	DDRO_DQ[12]
	AR69	DDRO_DQ[13]	DDRO_DQ[13]
	AU70	DDRO_DQ[14]	DDRO_DQ[14]
	AU69	DDRO_DQ[15]	DDRO_DQ[15]
	BB65	DDRO_DQ[16]	DDRO_DQ[32]
	AW65	DDRO_DQ[17]	DDRO_DQ[33]
	AW63	DDRO_DQ[18]	DDRO_DQ[34]
	AY63	DDRO_DQ[19]	DDRO_DQ[35]
	BA65	DDRO_DQ[20]	DDRO_DQ[36]
	AY65	DDRO_DQ[21]	DDRO_DQ[37]
	BA63	DDRO_DQ[22]	DDRO_DQ[38]
	BB63	DDRO_DQ[23]	DDRO_DQ[39]
Block 4	BA61	DDRO_DQ[24]	DDRO_DQ[40]
	AW61	DDRO_DQ[25]	DDRO_DQ[41]
	BB59	DDRO_DQ[26]	DDRO_DQ[42]
	AW59	DDRO_DQ[27]	DDRO_DQ[43]
	BB61	DDRO_DQ[28]	DDRO_DQ[44]
	AY61	DDRO_DQ[29]	DDRO_DQ[45]
	BA59	DDRO_DQ[30]	DDRO_DQ[46]
	AY59	DDRO_DQ[31]	DDRO_DQ[47]
	AY39	DDRO_DQ[32]	DDRI_DQ[0]
	AW39	DDRO_DQ[33]	DDRI_DQ[1]
	AY37	DDRO_DQ[34]	DDRI_DQ[2]
	AW37	DDRO_DQ[35]	DDRI_DQ[3]
Block 6	BB39	DDRO_DQ[36]	DDRI_DQ[4]
	BA39	DDRO_DQ[37]	DDRI_DQ[5]
	BA37	DDRO_DQ[38]	DDRI_DQ[6]
	BB37	DDRO_DQ[39]	DDRI_DQ[7]
	AY35	DDRO_DQ[40]	DDRI_DQ[8]
	AW35	DDRO_DQ[41]	DDRI_DQ[9]
	AY33	DDRO_DQ[42]	DDRI_DQ[10]
	AW33	DDRO_DQ[43]	DDRI_DQ[11]
	BB35	DDRO_DQ[44]	DDRI_DQ[12]
	BA35	DDRO_DQ[45]	DDRI_DQ[13]
	BA33	DDRO_DQ[46]	DDRI_DQ[14]
	BB33	DDRO_DQ[47]	DDRI_DQ[15]
Block 8	AY31	DDRO_DQ[48]	DDRI_DQ[32]
	AW31	DDRO_DQ[49]	DDRI_DQ[33]
	AY29	DDRO_DQ[50]	DDRI_DQ[34]
	AW29	DDRO_DQ[51]	DDRI_DQ[35]
	BB31	DDRO_DQ[52]	DDRI_DQ[36]
	BA31	DDRO_DQ[53]	DDRI_DQ[37]
	BA29	DDRO_DQ[54]	DDRI_DQ[38]
	BB29	DDRO_DQ[55]	DDRI_DQ[39]
	AY27	DDRO_DQ[56]	DDRI_DQ[40]
	AW27	DDRO_DQ[57]	DDRI_DQ[41]
	AY25	DDRO_DQ[58]	DDRI_DQ[42]
	AW25	DDRO_DQ[59]	DDRI_DQ[43]
Block 10	BB27	DDRO_DQ[60]	DDRI_DQ[44]
	BA27	DDRO_DQ[61]	DDRI_DQ[45]
	BA25	DDRO_DQ[62]	DDRI_DQ[46]
	BB25	DDRO_DQ[63]	DDRI_DQ[47]



	Pin	Interleave	Non-Interleave
Block 0	AM70	DDR0_QDSN[0]	DDR0_QDSN[0]
	AM69	DDR0_QDSP[0]	DDR0_QDSP[0]
	AT69	DDR0_QDSN[1]	DDR0_QDSN[1]
	AT70	DDR0_QDSP[1]	DDR0_QDSP[1]
Block 2	BA64	DDR0_QDSN[2]	DDR0_QDSN[4]
	AY64	DDR0_QDSP[2]	DDR0_QDSP[4]
	AY60	DDR0_QDSN[3]	DDR0_QDSN[5]
	BA60	DDR0_QDSP[3]	DDR0_QDSP[5]
Block 4	BA38	DDR0_QDSN[4]	DDR1_QDSN[0]
	AY38	DDR0_QDSP[4]	DDR1_QDSP[0]
	AY34	DDR0_QDSN[5]	DDR1_QDSN[1]
	BA34	DDR0_QDSP[5]	DDR1_QDSP[1]
Block 6	BA30	DDR0_QDSN[6]	DDR1_QDSN[4]
	AY30	DDR0_QDSP[6]	DDR1_QDSP[4]
	AY26	DDR0_QDSN[7]	DDR1_QDSN[5]
	BA26	DDR0_QDSP[7]	DDR1_QDSP[5]



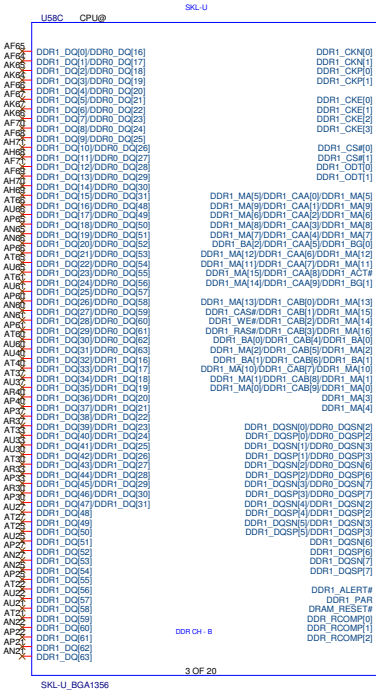
Pin	D0R3L	LPDDR3	DDR4
BA51	DDRO_MA[5]	DDRO_CAA[0]	DDRO_MA[5]
B854	DDRO_MA[9]	DDRO_CAA[1]	DDRO_MA[9]
BA52	DDRO_MA[6]	DDRO_CAA[2]	DDRO_MA[6]
AY52	DDRO_MA[8]	DDRO_CAA[3]	DDRO_MA[8]
AW52	DDRO_MA[7]	DDRO_CAA[4]	DDRO_MA[7]
AY55	DDRO_BA[2]	DDRO_CAA[5]	DDRO_BG[0]
AW54	DDRO_MA[12]	DDRO_CAA[6]	DDRO_MA[12]
BA54	DDRO_MA[11]	DDRO_CAA[7]	DDRO_MA[11]
BA55	DDRO_MA[15]	DDRO_CAA[8]	DDRO_ACT*
AY54	DDRO_MA[14]	DDRO_CAA[9]	DDRO_BG[1]
AU46	DDRO_MA[13]	DDRO_CAB[0]	DDRO_MA[13]
AU48	DDRO_CAS*	DDRO_CAB[1]	DDRO_MA[15]
AU56	DDRO_W#*	DDRO_CAB[2]	DDRO_MA[14]
AU50	DDRO_RAS*	DDRO_CAB[3]	DDRO_MA[16]
AU52	DDRO_BA[0]	DDRO_CAB[4]	DDRO_BA[0]
AY51	DDRO_CAB[2]	DDRO_CAB[5]	DDRO_MA[2]
AT58	DDRO_BA[1]	DDRO_CAB[6]	DDRO_BA[1]
AT50	DDRO_MA[10]	DDRO_CAB[7]	DDRO_MA[10]
B550	DDRO_MA[1]	DDRO_CAB[8]	DDRO_MA[1]
AY50	DDRO_MA[0]	DDRO_CAB[9]	DDRO_MA[0]
BA50	DDRO_MA[3]	Not Used	DDRO_MA[3]
B852	DDRO_MA[4]	Not Used	DDRO_MA[4]

↑
LOGIC

TABLE

	Pin	Interleave	Non-Interleave
Block 1	AF65	DDR1_DQ[0]	DDR0_DQ[16]
	AF64	DDR1_DQ[1]	DDR0_DQ[17]
	AK65	DDR1_DQ[2]	DDR0_DQ[18]
	AK64	DDR1_DQ[3]	DDR0_DQ[19]
	AF66	DDR1_DQ[4]	DDR0_DQ[20]
	AF67	DDR1_DQ[5]	DDR0_DQ[21]
	AK67	DDR1_DQ[6]	DDR0_DQ[22]
	AK66	DDR1_DQ[7]	DDR0_DQ[23]
	AF70	DDR1_DQ[8]	DDR0_DQ[24]
	AF68	DDR1_DQ[9]	DDR0_DQ[25]
	AH71	DDR1_DQ[10]	DDR0_DQ[26]
	AH68	DDR1_DQ[11]	DDR0_DQ[27]
	AF71	DDR1_DQ[12]	DDR0_DQ[28]
	AH70	DDR1_DQ[13]	DDR0_DQ[29]
	AF69	DDR1_DQ[14]	DDR0_DQ[30]
	AH69	DDR1_DQ[15]	DDR0_DQ[31]
Block 3	AT66	DDR1_DQ[16]	DDR0_DQ[48]
	AU66	DDR1_DQ[17]	DDR0_DQ[49]
	AP65	DDR1_DQ[18]	DDR0_DQ[50]
	AN65	DDR1_DQ[19]	DDR0_DQ[51]
	AN66	DDR1_DQ[20]	DDR0_DQ[52]
	AP66	DDR1_DQ[21]	DDR0_DQ[53]
	AT65	DDR1_DQ[22]	DDR0_DQ[54]
	AU65	DDR1_DQ[23]	DDR0_DQ[55]
	AT61	DDR1_DQ[24]	DDR0_DQ[56]
	AU61	DDR1_DQ[25]	DDR0_DQ[57]
	AP60	DDR1_DQ[26]	DDR0_DQ[58]
	AN60	DDR1_DQ[27]	DDR0_DQ[59]
	AN61	DDR1_DQ[28]	DDR0_DQ[60]
	AP61	DDR1_DQ[29]	DDR0_DQ[61]
	AT60	DDR1_DQ[30]	DDR0_DQ[62]
	AU60	DDR1_DQ[31]	DDR0_DQ[63]
Block 5	AU40	DDR1_DQ[32]	DDR1_DQ[16]
	AT40	DDR1_DQ[33]	DDR1_DQ[17]
	AT37	DDR1_DQ[34]	DDR1_DQ[18]
	AU37	DDR1_DQ[35]	DDR1_DQ[19]
	AR40	DDR1_DQ[36]	DDR1_DQ[20]
	AP40	DDR1_DQ[37]	DDR1_DQ[21]
	AP37	DDR1_DQ[38]	DDR1_DQ[22]
	AR37	DDR1_DQ[39]	DDR1_DQ[23]
	AT33	DDR1_DQ[40]	DDR1_DQ[24]
	AU33	DDR1_DQ[41]	DDR1_DQ[25]
	AU30	DDR1_DQ[42]	DDR1_DQ[26]
	AT30	DDR1_DQ[43]	DDR1_DQ[27]
	AR33	DDR1_DQ[44]	DDR1_DQ[28]
	AP33	DDR1_DQ[45]	DDR1_DQ[29]
	AR30	DDR1_DQ[46]	DDR1_DQ[30]
	AP30	DDR1_DQ[47]	DDR1_DQ[31]
Block 7	AU27	DDR1_DQ[48]	DDR1_DQ[48]
	AT27	DDR1_DQ[49]	DDR1_DQ[49]
	AT25	DDR1_DQ[50]	DDR1_DQ[50]
	AU25	DDR1_DQ[51]	DDR1_DQ[51]
	AP27	DDR1_DQ[52]	DDR1_DQ[52]
	AN27	DDR1_DQ[53]	DDR1_DQ[53]
	AN25	DDR1_DQ[54]	DDR1_DQ[54]
	AP25	DDR1_DQ[55]	DDR1_DQ[55]
	AT22	DDR1_DQ[56]	DDR1_DQ[56]
	AU22	DDR1_DQ[57]	DDR1_DQ[57]
	AU21	DDR1_DQ[58]	DDR1_DQ[58]
	AT21	DDR1_DQ[59]	DDR1_DQ[59]
	AN22	DDR1_DQ[60]	DDR1_DQ[60]
	AP22	DDR1_DQ[61]	DDR1_DQ[61]
	AP21	DDR1_DQ[62]	DDR1_DQ[62]
	AN21	DDR1_DQ[63]	DDR1_DQ[63]

LOGIC



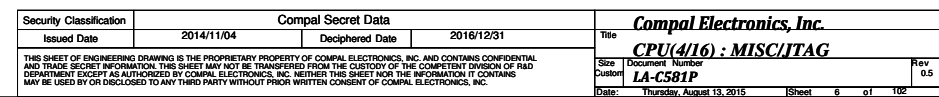
	Pin	Interleave	Non-Interleave
Block 1	AH66	DDR1_DQSN[0]	DDR0_DQSN[2]
	AH65	DDR1_DQSP[0]	DDR0_DQSP[2]
	AG69	DDR1_DQSN[1]	DDR0_DQSN[3]
Block 3	AR66	DDR1_DQSN[2]	DDR0_DQSN[6]
	AR65	DDR1_DQSP[2]	DDR0_DQSP[6]
	AR61	DDR1_DQSN[3]	DDR0_DQSN[7]
Block 5	AT38	DDR1_DQSN[4]	DDR1_DQSN[2]
	AT32	DDR1_DQSP[4]	DDR1_DQSP[2]
	AR32	DDR1_DQSN[5]	DDR1_DQSN[3]
Block 7	AR25	DDR1_DQSN[6]	DDR1_DQSN[6]
	AR27	DDR1_DQSP[6]	DDR1_DQSP[6]
	AR22	DDR1_DQSN[7]	DDR1_DQSN[7]
Block 7	AR21	DDR1_DQSP[7]	DDR1_DQSP[7]

LOGIC

TABLE

Pin	DDR3L	LPDDR3	DDR4
AY48	DDR1_MA[5]	DDR1_CAA[0]	DDR1_MA[5]
AP50	DDR1_MA[9]	DDR1_CAA[1]	DDR1_MA[9]
BA48	DDR1_MA[6]	DDR1_CAA[2]	DDR1_MA[6]
BB48	DDR1_MA[8]	DDR1_CAA[3]	DDR1_MA[8]
AP48	DDR1_MA[7]	DDR1_CAA[4]	DDR1_MA[7]
AP52	DDR1_BA[2]	DDR1_CAA[5]	DDR1_BG[0]
AN50	DDR1_MA[12]	DDR1_CAA[6]	DDR1_MA[12]
AN48	DDR1_MA[11]	DDR1_CAA[7]	DDR1_MA[11]
AN53	DDR1_MA[15]	DDR1_CAA[8]	DDR1_ACT#
AN52	DDR1_MA[14]	DDR1_CAA[9]	DDR1_BG[1]
BA43	DDR1_MA[13]	DDR1_CAB[0]	DDR1_MA[13]
AY43	DDR1_CAS#	DDR1_CAB[1]	DDR1_MA[15]
AY44	DDR1_WE#	DDR1_CAB[2]	DDR1_MA[14]
AW44	DDR1_BA[0]	DDR1_CAB[3]	DDR1_MA[16]
BB44	DDR1_BA[1]	DDR1_CAB[4]	DDR1_MA[10]
AY47	DDR1_MA[2]	DDR1_CAB[5]	DDR1_MA[2]
BA44	DDR1_BA[1]	DDR1_CAB[6]	DDR1_BA[1]
AW46	DDR1_MA[10]	DDR1_CAB[7]	DDR1_MA[10]
AY46	DDR1_MA[1]	DDR1_CAB[8]	DDR1_MA[1]
BA46	DDR1_MA[0]	DDR1_CAB[9]	DDR1_MA[0]
BB46	DDR1_MA[3]	Not Used	DDR1_MA[3]
BA47	DDR1_MA[4]	Not Used	DDR1_MA[4]

LOGIC



SPI0_MOSI (Boot Halt)	
HIGH	Disabled (Default)
LOW	Enabled

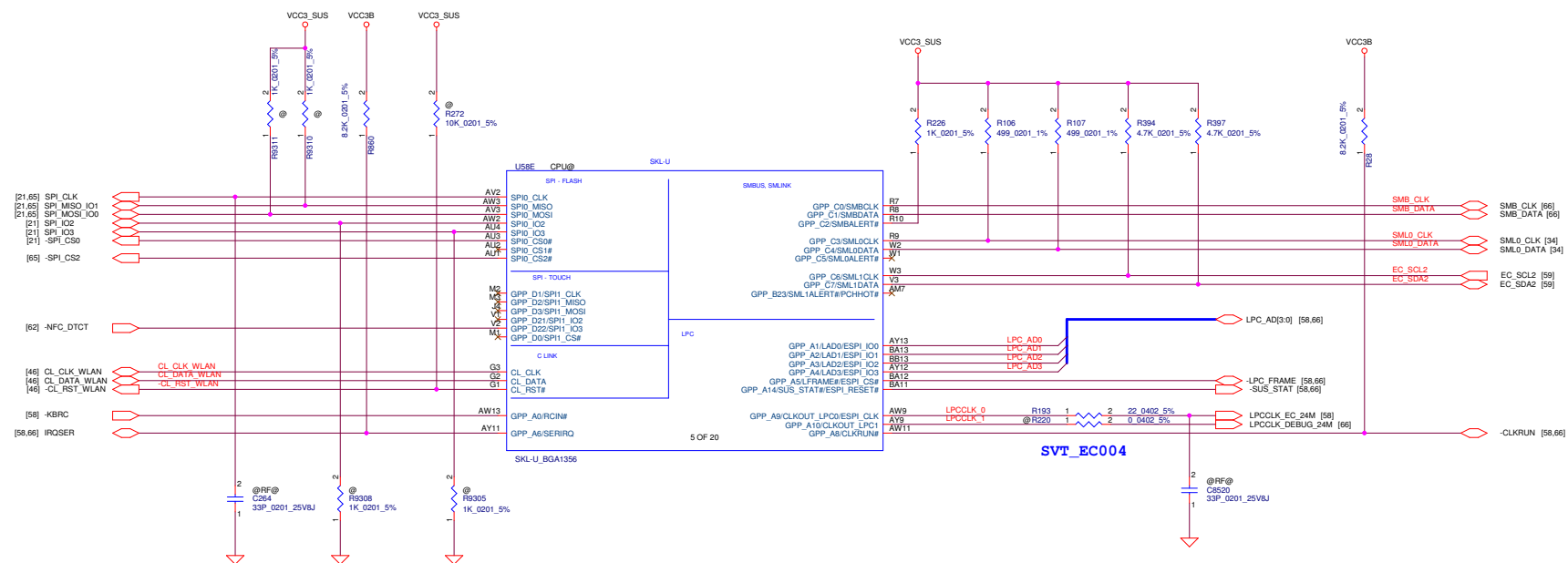
SPI0_MISO (JTAG ODT Diabler)	
HIGH	Enabled (Default)
LOW	Disabled

GPP_C5/SML0ALERT # (LPC or eSPI)	
HIGH	eSPI is selected
LOW	LPC is selected (Default)

← LOGIC

GPP_C2/SMBALERT# (TLS Confidentiality)	
HIGH	Enable ME Crypto TLS with Confidentiality
LOW	Disable ME Crypto TLS (Default)

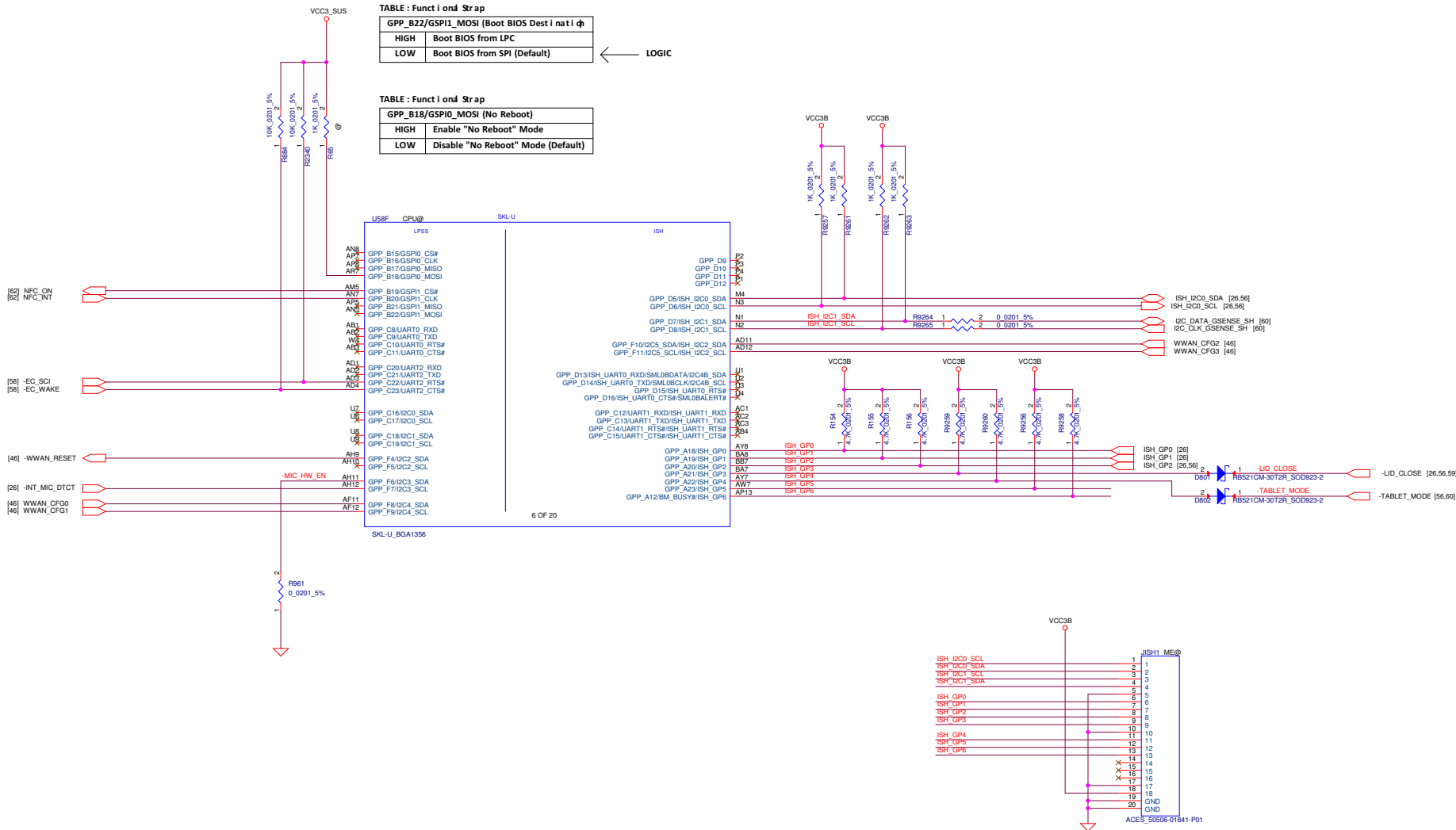
← LOGIC



SPI0_IO2 (Consent Strap)	
HIGH	Enabled (Default)
LOW	Disabled

SPI0_IO3 (A0 Personality Strap)	
HIGH	Disabled (Default)
LOW	Enabled

Security Classification	Compal Secret Data		Title
Issued Date	2014/11/04	Deciphered Date	2016/12/31
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAQ ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D TO ANY OTHER DEPARTMENT OR DIVISION OF COMPAQ ELECTRONICS, INC. WITHOUT THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAQ ELECTRONICS, INC.			Compal Electronics, Inc. CPU(5/16) : LPC/SPI/SMBUS/C-LINK Size (mm) 125.00 x 125.00 Date LA-C581P (Project) April 15 2015 (Sheet) 9 of 162



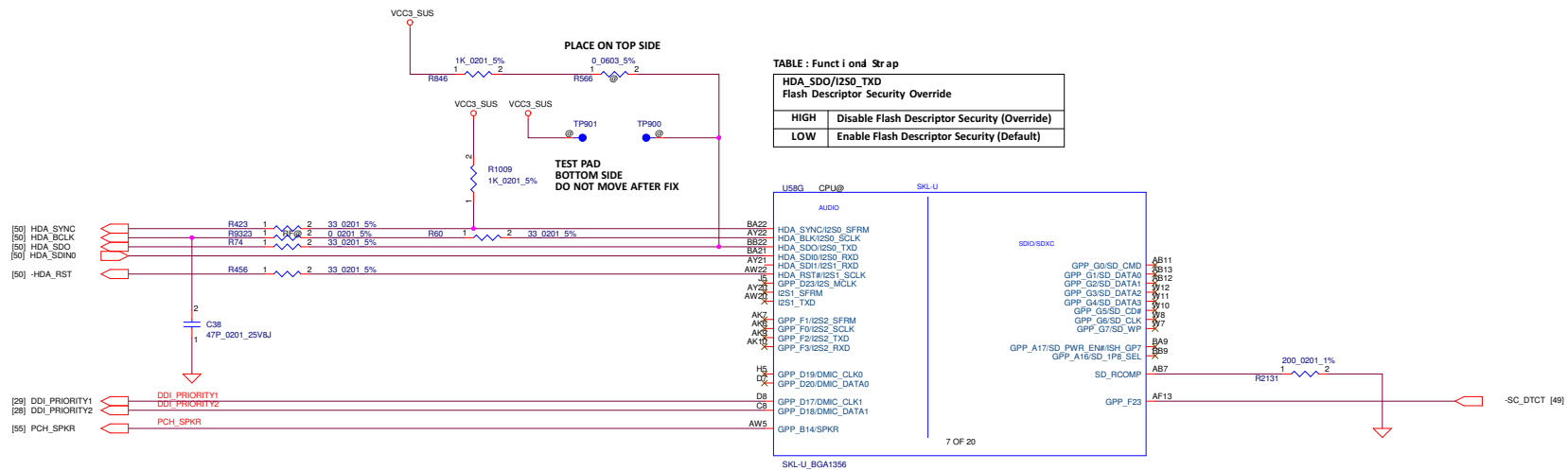


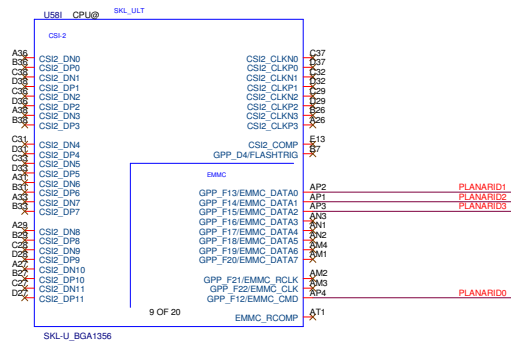
TABLE : Function Strap

HDA_SDO/I2SD_TXD Flash Descriptor Security Override	
HIGH	Disable Flash Descriptor Security (Override)
LOW	Enable Flash Descriptor Security (Default)

TABLE : Function Strap

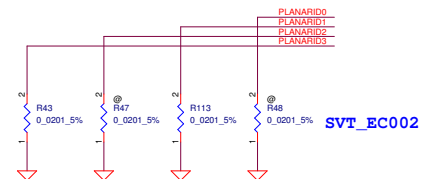
GPP_B14/SPKR (Top Swap Override)	
HIGH	Enable "Top Swap" Mode
LOW	Disable "Top Swap" Mode (Default)

← LOGIC



TABLE

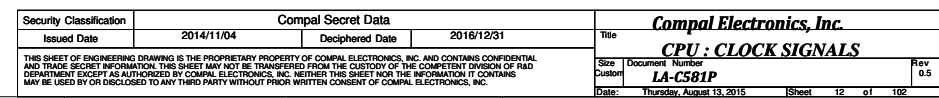
LEVEL	PLANAR ID			
	3	2	1	0
	R43	R47	R113	R48
1	NA	NA	NA	NA
0	ASM	ASM	ASM	ASM



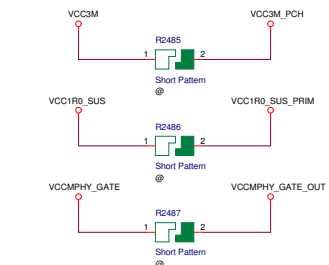
TABLE

LEVEL	PLANARID[3..0]
PDV	0001B
FVT	0010B
SIT	0011B
SIT-R	0100B
SVT	0101B

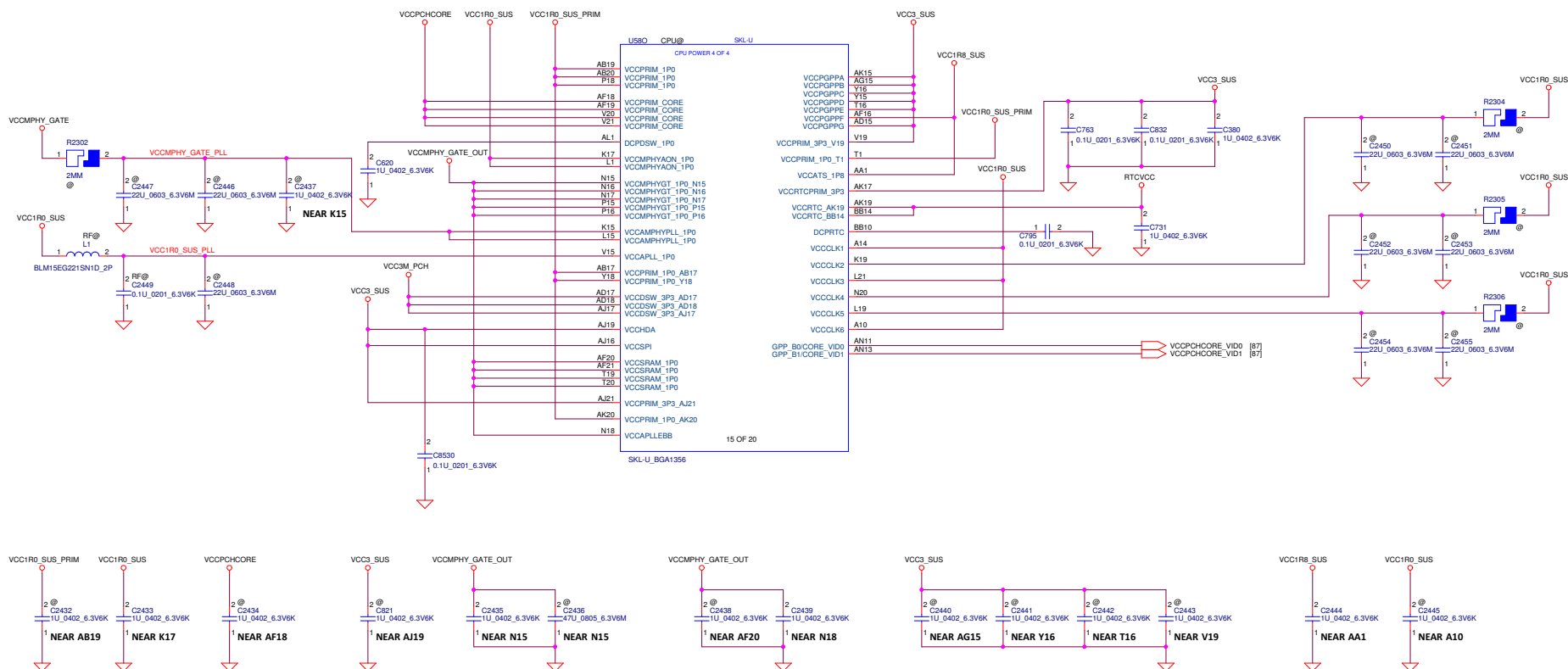
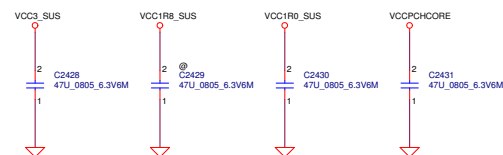
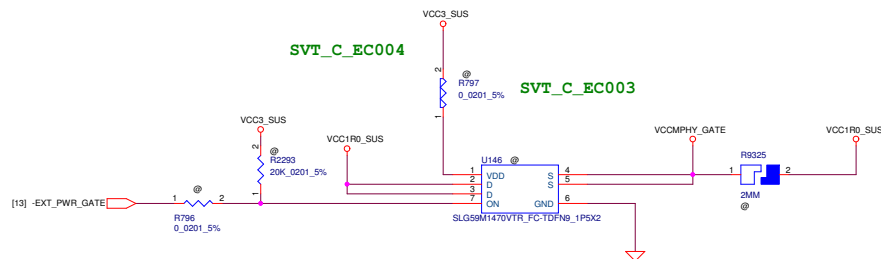
Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Document Number Rev
				Custom LA-CS81P 0.5
				Date: Thursday, August 13, 2015 Sheet 11 of 102



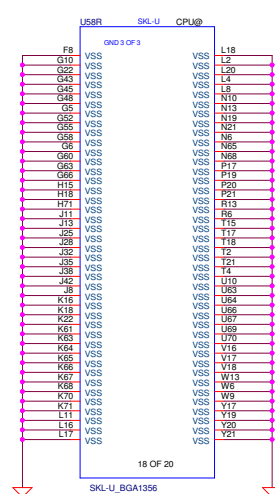
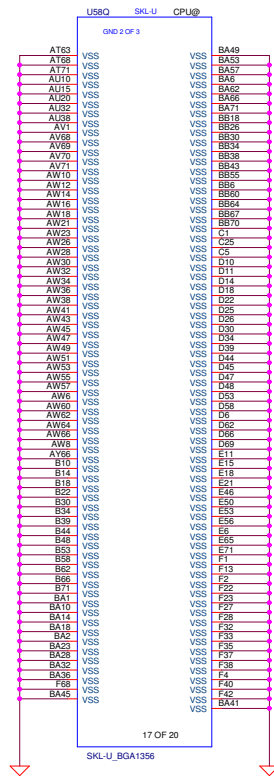
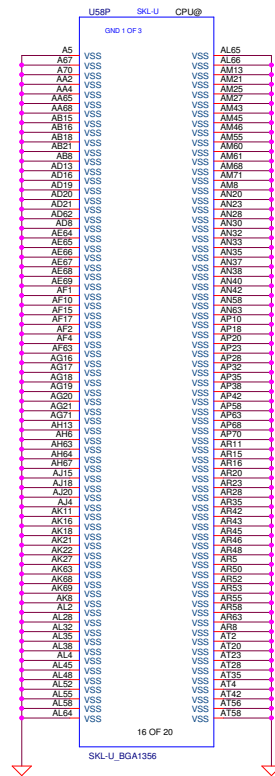
Short Pattern: R2485, R2486, R2487



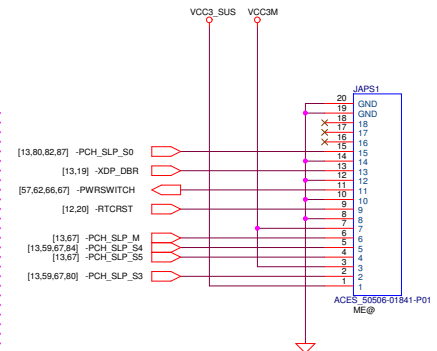
SVT C EC003



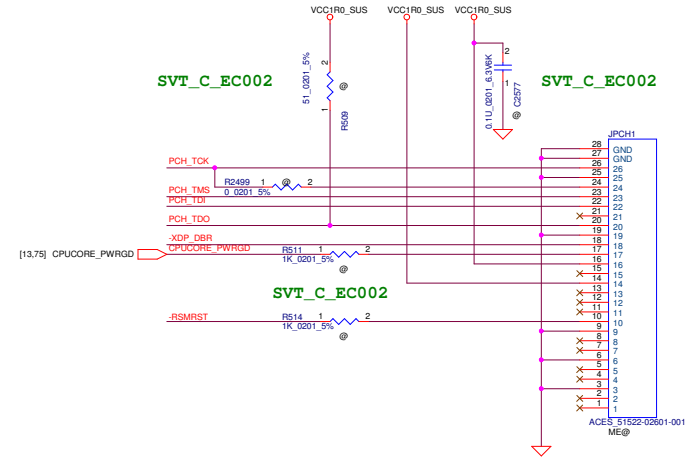
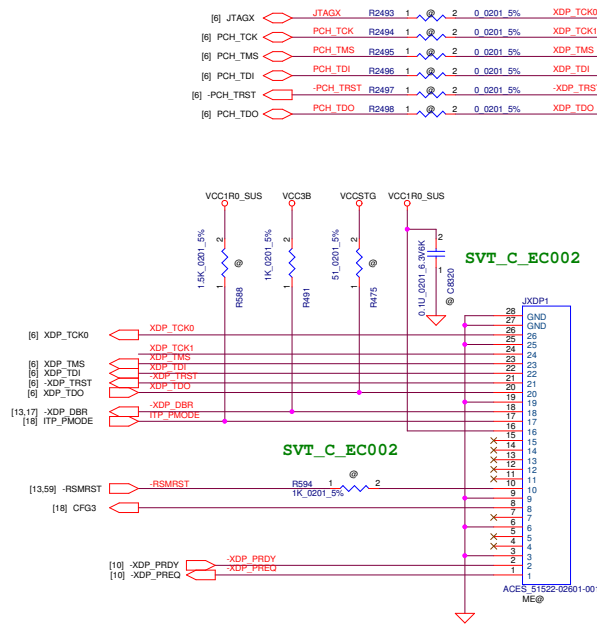
Security Classification	Compal Secret Data				Compal Electronics, Inc. CPU : PCH POWER		
Issued Date	2014/11/04	Deciphered Date	2016/12/31		Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND/OR UNCLASSIFIED INFORMATION THAT IS NOT BE TRANSMITTED OR DISCLOSED TO ANY OTHER PERSON OR ORGANIZATION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					Size		
					Docu- ment Number		Rev 0.5
					LA-C581P		
					Date: Thursday, August 13, 2015	Sheet 16 of 102	



APS/PETS Interface



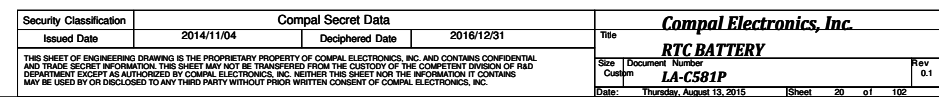
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	CPU : GND
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custord	1A-C581P
				Date:	Thursday, August 13, 2015
				Sheet	17 of 102

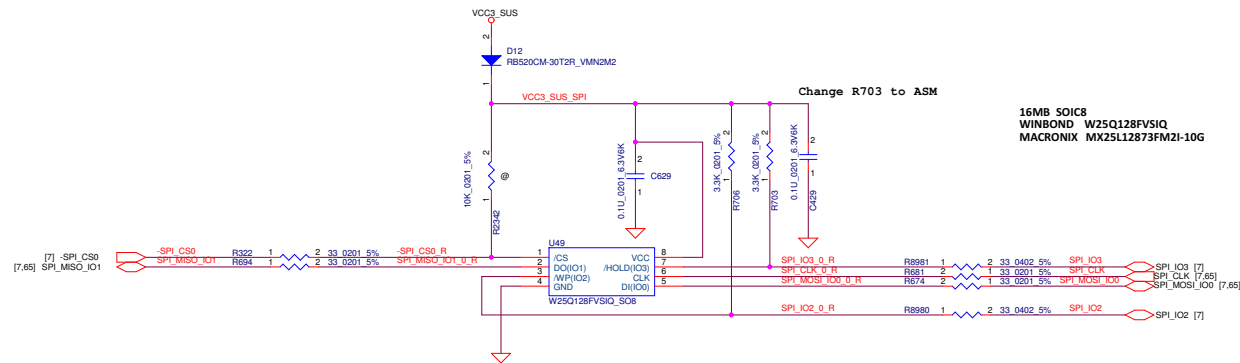


TABLE

	Individual Port	Merged Port	DCI 2.0 w/o Connector
R2	ASM	ASM	ASM
R471	ASM	NO_ASM	NO_ASM
R541	ASM	NO_ASM	NO_ASM
R515	ASM	NO_ASM	NO_ASM
R530	ASM	NO_ASM	NO_ASM
R475	ASM	ASM	ASM
J8	ASM	ASM	NO_ASM
C8320	ASM	ASM	NO_ASM
R491	ASM	ASM	ASM
R588	ASM	ASM	NO_ASM
R594	ASM	ASM	NO_ASM
R2499	NO_ASM	NO_ASM	NO_ASM
R509	ASM	NO_ASM	NO_ASM
J9	ASM	ASM	NO_ASM
C2577	ASM	ASM	NO_ASM
R511	ASM	ASM	NO_ASM
R514	ASM	ASM	NO_ASM
R2493	NO_ASM	ASM	ASM
R2494	NO_ASM	ASM	NO_ASM
R2495	NO_ASM	ASM	ASM
R2496	NO_ASM	ASM	ASM
R2497	NO_ASM	ASM	ASM
R2498	NO_ASM	ASM	ASM

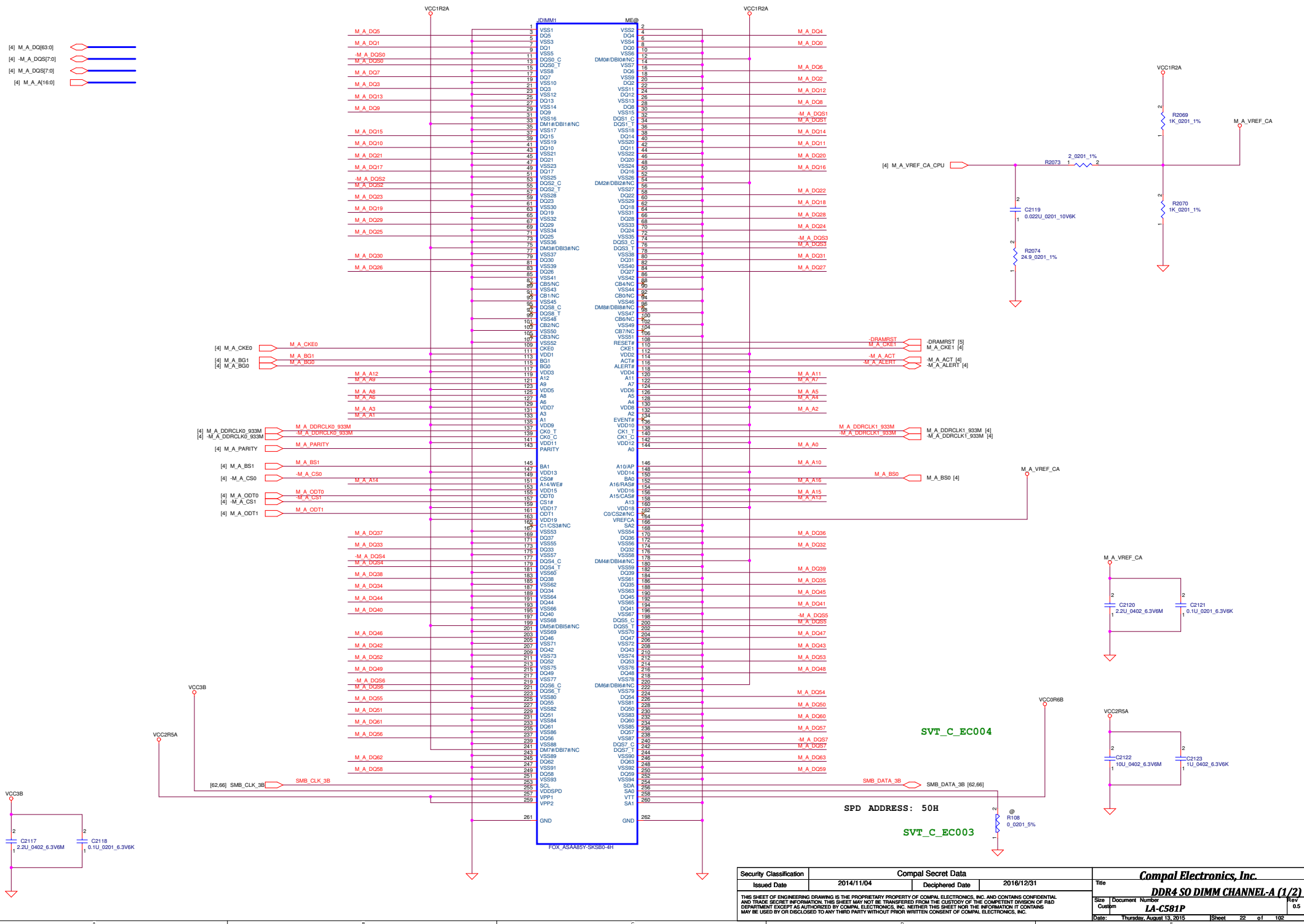
↑
LOGIC



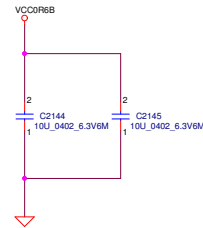
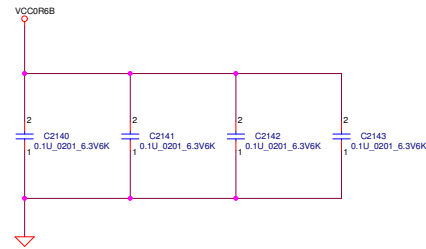
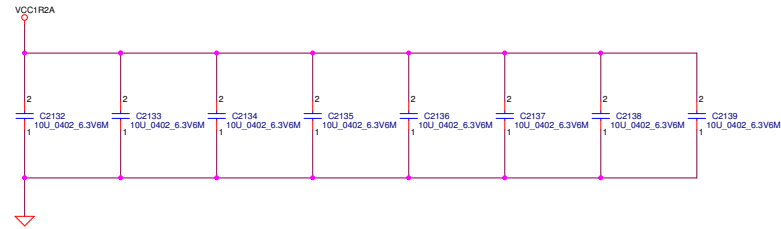
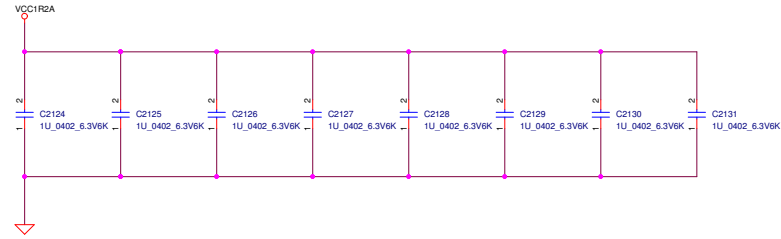


TABLE

SF100 PIN HEADER INTERFACE (TOP VIEW)							
1	VCC	D12.1	GND	GND	2		
3	CS#	R322.2	R681.2	CLK	4		
5	MISO	R694.2	R674.2	MOSI	6		
7	(KEY)	N/A	N/A	(RESET)	8		



Security Classification		Compal Secret Data		Compal Electronics, Inc. DDR4 SO DIMM CHANNEL-A (1/2)	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-C581P
				Date:	Thursday, January 13, 2015
				Sheet	22 of 102



Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Document Number	0.5
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	23 of 102

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	BLANK
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	24 of 102
				Rev	0.5

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	BLANK
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	25 of 102
				Rev	0.5

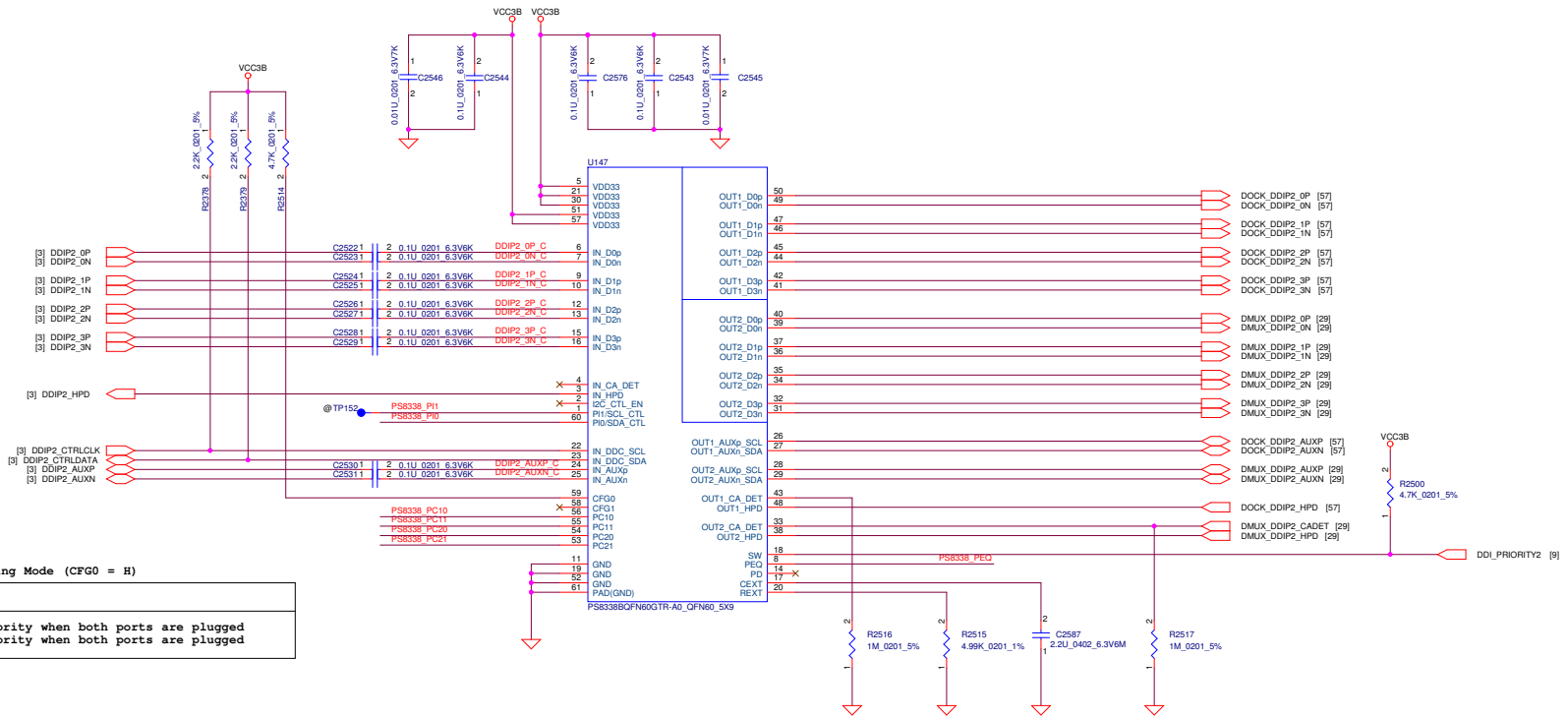


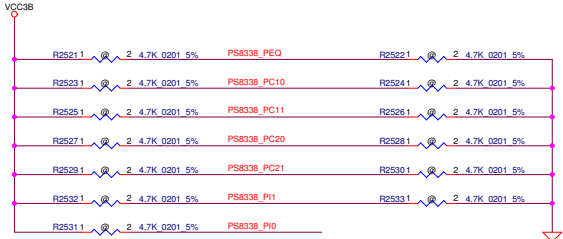
TABLE : Automatic Switching Mode (CFG0 = H)

SW (DDI_PRIORITY2)
L Port 1 has higher priority when both ports are plugged
H Port 2 has higher priority when both ports are plugged

PEQ (INT PD)	R2521	R2522
HEQ 14.5dB	ASM	NO_ASM
LLEQ 8.5dB	ASM	ASM
LEQ 11.5dB	NO_ASM	NO_ASM

	PC10 (INT PD)		PC20 (INT PD)	
	R2523	R2524	R2527	R2528
AUX interception DIS Output 800mV & 0dB	ASM	NO_ASM	ASM	NO_ASM
AUX interception DIS Output 400mV & 0dB	ASM	ASM	ASM	ASM
AUX interception EN	NO_ASM	NO_ASM	NO_ASM	NO_ASM

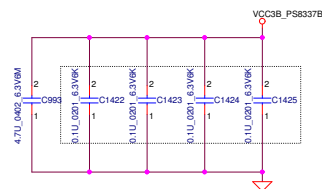
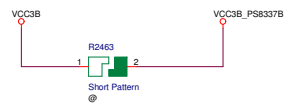
	PC11 (INT PD)		PC21 (INT PD)	
	R2525	R2526	R2529	R2530
Swing +20%	ASM	NO_ASM	ASM	NO_ASM
Swing -16.7%	ASM	ASM	ASM	ASM
Swing default	NO_ASM	NO_ASM	NO_ASM	NO_ASM



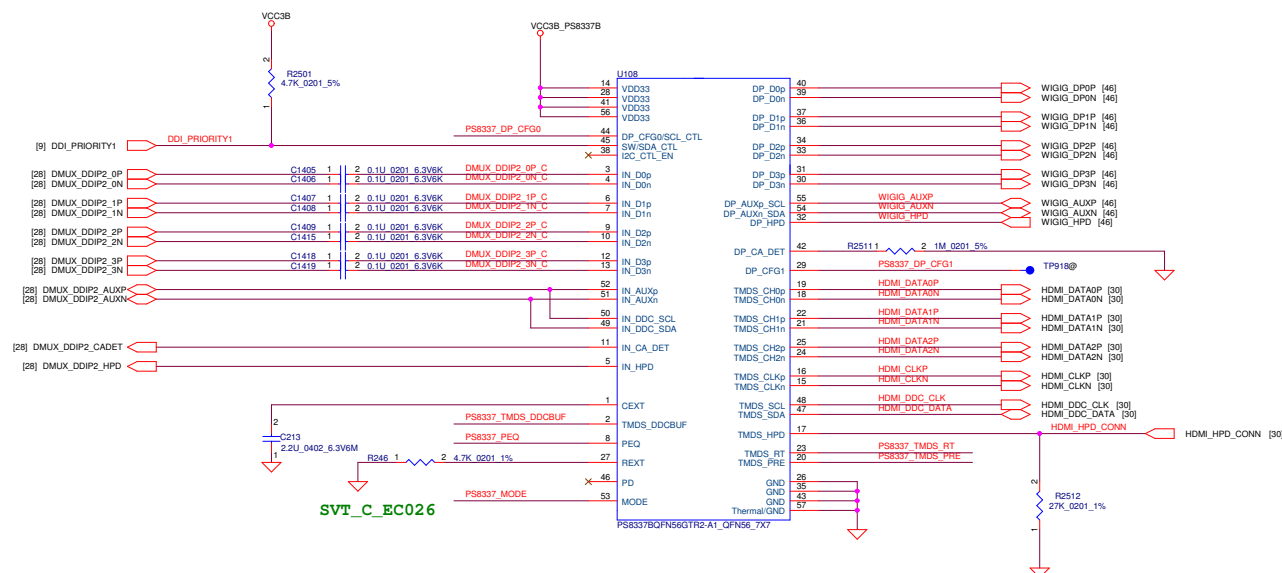
PI1 (INT PD)	R2532	R2533
Auto test EN & Offset cancellation EN	ASM	NO_ASM
Auto test DIS & Offset cancellation DIS	ASM	ASM
Auto test DIS & Offset cancellation EN	NO_ASM	NO_ASM

PI0 (INT PD)	R2531
Auto EQ DIS	ASM
Auto EQ EN	NO_ASM

Priority 1	H	L
Priority 2	H	L
H	HDMI	WiGig
L	Prolink	Prolink



SW (DDI_PRIORITY1)
L DP Port has higher priority when both ports are plugged H TMDS Port has higher priority when both ports are plugged



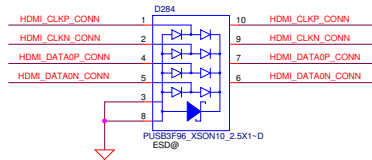
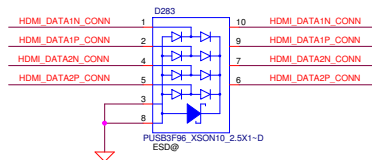
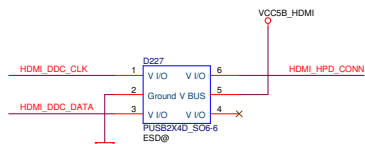
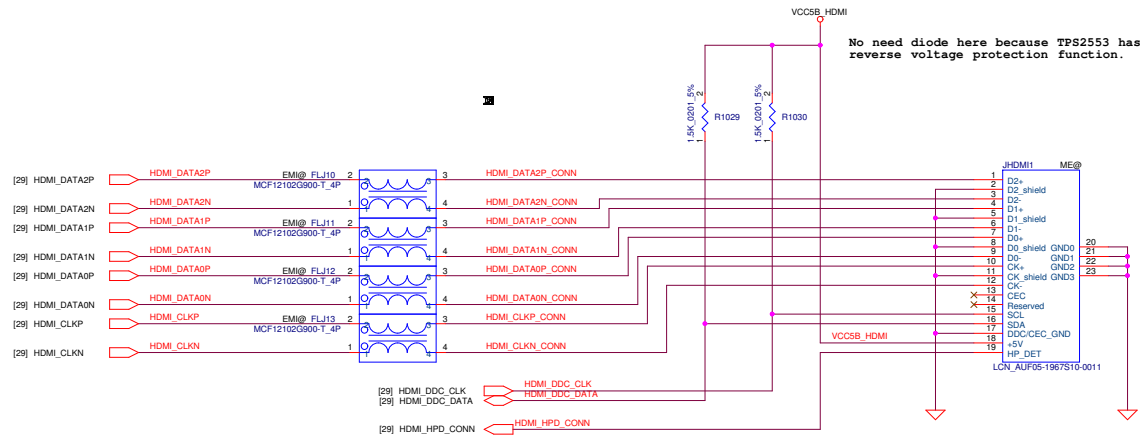
TMD5 DDCBUF (INT PD)	R1876	R1898	
DDC Active Buffer	ASM	NO_ASM	
DDC Pass Through w/ PU	ASM	ASM	
DDC Pass Through w/o PU	NO_ASM	NO_ASM	← LOGIC

PEQ (INT PD)	R1875	R1897	← LOGIC
HEQ 15dB	ASM	NO_ASM	
LLEQ 5dB	ASM	ASM	
LEQ 12dB	NO_ASM	NO_ASM	

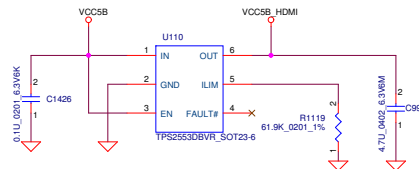
TMS PRE (INT PD)	R1877	R1899
1.5dB	ASM	NO_ASM
3.0dB	ASM	ASM
0dB	NO_ASM	NO_ASM

MODE (INT PD)	R146	R147
Auto HDMI ID disable	ASM	NO_ASM
Auto HDMI ID enable	ASM	ASM
Control HDMI ID disable	NO_ASM	NO_ASM

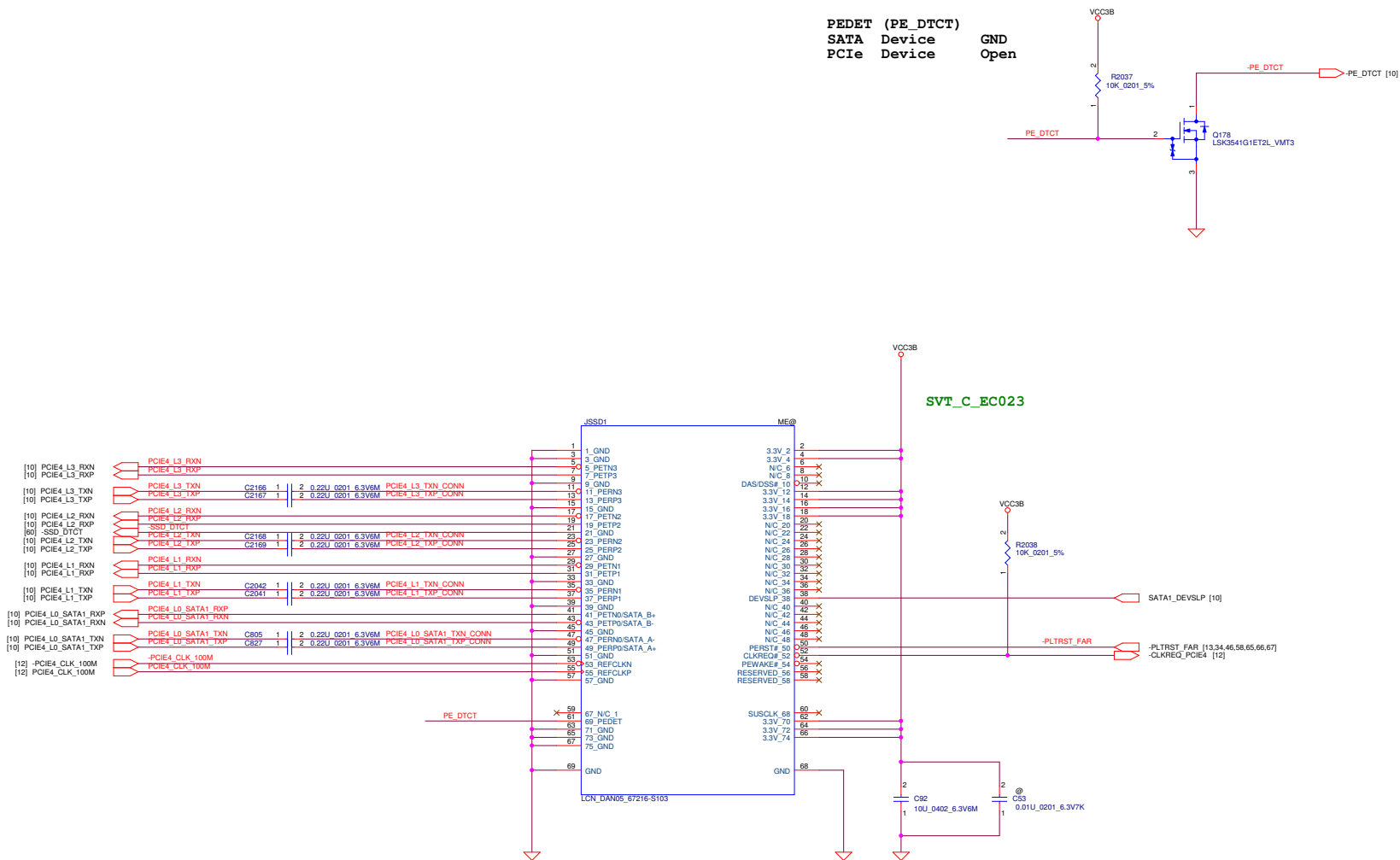
TMDS RT (INT PD)	R2509
OD w/ termination	ASM
OD	NO_ASM



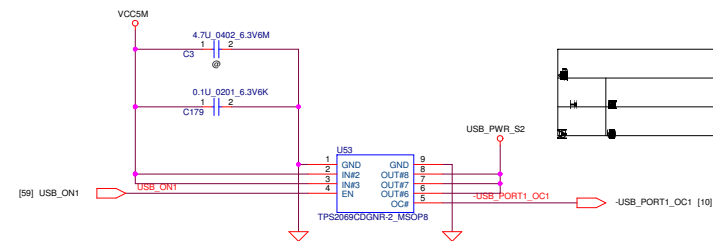
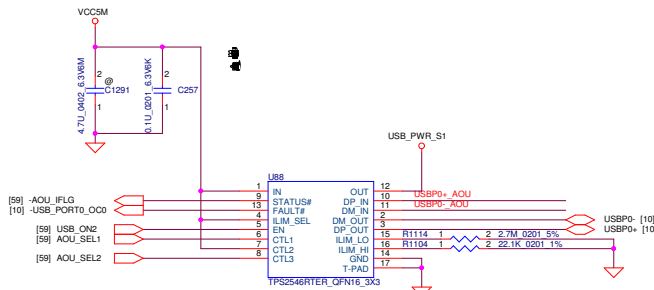
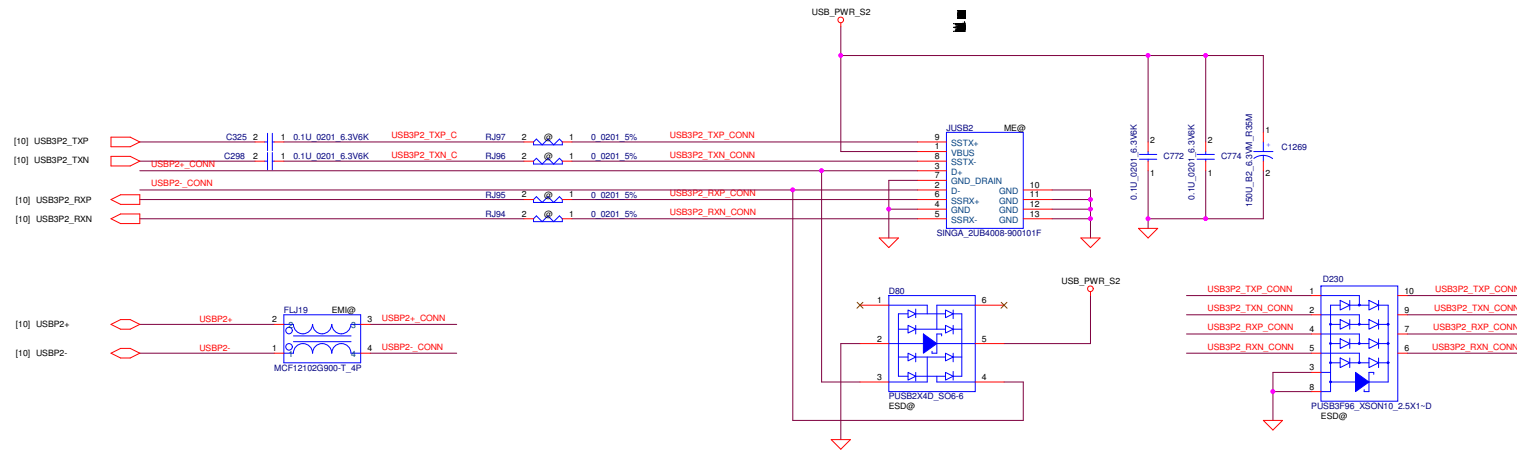
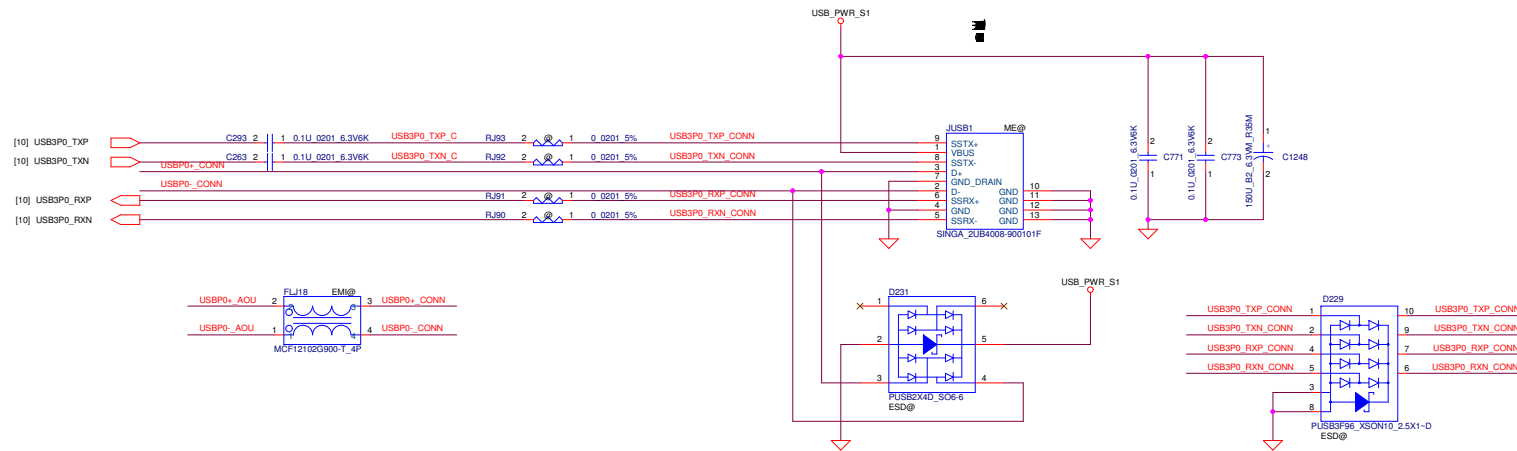
Current Limit Target : 400mA
Requirement : 300mA
HDMI Spec : 50mA - 500mA



Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	HDMI CONNECTOR
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					Rev 0.5
Size	Document	Number	LA-C581P	Date	Thursday, August 13, 2015
Sheet	30	of	102		



Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Document Number	0.5
				Customer	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	31 of 102



Security Classification		Compal Secret Data			
Issued Date		2014/11/04		Deciphered Date	
				2016/12/31	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					
				Title	
				Compal Electronics, Inc.	
				USB POWER/CONN	
Size				Document	
Custom				LA-C581P	
Date:				Thursday, August 13, 2015	
				Sheet 32 of 102	
				Rev 0.5	

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				BLANK		Rev 0.5
				Size	Document Number	
				Custom	LA-C581P	
				Date	Thursday, August 13, 2015	Sheet 33 of 102

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	BLANK
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	35 of 102

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	BLANK
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	38 of 102
				Rev	0.5

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				BLANK		Rev 0.5
				Size	Document Number	
				Custom	LA-C581P	
				Date	Thursday, August 13, 2015	Sheet 37 of 102

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	BLANK
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	38 of 102
				Rev	0.5

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	BLANK
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	39 of 102

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				BLANK		Rev 0.5
				Size	Document Number	
				Custom	LA-C581P	
				Date	Thursday, August 13, 2015	Sheet 49 of 102

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				BLANK		Rev 0.5
				Size	Document Number	
				Custom	LA-C581P	
				Date	Thursday, August 13, 2015	Sheet 41 of 102

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				BLANK		Rev 0.5
				Size	Document Number	
				Custom	LA-C581P	
				Date	Thursday, August 13, 2015	Sheet 42 of 102

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	BLANK
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	43 of 102

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	BLANK
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	44 of 102

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				BLANK		Rev 0.5
				Size	Document Number	
				Custom	LA-C581P	
				Date	Thursday, August 13, 2015	Sheet 45 of 102

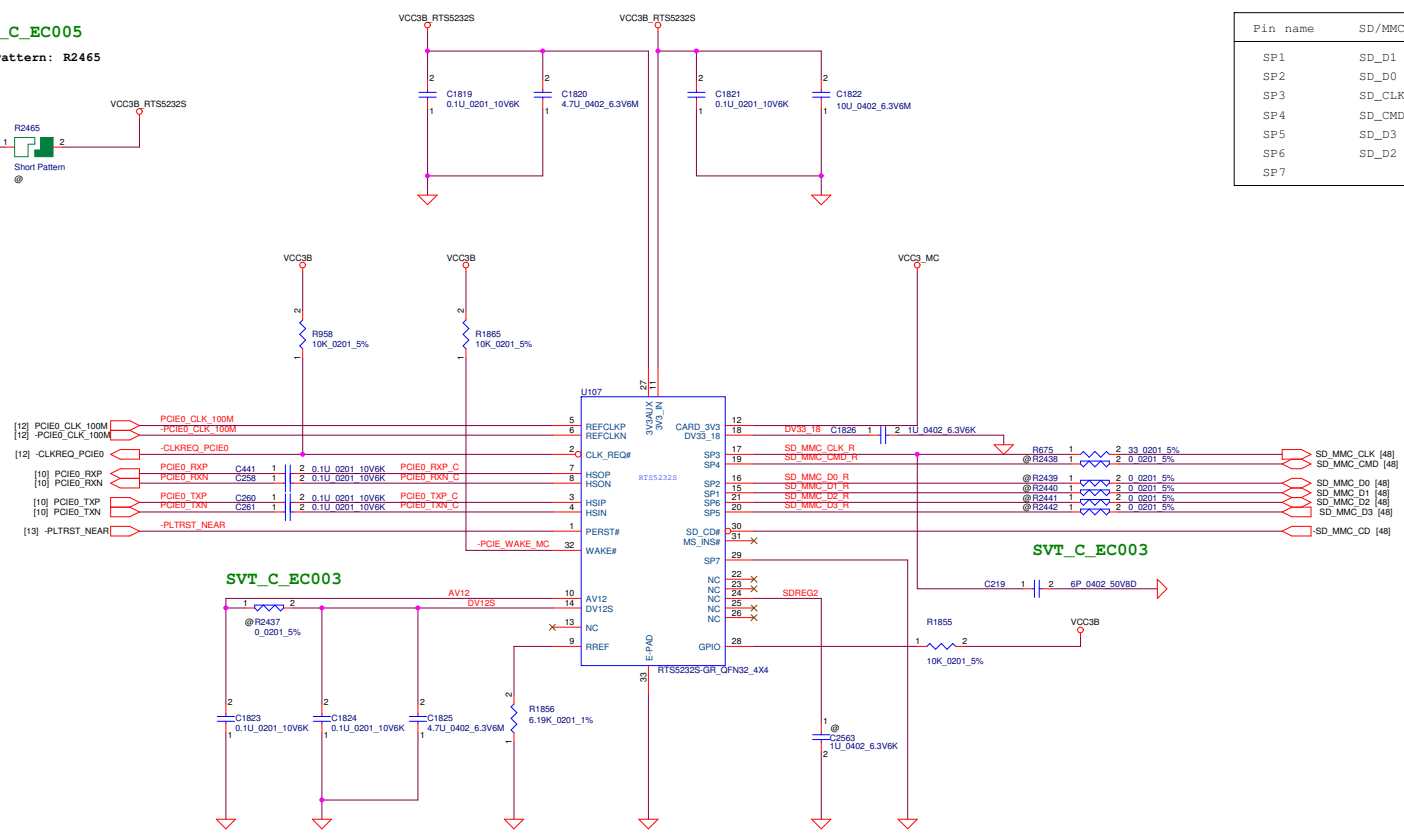
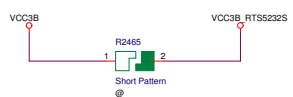
The schematic diagram illustrates the 3042S3 connector, which interfaces with various modules including WWAN, USB, SSIC, and UIM. The central component is the 3042S3 connector itself, with pins numbered 1 through 67. The diagram shows the following connections:

- WWAN Module:**
 - Pin 1: WWAN_CFG3
 - Pin 2: WWAN_CFG0
 - Pin 3: SSIC_RXN
 - Pin 4: SSIC_RXP
 - Pin 5: SSIC_TXN
 - Pin 6: SSIC_TXP
 - Pin 7: -WWAN_RESET
 - Pin 8: WWAN_CFG1
 - Pin 9: WWAN_CFG2
- USB Module:**
 - Pin 10: USBP1+
 - Pin 11: USBP1-
 - Pin 12: USBP1+ CONN
 - Pin 13: USBP1- CONN
- SSIC Module:**
 - Pin 14: SSIC_RXN
 - Pin 15: SSIC_RXP
 - Pin 16: SSIC_TXN
 - Pin 17: SSIC_TXP
- UIM Module:**
 - Pin 18: UIM_RESET
 - Pin 19: UIM_CLK
 - Pin 20: UIM_DATA
 - Pin 21: UIM_PWR
- Power and Ground:**
 - VCC3WAN
 - GND
 - ESD@
 - R1005 47K_0201_5%
 - C47 100P_0201_25V8J
 - R1056 1 2 200 0201 1%
 - RF@ C835 1 68P_0201_25V8
 - RF@ C823 1 4.7U_0402_5.3V6M
 - RF@ C825 1 33P_0201_25V8J
 - RF@ C826 1 33P_0201_25V8J
 - RF@ C827 1 33P_0201_25V8J
 - RF@ C828 1 33P_0201_25V8J
 - RF@ C829 1 33P_0201_25V8J
 - RF@ C830 1 33P_0201_25V8J
 - RF@ C831 1 33P_0201_25V8J
 - RF@ C832 1 33P_0201_25V8J
 - RF@ C833 1 33P_0201_25V8J
 - RF@ C834 1 33P_0201_25V8J
 - RF@ C835 1 33P_0201_25V8J
 - RF@ C836 1 33P_0201_25V8J
 - RF@ C837 1 33P_0201_25V8J
 - RF@ C838 1 33P_0201_25V8J
 - RF@ C839 1 33P_0201_25V8J
 - RF@ C840 1 33P_0201_25V8J
 - RF@ C841 1 33P_0201_25V8J
 - RF@ C842 1 33P_0201_25V8J
 - RF@ C843 1 33P_0201_25V8J
 - RF@ C844 1 33P_0201_25V8J
 - RF@ C845 1 33P_0201_25V8J
 - RF@ C846 1 33P_0201_25V8J
 - RF@ C847 1 33P_0201_25V8J
 - RF@ C848 1 33P_0201_25V8J
 - RF@ C849 1 33P_0201_25V8J
 - RF@ C850 1 33P_0201_25V8J
 - RF@ C851 1 33P_0201_25V8J
 - RF@ C852 1 33P_0201_25V8J
 - RF@ C853 1 33P_0201_25V8J
 - RF@ C854 1 33P_0201_25V8J
 - RF@ C855 1 33P_0201_25V8J
 - RF@ C856 1 33P_0201_25V8J
 - RF@ C857 1 33P_0201_25V8J
 - RF@ C858 1 33P_0201_25V8J
 - RF@ C859 1 33P_0201_25V8J
 - RF@ C860 1 33P_0201_25V8J
 - RF@ C861 1 33P_0201_25V8J
 - RF@ C862 1 33P_0201_25V8J
 - RF@ C863 1 33P_0201_25V8J
 - RF@ C864 1 33P_0201_25V8J
 - RF@ C865 1 33P_0201_25V8J
 - RF@ C866 1 33P_0201_25V8J
 - RF@ C867 1 33P_0201_25V8J
 - RF@ C868 1 33P_0201_25V8J
 - RF@ C869 1 33P_0201_25V8J
 - RF@ C870 1 33P_0201_25V8J
 - RF@ C871 1 33P_0201_25V8J
 - RF@ C872 1 33P_0201_25V8J
 - RF@ C873 1 33P_0201_25V8J
 - RF@ C874 1 33P_0201_25V8J
 - RF@ C875 1 33P_0201_25V8J
 - RF@ C876 1 33P_0201_25V8J
 - RF@ C877 1 33P_0201_25V8J
 - RF@ C878 1 33P_0201_25V8J
 - RF@ C879 1 33P_0201_25V8J
 - RF@ C880 1 33P_0201_25V8J
 - RF@ C881 1 33P_0201_25V8J
 - RF@ C882 1 33P_0201_25V8J
 - RF@ C883 1 33P_0201_25V8J
 - RF@ C884 1 33P_0201_25V8J
 - RF@ C885 1 33P_0201_25V8J
 - RF@ C886 1 33P_0201_25V8J
 - RF@ C887 1 33P_0201_25V8J
 - RF@ C888 1 33P_0201_25V8J
 - RF@ C889 1 33P_0201_25V8J
 - RF@ C890 1 33P_0201_25V8J
 - RF@ C891 1 33P_0201_25V8J
 - RF@ C892 1 33P_0201_25V8J
 - RF@ C893 1 33P_0201_25V8J
 - RF@ C894 1 33P_0201_25V8J
 - RF@ C895 1 33P_0201_25V8J
 - RF@ C896 1 33P_0201_25V8J
 - RF@ C897 1 33P_0201_25V8J
 - RF@ C898 1 33P_0201_25V8J
 - RF@ C899 1 33P_0201_25V8J
 - RF@ C900 1 33P_0201_25V8J
 - RF@ C901 1 33P_0201_25V8J
 - RF@ C902 1 33P_0201_25V8J
 - RF@ C903 1 33P_0201_25V8J
 - RF@ C904 1 33P_0201_25V8J
 - RF@ C905 1 33P_0201_25V8J
 - RF@ C906 1 33P_0201_25V8J
 - RF@ C907 1 33P_0201_25V8J
 - RF@ C908 1 33P_0201_25V8J
 - RF@ C909 1 33P_0201_25V8J
 - RF@ C910 1 33P_0201_25V8J
 - RF@ C911 1 33P_0201_25V8J
 - RF@ C912 1 33P_0201_25V8J
 - RF@ C913 1 33P_0201_25V8J
 - RF@ C914 1 33P_0201_25V8J
 - RF@ C915 1 33P_0201_25V8J
 - RF@ C916 1 33P_0201_25V8J
 - RF@ C917 1 33P_0201_25V8J
 - RF@ C918 1 33P_0201_25V8J
 - RF@ C919 1 33P_0201_25V8J
 - RF@ C920 1 33P_0201_25V8J
 - RF@ C921 1 33P_0201_25V8J
 - RF@ C922 1 33P_0201_25V8J
 - RF@ C923 1 33P_0201_25V8J
 - RF@ C924 1 33P_0201_25V8J
 - RF@ C925 1 33P_0201_25V8J
 - RF@ C926 1 33P_0201_25V8J
 - RF@ C927 1 33P_0201_25V8J
 - RF@ C928 1 33P_0201_25V8J
 - RF@ C929 1 33P_0201_25V8J
 - RF@ C930 1 33P_0201_25V8J
 - RF@ C931 1 33P_0201_25V8J
 - RF@ C932 1 33P_0201_25V8J
 - RF@ C933 1 33P_0201_25V8J
 - RF@ C934 1 33P_0201_25V8J
 - RF@ C935 1 33P_0201_25V8J
 - RF@ C936 1 33P_0201_25V8J
 - RF@ C937 1 33P_0201_25V8J
 - RF@ C938 1 33P_0201_25V8J
 - RF@ C939 1 33P_0201_25V8J
 - RF@ C940 1 33P_0201_25V8J
 - RF@ C941 1 33P_0201_25V8J
 - RF@ C942 1 33P_0201_25V8J
 - RF@ C943

[illegible]

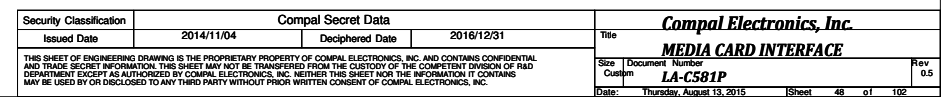
Security Classification	Compel Secret Data		Title		Compel Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2018/12/31	PCIE M.2 CARD SLOT		
THIS SHEET OF INFORMATION DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. IT IS NOT TO BE REPRODUCED, COPIED, TRANSMITTED, OR IN ANY MANNER DISCLOSED OR MADE DEPENDENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. WITHIN THIS SHEET OR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document	Rev
				Custom	LA-C581P	0.5
				Date:	Thursday, August 13, 2015	Sheet 46 of 102

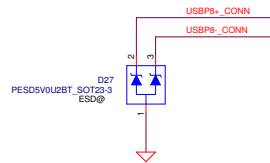
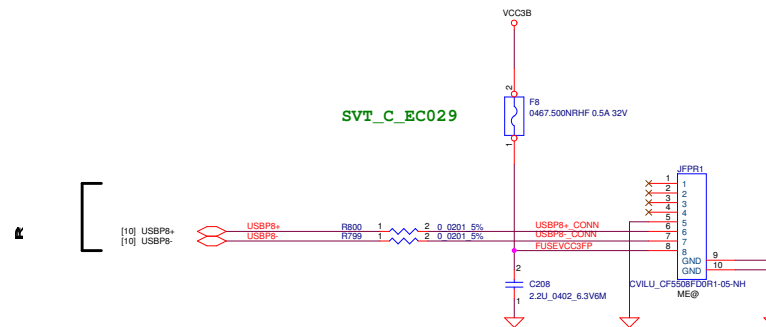
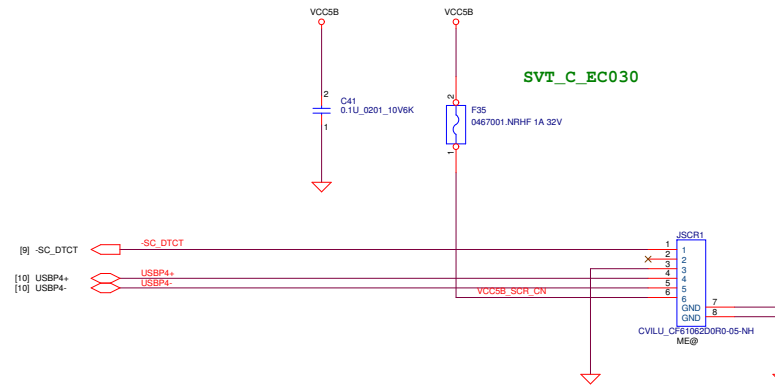
SVT_C_EC005
Short Pattern: R2465



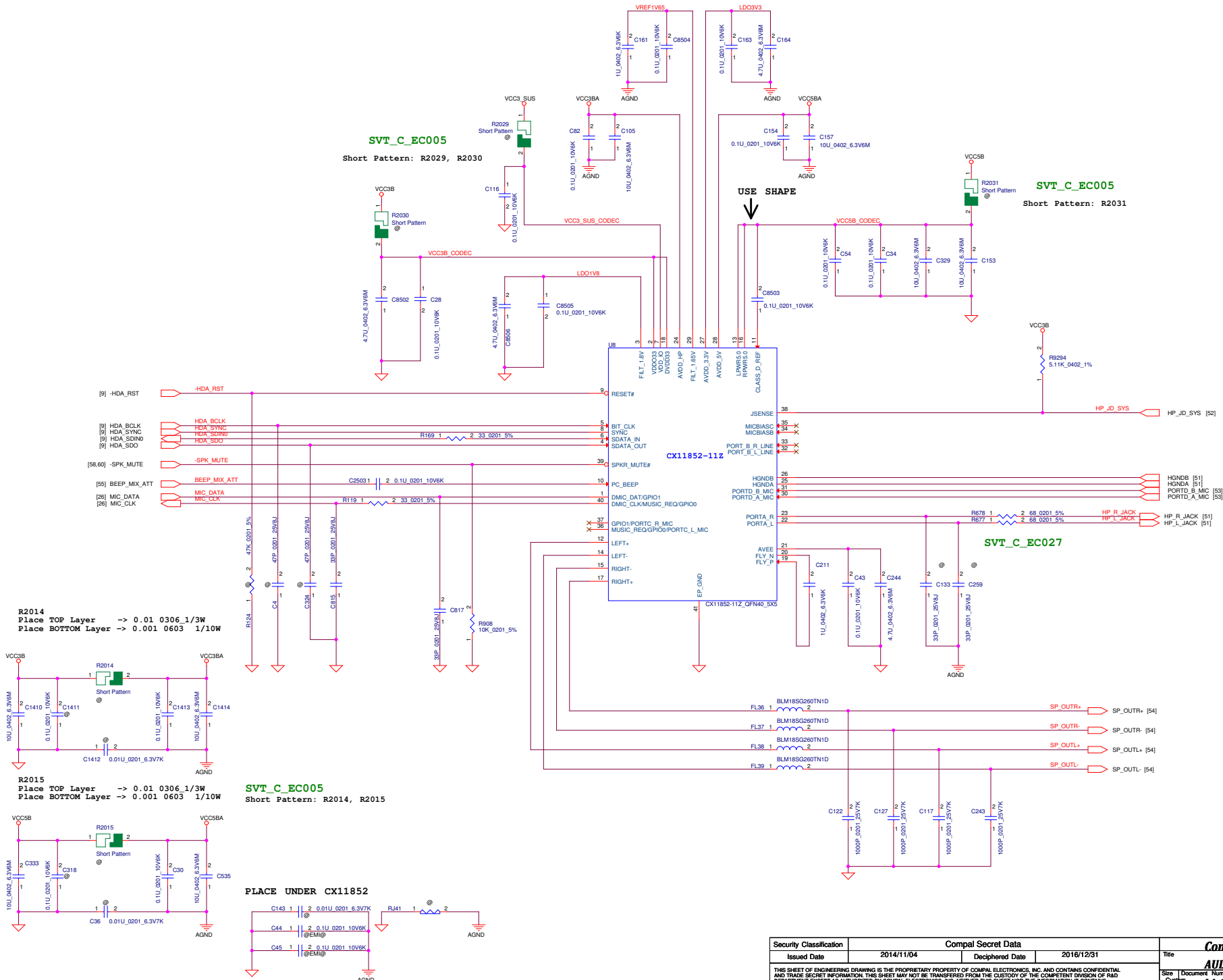
TABLE

Pin name	SD/MMC	MEMORSTICK
SP1	SD_D1	
SP2	SD_D0	MS_D1
SP3	SD_CLK	MS_D0
SP4	SD_CMD	MS_D2
SP5	SD_D3	MS_D3
SP6	SD_D2	MS_CLK
SP7		

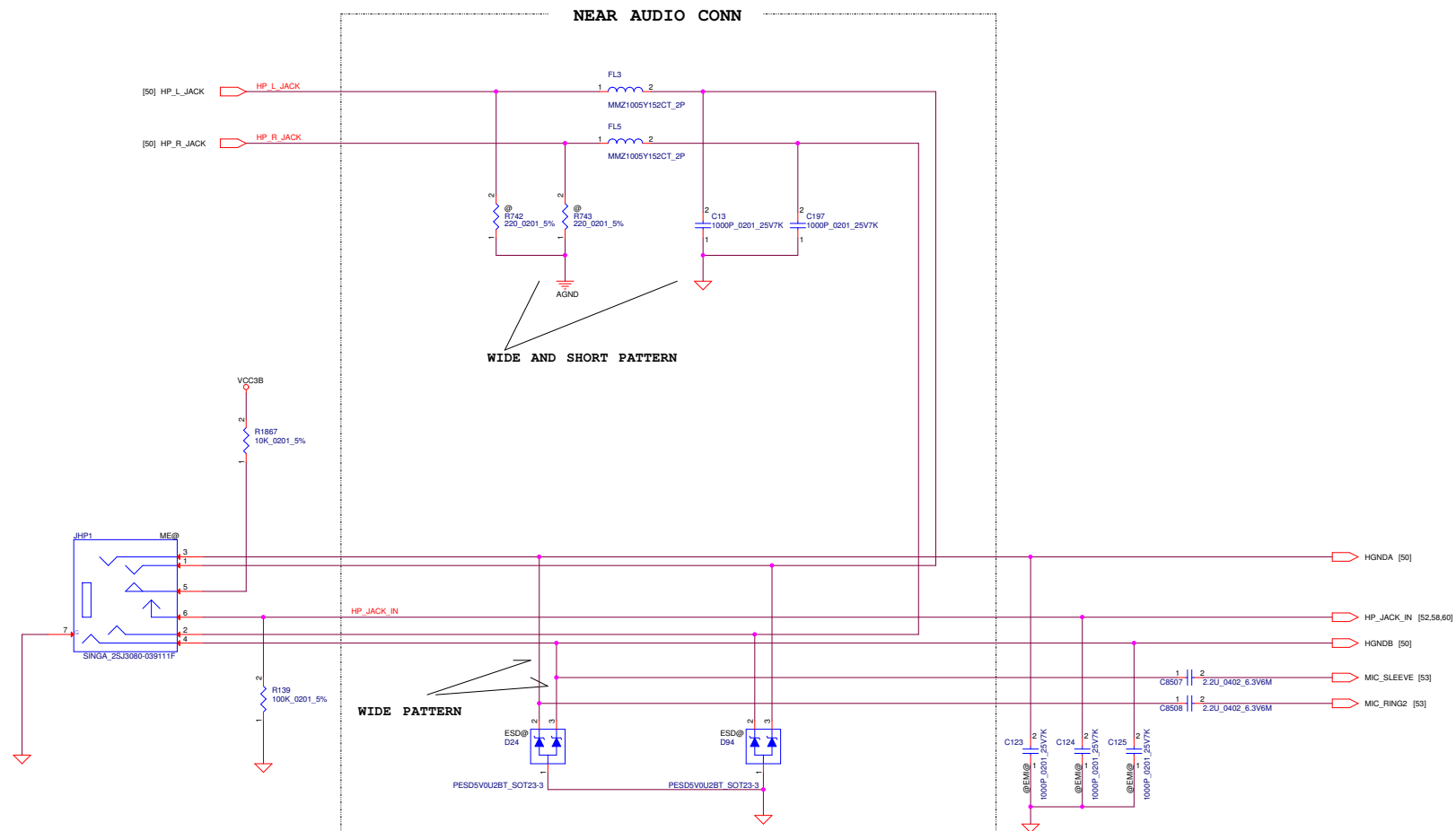




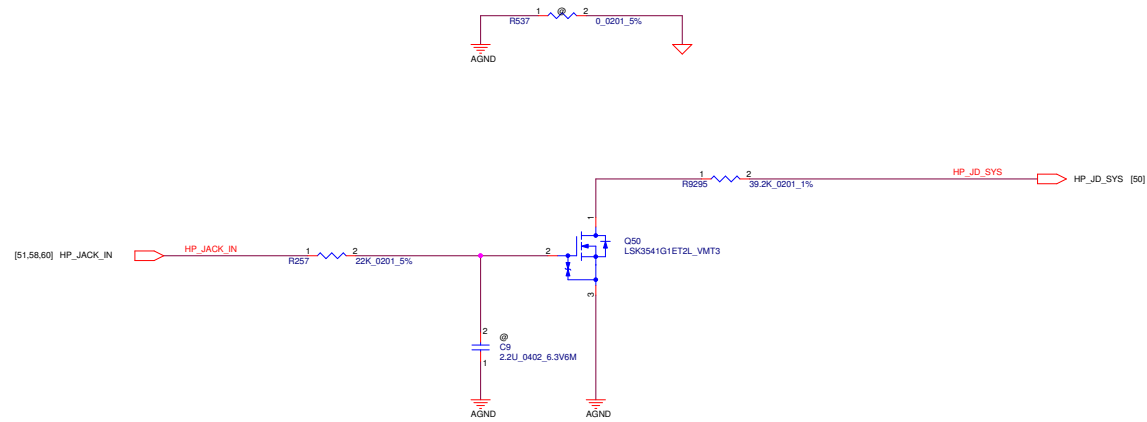
Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Document Number	0.5
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	49 of 102



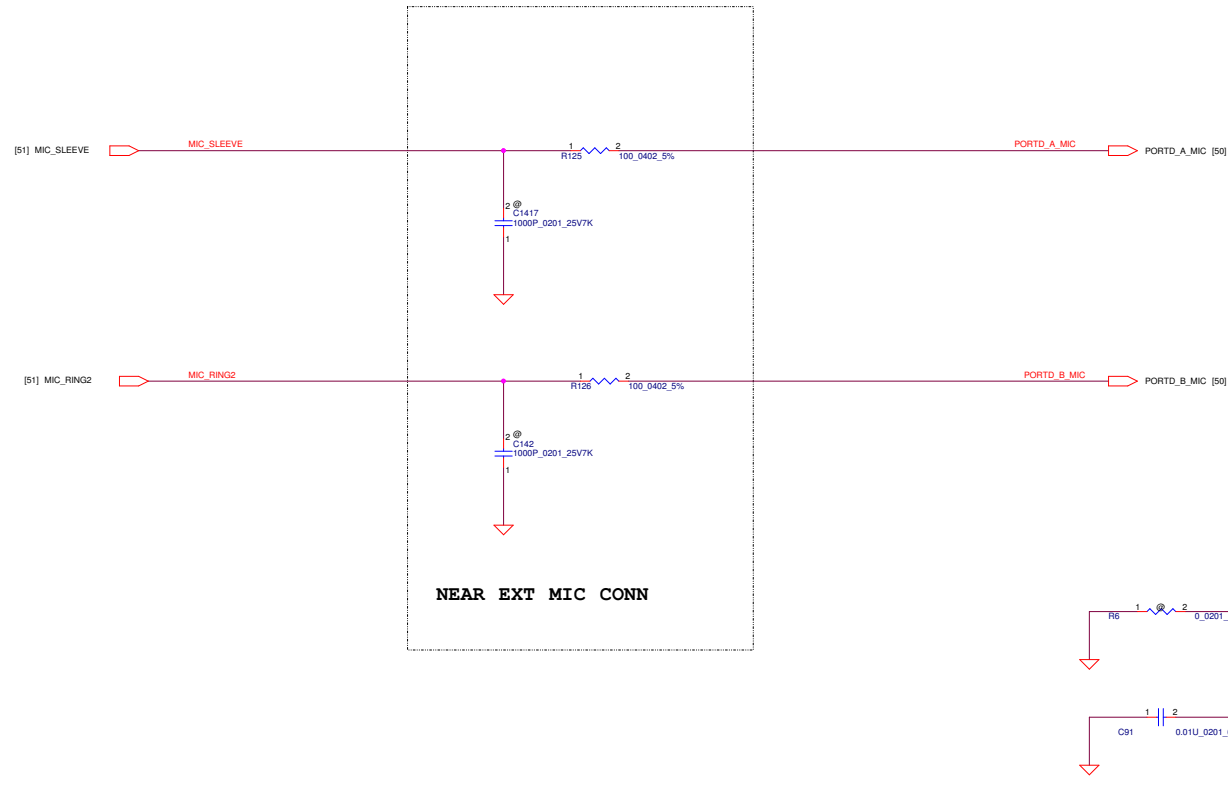
Security Classification		Compal Secret Data	
Issued Date	2014/11/04	Deciphered Date	2016/12/31
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		Compal Electronics, Inc. AUDIO CX11852 Size Document Number Custom LA-C581P Date: Thursday, August 13, 2015 Sheet: 50 of 102	
		Rev	0.5



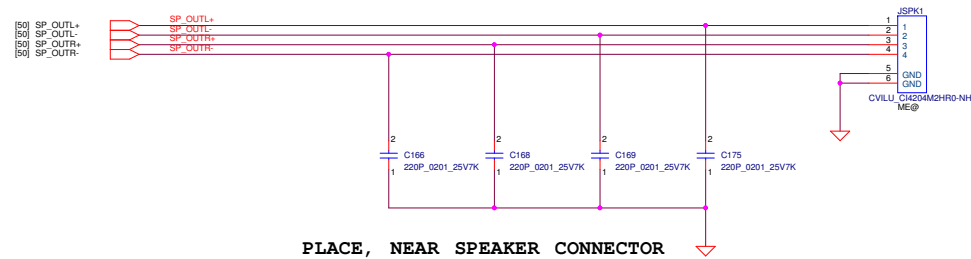
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		Deciphered Date		Title	
2014/11/04		2016/12/31		AUDIO CONNECTOR	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		Size Document Number		Rev	
		Custom LA-C581P		0.5	
Date		Thursday, August 13, 2015		Sheet 51 of 102	



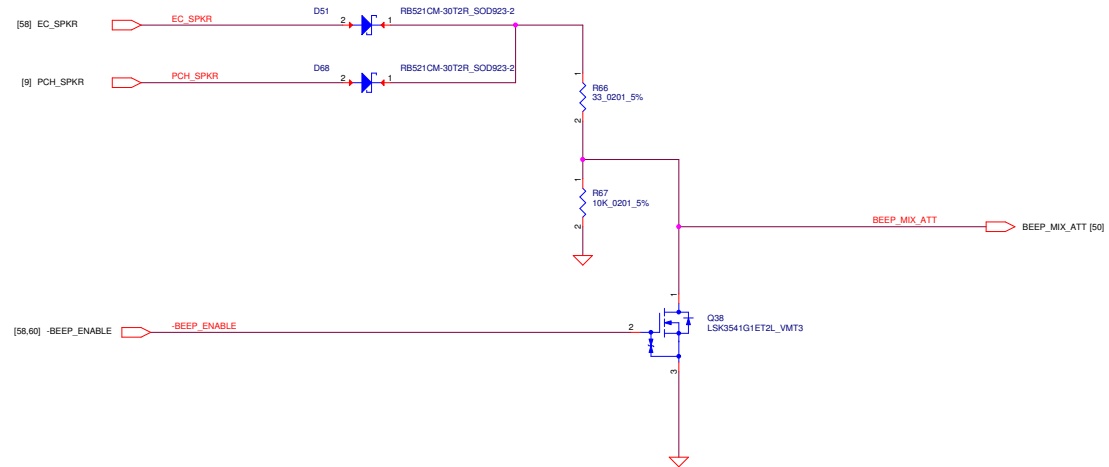
Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Document Number	0.5
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	52 of 102



Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Document Number	0.5
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	53 of 102



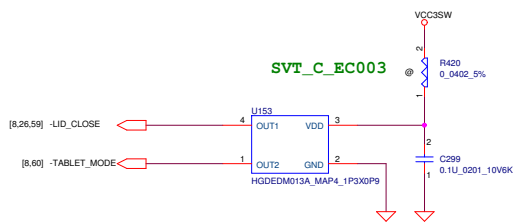
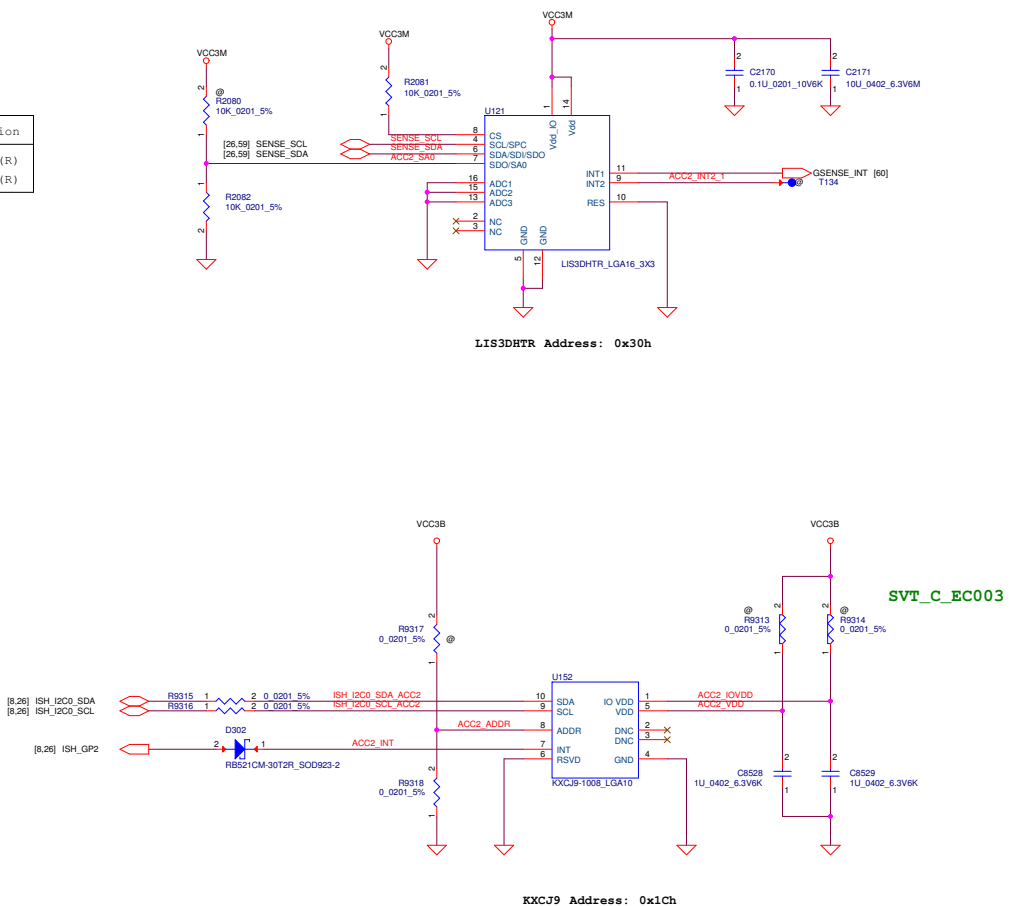
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		2014/11/04		Deciphered Date	
				2016/12/31	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		Title		AUDIO SPEAKER	
Size		Document Number		Rev	
Custom		LA-C581P		0.5	
Date		Thursday, August 13, 2015		Sheet 54 of 102	

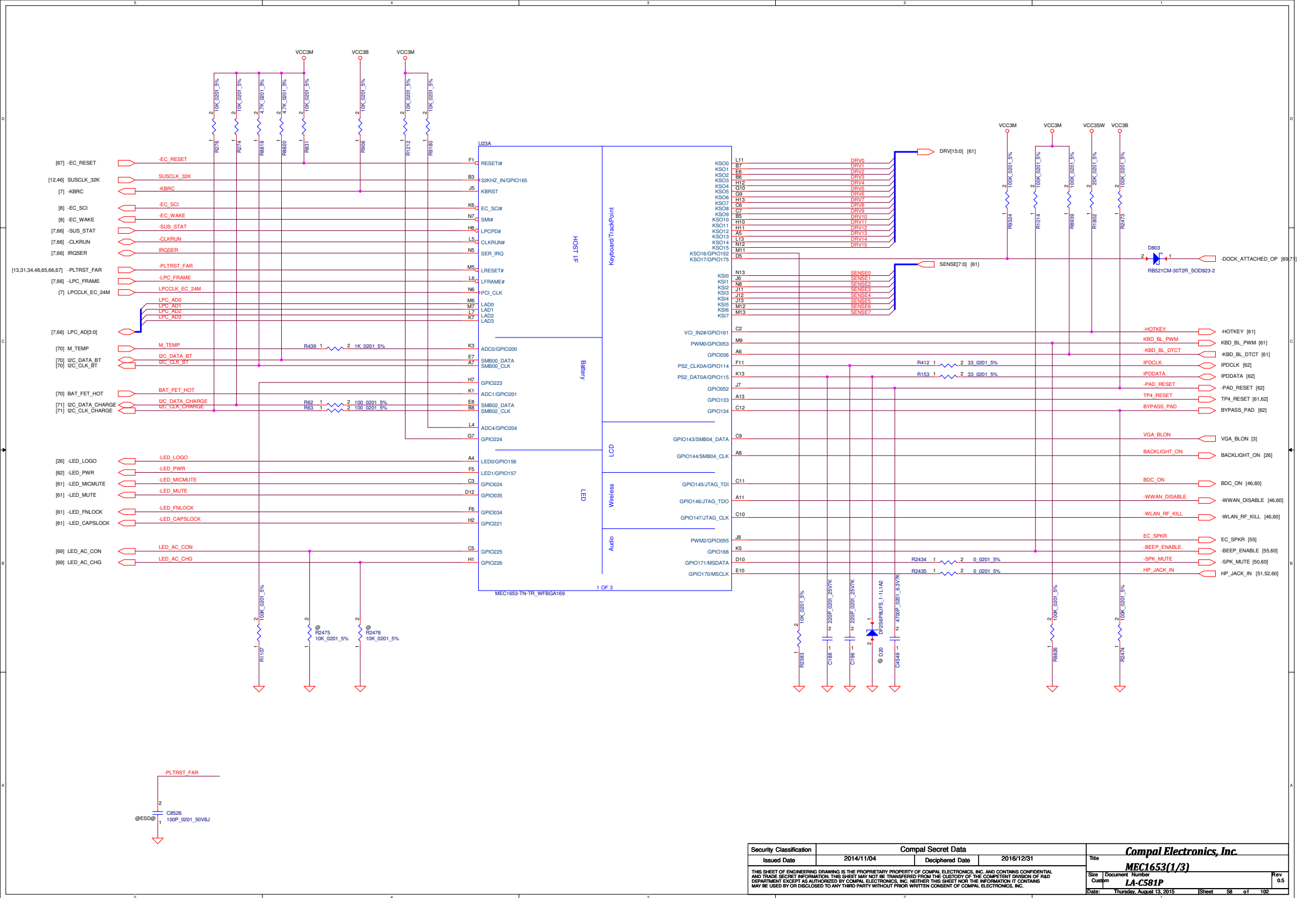


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		Deciphered Date		Title	
2014/11/04		2016/12/31		AUDIO BEEP/DAUGHTER CARD	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		Size Document Number		Rev	
		Custom		LA-C581P	
		Date		Thursday, August 13, 2015	
		Sheet		55 of 102	

Table with 2 columns: ACC2_SA0, Address Selection. Rows for H (32h (W) & 33h (R)) and L (30h (W) & 31h (R)).

Table with 2 columns: CS, Mode Selection. Rows for H (I2C Mode) and L (SPI Mode).





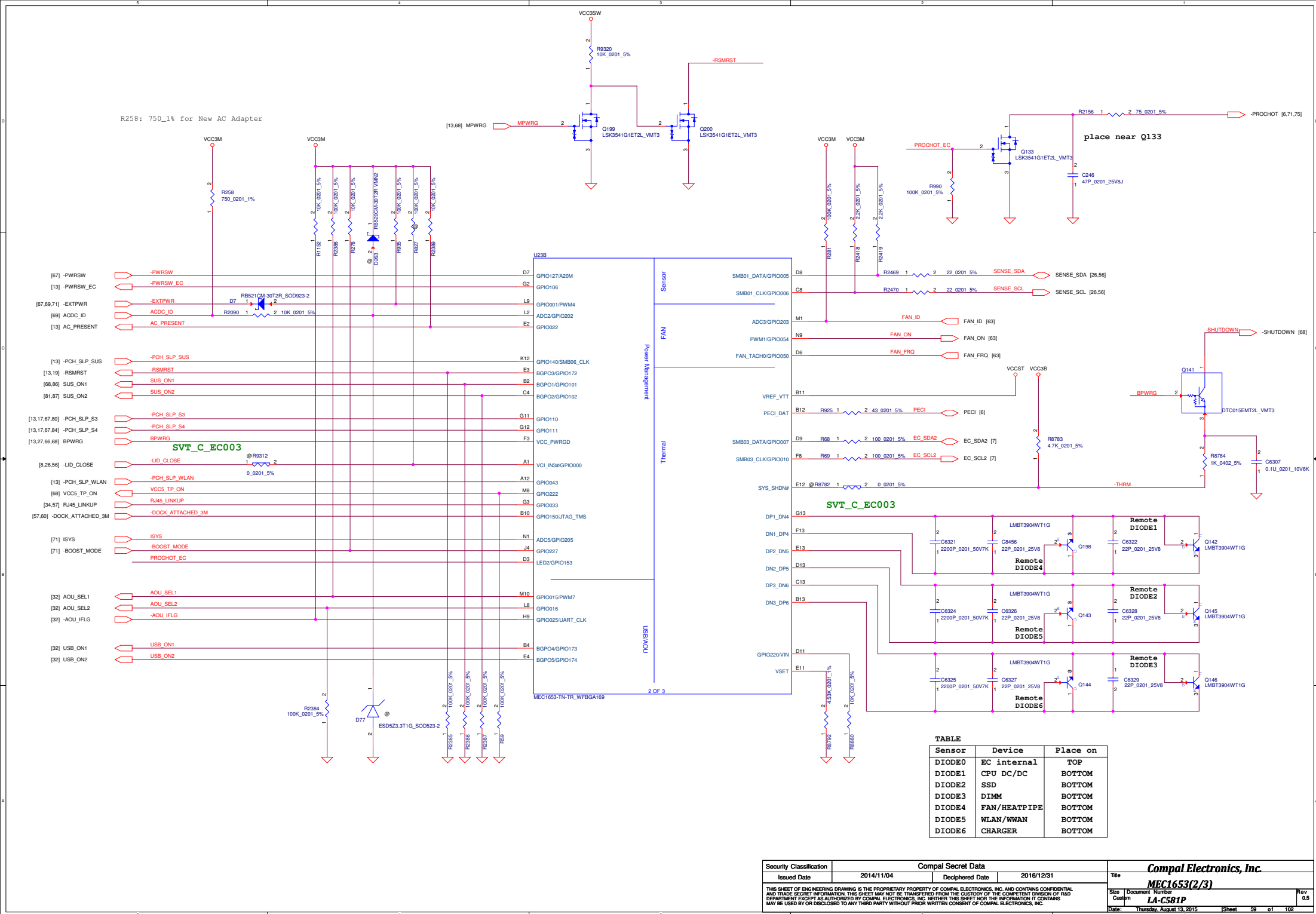


TABLE		
Sensor	Device	Place on
DIODE0	EC internal	TOP
DIODE1	CPU DC/DC	BOTTOM
DIODE2	SSD	BOTTOM
DIODE3	DIMM	BOTTOM
DIODE4	FAN/HEATPIPE	BOTTOM
DIODE5	WLAN/WWAN	BOTTOM
DIODE6	CHARGER	BOTTOM

Trace FIFO Debug Port			
	Enable	Disable	
R2434	22.5%	0.5%	
R2435	22.5%	0.5%	
R420	No_ASM	ASM	

SVT_C_EC005
Short Pattern: R2032

SVT_C_EC003

Change R2413 to NO_ASM

JTAG Debug Port

	Enable	Disable	Logic
R8796	ASM	NO_ASM	NO_ASM
R8797	NO_ASM	ASM	ASM
R8830	ASM	NO_ASM	ASM
R2429	ASM	NO_ASM	NO_ASM
R2430	ASM	NO_ASM	NO_ASM
R2431	ASM	NO_ASM	NO_ASM

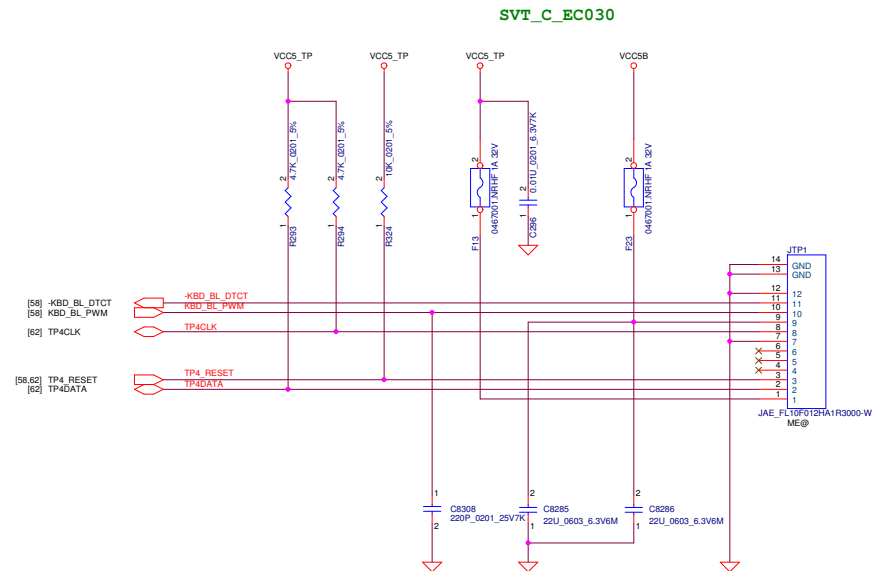
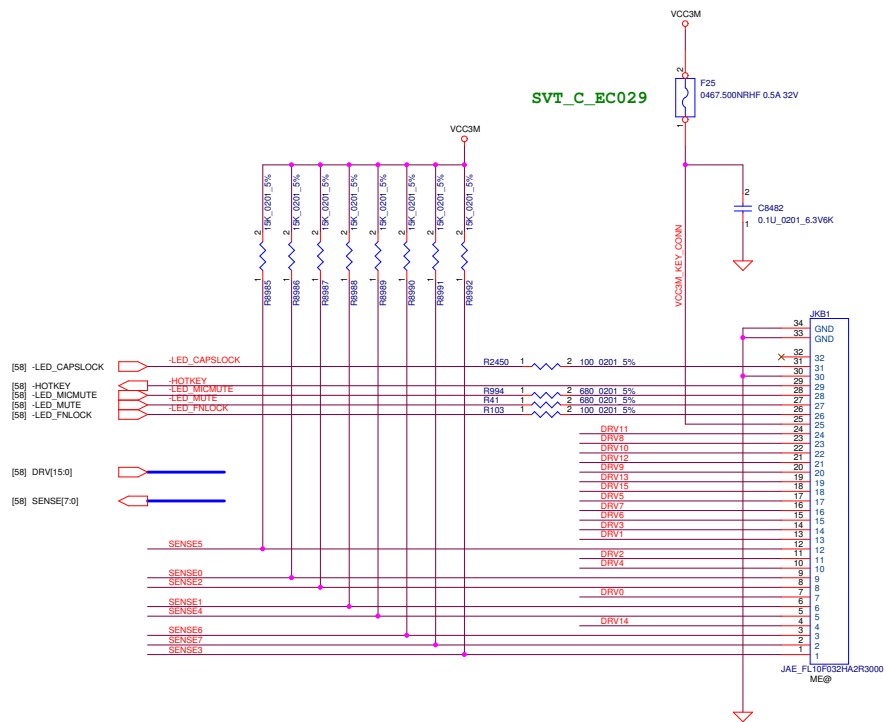
Security Classification		Compal Secret Data		Title	
Issued Date		Deciphered Date		MEC1653(3/3)	
2014/11/04		2016/12/31		Rev 0.5	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	
				LA-C581P	
				Date: Thursday, August 13, 2015	
				Sheet 69 of 102	

Compal Electronics, Inc.

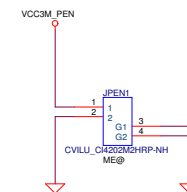
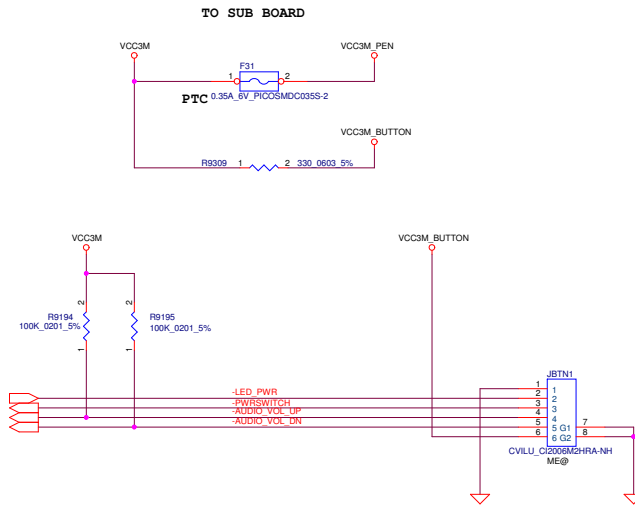
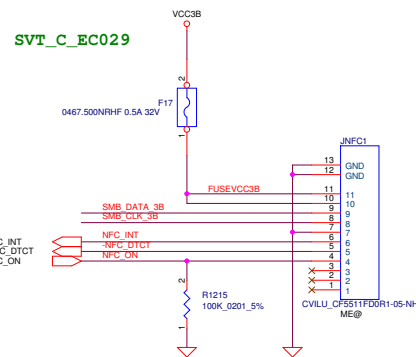
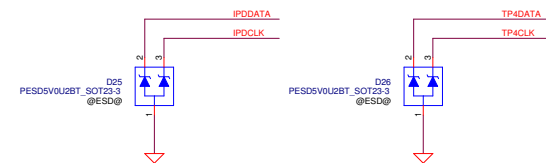
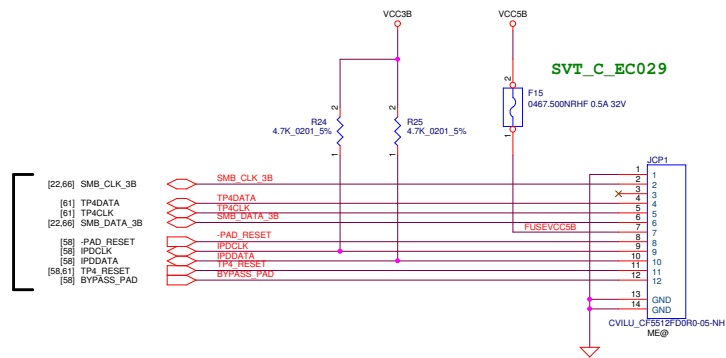
MEC1653(3/3)

LA-C581P

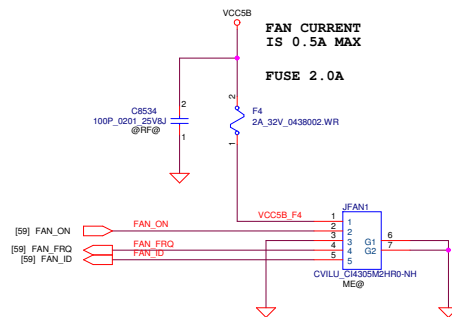
Rev 0.5



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Document Number	0.5
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	61 of 102



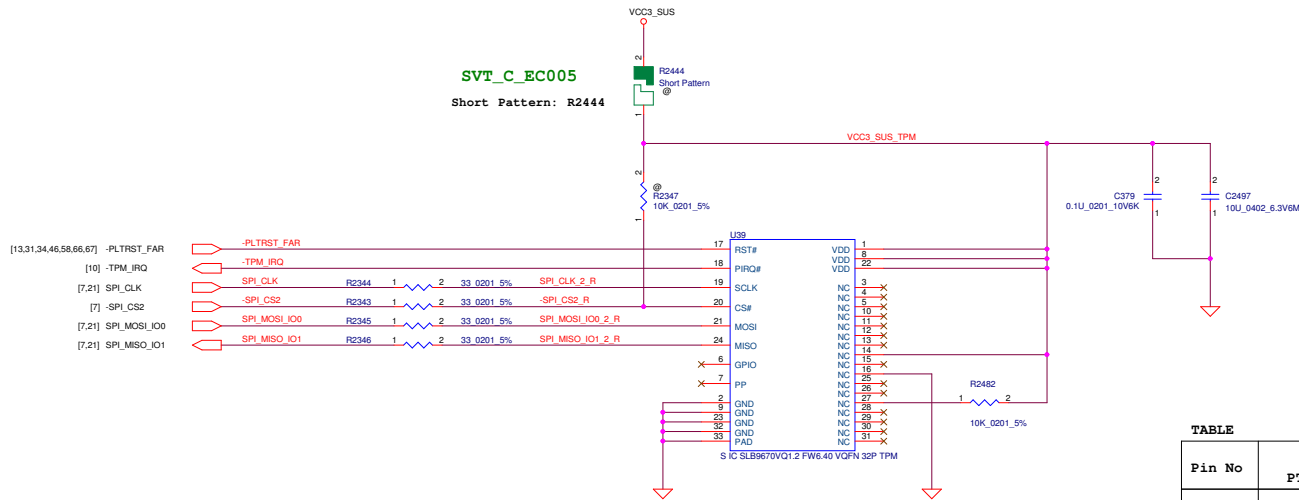
Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Document Number	0.5
				Customer	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	62 of 102



Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Rev 0.5
				Date	Thursday, August 13, 2015
				Sheet	63 of 102

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	BLANK
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	64 of 102



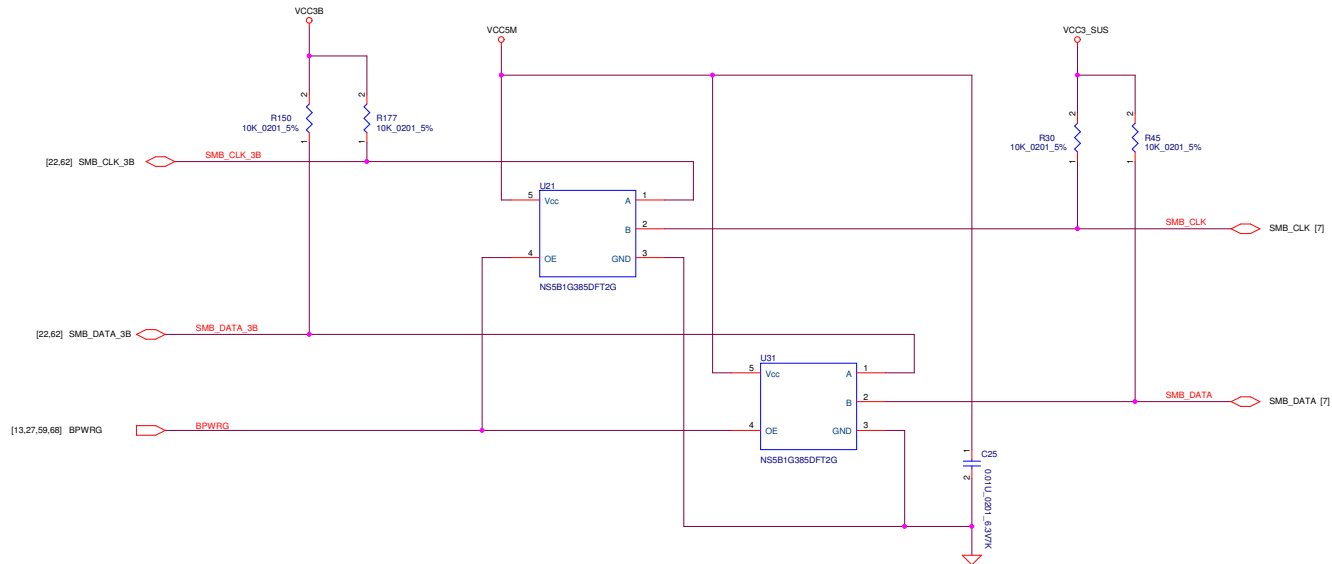
-Change FW version of Infineon TPM to 6.40
-Change Nuvoton parts number to NPCT650LB0YX

TABLE

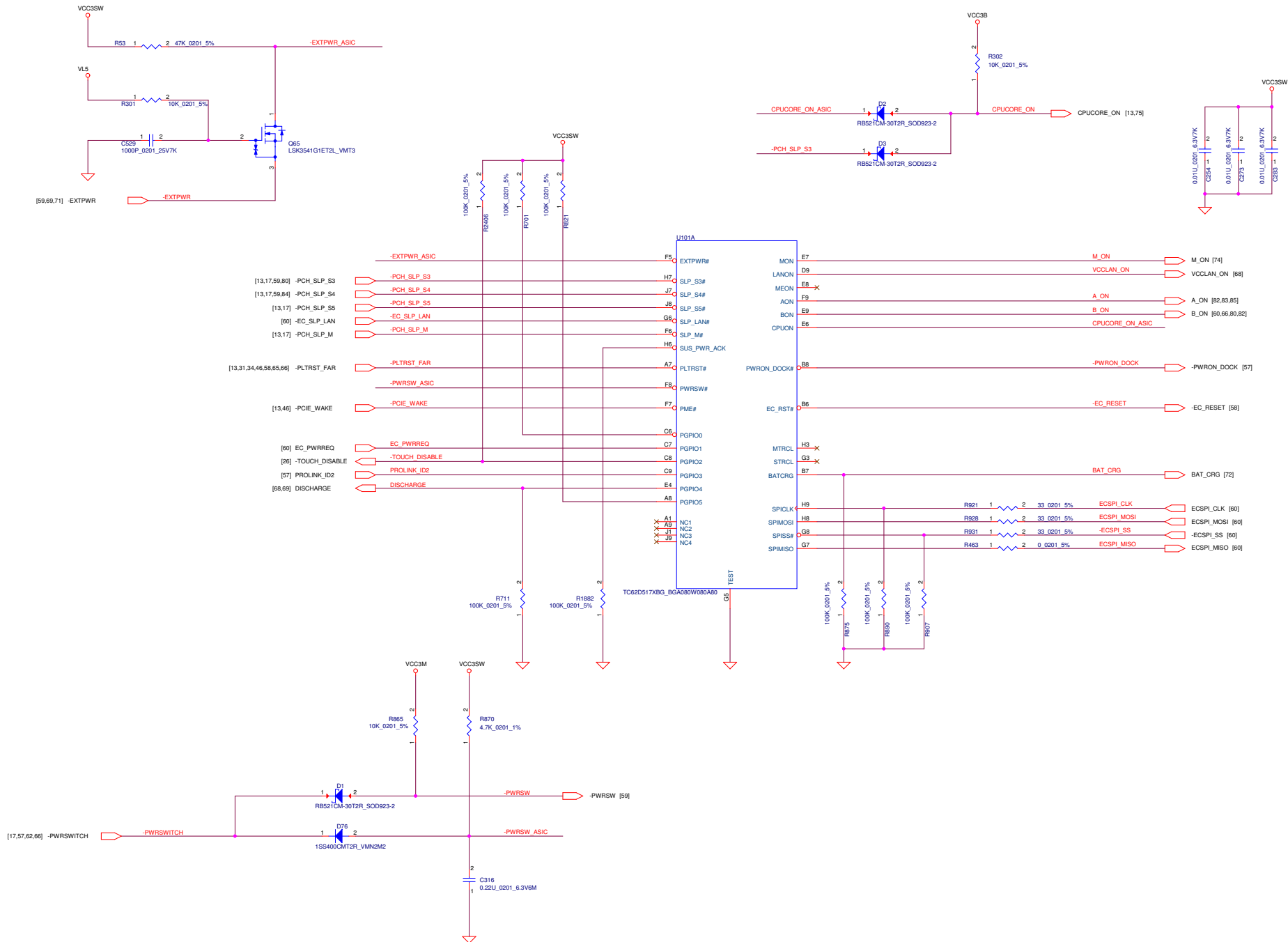
Pin No	TCG PTP Spec (v38)	Infineon SLB9670VQ1.2 FW 6.40	Nuvoton NPCT650LB0YX
1	VDD	VDD	VSB
2	GND	GND	NC
3	GPIO	NC	GPX/GPIO2
4	GPIO	NC	PP
5	NC	NC	TEST
6	VNC/GPIO	GPIO	GPIO3
7	GPIO/VDD	PP	NC
8	VDD	VDD	VDD
9	GND	GND	GND
10	VNC	NC	NC
11	NC	NC	NC
12	NC	NC	Reserved
13	VNC/GPIO	NC	GPIO4
14	VDD	NC	VDD
15	NC	NC	DNC
16	GND	NC	GND
17	SPI_RST#	RST#	SPI_RST#
18	SPI_PIRQ#	PIRQ#	SPI_PIRQ#
19	SPI_CLK	SCLK	SCLK
20	SPI_CS#	CS#	SCS#
21	MOSI	MOSI	MOSI
22	VDD	VDD	VDD
23	GND	GND	GND
24	MISO	MISO	MISO
25	NC	NC	NC
26	NC	NC	NC
27	NC	NC	(SERIRQ)
28	NC	NC	DNC
29	VNC/ GPIO	NC	GPIO0
30	VNC/ GPIO	NC	GPIO1
31	VNC	NC	NC
32	GND	GND	GND

SVT_C_EC001

SVT_EC004

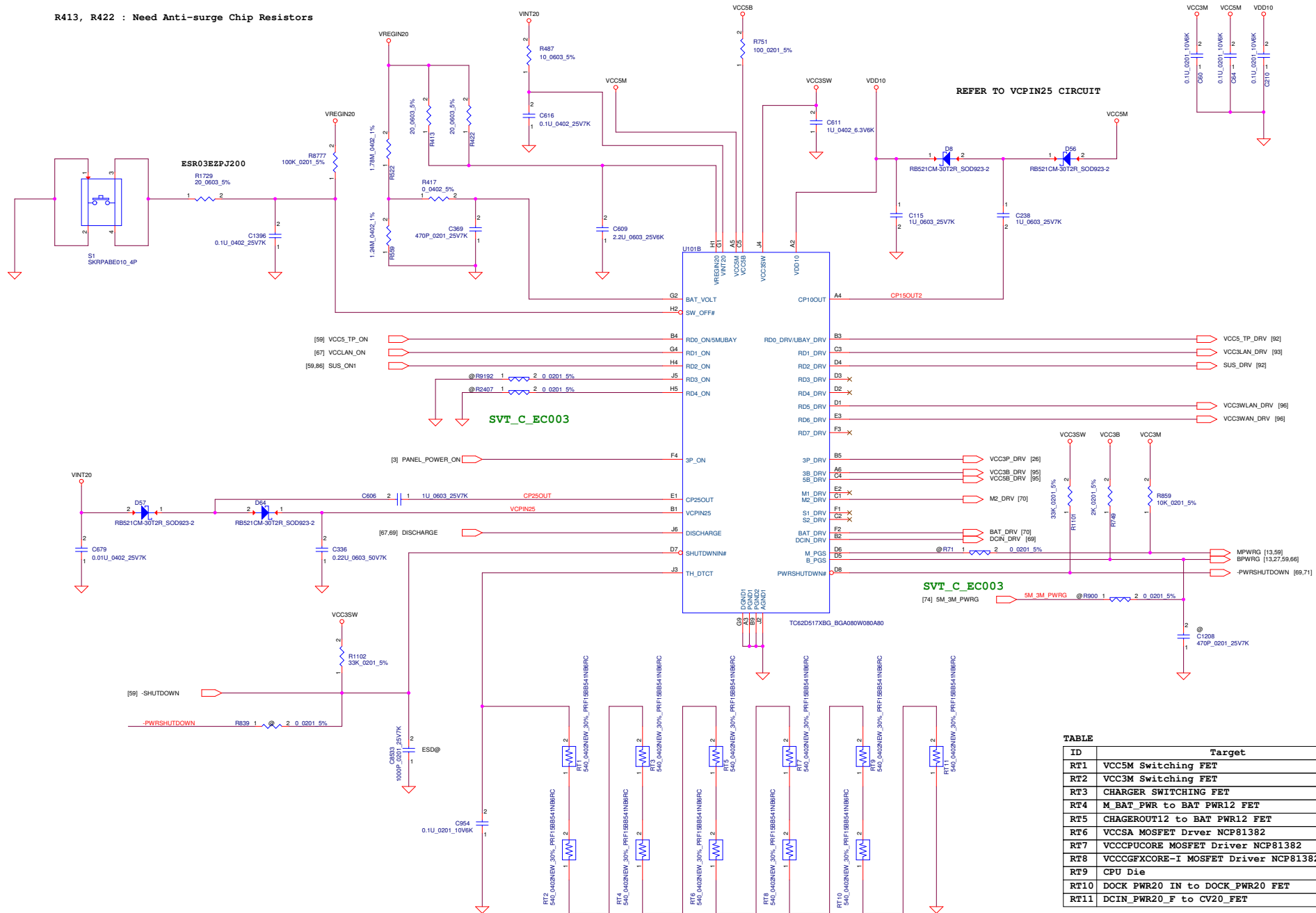


Security Classification	Compel Secret Data		Title	Compel Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2018/12/31	SMBUS SWITCH/LPC DEBUG POR	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL INFORMATION. THIS INFORMATION IS NOT TO BE REPRODUCED, COPIED, TRANSMITTED, OR DISCLOSED TO ANY OTHER PARTY WITHOUT THE WRITTEN CONSENT OF COMPAL ELECTRONICS, INC. THIS DRAWING IS THE PROPERTY OF COMPAL ELECTRONICS, INC. AND IS NOT TO BE REPRODUCED, COPIED, TRANSMITTED, OR DISCLOSED TO ANY OTHER PARTY WITHOUT THE WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.			Size	Document	Rev
			Custom	LA-C581P	0.5
			Date:	Thursday, August 13, 2015	Sheet 06 of 102



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Custom	0.5
				Date	Thursday, August 13, 2015
				Sheet	67 of 102

R413, R422 : Need Anti-surge Chip Resistors

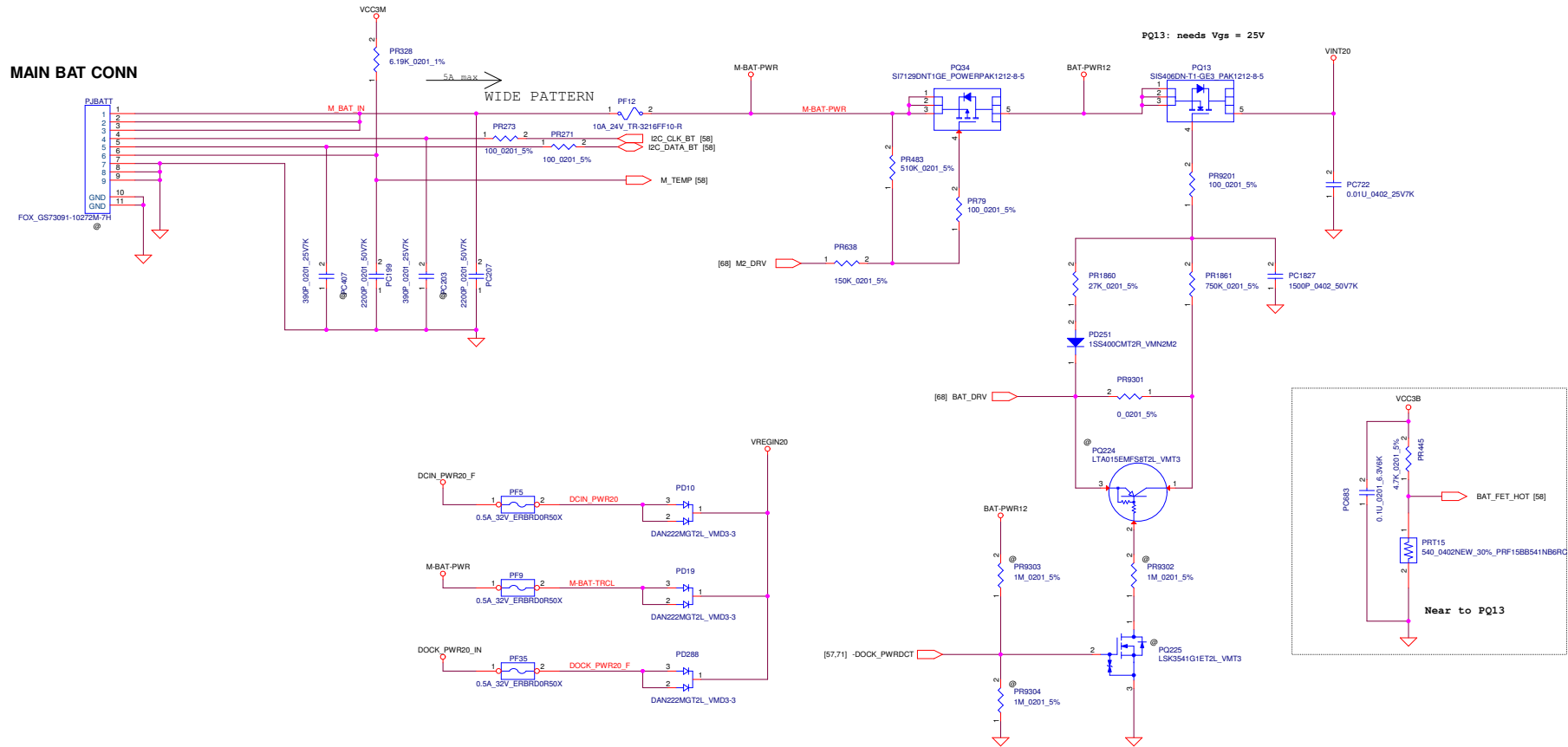


TABLE

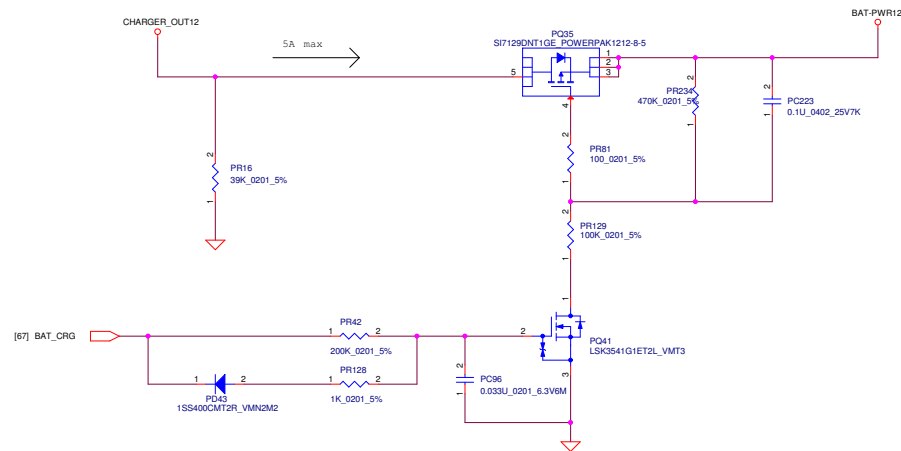
ID	Target
RT1	VCC5M Switching FET
RT2	VCC3M Switching FET
RT3	CHARGER SWITCHING FET
RT4	M_BAT_PWR to BAT_PWR12 FET
RT5	CHAGEROUT12 to BAT_PWR12 FET
RT6	VCCSA MOSFET Driver NCP81382
RT7	VCCPCUPCORE MOSFET Driver NCP81382
RT8	VCCCGFXCORE-I MOSFET Driver NCP81382
RT9	CPU Die
RT10	DOCK_PWR20_IN to DOCK_PWR20 FET
RT11	DCIN_PWR20_F to CV20_FET

Security Classification	Compal Secret Data		Title	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Compal Electronics, Inc. THINK ENGINE(2/2)
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT WITHOUT PERMISSION AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED OR BY DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.			Revision Number LA-C581P	Rev 0.5
Date:	Thursday, 4/29/13	2013	Sheet	68 of 100

MAIN BAT CONN



Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Document Number	Rev
				Custom	0.1
				Date	Thursday, August 13, 2015
				Sheet	79 of 102



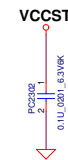
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Document Number	Rev
				Custom	0.1
				Date	Thursday, August 13, 2015
				Sheet	72 of 102

CHARGE SELECTOR

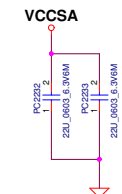
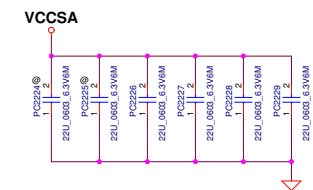
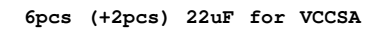
LA-C581P

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	BLANK
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	73 of 102



Security Classification		Compal Secret Data		Compal Electronics, Inc. DC/DC IMPVP8 CONTROLLER	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETITIVE DIVISION OF R&D ELECTRONIC TECHNOLOGY AUTHORIZED BY COMPAL ELECTRONICS, INC. WITHOUT THE INFORMATIC INFORMATION CONTAININGS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Document Number LA-C581P
				Date:	Thursday, August 13, 2015 Sheet 75 of 102



TDK	SPM6530T-R68M140KA
NEC TOKIN	MPLCH0630LR68-T70

TABLE	PR2151	0603	SIZE
Rohm	ESR03EZPJ2R2		
Pana	ERJPA3J2R2V		
Vishay	CRCW06032R2JNEAHF		
YDS	RN73S2CL-2R20-F		

Security Classification		Compul Secret Data		Title		Compul Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Size	Document Number	DC/DC VCCSA(NCPB1382)	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPUL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. IT IS NOT TO BE TRANSMITTED OR MADE AVAILABLE TO ANY OTHER PARTY WITHOUT THE WRITTEN PERMISSION OF COMPUL ELECTRONICS, INC. ANY REPRODUCTION OR DISSEMINATION OF THIS SHEET WITHOUT THE WRITTEN PERMISSION OF COMPUL ELECTRONICS, INC. IS STRICTLY PROHIBITED.				Custom	LA-C581P	Rev 0.1	
DATE: 2014-12-31 15:25:15				Sheet	78	of 102	

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	BLANK
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	79 of 102

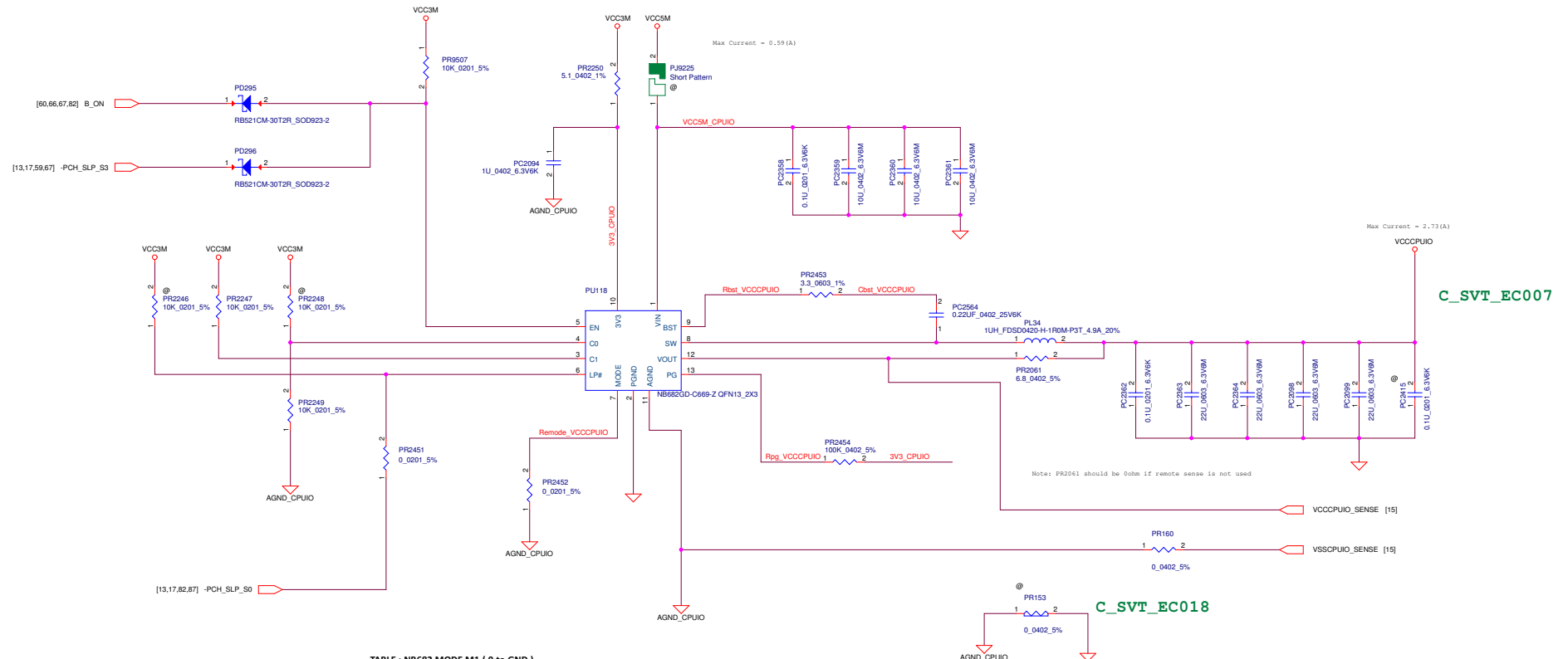
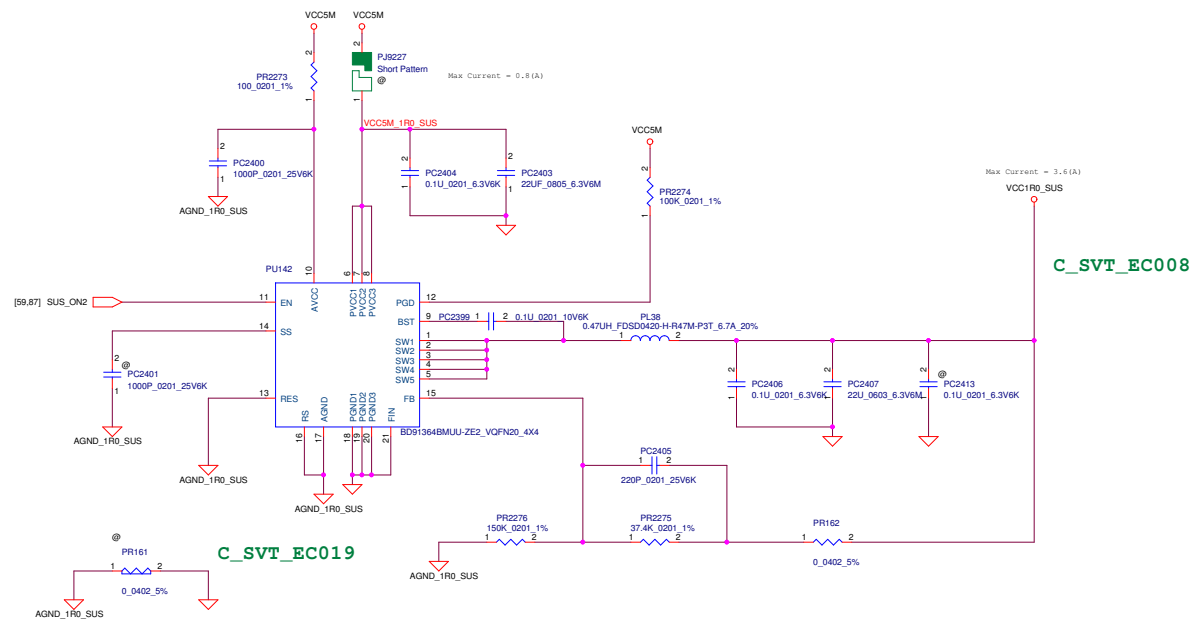


TABLE : NB682 MODE M1 (0 to GND)

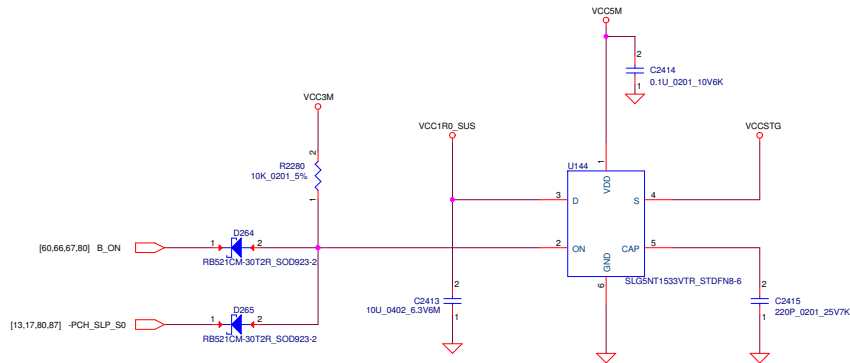
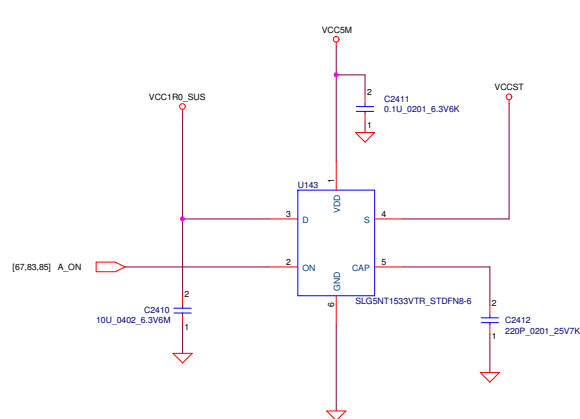
LP#	C1	C0	VOUT
0	X	X	0.000V
1	0	0	0.850V
1	0	1	0.875V
1	1	0	0.950V
1	1	1	0.975V

← SLP_S0#

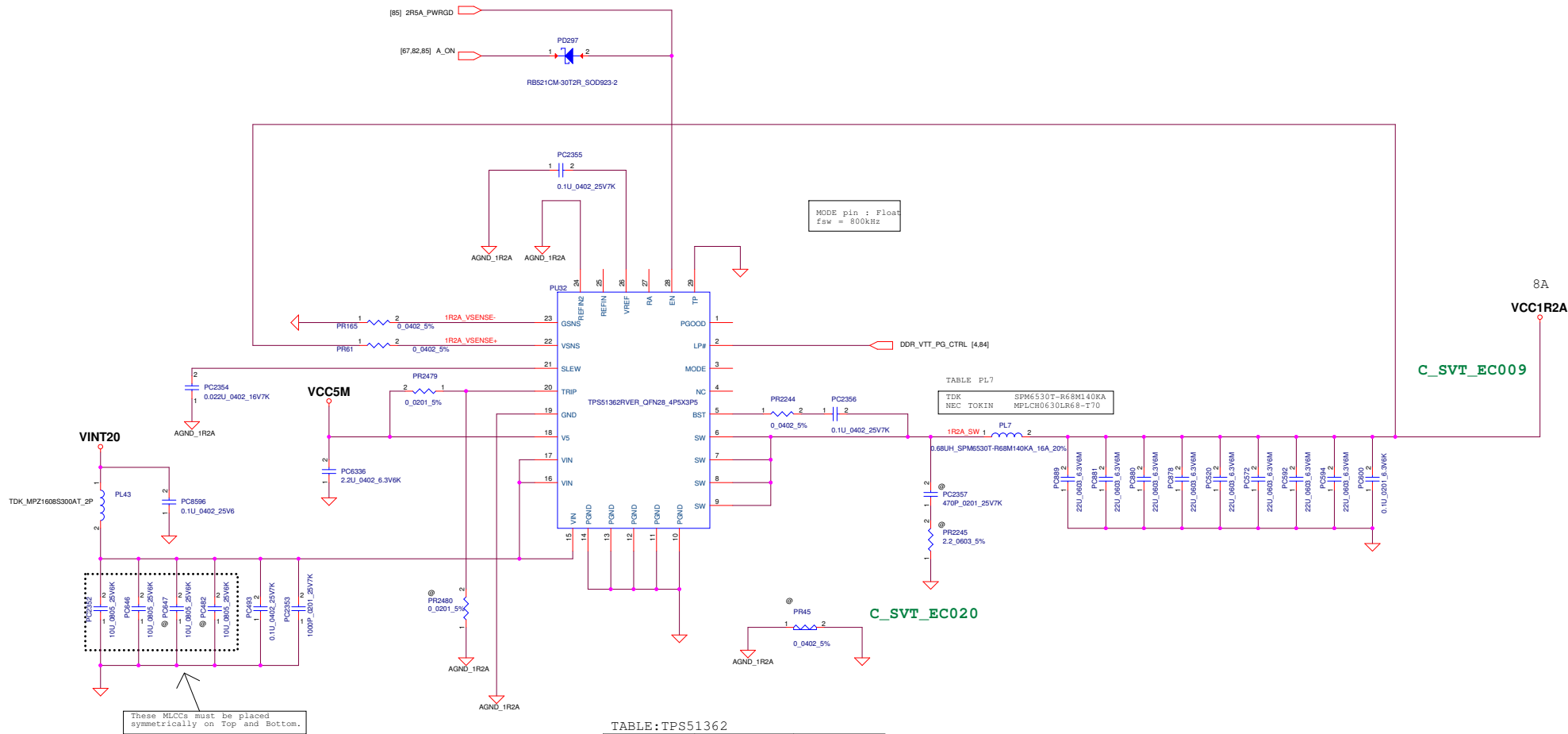
← LOGIC



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		Deciphered Date		Title	
2014/11/04		2016/12/31		DC/DC VCC1R0 SUS	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		Size		Rev	
		Document Number		0.1	
		Customer		LA-C581P	
		Date		Thursday, August 13, 2015	
		Sheet		81 of 102	



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		2014/11/04		Deciphered Date	
				2016/12/31	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		Title		LOAD SW VCCST & VCCSTG	
Size		Document Number		Rev	
Custom		LA-C581P		0.5	
Date		Thursday, August 13, 2015		Sheet 82 of 102	



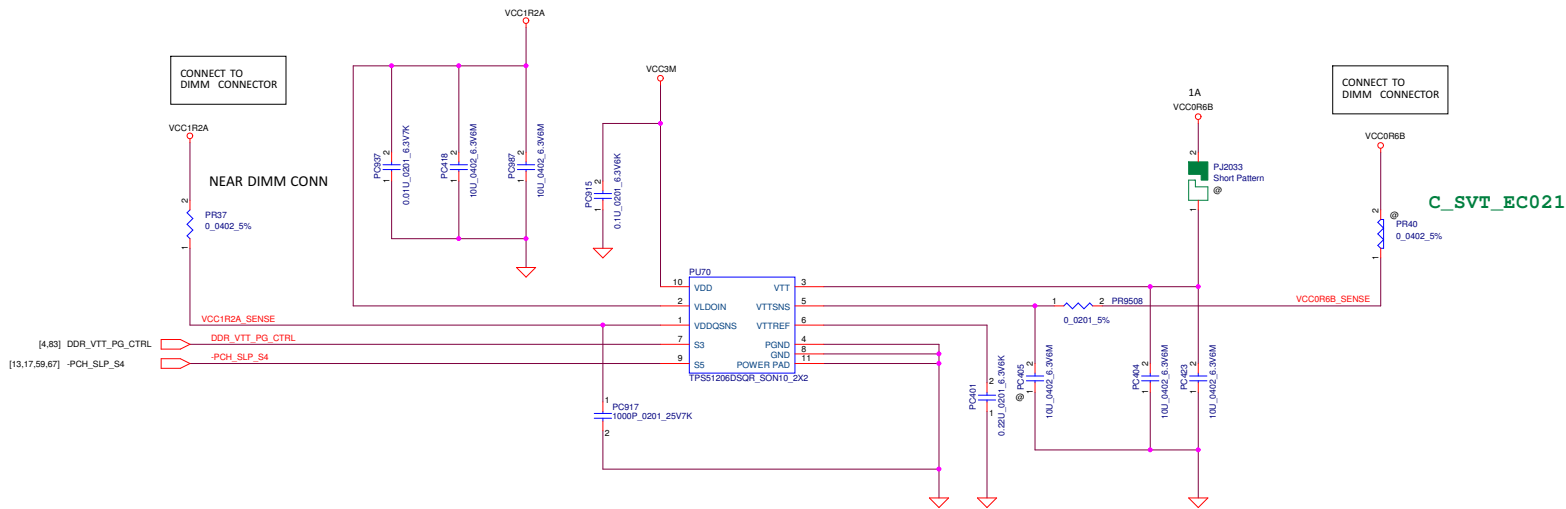
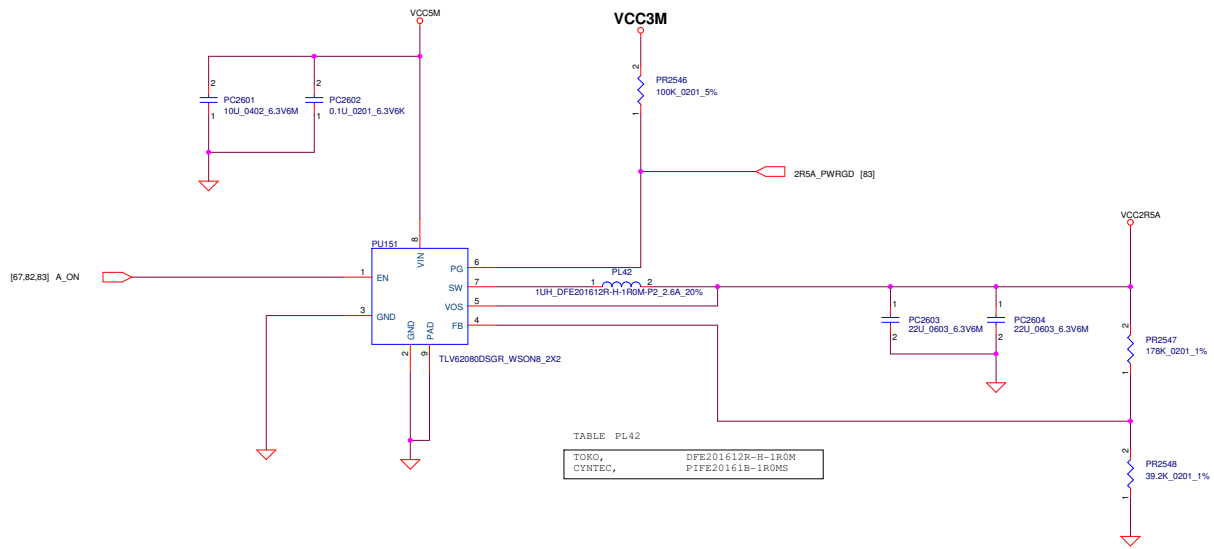
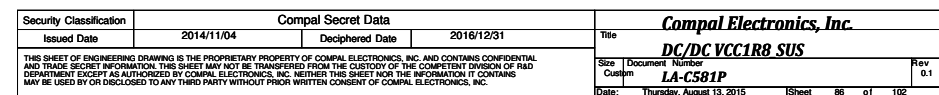


TABLE : TPS51206

S3	S5	VTT	VTTREF
High	High	ON	ON
Low	High	OFF(High-Z)	ON
Low	Low	OFF(Discharge)	OFF(Discharge)



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		2014/11/04		Deciphered Date	
				2016/12/31	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		Title		DC/DC VCC2R5A(TLV62080)	
		Size		Document Number	
				LA-C581P	
		Date		Thursday, August 13, 2015	
		Sheet		85 of 102	
				Rev	
				0.1	



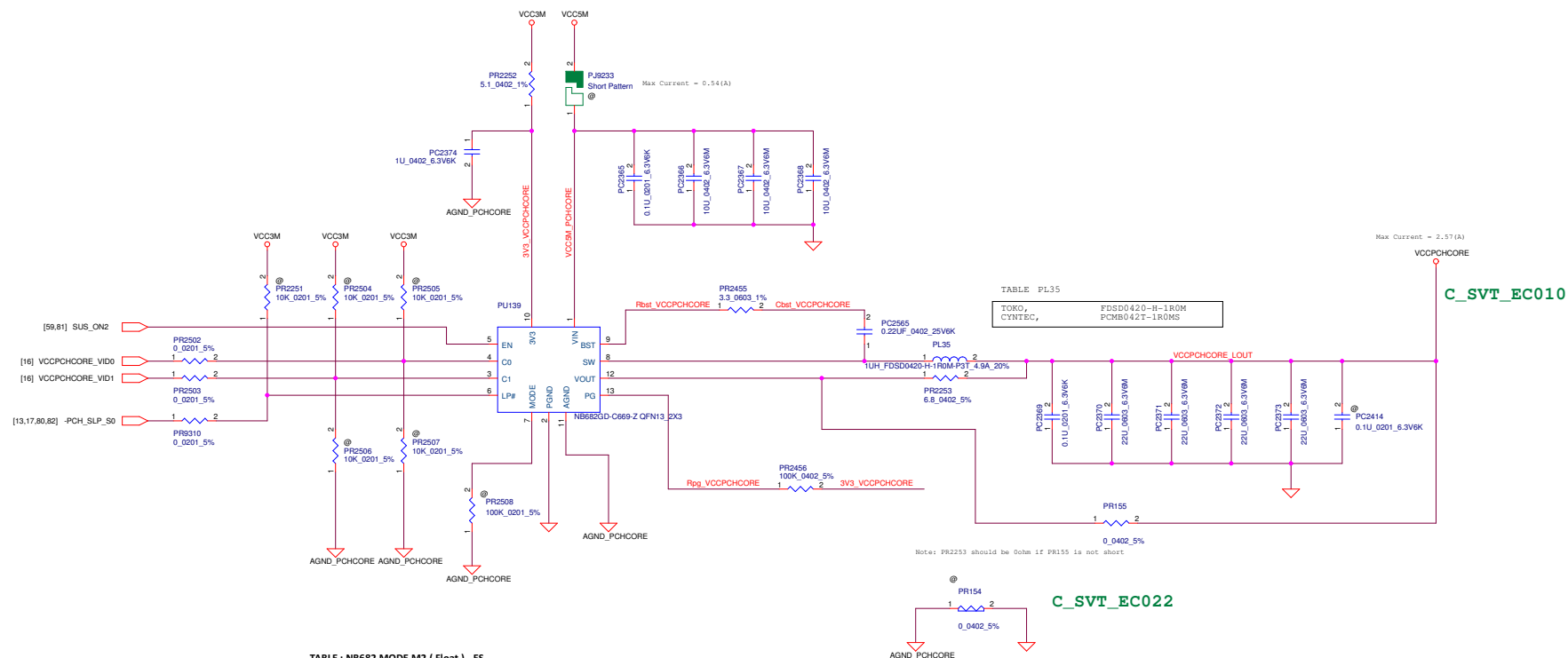


TABLE : NB682 MODE M2 (Float) - ES

LP#	C1	C0	VOUT
0	X	X	0.700V
1	0	0	0.850V
1	0	1	0.900V
1	1	0	0.950V
1	1	1	1.000V

← SLP_S0#

← DEFAULT

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	BLANK
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	88 of 102

BLANK

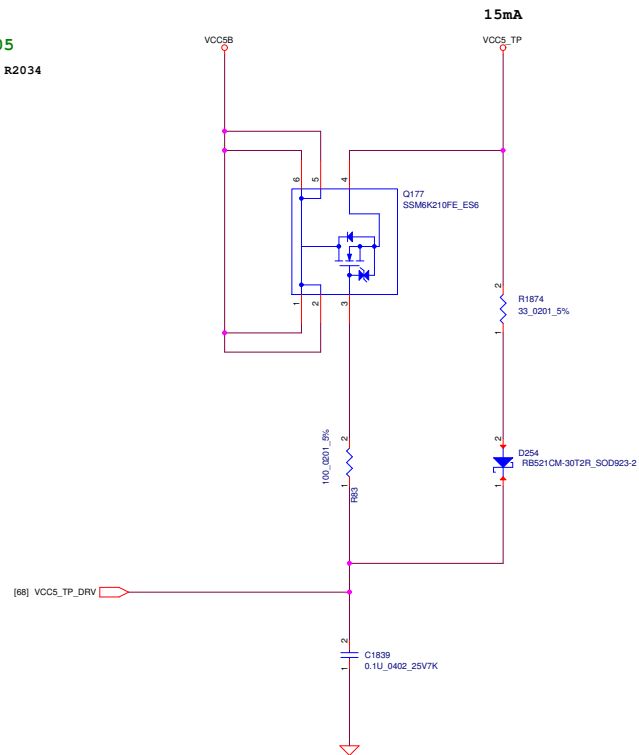
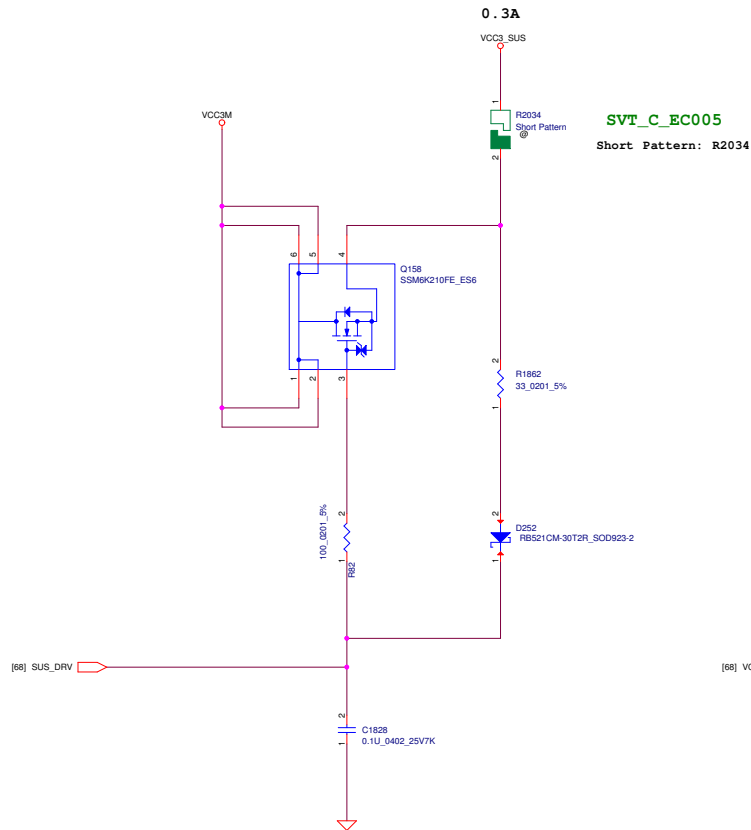
Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	BLANK
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	89 of 102

BLANK

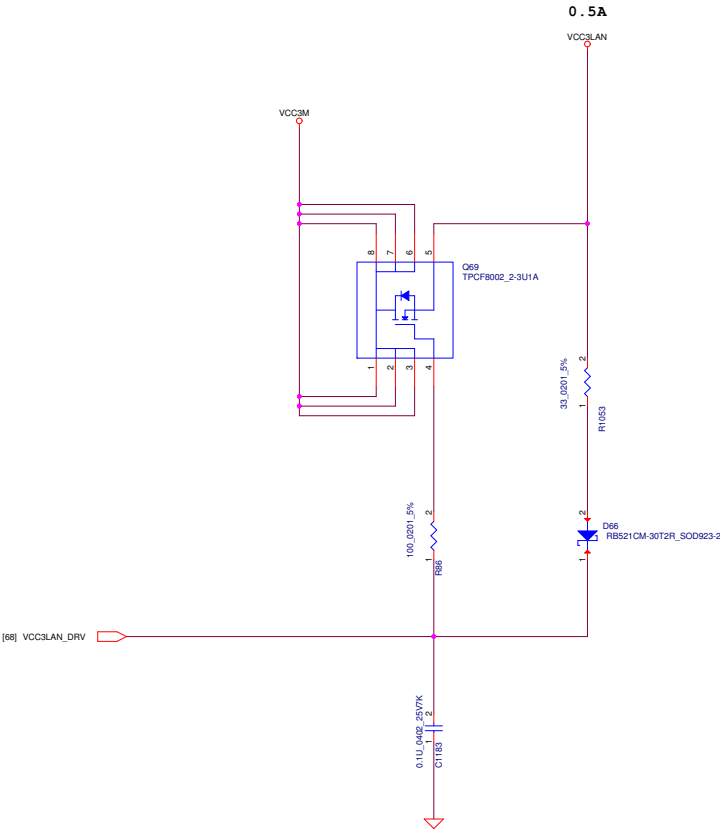
Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				BLANK		
				Size	Document Number	Rev
				Custom	LA-C581P	0.5
				Date	Thursday, August 13, 2015	Sheet 99 of 102

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	BLANK
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Document Number	Rev
				Custom LA-C581P	0.5
				Date: Thursday, August 13, 2015	Sheet 91 of 102



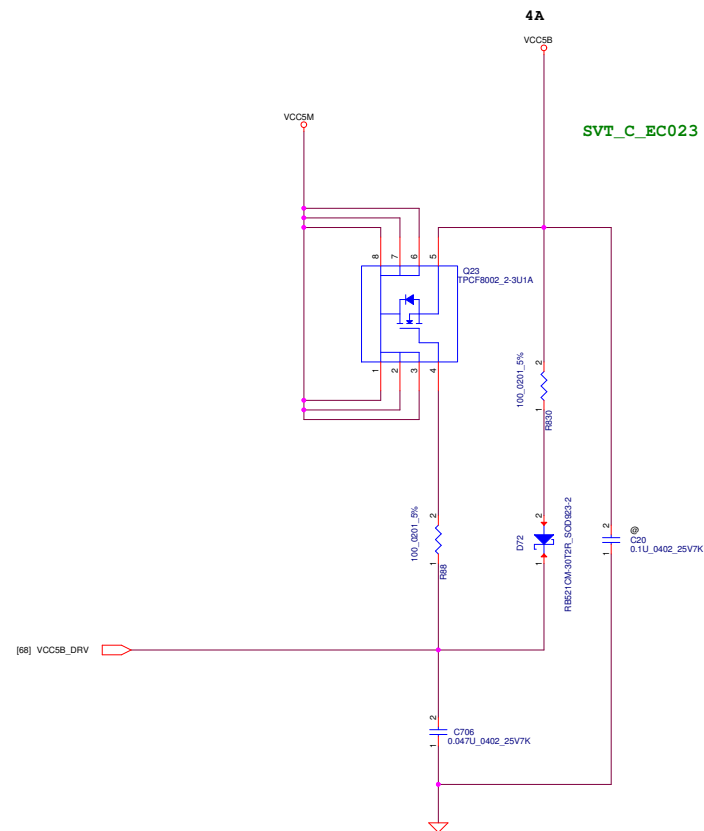
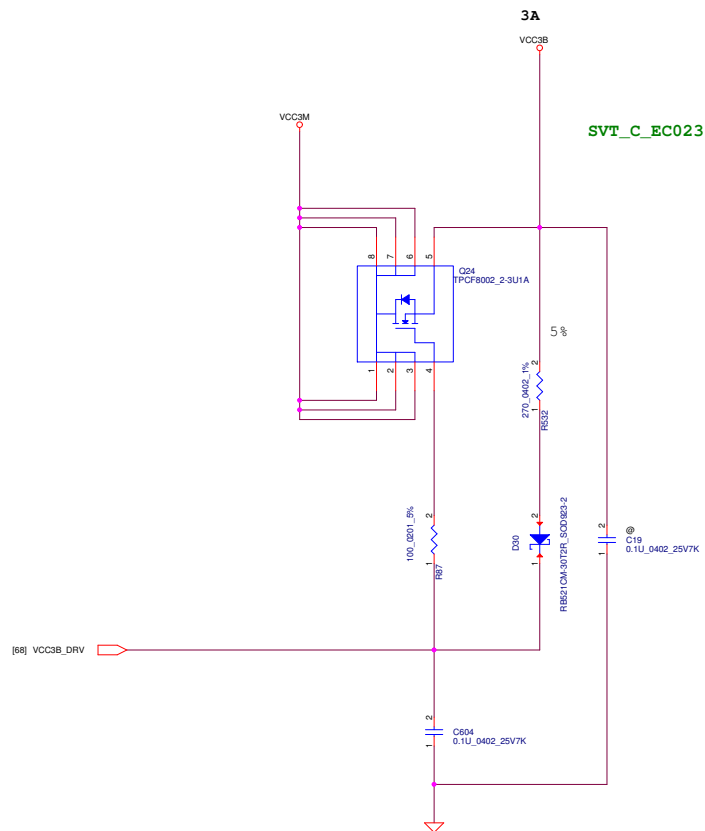
Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Document Number	Rev
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	92 of 102



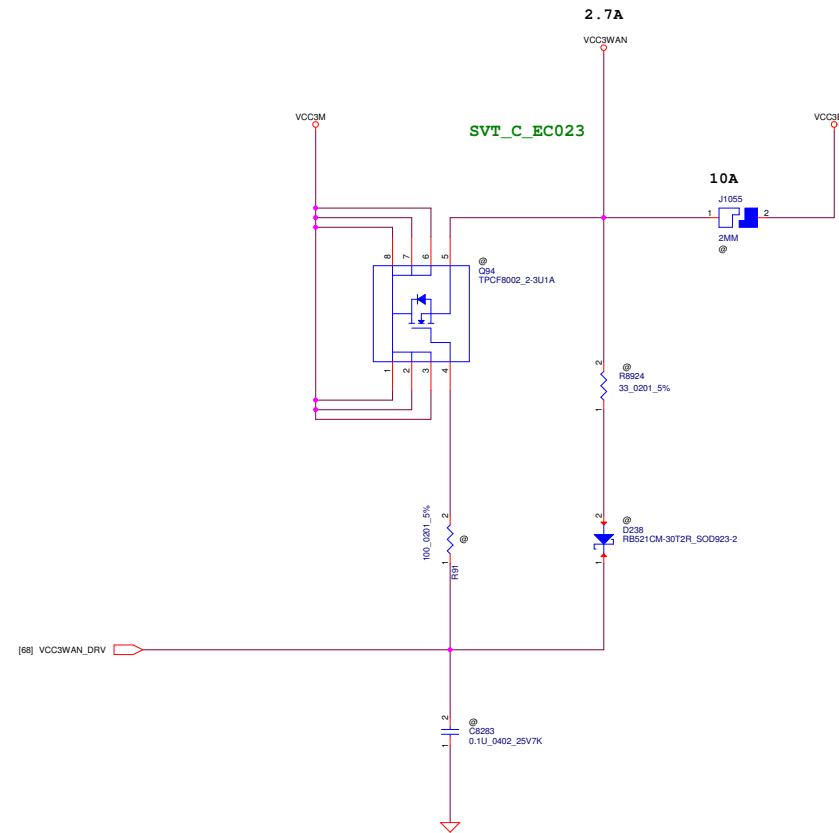
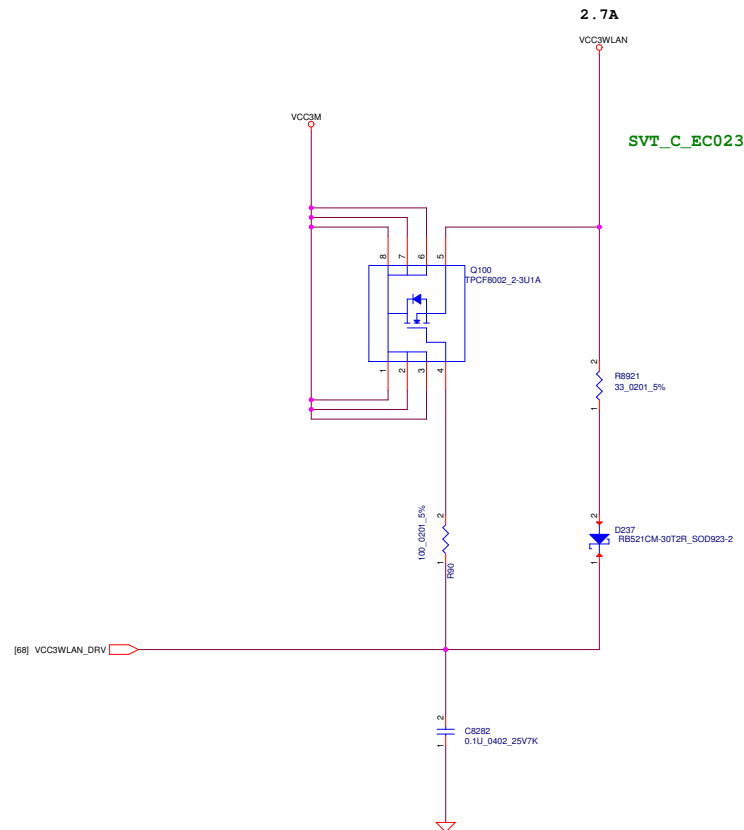
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		Deciphered Date		Title	
2014/11/04		2016/12/31		LOAD SW LAN	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		Size Document Number		Rev	
		Custom LA-C581P		0.5	
Date		Thursday, August 13, 2015		Sheet 93 of 102	

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				BLANK		Rev 0.5
				Size	Document Number	
				Custom	LA-C581P	
				Date	Thursday, August 13, 2015	Sheet 94 of 102



Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Rev 0.5
				Document Number LA-C581P	
				Date Thursday, August 13, 2015	Sheet 95 of 102



TABLE

AOAC	YES	NO
R1055	NO-ASM	ASM
Q94	ASM	NO-ASM
R91	ASM	NO-ASM
R8924	ASM	NO-ASM
C8283	ASM	NO-ASM
D238	ASM	NO-ASM

↑
LOGIC

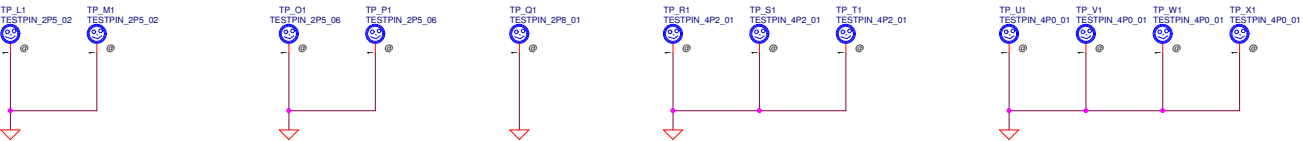
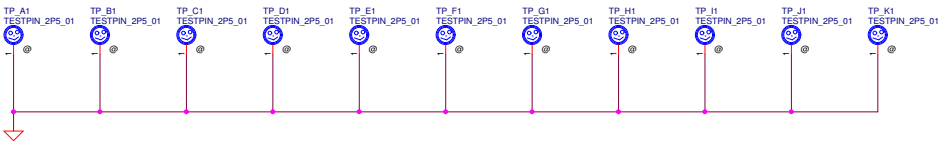
Security Classification		Compal Secret Data		Title	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Compal Electronics, Inc.	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Document Number Custom LA-C581P Date: Thursday, August 13, 2015 Sheet: 98 of 102	
				Rev: 0.5	

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	BLANK
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Document Number	Rev
				Custom LA-C581P	0.5
				Date: Thursday, August 13, 2015	Sheet 97 of 102

PTH FOR SCREW HOLE

Value	Hole Dia	Pad Dia		Qty
		TOP	BOTTOM	
TESTPIN_2P5_01	2.5	6	6	11
TESTPIN_2P5_02	2.5	7.4	7.4	2
TESTPIN_2P5_03	2.5	Square	0	1
TESTPIN_2P5_06	2.5	5	5	2
TESTPIN_2P8_01	2.8	0	Square	1
TESTPIN_4P3_01	4.3	6.5	6.5	3
TESTPIN_4P0_01	4.0	6.1	6.1	4



NPTH



FID
Board Area



Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Document Number	0.5
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	98 of 102

BLANK

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	BLANK
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	99 of 102

BLANK

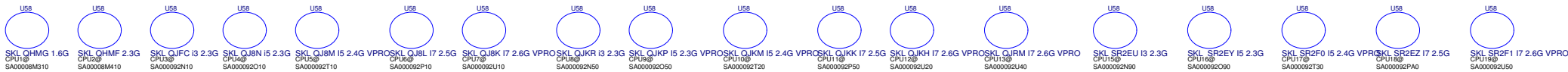
Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	BLANK
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	100 of 102

BLANK

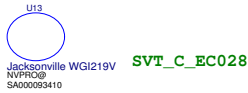
Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	BLANK
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	101 of 102



SVT_C_EC025



SVT_C_EC025



SVT_C_EC028

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2014/11/04	Deciphered Date	2016/12/31	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Document Number	Rev
				Custom	LA-C581P
				Date	Thursday, August 13, 2015
				Sheet	102 of 102