

CR-1

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1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.

2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.

3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV

ZONE

ECN

DESCRIPTION OF CHANGE

CK APPD

ENG APPD

DATE

DATE

?

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?

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Q26 MLB

GEN X

REV D

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POWER RAIL DEFINITIONS

	RUN	SLEEP	SHUTDOWN
+2_5V_MAIN	ON	ON	OFF
+3V_MAIN	ON	ON	OFF
+5V_MAIN	ON	ON	OFF
+5V_SLEEP	ON	OFF	OFF
+12V_MAIN	ON	ON	ON
+12V_SLEEP	ON	OFF	OFF
FW_PWR	ON	ON	OFF
+1.8V_SLEEP	ON	OFF	OFF
+MAXBUS_SLEEP	ON	OFF	OFF

SCHEMATIC AND PCB SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
051-6423	1	SCHEM, MLB, Q26, GENX	SCH1	CRITICAL	
820-1501	1	PCB, MLB, IMACG4	PCB1	CRITICAL	
825-2029	1	LBL, SER #, BARCODE	PCB1		
056-1158	1	DESIGN GUIDE, MCO, IMACG4	PCB1	CRITICAL	
057-0040	1	DFM, PNLZN DWG, MLB, Q26	PCB1	CRITICAL	
630-4578	1	630-4578, PCBA, H, Q26, EEE P9V	HYNIX		OMIT
630-4579	1	630-4579, PCBA, S, Q26, EEE P9W	SAMSUNG		OMIT

DIMENSIONS ARE IN MILLIMETERS

XX : _____

X.XX : _____

X.XXX : _____

ANGLES : _____

DO NOT SCALE DRAWING

THIRD ANGLE PROJECTION

METRIC

DRAFTER	/	DESIGN CK	/
ENG APPD	/	MFG APPD	/
QA APPD	/	DESIGNER	/
RELEASE	/	SCALE	NONE
MATERIAL/FINISH NOTED AS APPLICABLE		SIZE	D

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TITLE

SCHEM, MLB, Q26, GEN X

DRAWING NUMBER

051-6423

REV.

D

SHT

1

OF

74

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6

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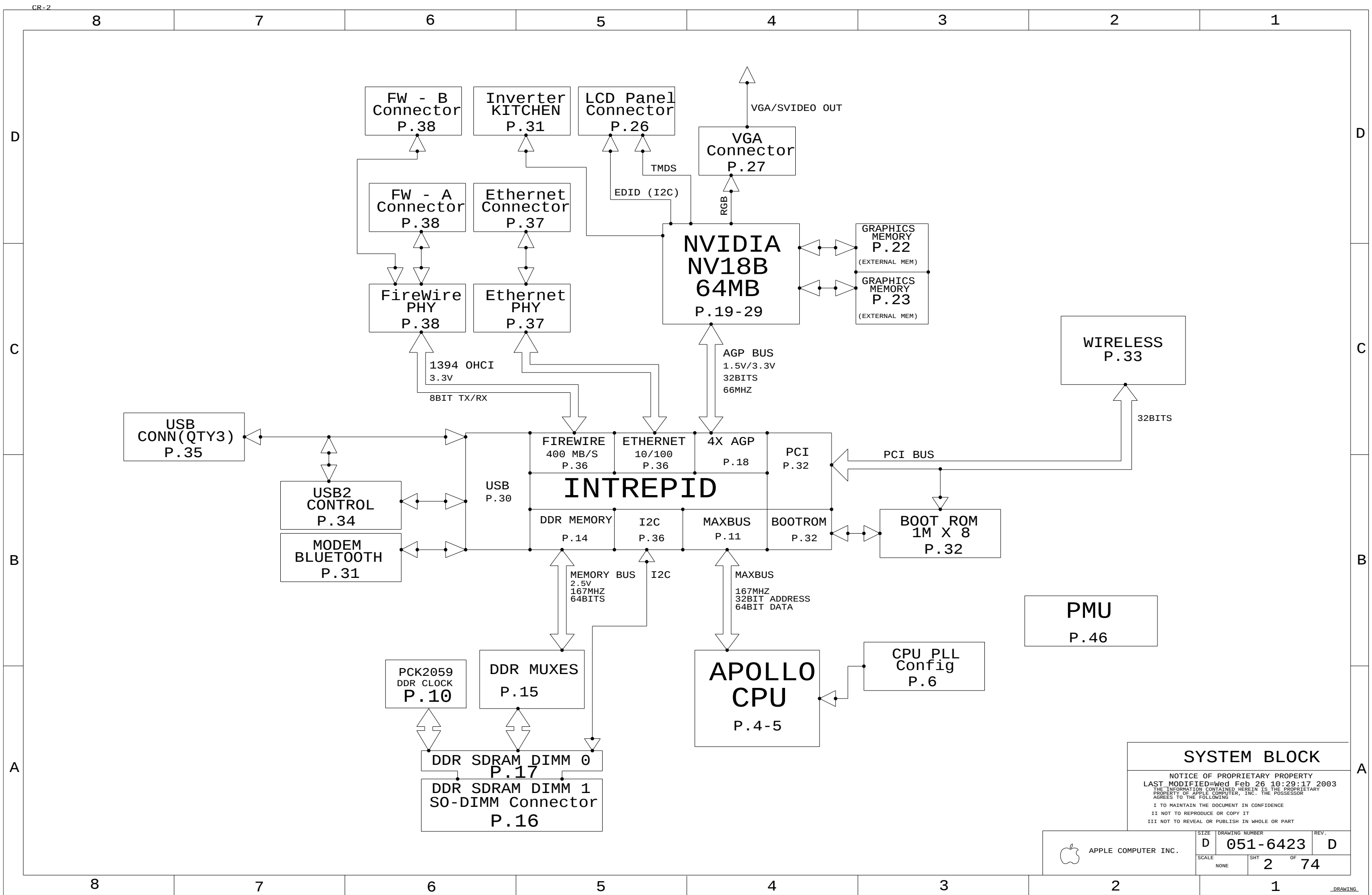
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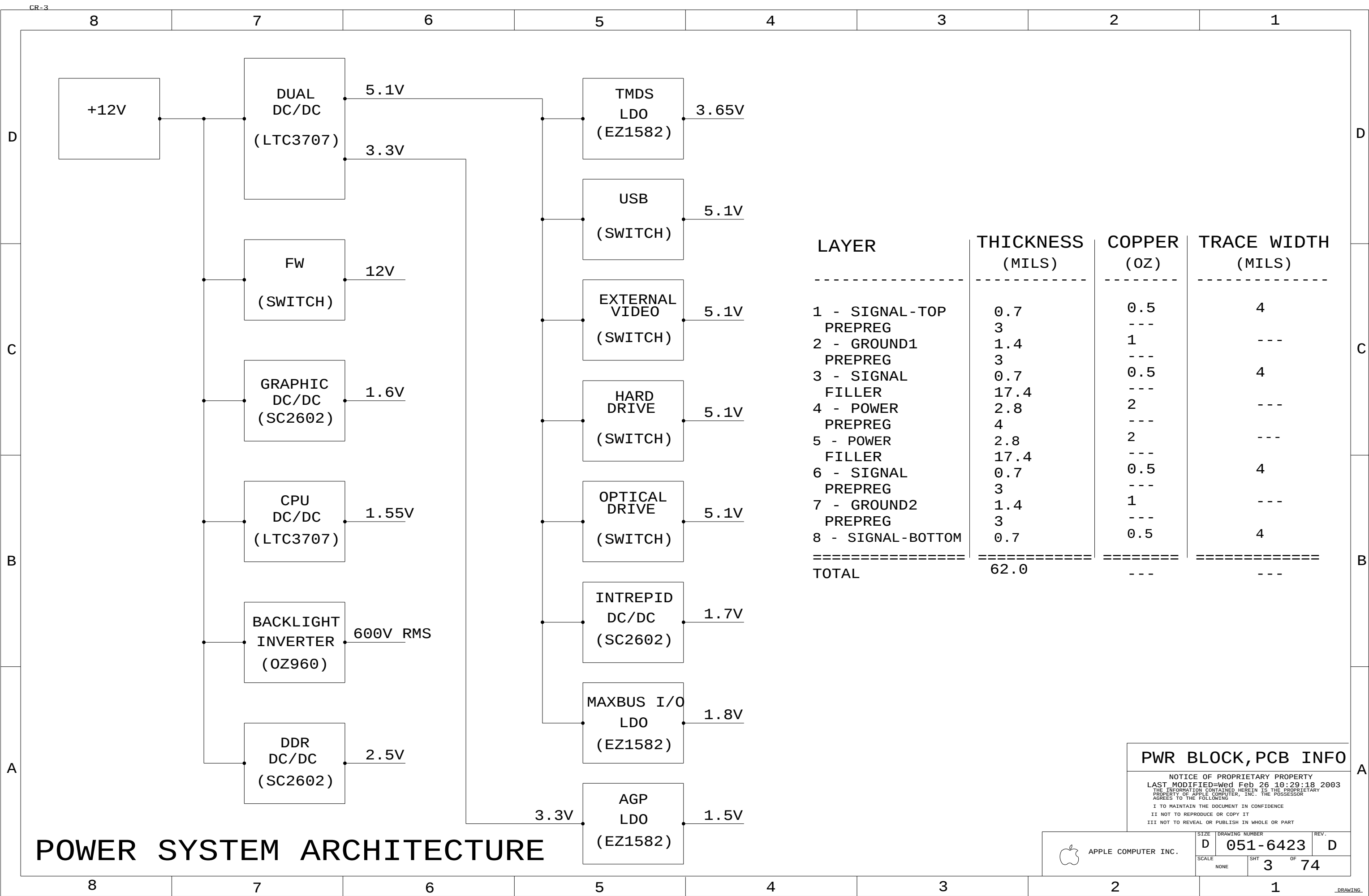
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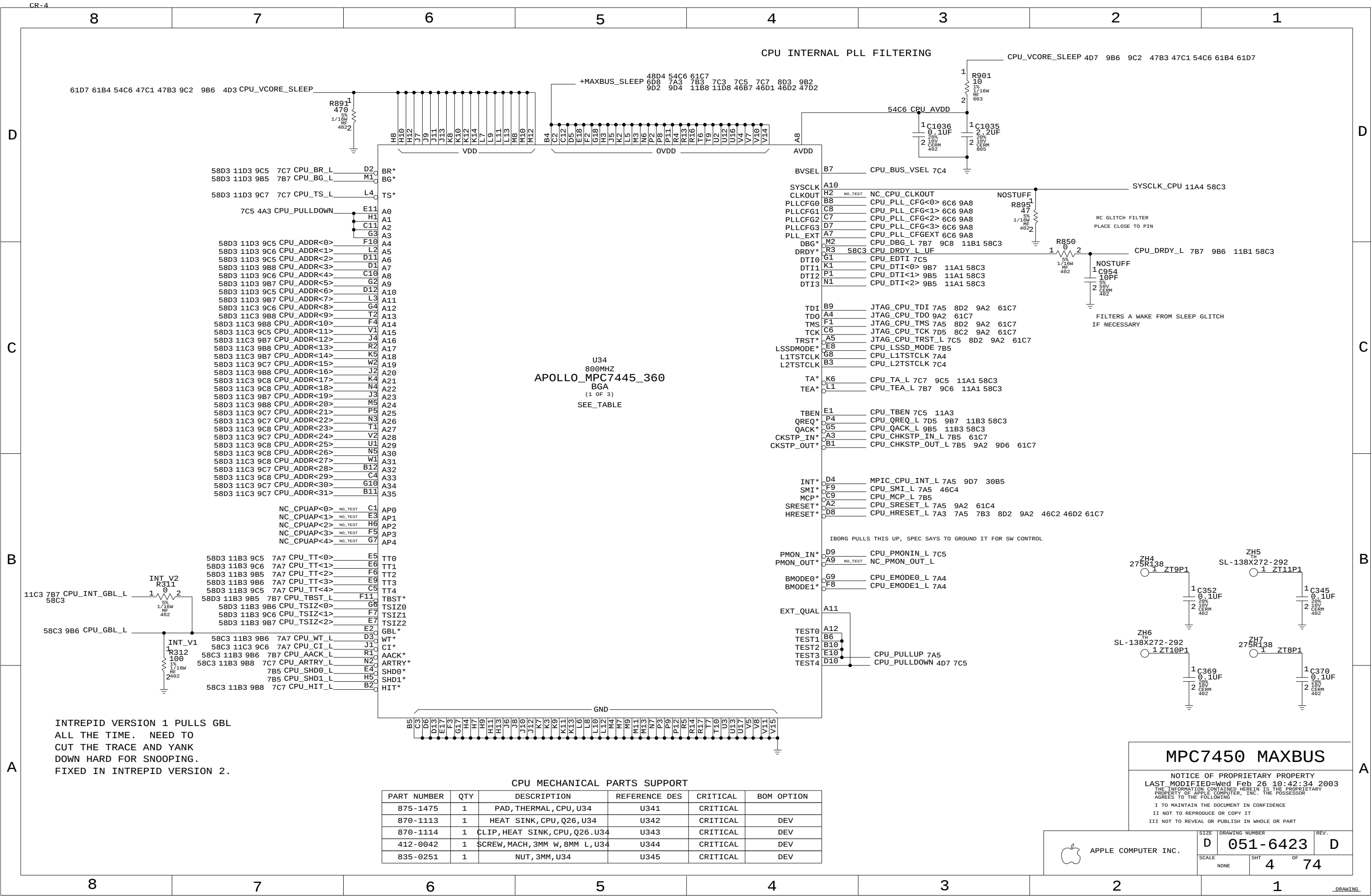
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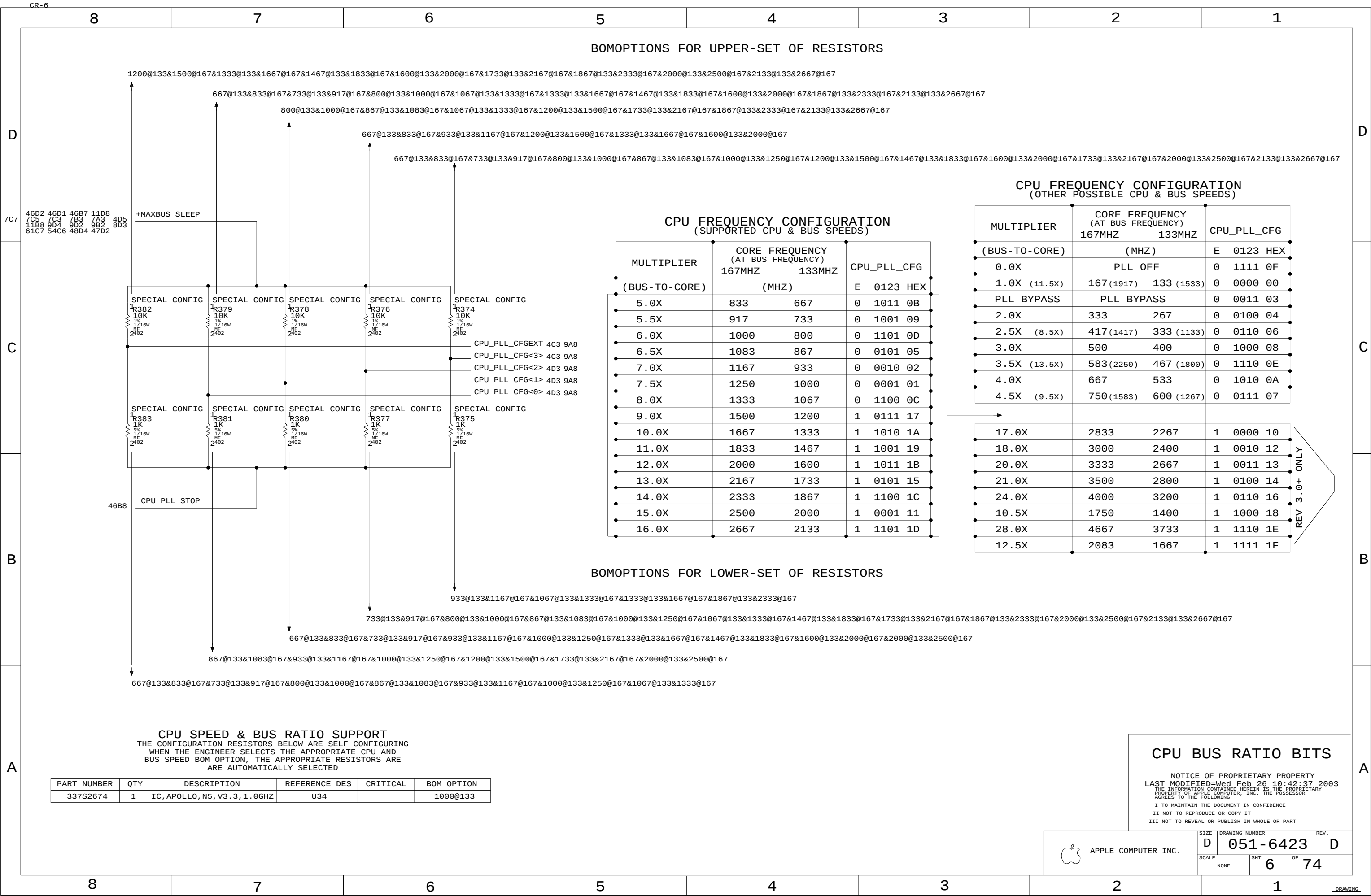
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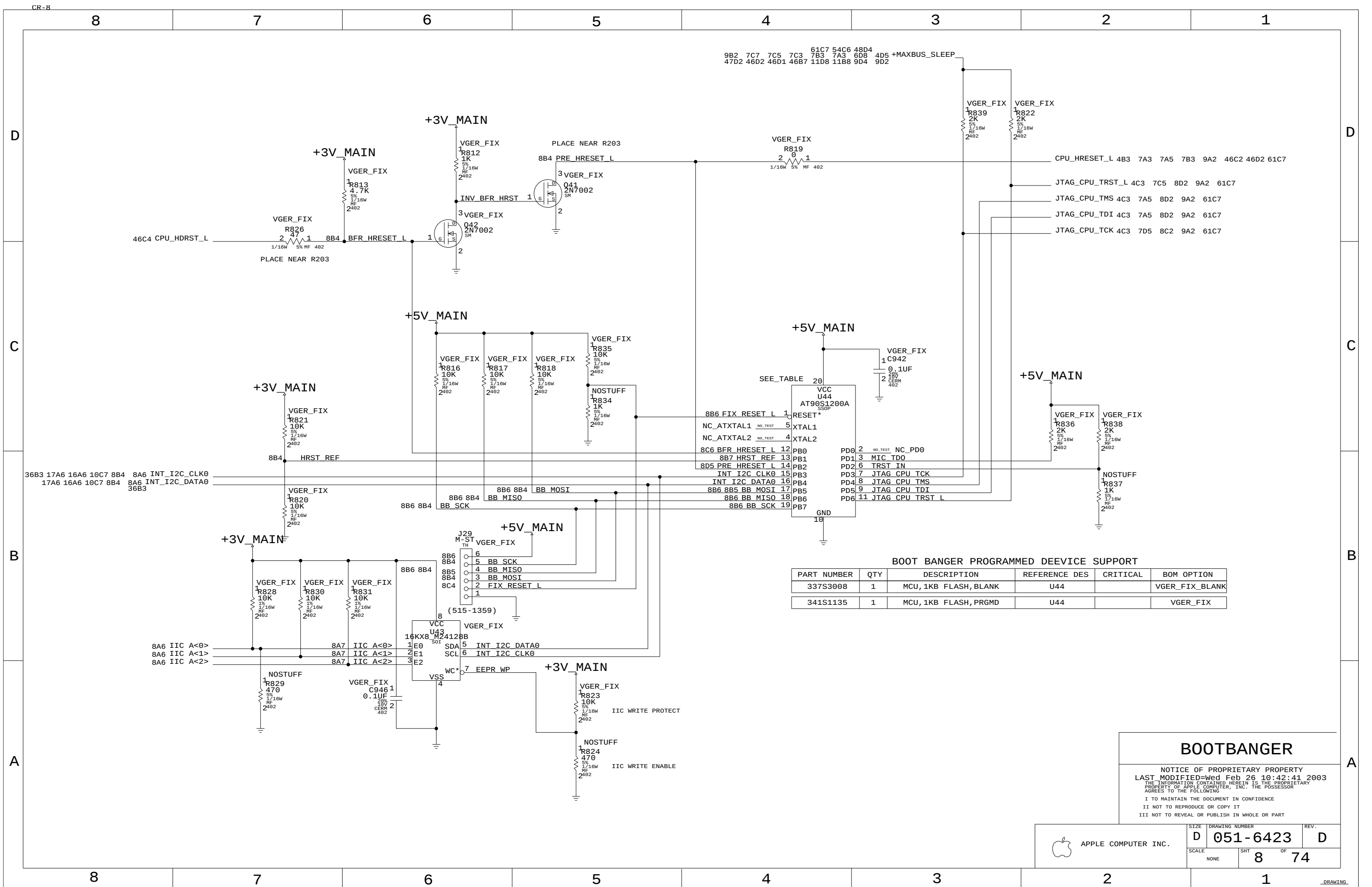
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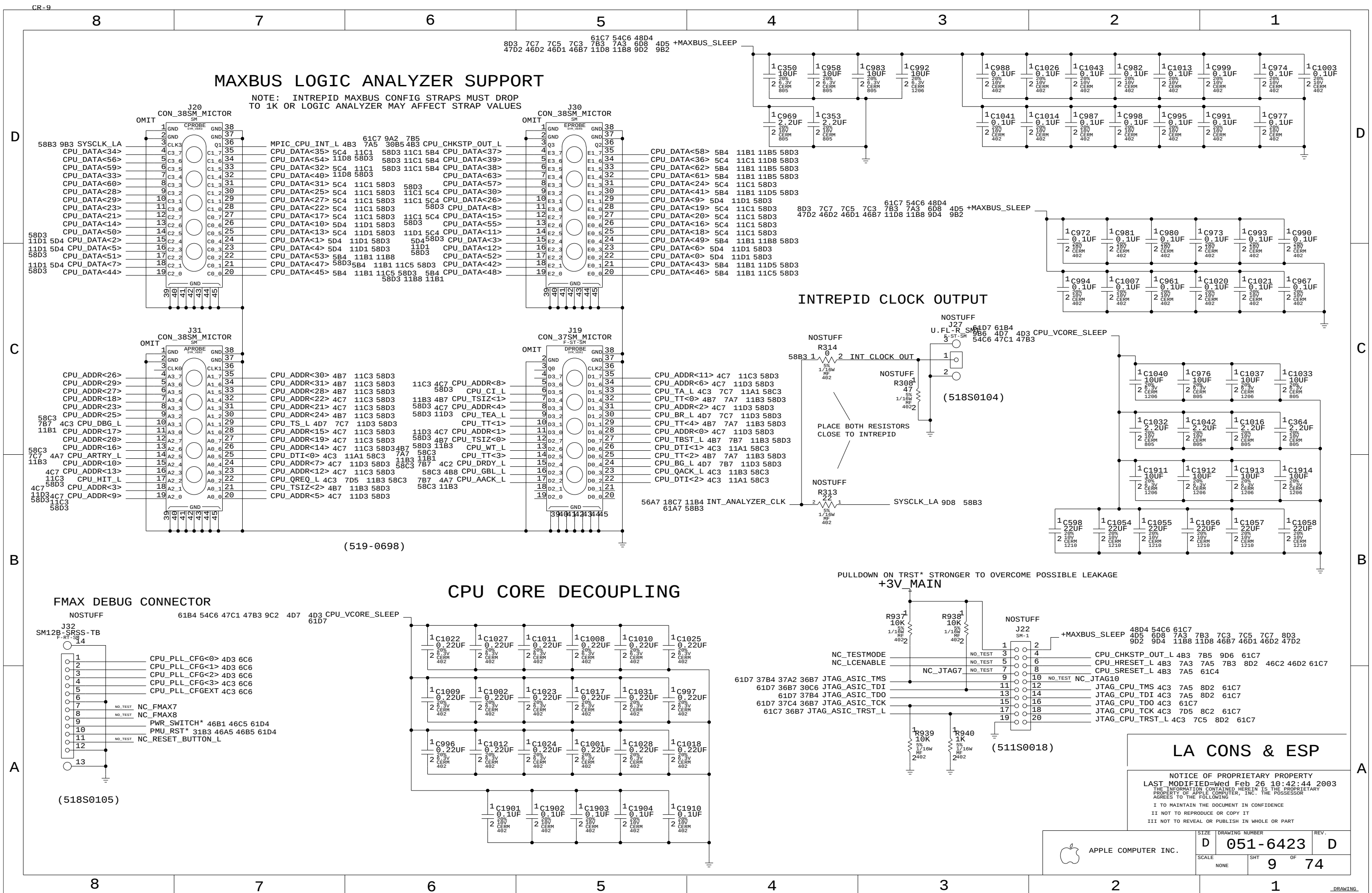


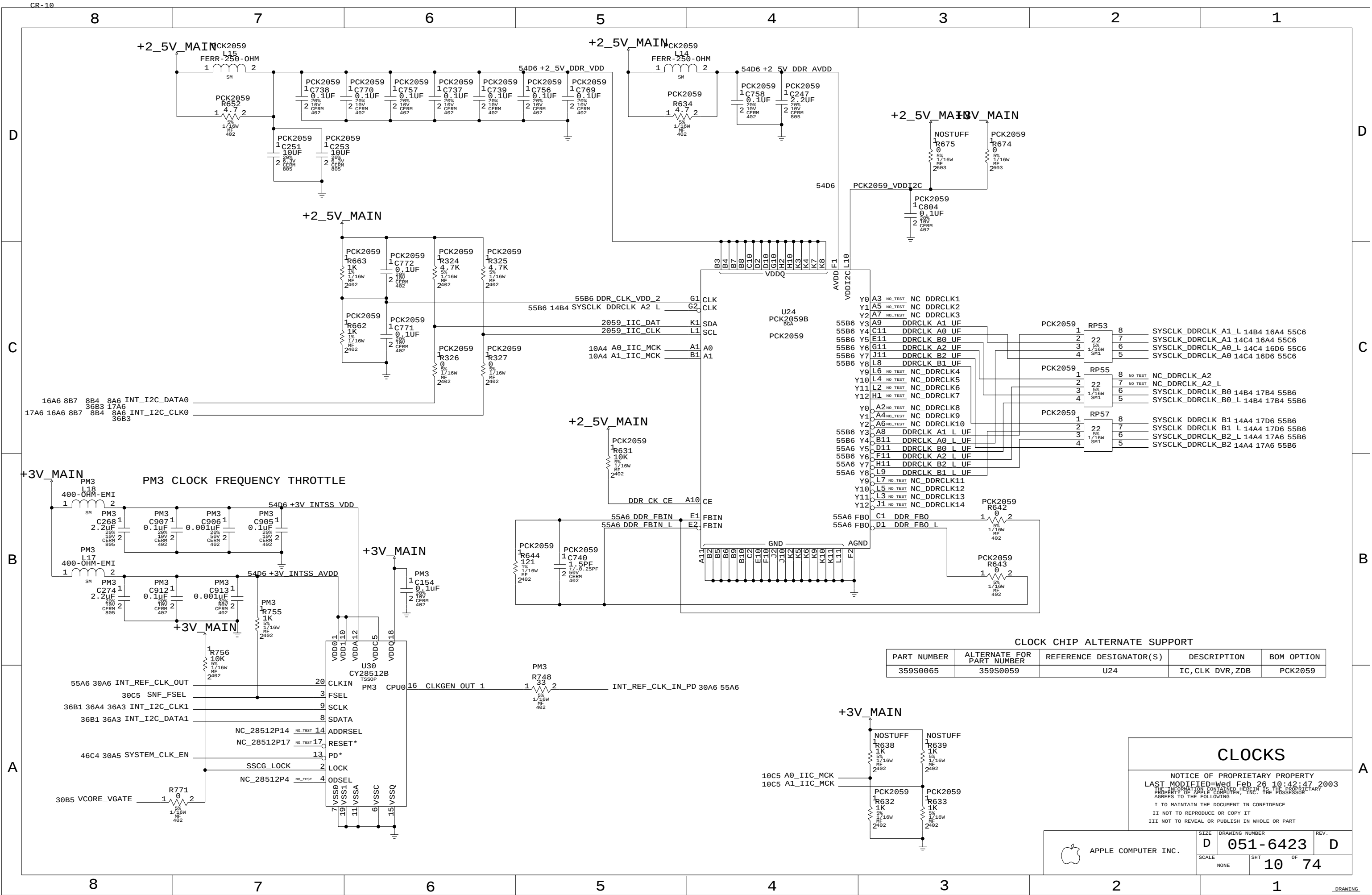


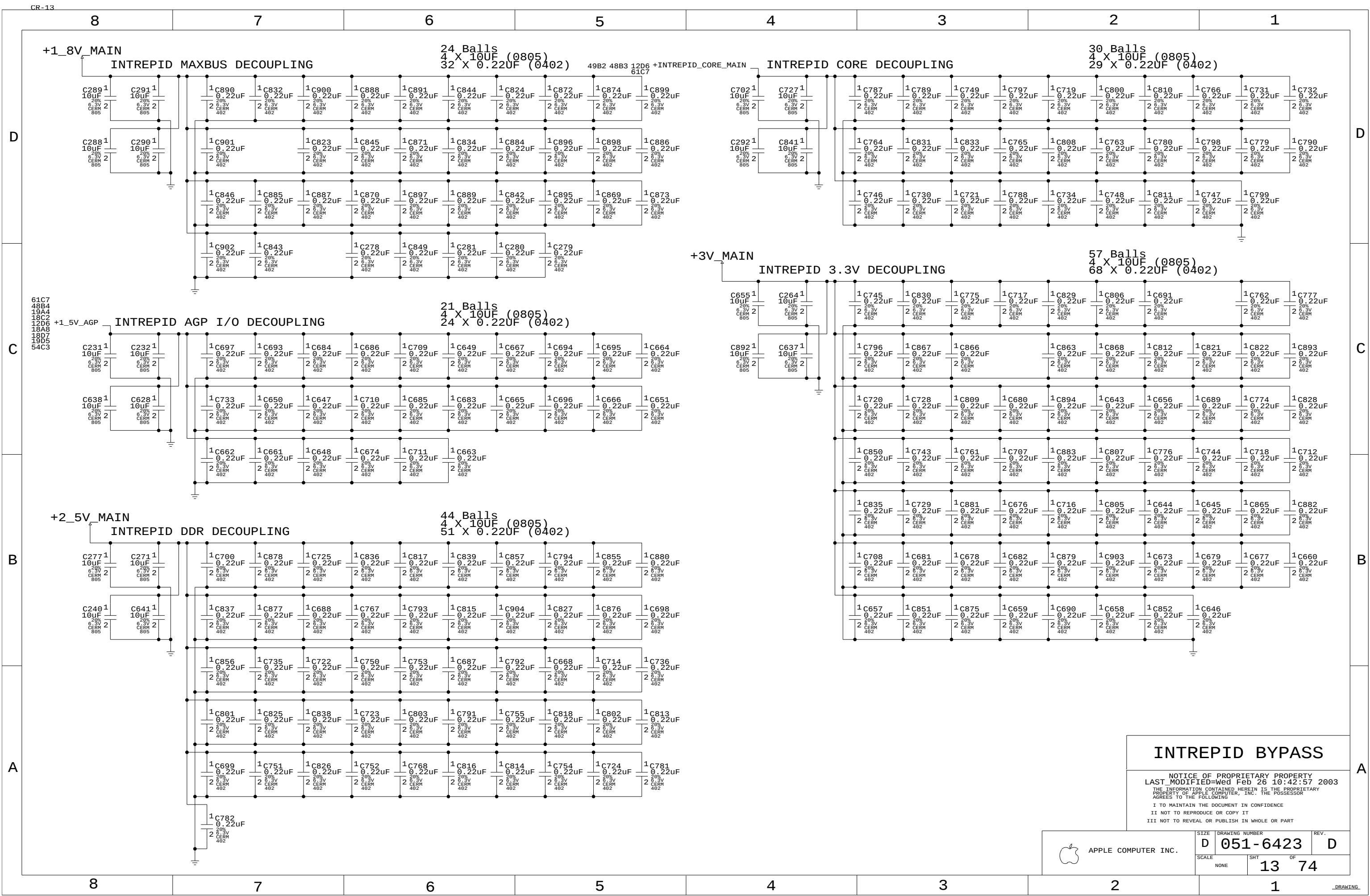


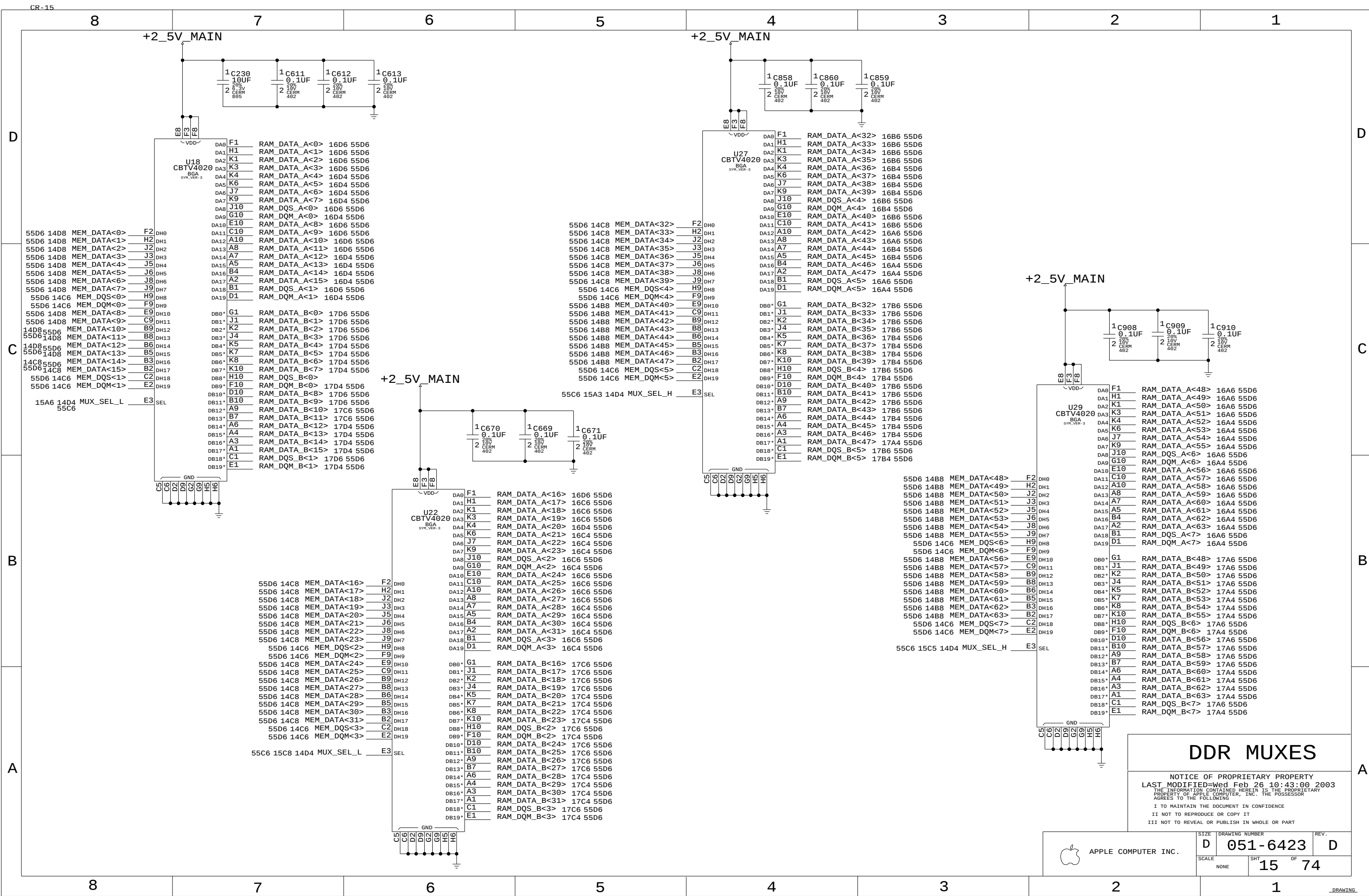




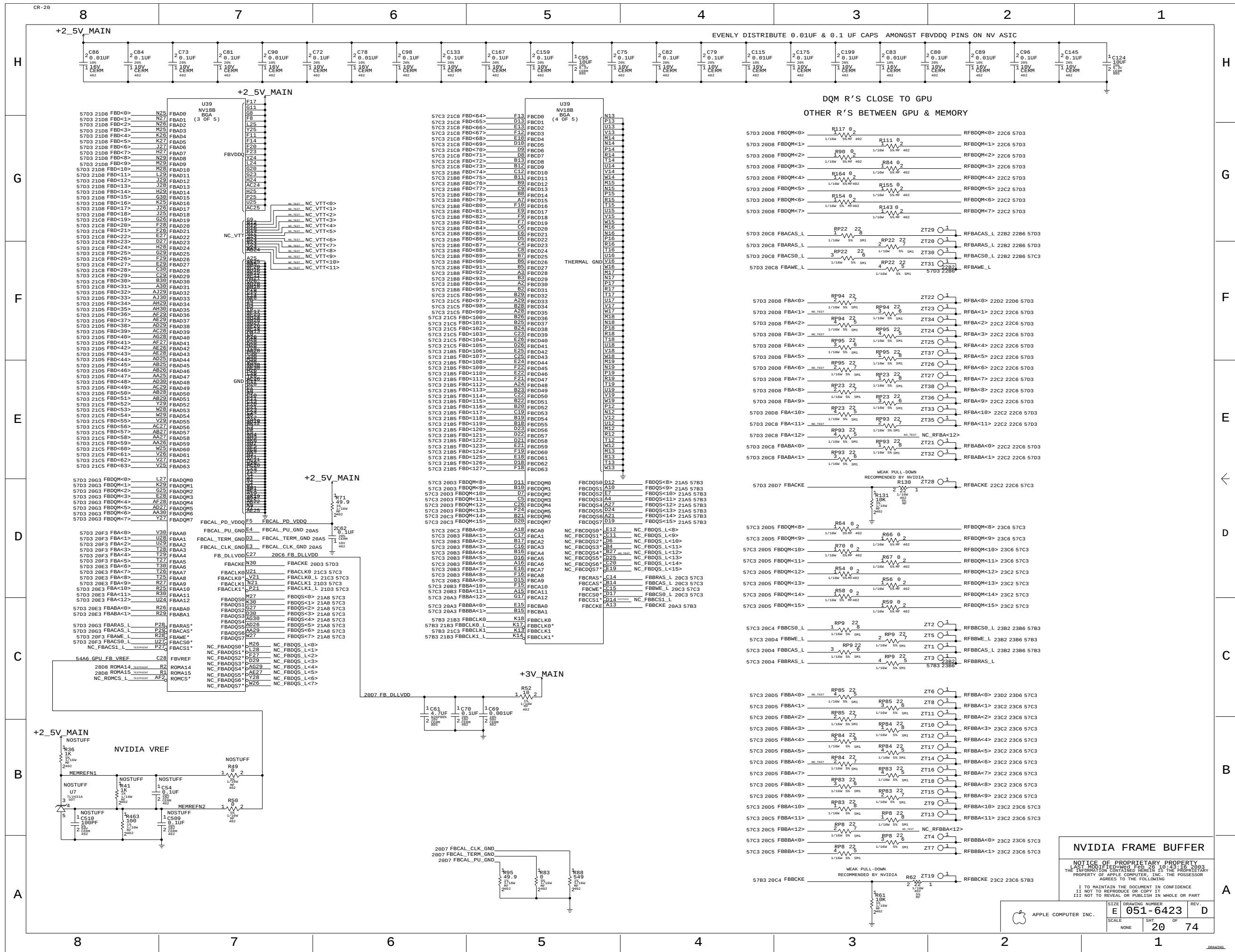












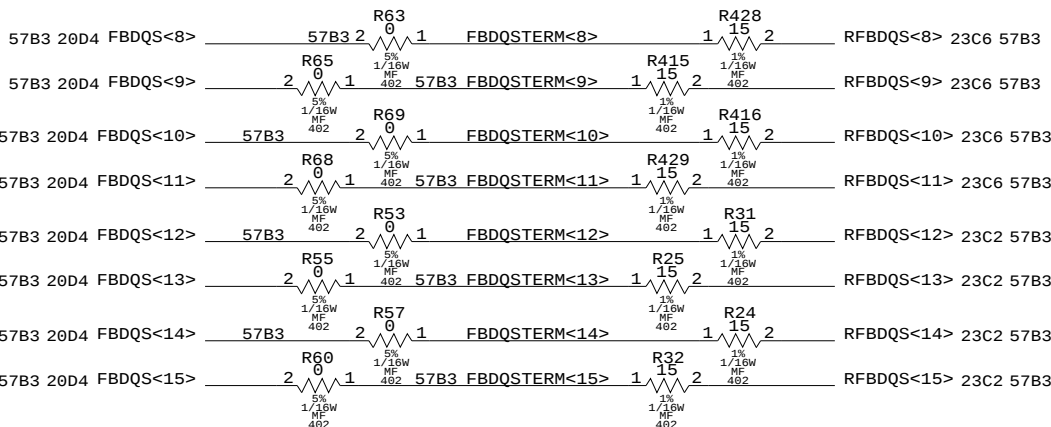
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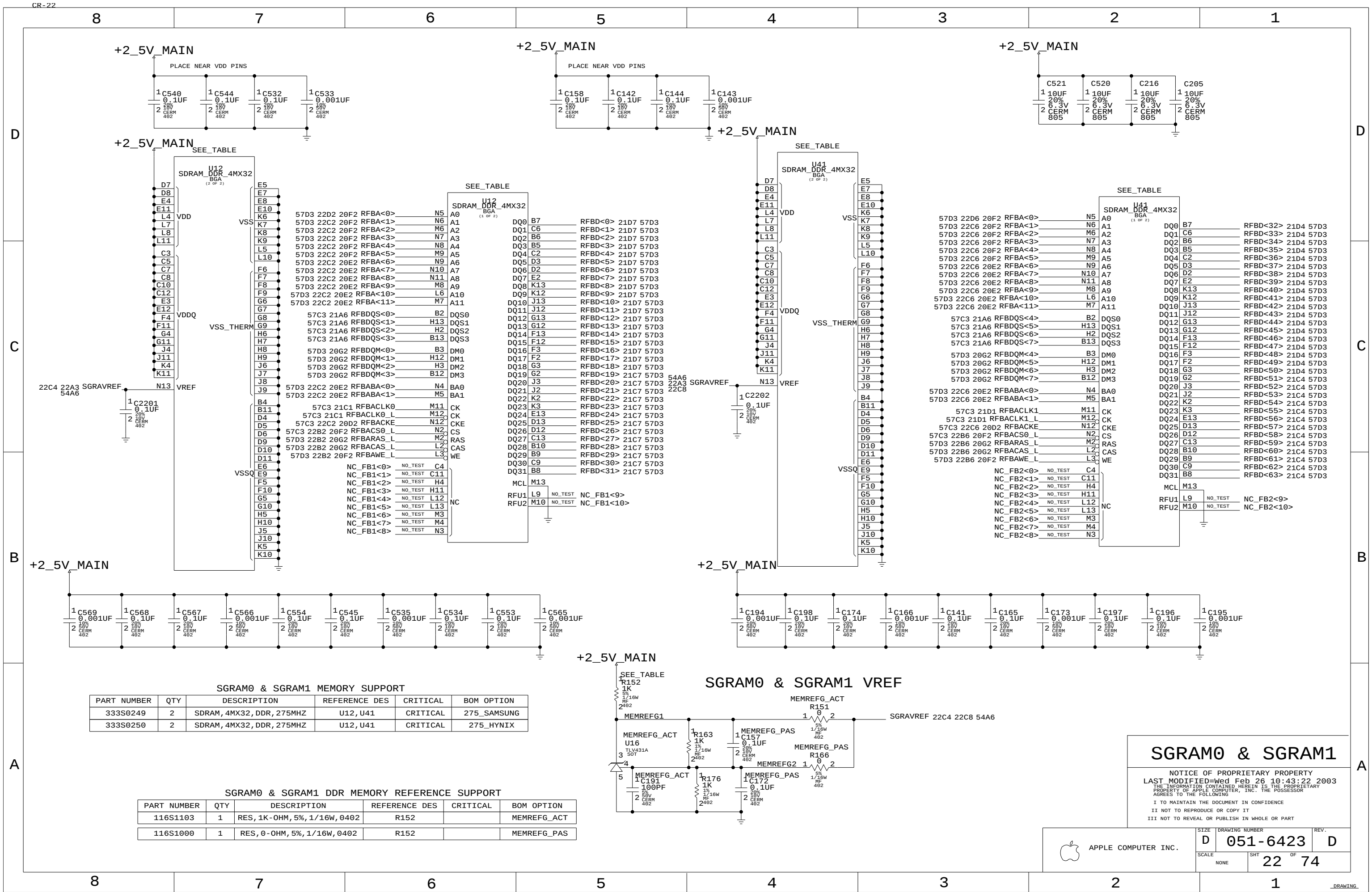
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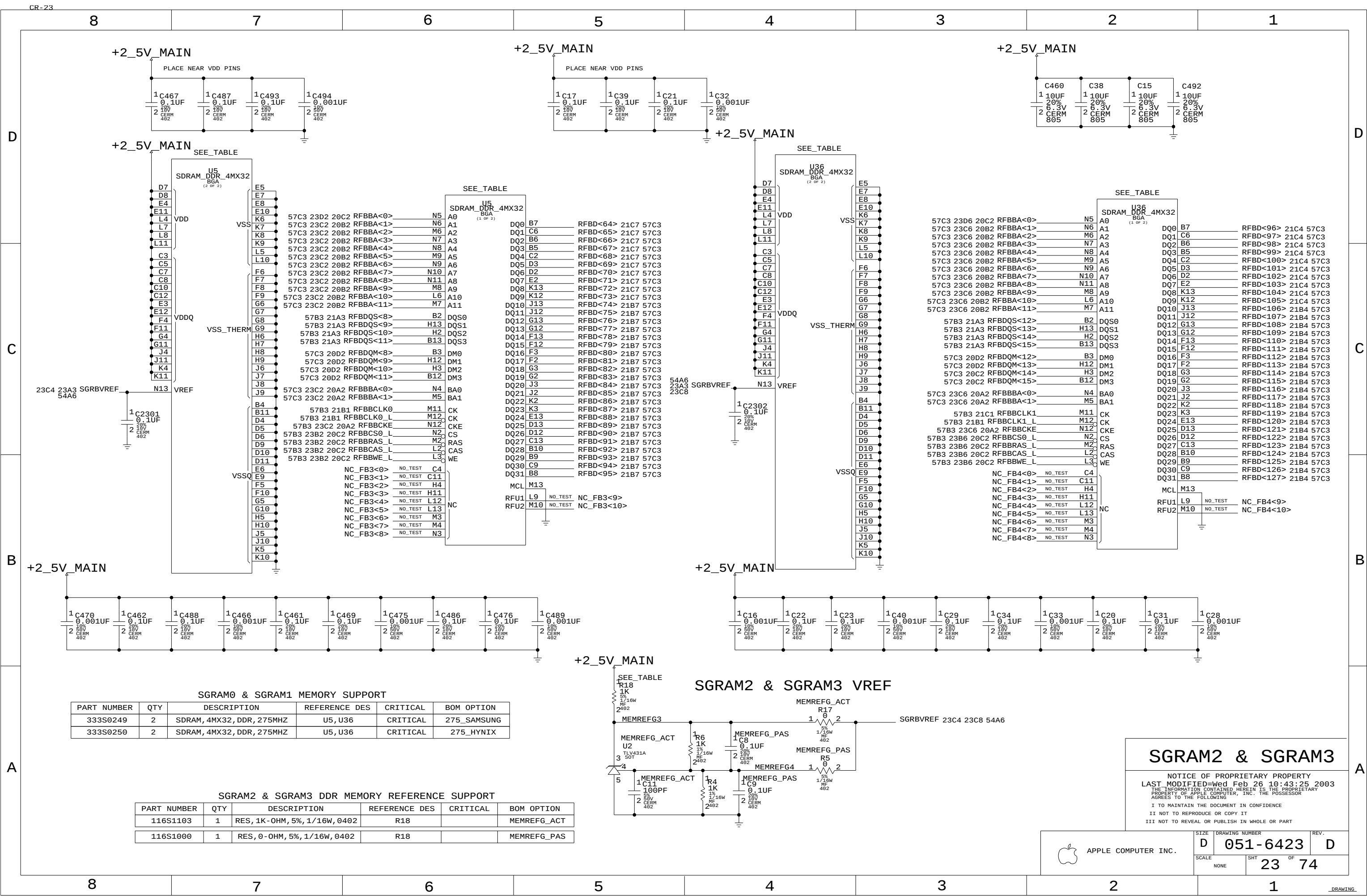
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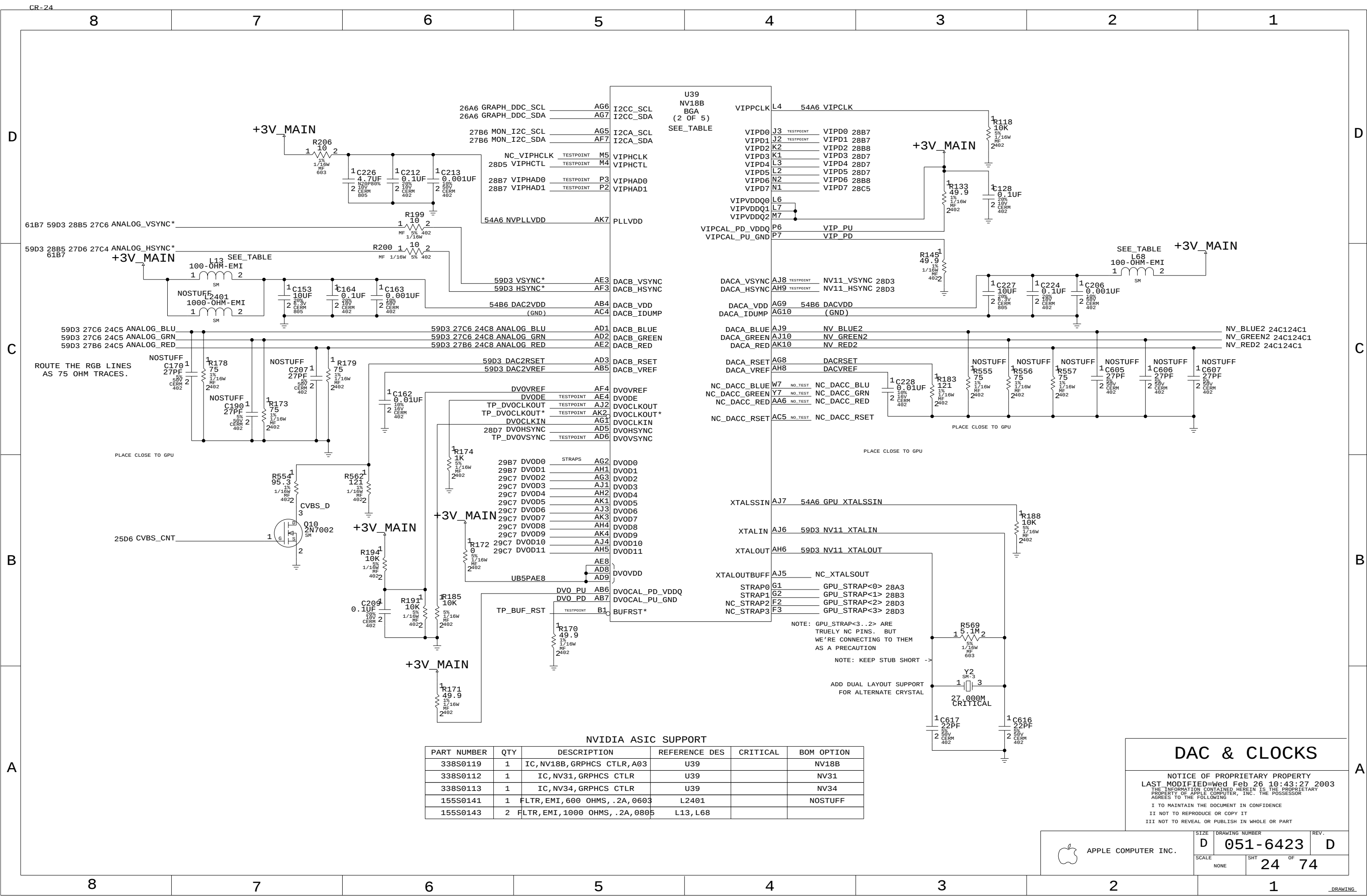
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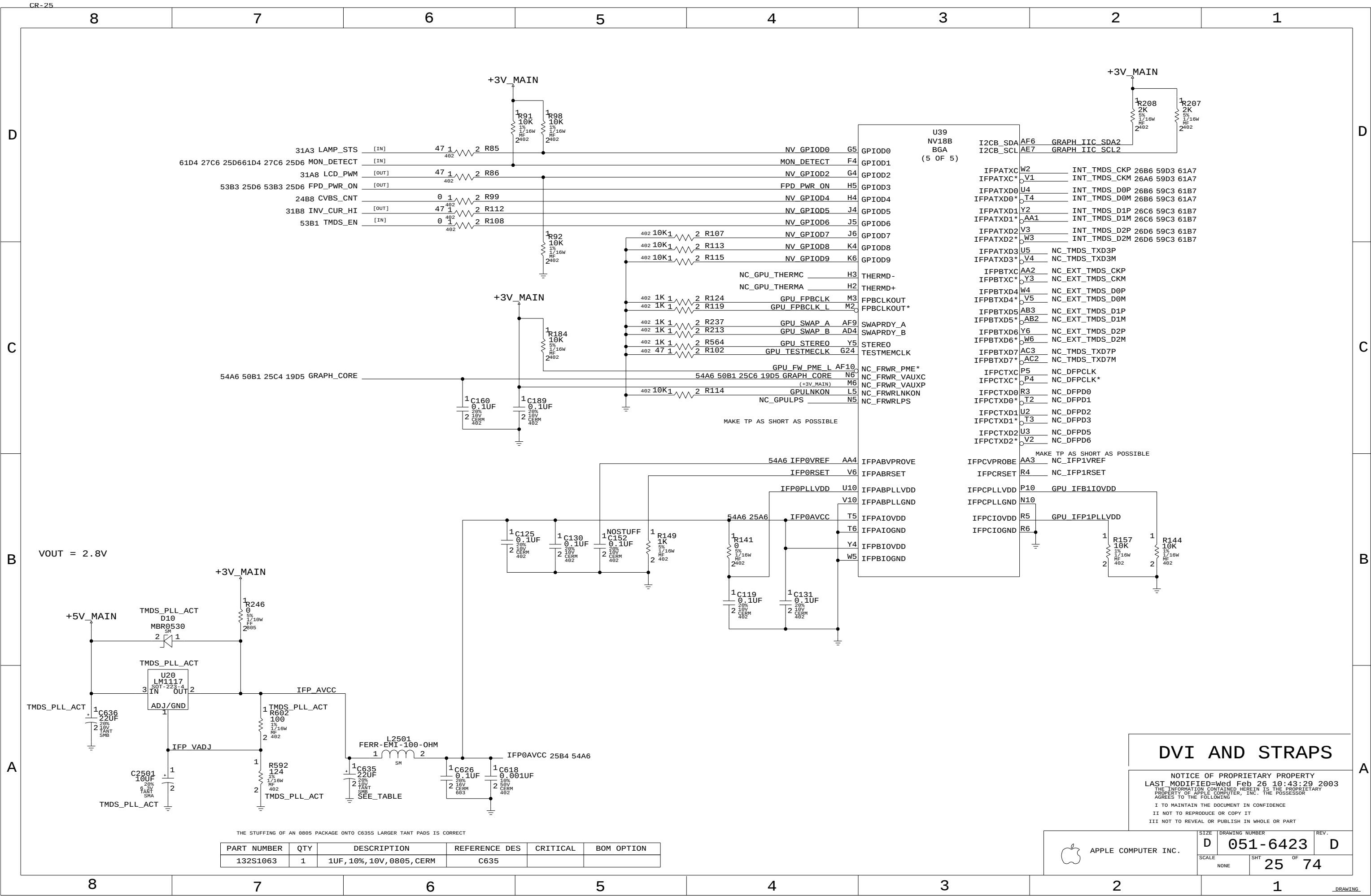


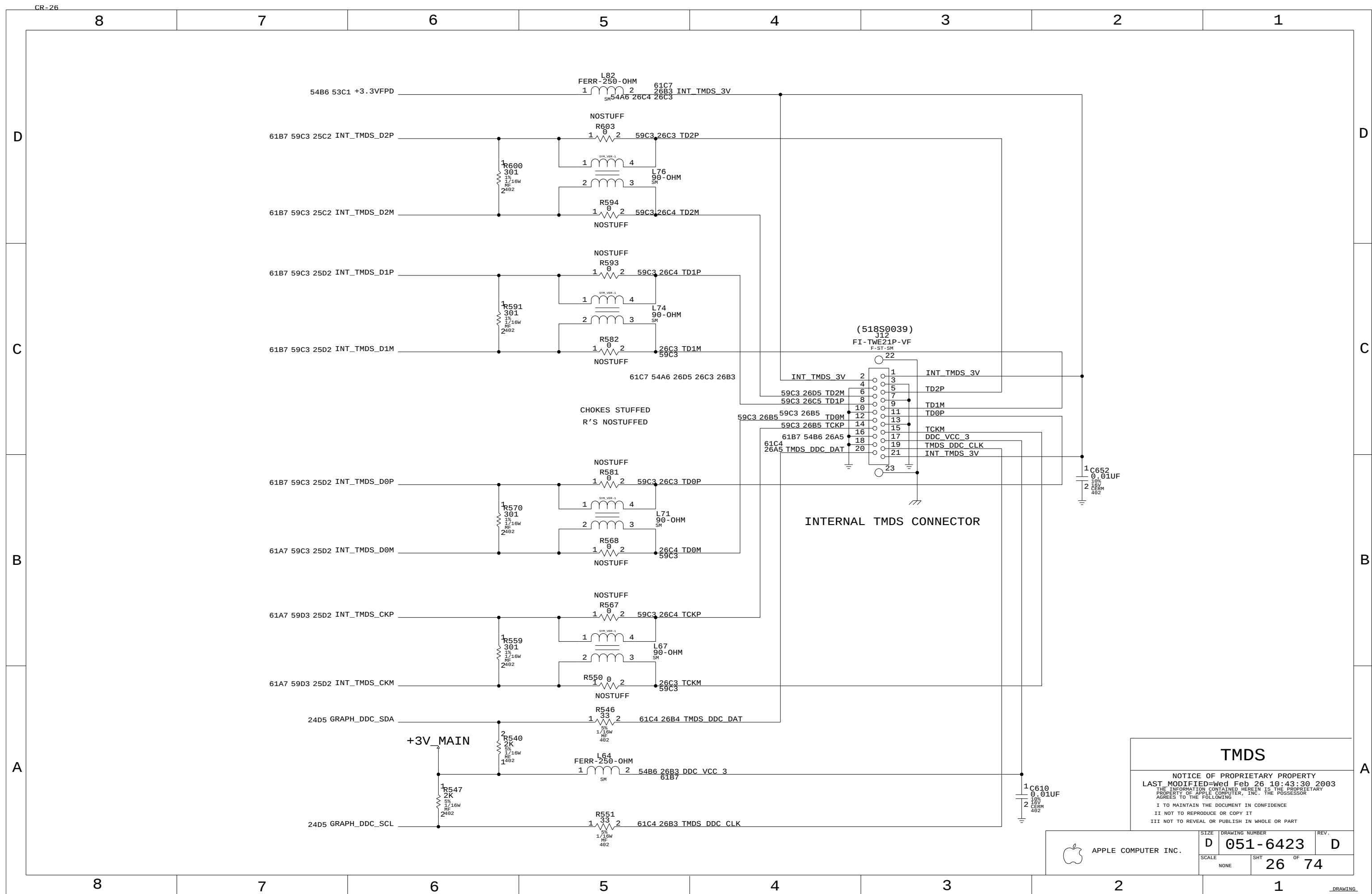
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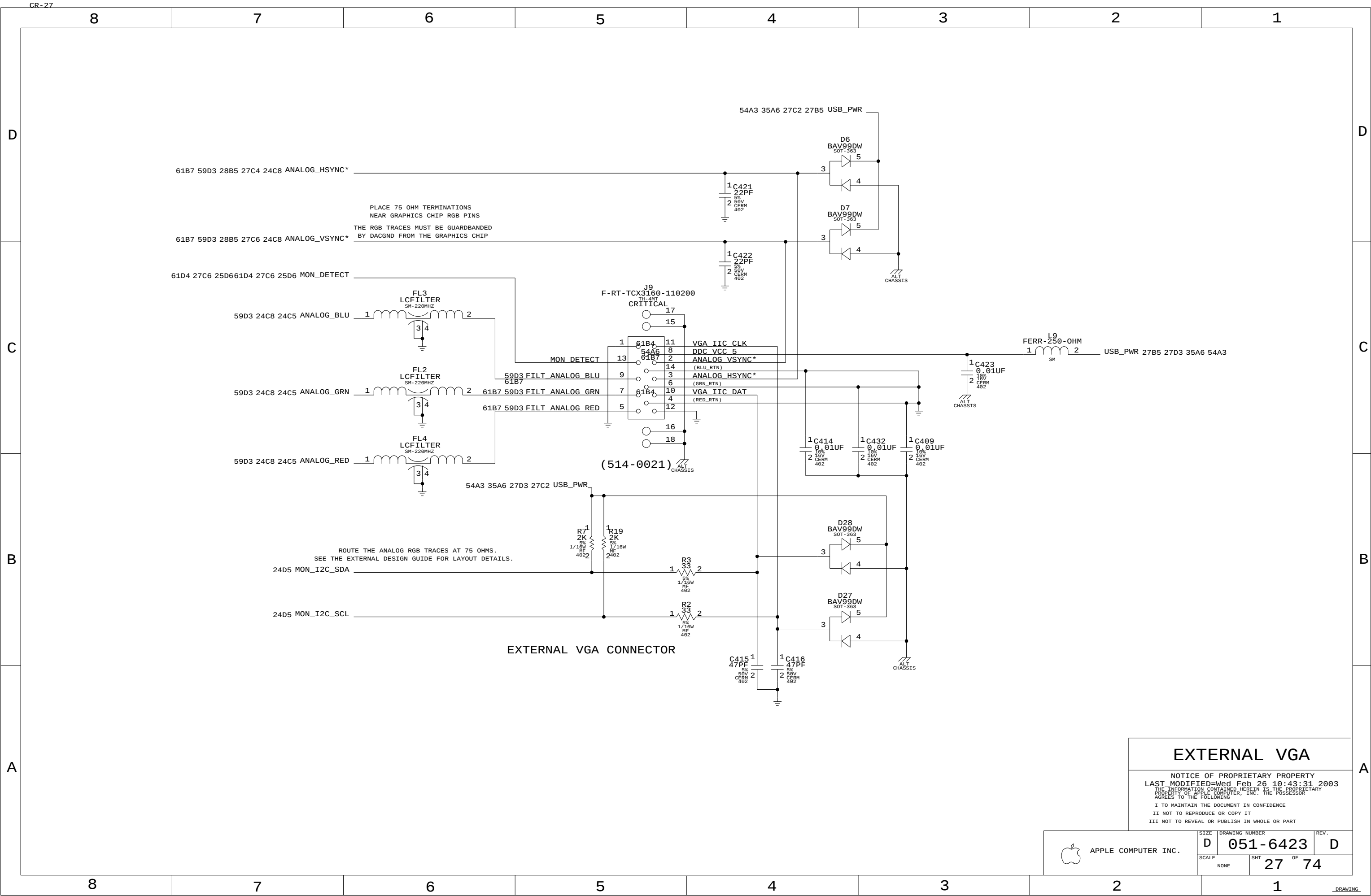












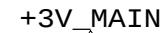
EXTERNAL VGA

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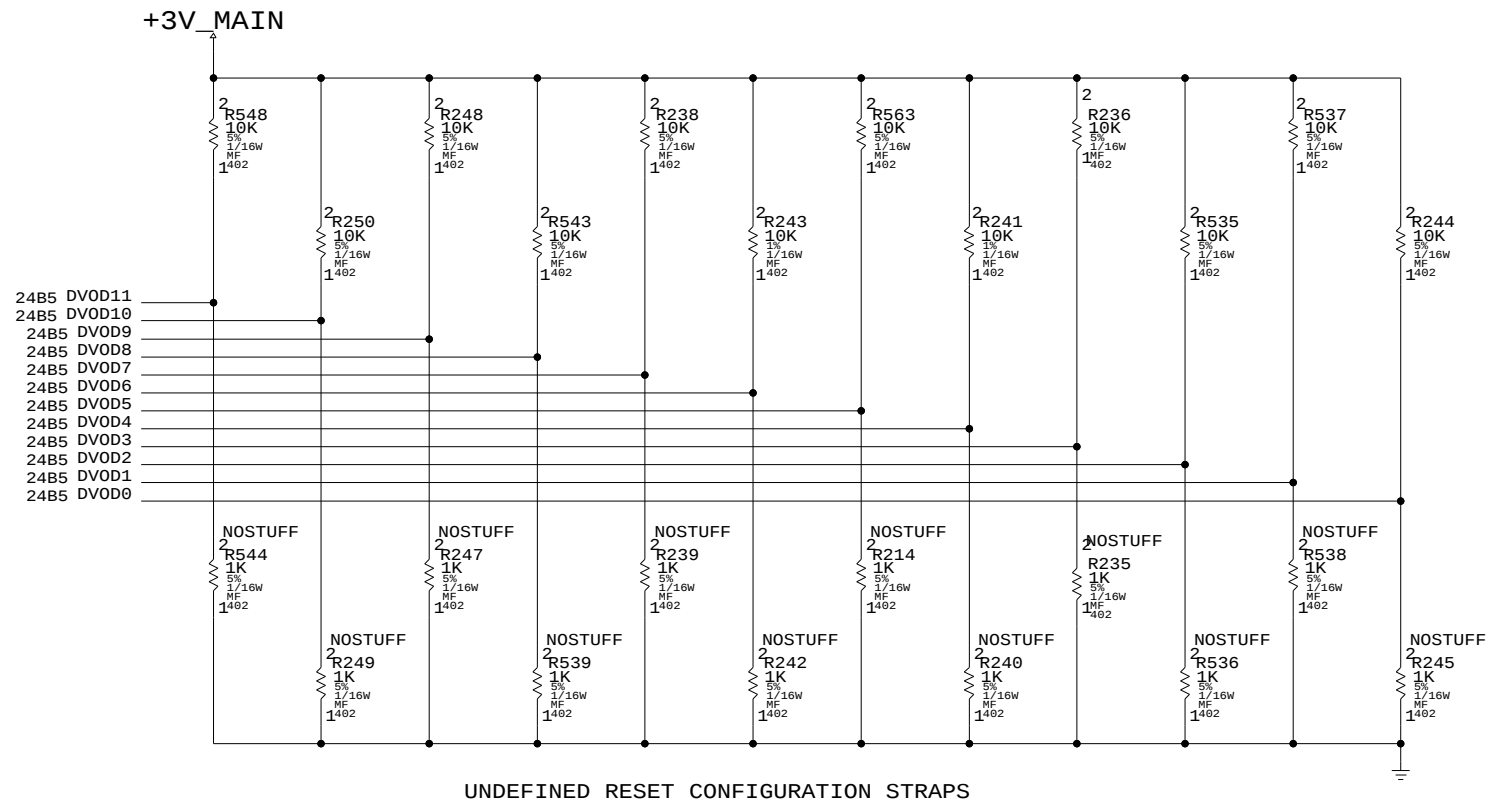
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(10) PCI ADDRESS BUS
[0] = [GPU_STRAP<0>]
0 = REVERSED
* 1 = NORMAL

NVIDIA STRAPS 1			
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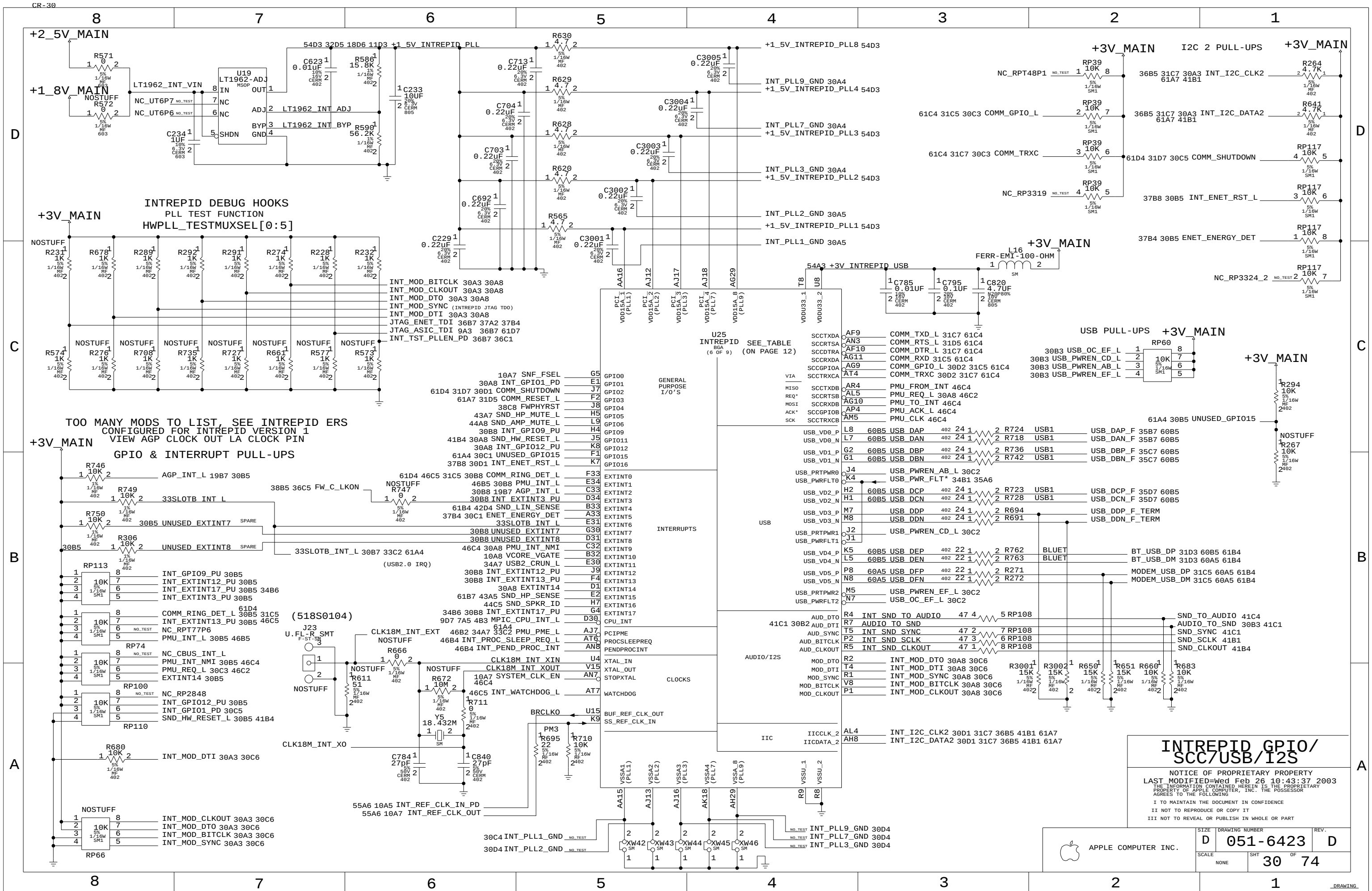


NVIDIA STRAPS 2

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	SCALE	SHT	OF	
	NONE	29	74	





8	7	6	5	4	3	2	1	<u>DRAWING</u>
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54B3 46D5 46C2 46B1 46A5 PMU_POWER

R706 10K 5% 1/16W MF 482

46B5 PMU_CNVS 46D4 PMU_AP (CE-)

61D4 46B5 46A5 9A8 PMU_RST* 46C4 PMU_EPM*

LOW FOR LOADING CODE

R705 10K 5% 1/16W MF 482

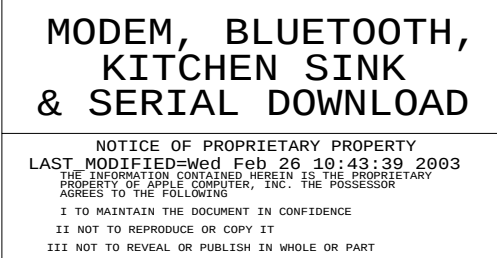
J24 SM

515S1392 OMIT

SERIAL DOWNLOAD CONNECTOR

(BUSY) PMU_P64 46C2
(SCLK) LID_SWITCH 46C4
(RXD) RESET_BUTTON* 46C4 61D4
(TXD) NMI_BUTTON* 46C4 61D4

NC_PMU_DL_10
NC_PMU_DL_12





PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
815-7245	1	WIRELESS CARD GUIDE, J25	J251		
452-0411	2	NUT, HEX, M2 X 1.5H, J25	J252, J253		
452-0412	2	SCREW, M2 X 0.4 X 6.0 L, J25	J254, J255		

WIRELESS PCI

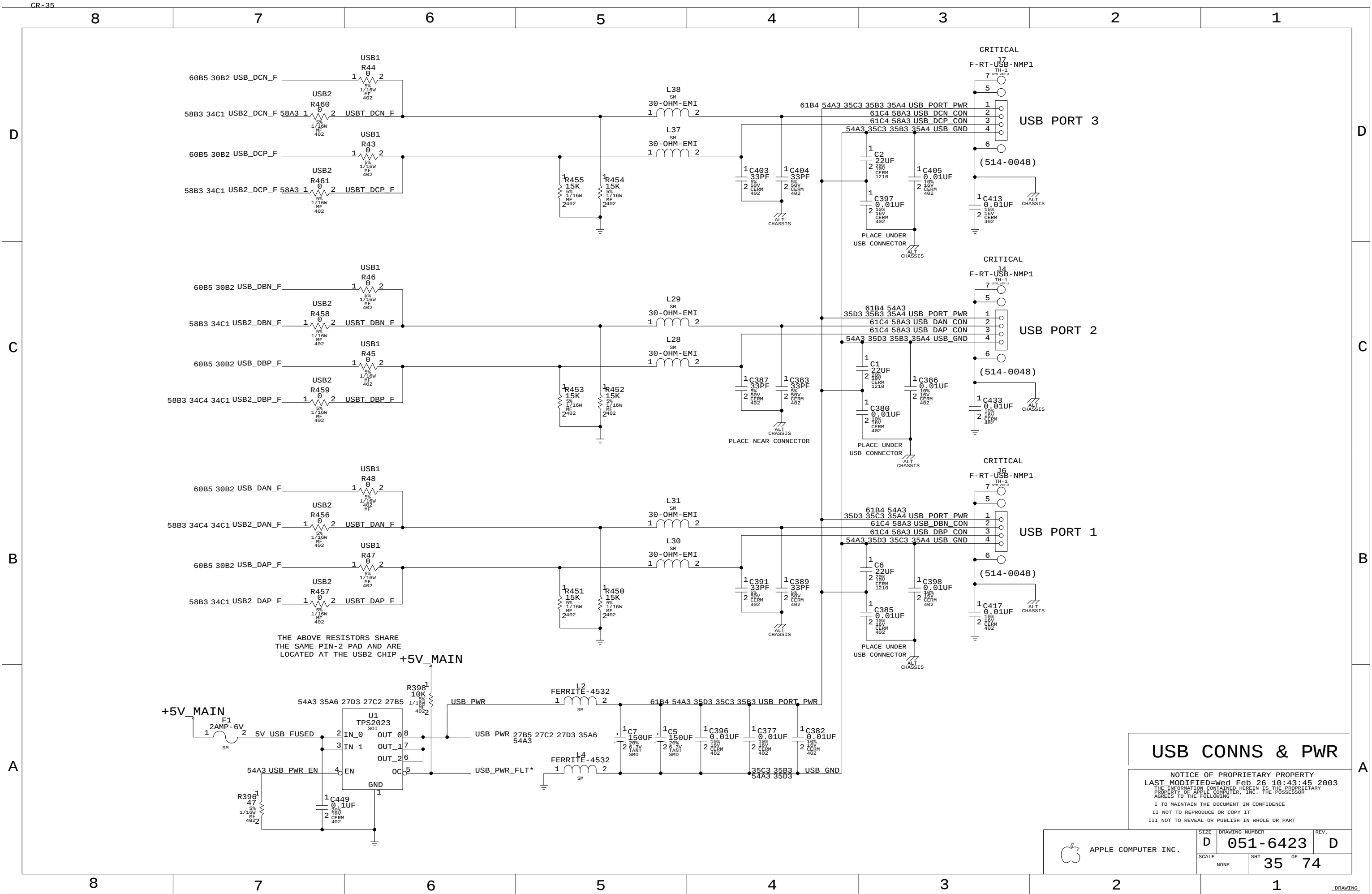
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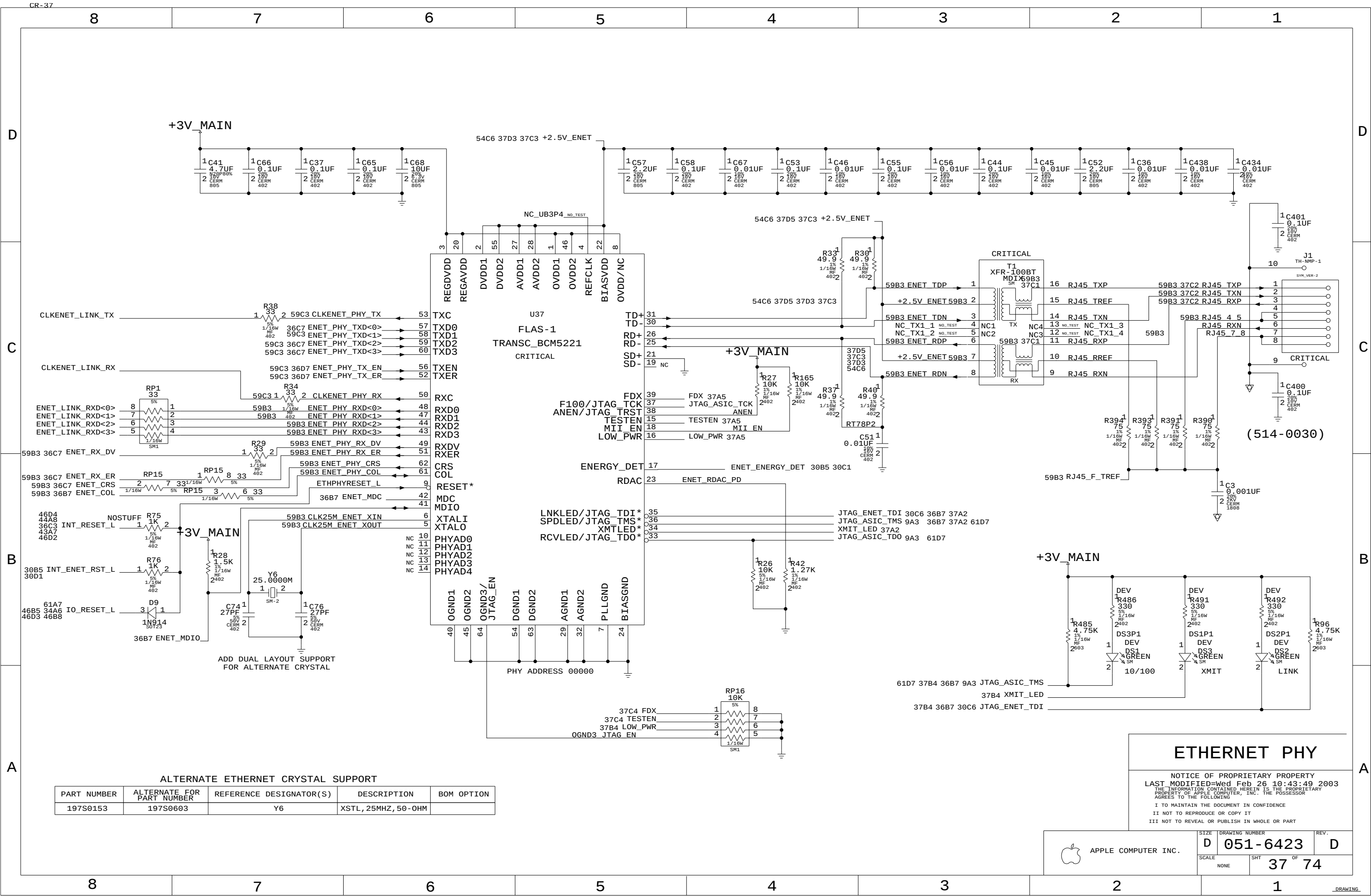
INTREPID ENET & FW

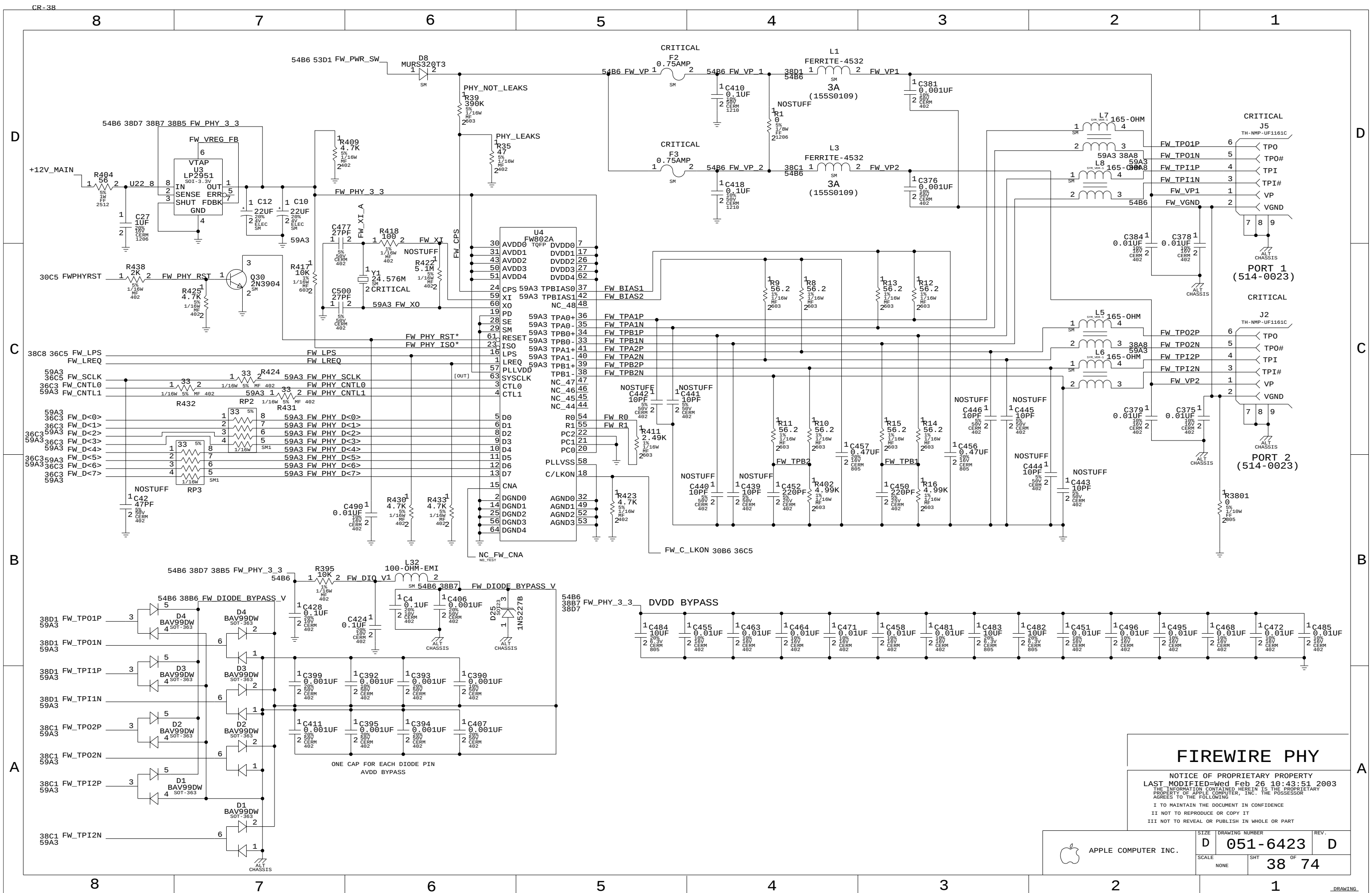
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DRAWING





FIREWIRE PHY

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NONE		38	74

OPTICAL DRIVE INTERFACE

ATA-100 INTERFACE

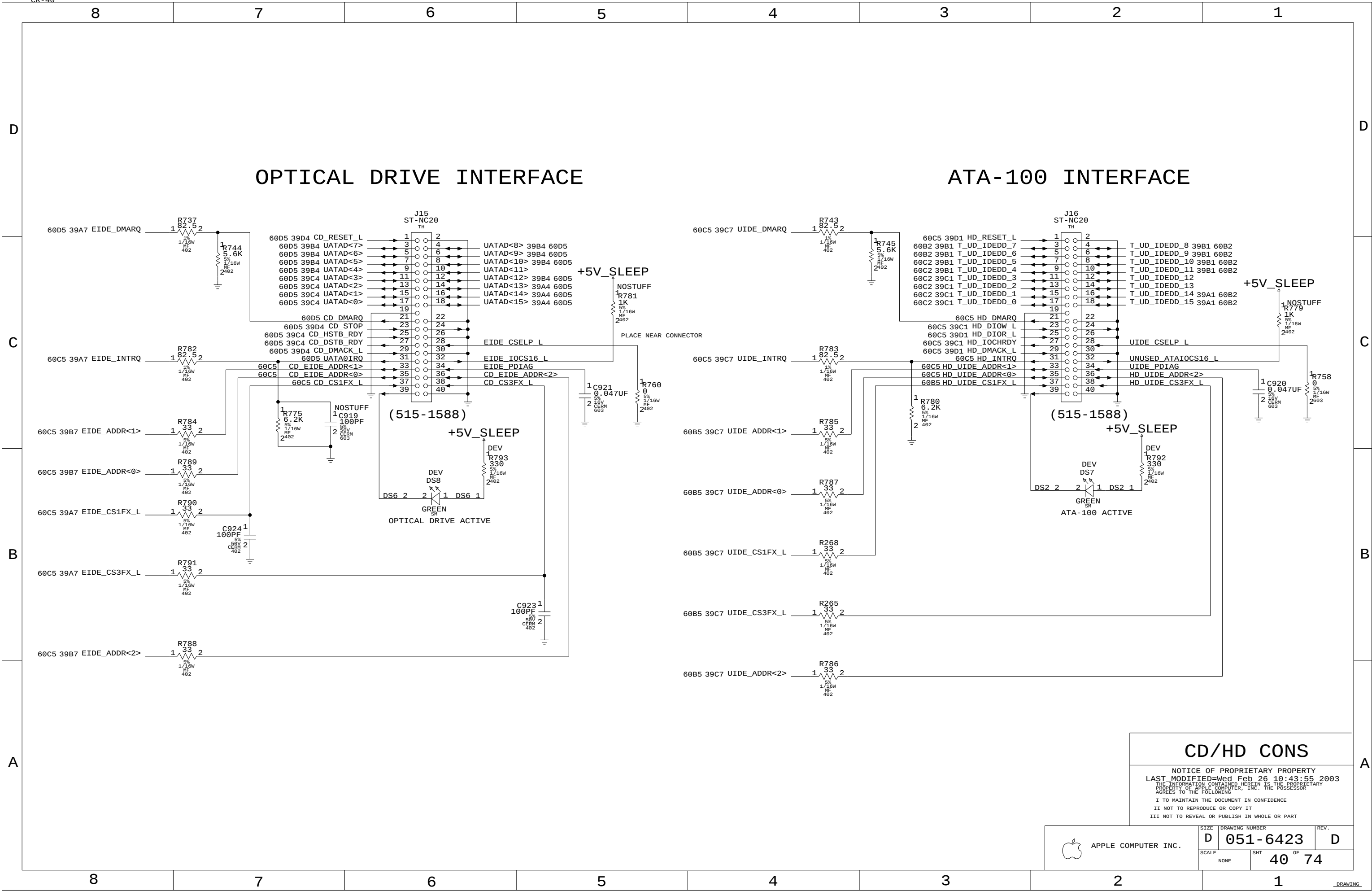
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APPLE COMPUTER INC.

D 051-6423 D

SCALE NONE SHT 40 OF 74



OPTICAL DRIVE INTERFACE

ATA-100 INTERFACE

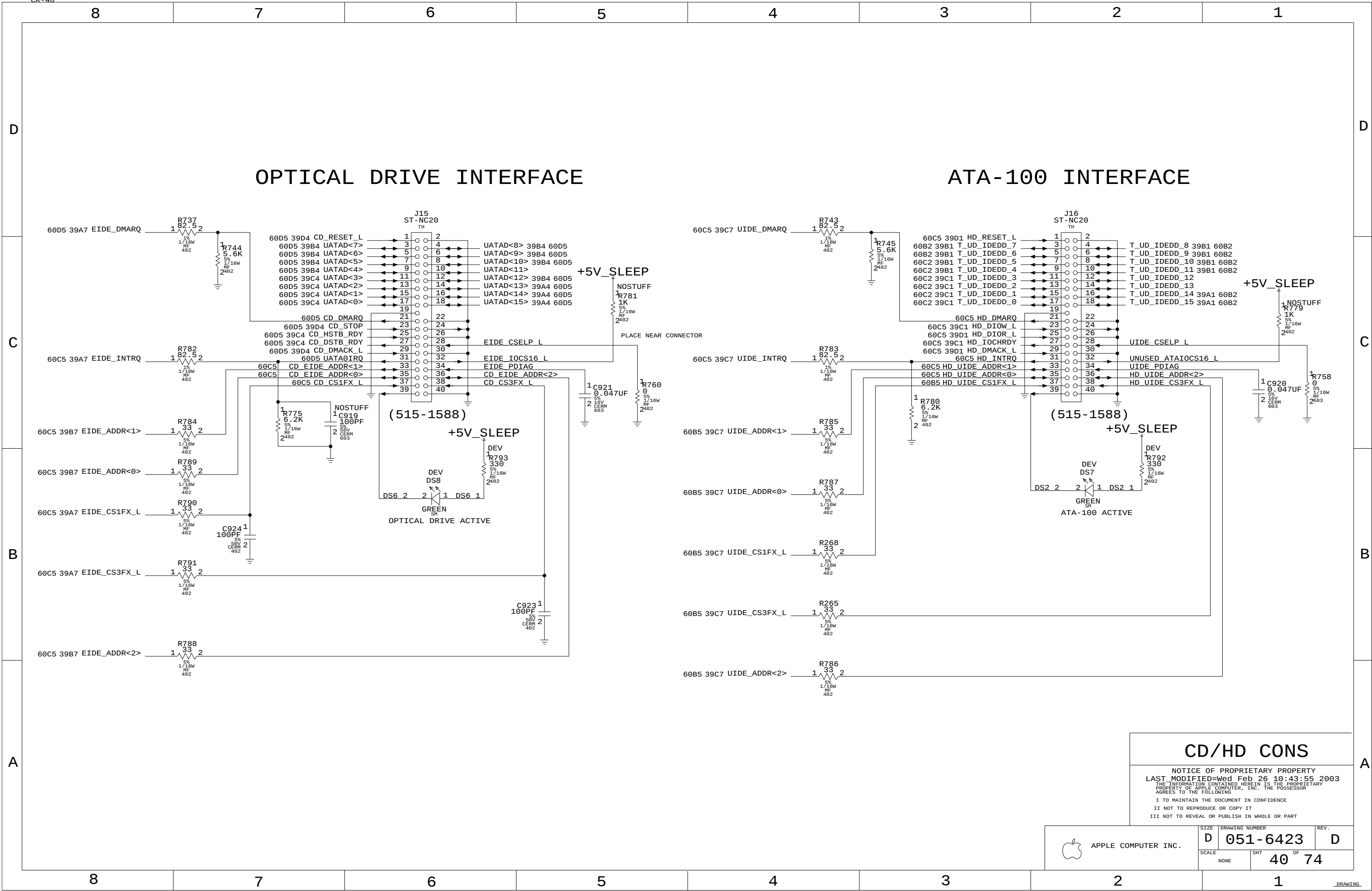
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OPTICAL DRIVE INTERFACE

ATA-100 INTERFACE

CD/HD CONS

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D 051-6423 D

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OPTICAL DRIVE INTERFACE

ATA-100 INTERFACE

CD/HD CONS

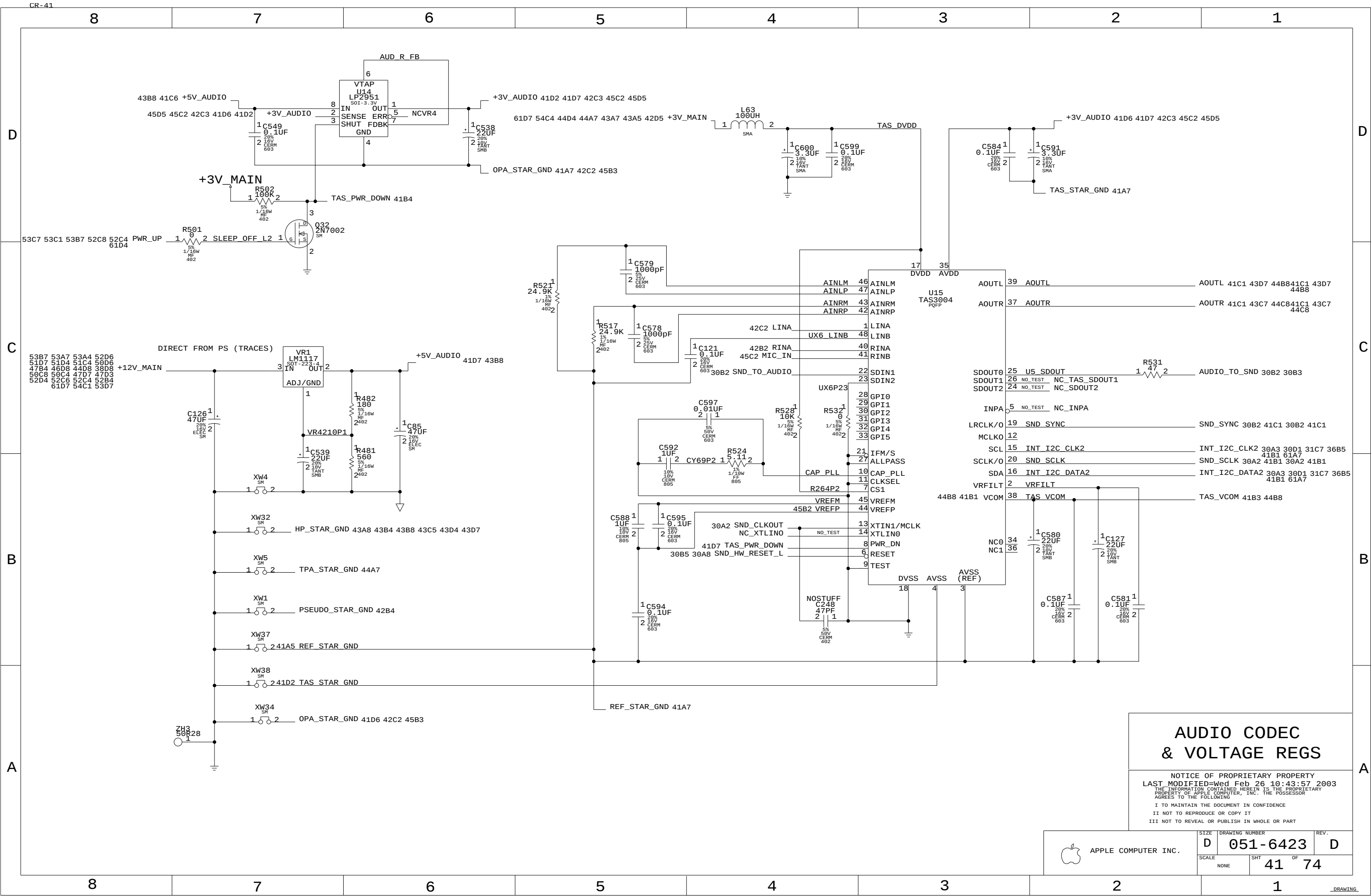
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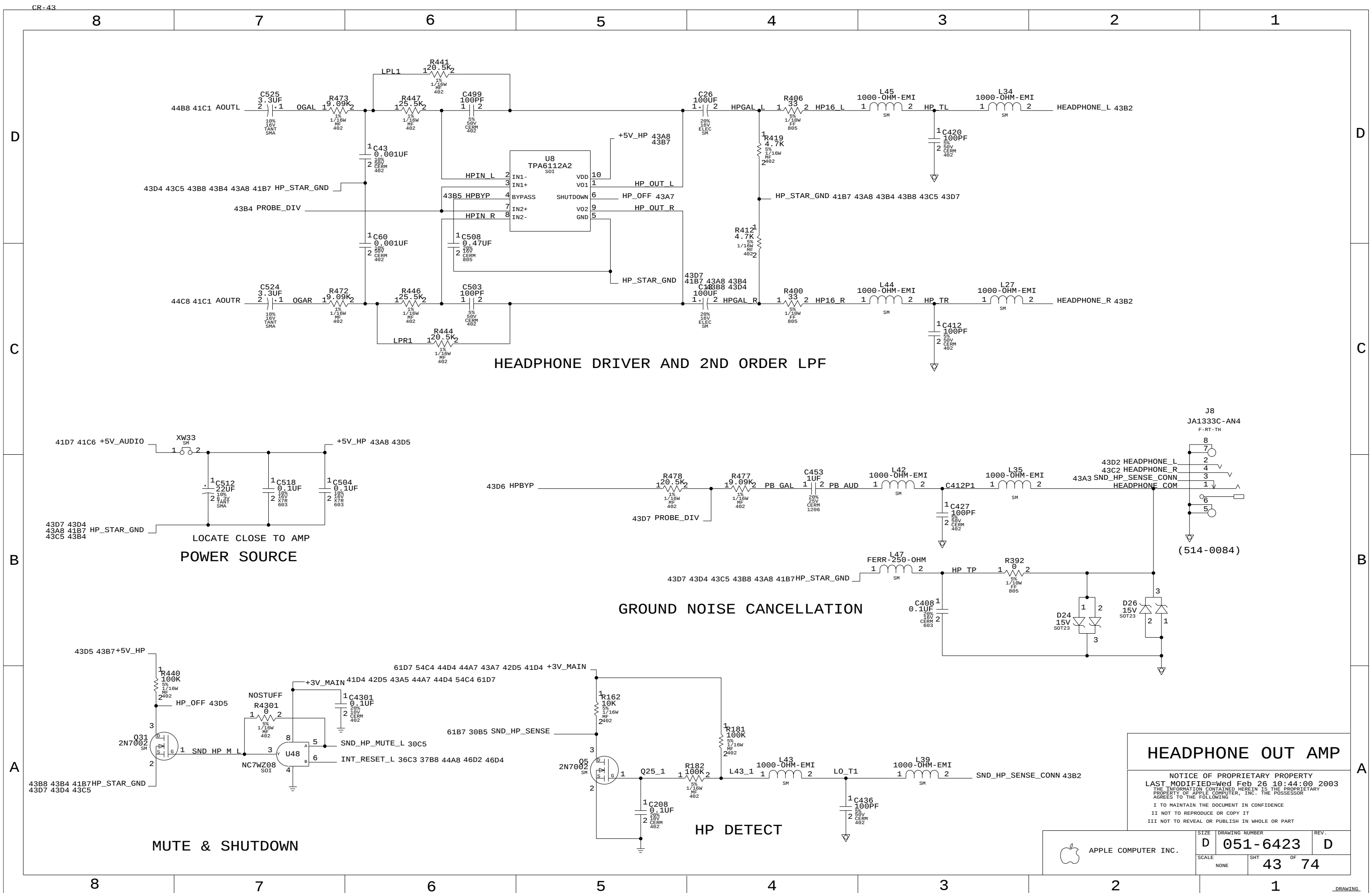


AUDIO CODEC
& VOLTAGE REGS

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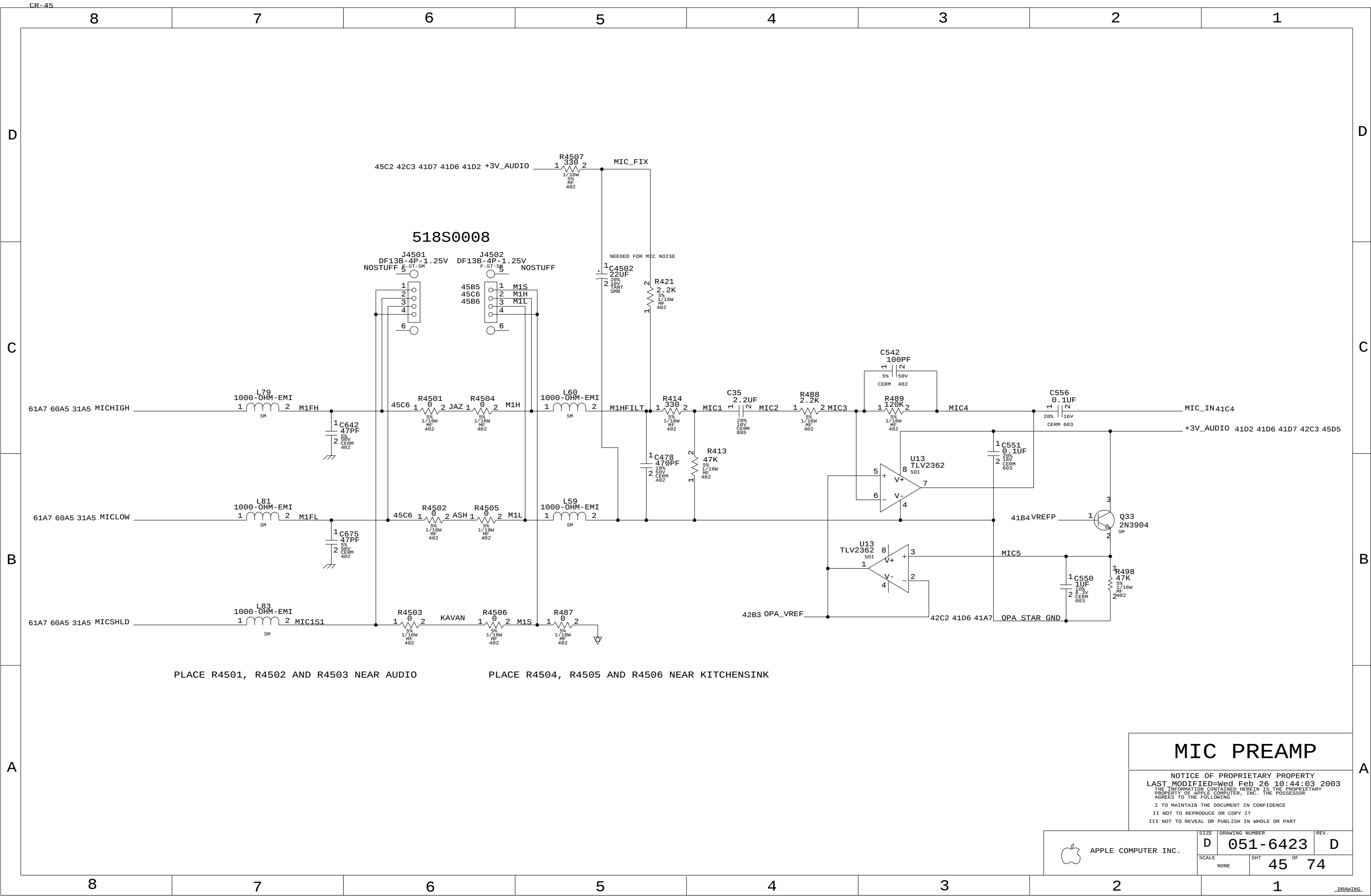
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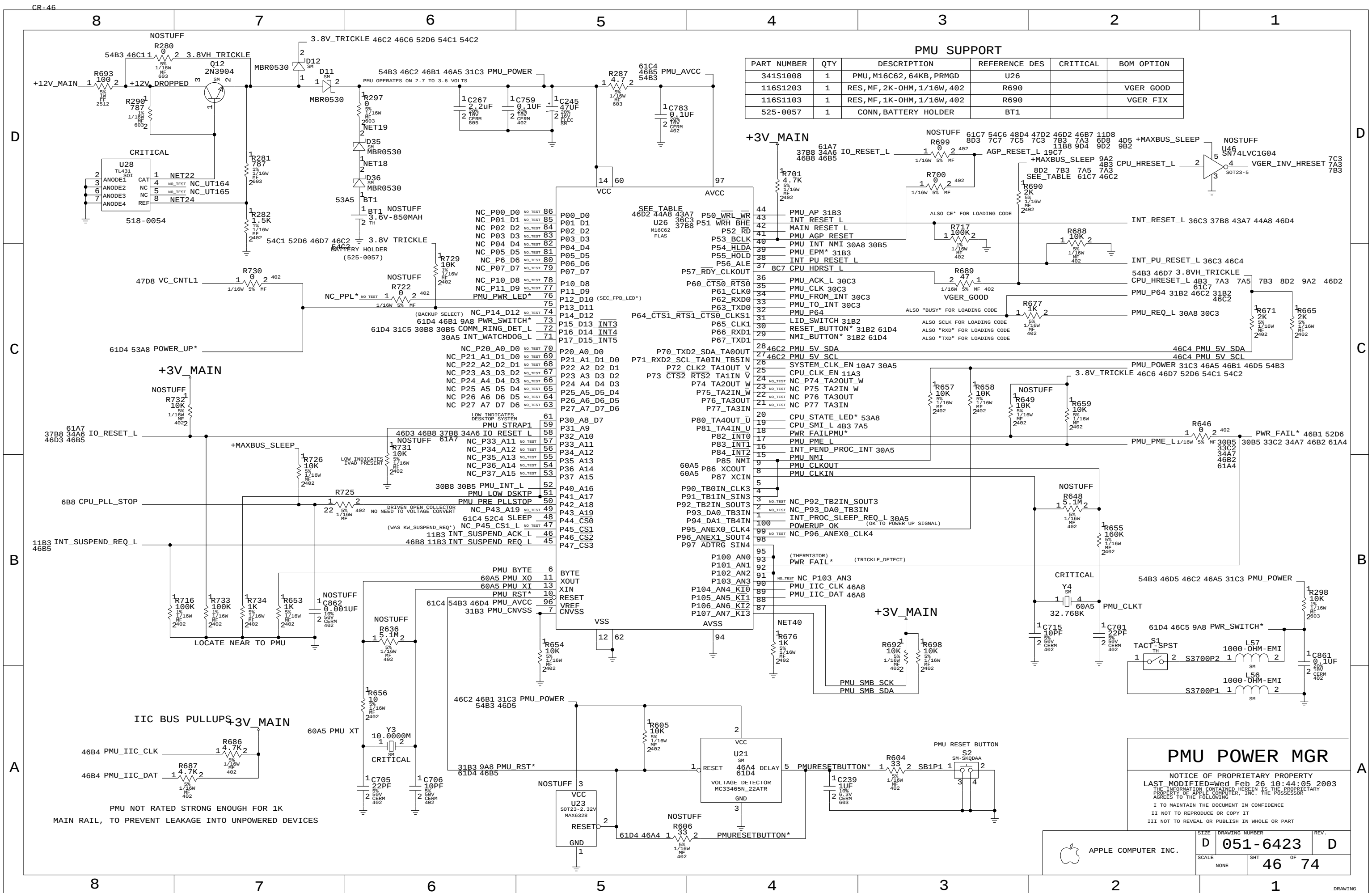
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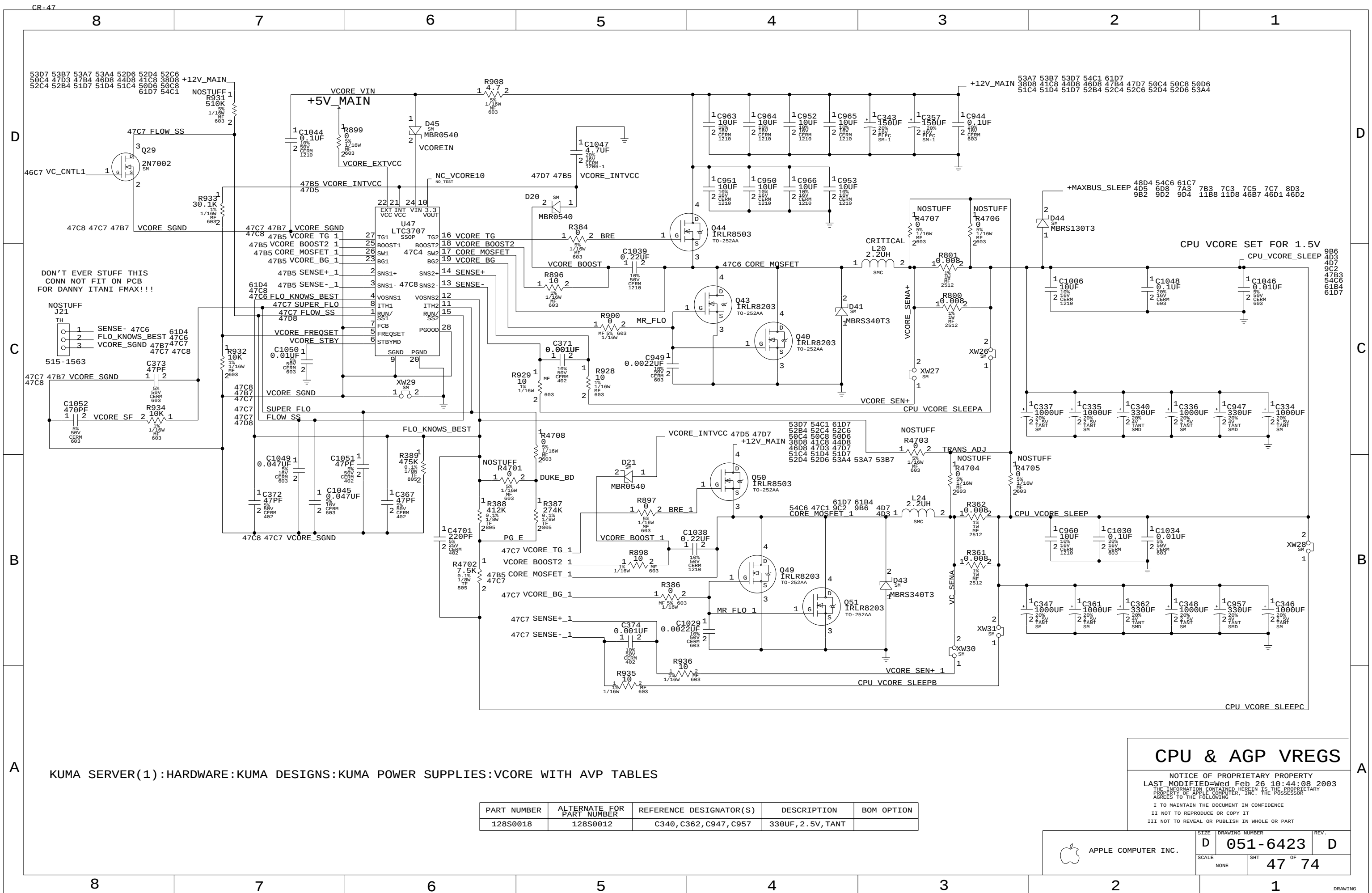
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SCALE		SHT	OF
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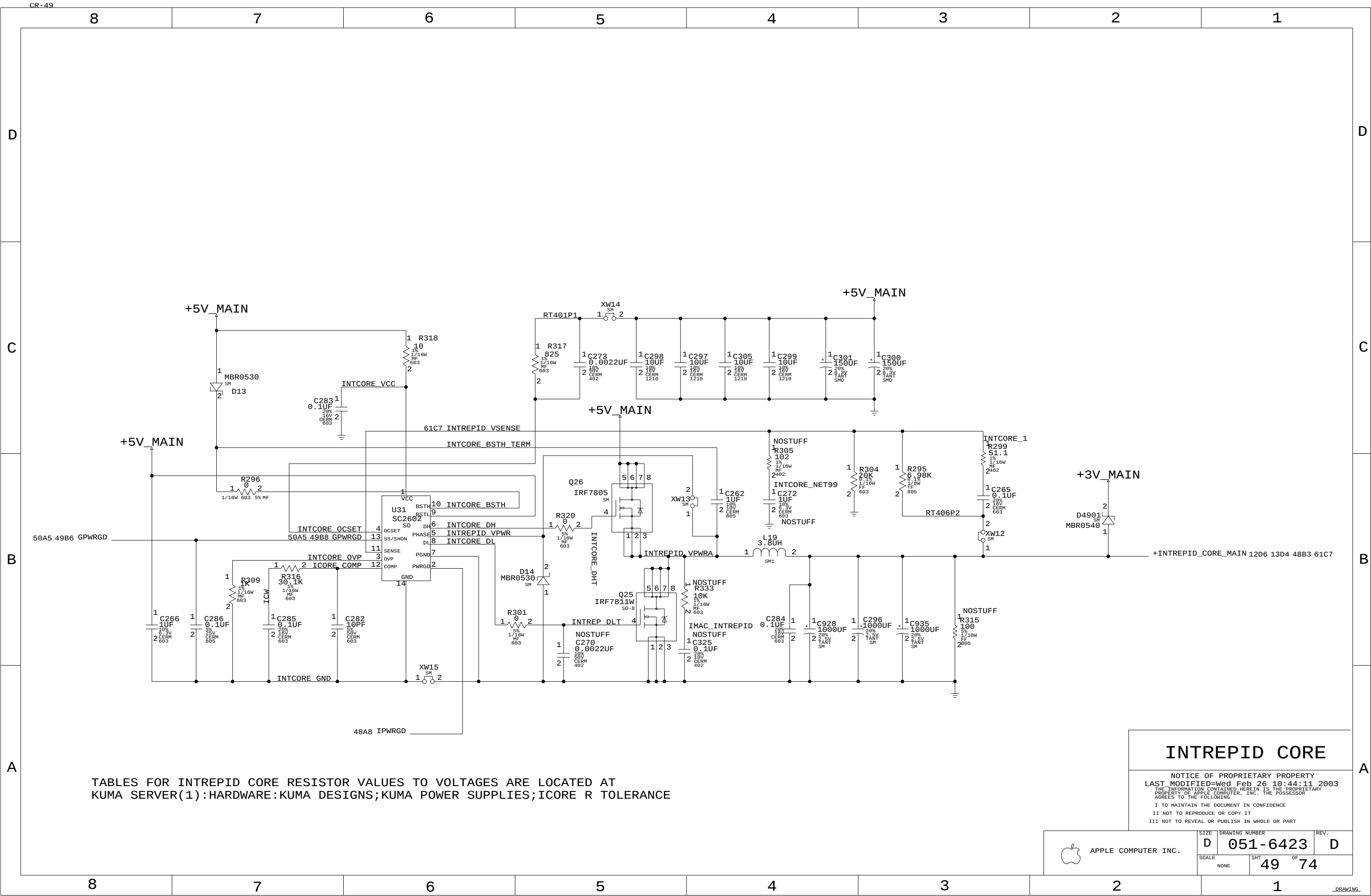
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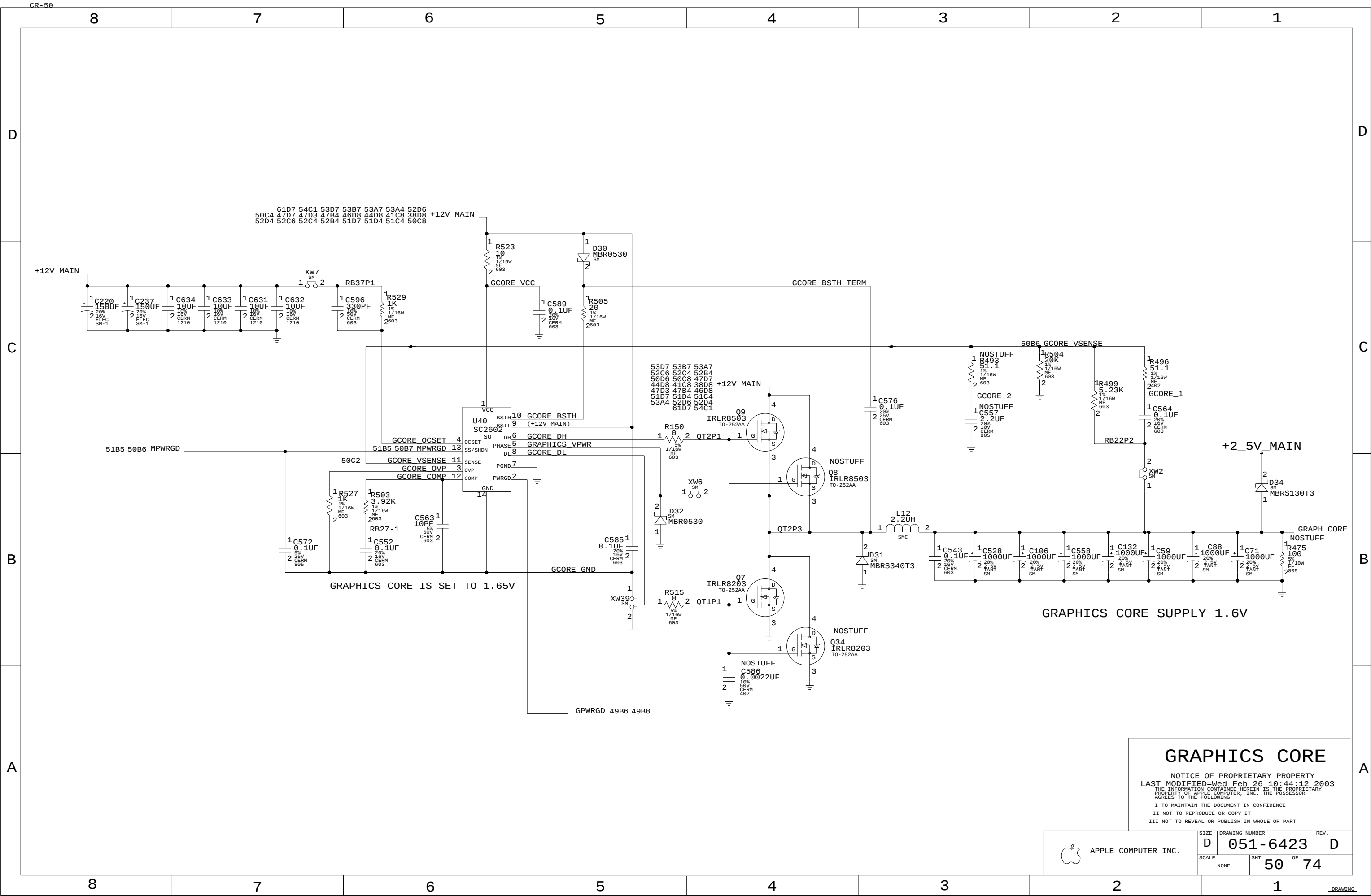
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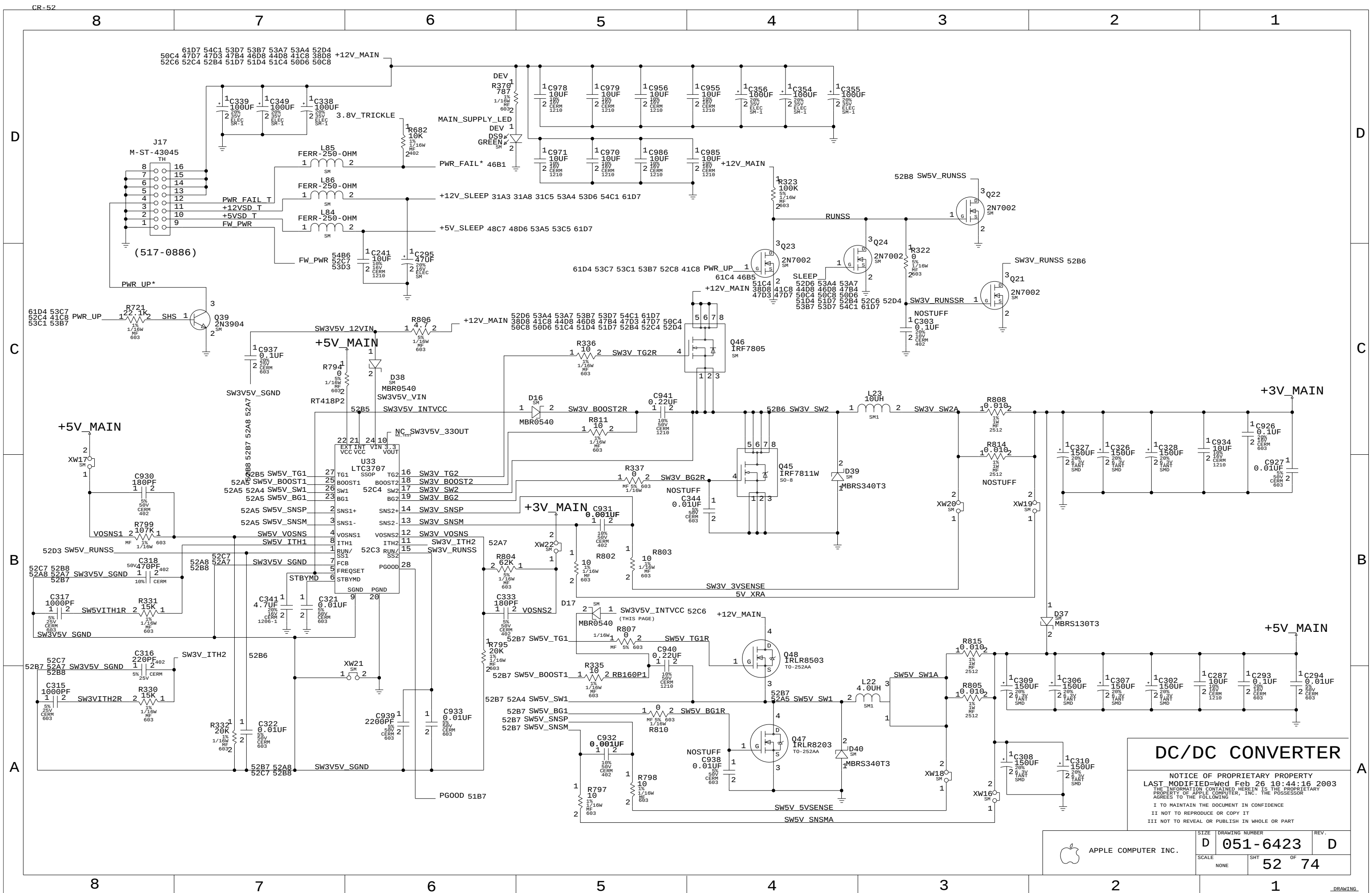
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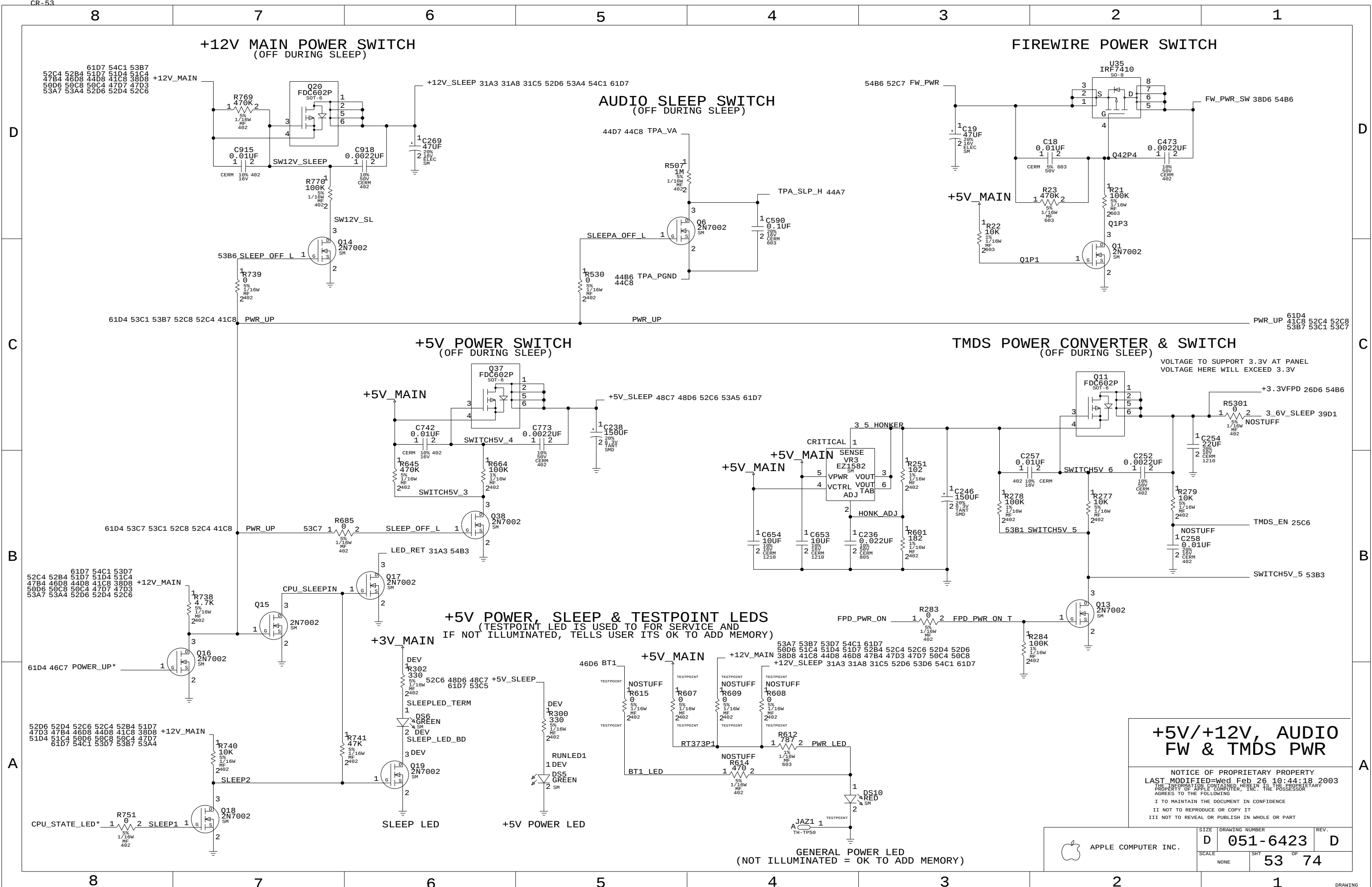




DC/DC CONVERTER

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+5V/+12V, AUDIO FW & TMDS PWR

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SCALE	DRAWING NUMBER		REV.
	D	051-6423	D
NONE		SHT	OF
		53	74

CLOCK POWER CONSTRAINT TABLE

	SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH	
OUT1	+2_5V_DDR_AVDD	10	2.5	20	10D4
OUT1	+2_5V_DDR_VDD	10	2.5	20	10D5
OUT1	+3V_INTSS_AVDD	10	3.3	20	10B7
OUT1	+3V_INTSS_VDD	10	3.3	20	10B6
OUT1	PCK2059_VDDI2C	10	2.5	20	10D4

CPU POWER CONSTRAINT TABLE

	SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
OUT	+MAXBUS_SLEEP	10	1.8	20
OUT	CPU_AVDD	10	1.85	20
OUT	CPU_VCORE_SLEEP	10	1.85	20

ETHERNET POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+2.5V_ENET	10	2.5	20

37C3 37D3 37D5

FIREWIRE POWER CONSTRAINT TABLE

	SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH	
OUT	FW_DIO_V	10	3.3	20	38B6
OUT	FW_DIODE_BYPASS_V	10	3.3	20	38B6 38B7
OUT	FW_PwR	10	24	20	52C7 53D3
OUT	FW_PwR_SW	10	24	20	38D6 53D1
OUT	FW_PHY_3_3	10	3.3	20	38B5 38B7 38D7
OUT	FW_VGND	10	0	20	38D1
OUT	FW_VP	10	12	20	38D5
OUT	FW_VP1	10	12	20	38D1 38D3
OUT	FW_VP2	10	12	20	38C1 38D3
OUT	FW_VP_1	10	12	20	38D4
OUT	FW_VP_2	10	12	20	38D4

GRAPHICS POWER CONSTRAINT TABLE

	SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH	
	+3.3VFPD	10	3.6	20	26D6 53C1
OUT	DAC2VDD	10	3.3	20	24C5
OUT	DACVDD	10	3.3	20	24C4
OUT	DDC_VCC_3	10	3.3	20	26A5 26B3 61B7
OUT	DDC_VCC_5	10	5	20	27C4 61B7
OUT	DDR_VREF	10	1.25	20	16B2 16D1 16D8 17D8
OUT	IFP0AVCC	10	3.8	20	25A6 25B4
OUT	IFP0VREF	10	3.8	20	25B4
OUT	INT_TMD5_3V	10	3.6	20	26B3 26C3 26C4 26D5 61C7
OUT	GPU_AGP_VREF	10	0.75	20	19A3 19A8
OUT	GPU_FB_VREF	10	1.25	20	20C8
OUT	GRAPH_CORE	10	1.6	20	19D5 25C4 25C6 50B1
OUT	NVPLLVD	10	3.3	20	24D5
OUT	SGRAVREF	10	1.25	20	22A3 22C4 22C8
OUT	SGRBVREF	10	1.25	20	23A3 23C4 23C8

GPU 50PULLUP	1.5	OFFP	19A5
GPU 50PULLDWN	0	OFFP	19A5
GPU TMODE	0	OFFP	19A5

GPU_XTALSSIN	0	OFFP	24B4
VIPCLK	0	OFFP	24D4

CSLOT_IOWAIT_L	3.3	OFFP	3B7
EIDE_CSELP_L	0	OFFP	4C6
EIDE_IOCS16_L	5	OFFP	4C6
UIDE_CSELP_L	0	OFFP	4C2
UNUSED_ATAIOCSP16_L	5	OFFP	4C2

INTREPID POWER CONSTRAINT TABLE

	SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
OUT	+1_5V_INTREPID_PLL	10	1.5	20
OUT	+1_5V_INTREPID_PLL1	10	1.5	20
OUT	+1_5V_INTREPID_PLL2	10	1.5	20
OUT	+1_5V_INTREPID_PLL3	10	1.5	20
OUT	+1_5V_INTREPID_PLL4	10	1.5	20
OUT	+1_5V_INTREPID_PLL5	10	1.5	20
OUT	+1_5V_INTREPID_PLL6	10	1.5	20
OUT	+1_5V_INTREPID_PLL7	10	1.5	20
OUT	+1_5V_INTREPID_PLL8	10	1.5	20
OUT	+1_5V_AGP	10	1.5	20
OUT	INT_AGP_VREF	10	0.75	20

MAIN POWER CONSTRAINT TABLE

	SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
⚡	+1.8V_MAIN	10	1.8	20
⚡	+2.5V_MAIN	10	2.5	20
⚡	+3V_MAIN	10	3.3	20
⚡	3.8V_TRICKLE	10	3.8	20
⚡	+5V_MAIN	10	5	20
⚡	+12V_MAIN	10	12	20
⚡	+12V_SLEEP	10	12	20
⚡	GND	10	0	20
⚡	AGND	10	0	20
⚡	ANALOGGND	10	0	20
⚡	ALTCFGND	10	0	20
⚡	CHGND	10	0	20

PMU POWER CONSTRAINT TABLE

	SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
OUT	3.8VH_TRICKLE	10	3.8	20
OUT	PMU_AVCC	10	3.5	20
OUT	PMU_POWER	10	3.5	20

SYSTEM POWER CONSTRAINT TABLE

	SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
OUT	+12VSD_FILT	10	12	20
OUT	FAN_12V_FILT	10	12	20
OUT	KSSVSD	10	5	20
OUT	LED_5V	10	5	20
OUT	LED_5V_FILT	10	5	20
OUT	LED_RET	10	0	20
OUT	LED_RET_FILT	10	0	20

USB POWER CONSTRAINT TABLE

	SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
INT1	+3V_INTREPID_USB	10	3.3	20
INT2	NEC_AVDD	10	3.3	20
INT3	USB_GND	10	0	20
INT4	USB_PORT_PWR	10	5	20
INT5	USB_PWR	10	5	20
INT6	USB_PWR_EN	10	0	20

POWER CONSTRAINTS

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<table><tr><th>GROUP</th><th>SIG_NAME</th><th>MATCHED_DELAY</th><th>MAX VIAS</th><th>DELAY_RULE</th><th>STUB_LENGTH</th><th>NET_SPACING_TYPE</th><th>MAX EXPOSED LENGTH</th><th>PULSE_PARAM</th><th></th></tr><tr><td rowspan="32">D</td><td>FBD<0..63></td><td>GPU_FBDATA_A:::225</td><td>4</td><td>:::800</td><td></td><td></td><td></td><td>300 MHZ</td><td>20E8 20F8 20G8 21C5 21C8 21D5 21D8</td></tr><tr><td>FEBD<0..63></td><td>RAM_FBDATA_A:::300</td><td>4</td><td>:::1000</td><td></td><td></td><td></td><td>300 MHZ</td><td>21C4 21C7 21D4 21D7 22B1 22B5 22C1 22C5</td></tr><tr><td>FBDQM<0..7></td><td>GPU_FBDQM_A:::200</td><td>4</td><td>:::800</td><td></td><td></td><td></td><td>300 MHZ</td><td>20D8 20G3</td></tr><tr><td>FEBDQM<0..7></td><td>RAM_FBDQM_A:::200</td><td>4</td><td>:::1000</td><td></td><td></td><td></td><td>300 MHZ</td><td>20G2 22C2 22C6</td></tr><tr><td>FBA<0..12></td><td>GPU_FBADDR_A:::200</td><td>4</td><td>:::700</td><td></td><td></td><td></td><td>300 MHZ</td><td>20C8 20D8 20E3 20F3</td></tr><tr><td>FEBBA<0..11></td><td>RAM_FBADDR_A:::530</td><td>5</td><td>:::2400</td><td>2350</td><td></td><td></td><td>300 MHZ</td><td>20E2 20F2 22C2 22C6 22D2 22D6</td></tr><tr><td>FEBABA<0..1></td><td>GPU_FBADDR_A:::200</td><td>4</td><td>:::600</td><td></td><td></td><td></td><td>300 MHZ</td><td>20C8 20E3</td></tr><tr><td>FEBABA<0..1></td><td>RAM_FBADDR_A:::530</td><td>5</td><td>:::2400</td><td>50</td><td></td><td></td><td>300 MHZ</td><td>20E2 22C2 22C6</td></tr><tr><td>FEBARAS_L</td><td>GPU_FBCNTL_A:::200</td><td>4</td><td>:::400</td><td></td><td></td><td></td><td>300 MHZ</td><td>20C8 20G3</td></tr><tr><td>FEBACAS_L</td><td>GPU_FBCNTL_A:::200</td><td>4</td><td>:::400</td><td></td><td></td><td></td><td>300 MHZ</td><td>20C8 20G3</td></tr><tr><td>FBAWE_L</td><td>GPU_FBCNTL_A:::200</td><td>4</td><td>:::400</td><td></td><td></td><td></td><td>300 MHZ</td><td>20C8 20F3</td></tr><tr><td>FEBACS0_L</td><td>GPU_FBCNTL_A:::200</td><td>4</td><td>:::400</td><td></td><td></td><td></td><td>300 MHZ</td><td>20C8 20F3</td></tr><tr><td>FEBACKE</td><td>GPU_FBCNTL_A:::200</td><td>5</td><td>:::400</td><td>100</td><td></td><td></td><td>300 MHZ</td><td>20D3 20D7</td></tr><tr><td>FEBARAS_L</td><td>RAM_FBCNTL_A:::350</td><td>5</td><td>:::2700</td><td>50</td><td></td><td></td><td>300 MHZ</td><td>20G2 22B2 22B6</td></tr><tr><td>FEBACAS_L</td><td>RAM_FBCNTL_A:::500</td><td>5</td><td>:::2700</td><td>50</td><td></td><td></td><td>300 MHZ</td><td>20G2 22B2 22B6</td></tr><tr><td>FEBawe_L</td><td>RAM_FBCNTL_A:::500</td><td>5</td><td>:::2700</td><td>50</td><td></td><td></td><td>300 MHZ</td><td>20F2 22B2 22B6</td></tr><tr><td>FEBACS0_L</td><td>RAM_FBCNTL_A:::350</td><td>5</td><td>:::2700</td><td>50</td><td></td><td></td><td>300 MHZ</td><td>20F2 22B2 22B6</td></tr><tr><td>FEBACKE</td><td>RAM_FBCNTL_A:::500</td><td>5</td><td>:::2700</td><td>50</td><td></td><td></td><td>300 MHZ</td><td>20D2 22C2 22C6</td></tr><tr><td>FBDQS<0..7></td><td>GPU_FBDQS_A:::100</td><td>3</td><td>:::350</td><td></td><td></td><td></td><td></td><td>300 MHZ</td><td>20C7 21A8</td></tr><tr><td>FBDQSTERM<0..7></td><td>FB_DQSTERM_A:::50</td><td>3</td><td>:::1500</td><td></td><td></td><td>10 MIL SPACING</td><td></td><td>300 MHZ</td><td>21A7</td></tr><tr><td>FBDQS<0..7></td><td>RAM_FBDQS_A:::55</td><td>3</td><td>:::150</td><td></td><td></td><td>10 MIL SPACING</td><td></td><td>300 MHZ</td><td>21A6 22C2 22C6</td></tr><tr><td>FEBCLK0</td><td>GPU_FBCLK_A:::50</td><td>3</td><td>:::150</td><td></td><td></td><td></td><td>200</td><td>300 MHZ</td><td>20D7 21C3</td></tr><tr><td>FEBCLK0_L</td><td>GPU_FBCLK_A:::50</td><td>3</td><td>:::150</td><td></td><td></td><td></td><td>200</td><td>300 MHZ</td><td>20D7 21C3</td></tr><tr><td>FEBCLK1</td><td>GPU_FBCLK_A:::50</td><td>3</td><td>:::150</td><td></td><td></td><td></td><td>200</td><td>300 MHZ</td><td>20D7 21D3</td></tr><tr><td>FEBCLK1_L</td><td>GPU_FBCLK_A:::50</td><td>3</td><td>:::150</td><td></td><td></td><td></td><td>200</td><td>300 MHZ</td><td>20D7 21D3</td></tr><tr><td>FEBACLK1</td><td>RAM_FBCLK_A:::80</td><td>3</td><td>:::2500</td><td></td><td></td><td></td><td>200</td><td>300 MHZ</td><td>21D1 22C2</td></tr><tr><td>FEBACLK1_L</td><td>RAM_FBCLK_A:::80</td><td>3</td><td>:::2500</td><td></td><td></td><td></td><td>200</td><td>300 MHZ</td><td>21D1 22C2</td></tr><tr><td>FEBACLK0</td><td>RAM_FBCLK_A:::70</td><td>3</td><td>:::2500</td><td></td><td></td><td></td><td>200</td><td>300 MHZ</td><td>21C1 22C6</td></tr><tr><td>FEBACLK0_L</td><td>RAM_FBCLK_A:::70</td><td>3</td><td>:::2500</td><td></td><td></td><td></td><td>200</td><td>300 MHZ</td><td>21C1 22C6</td></tr><tr><td rowspan="32">C</td><td>FBD<64..127></td><td>GPU_FBDATA_B:::225</td><td>4</td><td>:::800</td><td></td><td></td><td></td><td>300 MHZ</td><td>20E5 20F5 20G5 21B5 21B8 21C5 21C8</td></tr><tr><td>FEBD<64..127></td><td>RAM_FBDATA_B:::325</td><td>4</td><td>:::1000</td><td></td><td></td><td></td><td>300 MHZ</td><td>21B4 21B7 21C4 21C7 23B1 23B5 23C1 23C5</td></tr><tr><td>FBDQM<8..15></td><td>GPU_FBDQM_B:::120</td><td>4</td><td>:::800</td><td></td><td></td><td></td><td>300 MHZ</td><td>20C3 20D3 20D5</td></tr><tr><td>FEBDQM<8..15></td><td>RAM_FBDQM_B:::120</td><td>4</td><td>:::1000</td><td></td><td></td><td></td><td>300 MHZ</td><td>20C2 20D2 23C2 23C6</td></tr><tr><td>FBBBA<0..12></td><td>GPU_FBADDR_B:::120</td><td>4</td><td>:::600</td><td></td><td></td><td></td><td>300 MHZ</td><td>20A3 20B3 20C3 20C5 20D5</td></tr><tr><td>FEBBA<0..11></td><td>RAM_FBADDR_B:::370</td><td>5</td><td>:::2400</td><td>50</td><td></td><td></td><td>300 MHZ</td><td>20B2 20C2 23C2 23C6 23D2 23D6</td></tr><tr><td>FEBBA<0..1></td><td>GPU_FBADDR_B:::120</td><td>4</td><td>:::600</td><td></td><td></td><td></td><td>300 MHZ</td><td>20A3 20C5</td></tr><tr><td>FEBBBA<0..1></td><td>RAM_FBADDR_B:::370</td><td>5</td><td>:::2400</td><td>50</td><td></td><td></td><td>300 MHZ</td><td>20A2 23C2 23C6</td></tr><tr><td>FBBRAS_L</td><td>GPU_FBCNTL_B:::120</td><td>4</td><td>:::400</td><td></td><td></td><td></td><td>300 MHZ</td><td>20C3 20D4</td></tr><tr><td>FBBCAS_L</td><td>GPU_FBCNTL_B:::120</td><td>4</td><td>:::400</td><td></td><td></td><td></td><td>300 MHZ</td><td>20C3 20D4</td></tr><tr><td>FBBWE_L</td><td>GPU_FBCNTL_B:::120</td><td>4</td><td>:::400</td><td></td><td></td><td></td><td>300 MHZ</td><td>20C3 20D4</td></tr><tr><td>FBBCS0_L</td><td>GPU_FBCNTL_B:::120</td><td>4</td><td>:::400</td><td></td><td></td><td></td><td>300 MHZ</td><td>20C3 20C4</td></tr><tr><td>FBBCKE</td><td>GPU_FBCNTL_B:::120</td><td>5</td><td>:::400</td><td>100</td><td></td><td></td><td>300 MHZ</td><td>20A3 20C4</td></tr><tr><td>FBBBRAS_L</td><td>RAM_FBCNTL_B:::2000</td><td>5</td><td>:::3500</td><td>3550</td><td></td><td></td><td>300 MHZ</td><td>20C2 23B2 23B6</td></tr><tr><td>FBBBCAS_L</td><td>RAM_FBCNTL_B:::2000</td><td>5</td><td>:::3500</td><td>3550</td><td></td><td></td><td>300 MHZ</td><td>20C2 23B2 23B6</td></tr><tr><td>FBBBWE_L</td><td>RAM_FBCNTL_B:::2000</td><td>5</td><td>:::3500</td><td>3550</td><td></td><td></td><td>300 MHZ</td><td>20C2 23B2 23B6</td></tr><tr><td>FBBBCS0_L</td><td>RAM_FBCNTL_B:::2000</td><td>5</td><td>:::3500</td><td>3550</td><td></td><td></td><td>300 MHZ</td><td>20C2 23B2 23B6</td></tr><tr><td>FBBBCKE</td><td>RAM_FBCNTL_B:::2000</td><td>5</td><td>:::3500</td><td>3550</td><td></td><td></td><td>300 MHZ</td><td>20A2 23C2 23C6</td></tr><tr><td>FBDQS<8..15></td><td>GPU_FBDQS_B:::100</td><td>3</td><td>:::350</td><td></td><td></td><td>10 MIL SPACING</td><td></td><td>300 MHZ</td><td>20D4 21A5</td></tr><tr><td>FBDQSTERM<8..15></td><td>FB_FBDQSTERM_B:::60</td><td>3</td><td>:::1500</td><td></td><td></td><td>10 MIL SPACING</td><td></td><td>300 MHZ</td><td>21A4</td></tr><tr><td>FBDQS<8..15></td><td>RAM_FBDQS_B:::50</td><td>3</td><td>:::150</td><td></td><td></td><td>10 MIL SPACING</td><td></td><td>300 MHZ</td><td>21A3 23C2 23C6</td></tr><tr><td>FBBCLK0</td><td>GPU_FBCLK_B:::50</td><td>3</td><td>:::150</td><td></td><td></td><td></td><td>200</td><td>300 MHZ</td><td>20C5 21B3</td></tr><tr><td>FBBCLK0_L</td><td>GPU_FBCLK_B:::50</td><td>3</td><td>:::150</td><td></td><td></td><td></td><td>200</td><td>300 MHZ</td><td>20C5 21B3</td></tr><tr><td>FBBCLK1</td><td>GPU_FBCLK_B:::50</td><td>3</td><td>:::150</td><td></td><td></td><td></td><td>200</td><td>300 MHZ</td><td>20C5 21C3</td></tr><tr><td>FBBCLK1_L</td><td>GPU_FBCLK_B:::50</td><td>3</td><td>:::150</td><td></td><td></td><td></td><td>200</td><td>300 MHZ</td><td>20C5 21B3</td></tr><tr><td>FBBBCLK1</td><td>RAM_FBCLK_B:::90</td><td>4</td><td>:::2500</td><td></td><td></td><td></td><td>200</td><td>300 MHZ</td><td>21C1 23C2</td></tr><tr><td>FBBBCLK1_L</td><td>RAM_FBCLK_B:::90</td><td>3</td><td>:::2500</td><td></td><td></td><td></td><td>200</td><td>300 MHZ</td><td>21B1 23C2</td></tr><tr><td>FBBBCLK0</td><td>RAM_FBCLK_B:::90</td><td>3</td><td>:::2500</td><td></td><td></td><td></td><td>200</td><td>300 MHZ</td><td>21B1 23C6</td></tr><tr><td>FBBBCLK0_L</td><td>RAM_FBCLK_B:::90</td><td>3</td><td>:::2500</td><td></td><td></td><td></td><td>200</td><td>300 MHZ</td><td>21B1 23C6</td></tr><tr><td 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VIAS	DELAY_RULE	STUB_LENGTH	NET_SPACING_TYPE	MAX EXPOSED LENGTH	PULSE_PARAM		D	FBD<0..63>	GPU_FBDATA_A:::225	4	:::800				300 MHZ	20E8 20F8 20G8 21C5 21C8 21D5 21D8	FEBD<0..63>	RAM_FBDATA_A:::300	4	:::1000				300 MHZ	21C4 21C7 21D4 21D7 22B1 22B5 22C1 22C5	FBDQM<0..7>	GPU_FBDQM_A:::200	4	:::800				300 MHZ	20D8 20G3	FEBDQM<0..7>	RAM_FBDQM_A:::200	4	:::1000				300 MHZ	20G2 22C2 22C6	FBA<0..12>	GPU_FBADDR_A:::200	4	:::700				300 MHZ	20C8 20D8 20E3 20F3	FEBBA<0..11>	RAM_FBADDR_A:::530	5	:::2400	2350			300 MHZ	20E2 20F2 22C2 22C6 22D2 22D6	FEBABA<0..1>	GPU_FBADDR_A:::200	4	:::600				300 MHZ	20C8 20E3	FEBABA<0..1>	RAM_FBADDR_A:::530	5	:::2400	50			300 MHZ	20E2 22C2 22C6	FEBARAS_L	GPU_FBCNTL_A:::200	4	:::400				300 MHZ	20C8 20G3	FEBACAS_L	GPU_FBCNTL_A:::200	4	:::400				300 MHZ	20C8 20G3	FBAWE_L	GPU_FBCNTL_A:::200	4	:::400				300 MHZ	20C8 20F3	FEBACS0_L	GPU_FBCNTL_A:::200	4	:::400				300 MHZ	20C8 20F3	FEBACKE	GPU_FBCNTL_A:::200	5	:::400	100			300 MHZ	20D3 20D7	FEBARAS_L	RAM_FBCNTL_A:::350	5	:::2700	50			300 MHZ	20G2 22B2 22B6	FEBACAS_L	RAM_FBCNTL_A:::500	5	:::2700	50			300 MHZ	20G2 22B2 22B6	FEBawe_L	RAM_FBCNTL_A:::500	5	:::2700	50			300 MHZ	20F2 22B2 22B6	FEBACS0_L	RAM_FBCNTL_A:::350	5	:::2700	50			300 MHZ	20F2 22B2 22B6	FEBACKE	RAM_FBCNTL_A:::500	5	:::2700	50			300 MHZ	20D2 22C2 22C6	FBDQS<0..7>	GPU_FBDQS_A:::100	3	:::350					300 MHZ	20C7 21A8	FBDQSTERM<0..7>	FB_DQSTERM_A:::50	3	:::1500			10 MIL SPACING		300 MHZ	21A7	FBDQS<0..7>	RAM_FBDQS_A:::55	3	:::150			10 MIL SPACING		300 MHZ	21A6 22C2 22C6	FEBCLK0	GPU_FBCLK_A:::50	3	:::150				200	300 MHZ	20D7 21C3	FEBCLK0_L	GPU_FBCLK_A:::50	3	:::150				200	300 MHZ	20D7 21C3	FEBCLK1	GPU_FBCLK_A:::50	3	:::150				200	300 MHZ	20D7 21D3	FEBCLK1_L	GPU_FBCLK_A:::50	3	:::150				200	300 MHZ	20D7 21D3	FEBACLK1	RAM_FBCLK_A:::80	3	:::2500				200	300 MHZ	21D1 22C2	FEBACLK1_L	RAM_FBCLK_A:::80	3	:::2500				200	300 MHZ	21D1 22C2	FEBACLK0	RAM_FBCLK_A:::70	3	:::2500				200	300 MHZ	21C1 22C6	FEBACLK0_L	RAM_FBCLK_A:::70	3	:::2500				200	300 MHZ	21C1 22C6	C	FBD<64..127>	GPU_FBDATA_B:::225	4	:::800				300 MHZ	20E5 20F5 20G5 21B5 21B8 21C5 21C8	FEBD<64..127>	RAM_FBDATA_B:::325	4	:::1000				300 MHZ	21B4 21B7 21C4 21C7 23B1 23B5 23C1 23C5	FBDQM<8..15>	GPU_FBDQM_B:::120	4	:::800				300 MHZ	20C3 20D3 20D5	FEBDQM<8..15>	RAM_FBDQM_B:::120	4	:::1000				300 MHZ	20C2 20D2 23C2 23C6	FBBBA<0..12>	GPU_FBADDR_B:::120	4	:::600				300 MHZ	20A3 20B3 20C3 20C5 20D5	FEBBA<0..11>	RAM_FBADDR_B:::370	5	:::2400	50			300 MHZ	20B2 20C2 23C2 23C6 23D2 23D6	FEBBA<0..1>	GPU_FBADDR_B:::120	4	:::600				300 MHZ	20A3 20C5	FEBBBA<0..1>	RAM_FBADDR_B:::370	5	:::2400	50			300 MHZ	20A2 23C2 23C6	FBBRAS_L	GPU_FBCNTL_B:::120	4	:::400				300 MHZ	20C3 20D4	FBBCAS_L	GPU_FBCNTL_B:::120	4	:::400				300 MHZ	20C3 20D4	FBBWE_L	GPU_FBCNTL_B:::120	4	:::400				300 MHZ	20C3 20D4	FBBCS0_L	GPU_FBCNTL_B:::120	4	:::400				300 MHZ	20C3 20C4	FBBCKE	GPU_FBCNTL_B:::120	5	:::400	100			300 MHZ	20A3 20C4	FBBBRAS_L	RAM_FBCNTL_B:::2000	5	:::3500	3550			300 MHZ	20C2 23B2 23B6	FBBBCAS_L	RAM_FBCNTL_B:::2000	5	:::3500	3550			300 MHZ	20C2 23B2 23B6	FBBBWE_L	RAM_FBCNTL_B:::2000	5	:::3500	3550			300 MHZ	20C2 23B2 23B6	FBBBCS0_L	RAM_FBCNTL_B:::2000	5	:::3500	3550			300 MHZ	20C2 23B2 23B6	FBBBCKE	RAM_FBCNTL_B:::2000	5	:::3500	3550			300 MHZ	20A2 23C2 23C6	FBDQS<8..15>	GPU_FBDQS_B:::100	3	:::350			10 MIL SPACING		300 MHZ	20D4 21A5	FBDQSTERM<8..15>	FB_FBDQSTERM_B:::60	3	:::1500			10 MIL SPACING		300 MHZ	21A4	FBDQS<8..15>	RAM_FBDQS_B:::50	3	:::150			10 MIL SPACING		300 MHZ	21A3 23C2 23C6	FBBCLK0	GPU_FBCLK_B:::50	3	:::150				200	300 MHZ	20C5 21B3	FBBCLK0_L	GPU_FBCLK_B:::50	3	:::150				200	300 MHZ	20C5 21B3	FBBCLK1	GPU_FBCLK_B:::50	3	:::150				200	300 MHZ	20C5 21C3	FBBCLK1_L	GPU_FBCLK_B:::50	3	:::150				200	300 MHZ	20C5 21B3	FBBBCLK1	RAM_FBCLK_B:::90	4	:::2500				200	300 MHZ	21C1 23C2	FBBBCLK1_L	RAM_FBCLK_B:::90	3	:::2500				200	300 MHZ	21B1 23C2	FBBBCLK0	RAM_FBCLK_B:::90	3	:::2500				200	300 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		FEBD<64..127>	RAM_FBDATA_B:::325	4	:::1000				300 MHZ	21B4 21B7 21C4 21C7 23B1 23B5 23C1 23C5																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																													
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FBBBA<0..12>		GPU_FBADDR_B:::120	4	:::600				300 MHZ	20A3 20B3 20C3 20C5 20D5																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																														
FEBBA<0..11>		RAM_FBADDR_B:::370	5	:::2400	50			300 MHZ	20B2 20C2 23C2 23C6 23D2 23D6																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																														
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FBBCKE		GPU_FBCNTL_B:::120	5	:::400	100			300 MHZ	20A3 20C4																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																														
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FBDQS<8..15>		GPU_FBDQS_B:::100	3	:::350			10 MIL SPACING		300 MHZ	20D4 21A5																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																													
FBDQSTERM<8..15>		FB_FBDQSTERM_B:::60	3	:::1500			10 MIL SPACING		300 MHZ	21A4																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																													
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<table><tr><td colspan="10">SIGNAL CONSTRAINTS</td><td colspan="8">NOTICE OF PROPRIETARY PROPERTY LAST MODIFIED=Wed Feb 26 10:30:28 2003 THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING I TO MAINTAIN THE DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART</td></tr><tr><td colspan="10">APPLE COMPUTER INC.</td><td>SIZE D</td><td colspan="4">DRAWING NUMBER 051-6423</td><td colspan="4">REV. D</td></tr><tr><td colspan="10">SCALE NONE</td><td colspan="2">SHT 57</td><td colspan="2">OF 74</td><td colspan="4"></td></tr></table>																		SIGNAL CONSTRAINTS										NOTICE OF PROPRIETARY PROPERTY LAST MODIFIED=Wed Feb 26 10:30:28 2003 THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING I TO MAINTAIN THE DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART								APPLE COMPUTER INC.										SIZE D	DRAWING NUMBER 051-6423				REV. D				SCALE NONE										SHT 57		OF 74																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																				
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GROUP	SIG_NAME	MATCHED_DELAY	MAX_VIAS	DELAY_RULE	STUB_LENGTH	NET_SPACING_TYPE	NO_TEST	PULSE_PARAM	MAX_EXPOSED_LENGTH
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C

MAXBUS

F<GPU_ADDR<0...31> CPU_ADDR_GROUP:::71250

:::5000

250

166 MHZ

487

4C7

9B7

9B8

9C5

9C6

9C7

9C8

11C3

11D3

11D8

F<GPU_DATA<0...63> CPU_DATA_GROUP:::71400

:::5000

1550

166 MHZ

544

5B4

5C4

5D4

9C5

9C6

9C7

9C8

9D5

9D6

9D7

9D8

F<GPU_BR_L CPU_CNTL_GROUP:::71100

:::5000

250

10 MIL SPACING

166 MHZ

4D7

7C7

9C5

11D3

11B1

11B5

11B8

11C1

11C5

11D1

11D5

F<GPU_BG_L CPU_CNTL_GROUP:::71100

:::5000

250

10 MIL SPACING

166 MHZ

4D7

7B7

9B5

11D3

F<GPU_TS_L CPU_CNTL_GROUP:::71000

:::5000

250

10 MIL SPACING

166 MHZ

4D7

7C7

9C7

11D3

F<GPU_TT<0...4> CPU_CNTL_GROUP:::71300

:::5000

250

10 MIL SPACING

166 MHZ

4B7

7A7

9B5

9B6

9C5

9C6

11B3

F<GPU_TBST_L CPU_CNTL_GROUP:::71100

:::5000

250

10 MIL SPACING

166 MHZ

4B7

7B7

9B5

11B3

F<GPU_TSIZ<0...2> CPU_CNTL_GROUP:::71260

:::5000

250

10 MIL SPACING

166 MHZ

4B7

9B6

9B7

9C6

11B3

F<GPU_ARTRY_L CPU_CNTL_GROUP:::71000

:::5000

250

10 MIL SPACING

166 MHZ

4A7

7C7

9B8

11B3

F<GPU_AACK_L CPU_CNTL_GROUP:::71000

:::1000

250

10 MIL SPACING

166 MHZ

4A7

7B7

9B6

11B3

F<GPU_GBL_L CPU_CNTL_GROUP:::71000

:::3000

250

10 MIL SPACING

166 MHZ

4B8

9B6

F<GPU_INT_GBL_L CPU_CNTL_GROUP:::71100

:::3500

250

10 MIL SPACING

166 MHZ

4B8

7B7

11C3

F<GPU_CI_L CPU_CNTL_GROUP:::71000

:::5000

250

10 MIL SPACING

166 MHZ

4A7

7A7

9C6

11C3

F<GPU_HIT_L CPU_CNTL_GROUP:::71000

:::5000

250

10 MIL SPACING

166 MHZ

4A7

7C7

9B8

11B3

F<GPU_DBG_L CPU_CNTL_GROUP:::71000

:::3500

250

10 MIL SPACING

166 MHZ

4C3

7B7

9C8

11B1

F<GPU_DRDY_L CPU_CNTL_GROUP:::71100

:::500

250

10 MIL SPACING

166 MHZ

4C2

7B7

9B6

11B1

F<GPU_WT_L CPU_CNTL_GROUP:::71000

:::5000

250

10 MIL SPACING

166 MHZ

4B7

7A7

9B6

11B3

F<GPU_DRDY_L_UF 2

:::5000

250

10 MIL SPACING

166 MHZ

4C3

F<GPU_DTI<0...2> CPU_CNTL_GROUP:::71150

:::5000

250

10 MIL SPACING

166 MHZ

4C3

9B5

9B7

11A1

F<GPU_TA_L CPU_CNTL_GROUP:::71100

:::5000

250

10 MIL SPACING

166 MHZ

4C3

7C7

9C5

11A1

F<GPU_TEA_L CPU_CNTL_GROUP:::71100

:::5000

250

10 MIL SPACING

166 MHZ

4C3

7B7

9C6

11A1

F<GPU_QREQ_L CPU_CNTL_GROUP:::71000

:::5000

250

10 MIL SPACING

166 MHZ

4C3

7D5

9B7

11B3

F<GPU_QACK_L CPU_CNTL_GROUP:::71000

:::4000

250

10 MIL SPACING

166 MHZ

4C3

9B5

11B3

F<SYSCLK_CPU_UF 2

:::150

10 MIL SPACING

166 MHZ

315

11A3

F<SYSCLK_CPU 4

:::2200:2400

200

10 MIL SPACING

166 MHZ

315

4D2

11A4

F<INT_CPU_FB_OUT 3

:::1000

200

166 MHZ

315

11B3

F<GPU_FBO_PLUS1 3

:::200

200

166 MHZ

315

11A4

F<GPU_FBI_PLUS1 3

:::1400:1500

200

166 MHZ

315

11A4

F<GPU_FB_MINUS3 4

:::900:1000

200

166 MHZ

315

11A4

F<INT_CPU_FB_IN 4

:::1000

200

166 MHZ

315

11B3

F<GPU_FB_PLUS2 3

:::900:1000

166 MHZ

315

11A5

F<GPU_FB_PLUS3 3

:::2900:3000

166 MHZ

315

11A4

F<INT_ANALYZER_CLK 3

:::300

166 MHZ

9B4

11B4

18C7

56A7

61A7

F<SYSCLK_LA 2

:::2000

166 MHZ

9B3

9D8

F<INT_CLOCK_OUT 3

:::3000

166 MHZ

9C4

A SIGNAL CONSTRAINTS A

NOTICE OF PROPRIETARY PROPERTY
LAST MODIFIED=Wed Feb 26 10:30:28 2003
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY
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AGREES TO THE FOLLOWING
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II NOT TO REPRODUCE OR COPY IT
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



CR-59							
8		7		6		5	

GROUP	SIG_NAME	MATCHED_DELAY	MAX_VIAS	DELAY_RULE	STUB_LENGTH	NET_SPACING_TYPE	MAX_EXPOSED_LENGTH	PULSE_PARAM	
CD DRIVE BUS	FLG0 IDE_RST_L			:::3500:5500				33 MHZ	39A7 39D5
137	FLG0 IDE_DMACK_L			:::3500:5500				33 MHZ	39A7 39D5
138	FLG0 IDE_STOP			:::5500				33 MHZ	39A7 39D5
139	FLG0 IDE_HSTB_RDY			:::5500				33 MHZ	39A7 39C5
126	FLG0 IDE_DSTB_RDY			:::3500:5500				33 MHZ	39A7 39C5
121	FLG0 IDE_DATA<0..15>			:::3500:5500				33 MHZ	39A5 39B5 39B7 39C5
123	FLG0 CD_RESET_L			:::1000				33 MHZ	39D4 40C6
123	FLG0 CD_DMACK_L			:::4000				33 MHZ	39D4 40C6
124	FLG0 CD_STOP			:::5000				33 MHZ	39D4 40C6
126	FLG0 CD_HSTB_RDY			:::5000				33 MHZ	39C4 40C6
126	FLG0 CD_DSTB_RDY			:::1000				33 MHZ	39C4 40C6
127	FLG0 UATAD<0..15>			:::1000				33 MHZ	39A4 39B4 39C4 40C6
126	FLG0 CD_DMARQ			:::1000				33 MHZ	40C6
126	FLG0 IDE_DMARQ			:::3500:5500				33 MHZ	39A7 40C8
136	FLG0 UATA0IRQ			:::1000				33 MHZ	40C6
131	FLG0 IDE_INTRQ			:::3500:5500				33 MHZ	39A7 40C8
132	FLG0 CD_EIDE_ADDR<0..2>			:::1000				33 MHZ	40C6
133	FLG0 IDE_ADDR<0..2>			:::3500:5500				33 MHZ	39B7 40A8 40B8
134	FLG0 CD_CS1FX_L			:::1000				33 MHZ	40C6
135	FLG0 IDE_CS1FX_L			:::3500:5500				33 MHZ	39A7 40B8
136	FLG0 CD_CS3FX_L			:::1000				33 MHZ	40C6
137	FLG0 IDE_CS3FX_L			:::3500:5500				33 MHZ	39A7 40B8
138	FLG0								
HD DRIVE BUS	FLG0 IDE_RST_L	HD_DATA:::5500		:::100:6000	500			100 MHZ	39C7 39D3
139	FLG0 IDE_DMACK_L	HD_DATA:::5500		:::100:6000	500			100 MHZ	39C7 39D3
140	FLG0 IDE_DIOR_L	HD_DATA:::5500		:::100:6000	500			100 MHZ	39C7 39D3
141	FLG0 IDE_DIOW_L	HD_DATA:::5500		:::6000				100 MHZ	39C7 39D3
142	FLG0 IDE_IOCHRDY	HD_DATA:::5500		:::100:6000	500			100 MHZ	39C3 39C7
143	FLG0 IDE_DATA<0..15>	HD_DATA:::5500		:::100:6000	500			100 MHZ	39C3 39C7
144	FLG0 HD_RESET_L			:::1000				100 MHZ	39A3 39B3 39C3 39C7
145	FLG0 HD_DMACK_L			:::1000				100 MHZ	39D1 40C3
146	FLG0 HD_DIOR_L			:::5500				100 MHZ	39D1 40C3
147	FLG0 HD_DIOW_L			:::5500				100 MHZ	39C1 40C3
148	FLG0 HD_IOCHRDY			:::1000				100 MHZ	39C1 40C3
149	FLG0								
	FLG0 HD_DMARQ			:::1000				100 MHZ	40C3
150	FLG0 IDE_DMARQ	HD_DATA:::5500		:::100:6000				100 MHZ	39C7 40C4
151	FLG0 HD_INTRQ			:::1000	500			100 MHZ	40C3
152	FLG0 IDE_INTRQ	HD_DATA:::5500		:::100:6000	500			100 MHZ	39C7 40C4
153	FLG0 HD_UIDE_ADDR<0..2>			:::1000				100 MHZ	40C2 40C3
154	FLG0 IDE_ADDR<0..2>	HD_DATA:::5500		:::100:6000				100 MHZ	39C7 40A4 40B4
155	FLG0 HD_UIDE_CS1FX_L			:::6000				100 MHZ	40C3
156	FLG0 IDE_CS1FX_L	HD_DATA:::5500		:::6000				100 MHZ	39C7 40B4
157	FLG0 HD_UIDE_CS3FX_L			:::6000				100 MHZ	40C2
158	FLG0 IDE_CS3FX_L	HD_DATA:::5500		:::6000				100 MHZ	39C7 40B4
159	FLG0								
160	FLG0 CLK_18M_INT_XOUT		3	:::1000	100	8 MIL SPACING		18.432 MHZ	60B5
161	FLG0 CLK_18M_INT_XOUT		3	:::1000	100	8 MIL SPACING		18.432 MHZ	60B5

	SIG_NAME	DELAY_RULE	PULSE_PARAM
I1	FLAB_T_UD_IDEDD_0	:::1000	100 MHZ
I2	FLAB_T_UD_IDEDD_1	:::1000	100 MHZ
I3	FLAB_T_UD_IDEDD_2	:::1000	100 MHZ
I4	FLAB_T_UD_IDEDD_3	:::1000	100 MHZ
I5	FLAB_T_UD_IDEDD_4	:::1000	100 MHZ
I6	FLAB_T_UD_IDEDD_5	:::1000	100 MHZ
I7	FLAB_T_UD_IDEDD_6	:::1000	100 MHZ
I8	FLAB_T_UD_IDEDD_7	:::1000	100 MHZ
I9	FLAB_T_UD_IDEDD_8	:::1000	100 MHZ
I10	FLAB_T_UD_IDEDD_9	:::1000	100 MHZ
I11	FLAB_T_UD_IDEDD_10	:::1000	100 MHZ
I12	FLAB_T_UD_IDEDD_11	:::1000	100 MHZ
I14	FLAB_T_UD_IDEDD_12	:::1000	100 MHZ
I13	FLAB_T_UD_IDEDD_13	:::1000	100 MHZ
I16	FLAB_T_UD_IDEDD_14	:::1000	100 MHZ
I15	FLAB_T_UD_IDEDD_15	:::1000	100 MHZ

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APPLE COMPUTER INC.

SIZE	DRAWING NUMBER		REV.
D	051-6423		
SCALE	SHT	OF	
NONE	60	74	

CR-61																																			
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D															D																				
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D	*** Signal Cross-Reference *** --- for the entire design ---														D			
C															C			
B															B			
A															A			
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[illegible]

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D	SW3V_TG2 52B6			T_UD_IDEDD_2 39C1 40C3 60C2			USB_DCN 30B3 60B5			ZT11P1 4B1					
	SW3V_TG2R 52C5			T_UD_IDEDD_3 39C1 40C3 60C2			USB_DCN_CON 35D3 58A3 61C4								
	SW3V_VOSNS 52B6			T_UD_IDEDD_4 39B1 40C3 60C2			USB_DCN_F 30B2 35D7 60B5								
	SW5VITH1R 52B8			T_UD_IDEDD_5 39B1 40C3 60C2			USB_DCP 30B3 60B5								
	SW5V_5VSENSE 52A4			T_UD_IDEDD_6 39B1 40C3 60B2			USB_DCP_CON 35D3 58A3 61C4								
	SW5V_BG1 52A5 52B7			T_UD_IDEDD_7 39B1 40C3 60B2			USB_DCP_F 30B2 35D7 60B5								
	SW5V_BG1R 52A5			T_UD_IDEDD_8 39B1 40C2 60B2			USB_DDN 30B3								
	SW5V_B00ST1 52A5 52B7			T_UD_IDEDD_9 39B1 40C2 60B2			USB_DDN_F_TERM 30B2								
	SW5V_ITH1 52B7			T_UD_IDEDD_10 39B1 40C2 60B2			USB_DDP 30B3								
	SW5V_RUNSS 52B8 52D3			T_UD_IDEDD_11 39B1 40C2 60B2			USB_DDP_F_TERM 30B2								
C	SW5V_SNSM 52A5 52B7			T_UD_IDEDD_12 39B1 40C2 60B2			USB_DEN 30B3 60B5								
	SW5V_SNSMA 52A4			T_UD_IDEDD_13 39A1 40C2 60B2			USB_DEP 30B3 60B5								
	SW5V_SNSP 52A5 52B7			T_UD_IDEDD_14 39A1 40C2 60B2			USB_DFN 30B3 60A5								
	SW5V_SW1 52A4 52A5 52B7			T_UD_IDEDD_15 39A1 40C2 60B2			USB_DFP 30B3 60A5								
	SW5V_SW1A 52A3			U5_SDOUT 41C3			USB_GND 35A4 35B3 35C3 35D3 54A3								
	SW5V_TG1 52B5 52B7			U22_8 38D8			USB_OC_EF_L 30B3 30C2								
	SW5V_TG1R 52B4			U4202P2 42C3			USB_PORT_PWR 35A4 35B3 35C3 35D3 54A3 61B4								
	SW5V_VOSNS 52B7			U4202P3 42C3			USB_PWR 27B5 27C2 27D3 35A6 54A3								
	SW12V_SL 53C7			U4202P5 42B3			USB_PWREN_AB_L 30B3 30C2								
	SW12V_SLEEP 53D7			U4202P6 42B3			USB_PWREN_CD_L 30B3 30C2								
B	SWITCH5V_3 53B6			U4202P7 42B3			USB_PWREN_EF_L 30B3 30C2								
	SWITCH5V_4 53B6			UATA0IRQ 40C6 60D5			USB_PWR_EN 35A7 54A3								
	SWITCH5V_5 53B1 53B3			UATAD<15..0> 39A4 39B4 39C4 40C6 60D5			USB_PWR_FLT* 30B3 34B1 35A6								
	SWITCH5V_6 53B2			UB5PAE8 24B5			UX6P23 41C4								
	SYSCLK_CPU 4D2 11A4 58C3			UIDE_ADDR<2..0> 39C7 40A4 40B4 60B5			UX6_LINB 41C4								
	SYSCLK_CPU_UF 11A3 58C3			UIDE_CS1FX_L 39C7 40B4 60B5			U_USB_PWR_FLT* 34B4								
	SYSCLK_DDRCLK_A0 10C2 14C4 16D6 55C6			UIDE_CS3FX_L 39C7 40B4 60B5			VCOREIN 47D6								
	SYSCLK_DDRCLK_A0_L 10C2 14C4 16D6 55C6			UIDE_CSELP_L 40C2 54A6			VCORE_BG 47C6								
	SYSCLK_DDRCLK_A0_L_UF 14B6 14C5 55C6			UIDE_DATA<15..0> 39A3 39B3 39C3 39C7 39D7 60C5			VCORE_BG_1 47B5 47C7								
	SYSCLK_DDRCLK_A0_UF 14B6 14C5 55C6			UIDE_DIOR_L 39C7 39D3 60C5			VCORE_B00ST 47C5								
A	SYSCLK_DDRCLK_A1 10C2 14C4 16A4 55C6			UIDE_DIOW_L 39C3 39C7 60C5			VCORE_B00ST2 47C6								
	SYSCLK_DDRCLK_A1_L 10C2 14B4 16A4 55C6			UIDE_DMACK_L 39C7 39D3 60C5			VCORE_B00ST2_1 47B5 47C7								
	SYSCLK_DDRCLK_A1_L_UF 14B5 14B6 55C6			UIDE_DMARQ 39C7 40C4 60C5			VCORE_B00ST_1 47B5								
	SYSCLK_DDRCLK_A1_UF 14B6 14C5 55C6			UIDE_INTRQ 39C7 40C4 60C5			VCORE_EXTVCC 47D7								
	SYSCLK_DDRCLK_A2_L 10C5 14B4 55B6			UIDE_IOCHRDY 39C3 39C7 60C5			VCORE_FREQSET 47C7								
	SYSCLK_DDRCLK_A2_L_UF 14B5 14B6 55C6			UIDE_PDIA6 40C2			VCORE_INTVCC 47B5 47D5 47D7								
	SYSCLK_DDRCLK_A2_UF 14B5 14B6 55C6			UIDE_REF 39C7			VCORE_SEN+ 47C3								
	SYSCLK_DDRCLK_B0 10C2 14B4 17B4 55B6			UIDE_RST_L 39C7 39D3 60C5			VCORE_SEN+_1 47A3								
	SYSCLK_DDRCLK_B0_L 10C2 14B4 17B4 55B6			UNUSED_ATAIOCS16_L 40C2 54A6			VCORE_SENA+ 47C3								
	SYSCLK_DDRCLK_B0_L_UF 14B5 14B6 55B6			UNUSED_EXTINT7 30B5 30B8			VCORE_SF 47C8								
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APPLE COMPUTER INC.		D 051-6423		D		SCALE NONE		SHT 66 OF 74		NOTICE OF PROPRIETARY PROPERTY					

8			7			6			5			4			3			2			1					
D	*** Unit Cross-Reference *** --- for the entire design ---												D													
	BS1	PCB_STANDOFF	31B5	C74	CAP	37B7	C155	CAP_P	44D7	C236	CAP	53B3		NOTICE OF PROPRIETARY PROPERTY THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING I TO MAINTAIN THE DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART												
	BS2	PCB_STANDOFF	31B6	C75	CAP	20H5	C156	CAP_P	44C7	C237	CAP_P	50C8														
	BS3	PCB_STANDOFF	31D1	C76	CAP	37B7	C157	CAP	22A4	C238	CAP_P	53C5														
	BS4	PCB_STANDOFF	31D1	C77	CAP	34D3	C158	CAP	22D5	C239	CAP	46A4														
	BT1	BATTERY	46D6	C78	CAP	20H6	C159	CAP	20H5	C240	CAP	13B8														
	C1	CAP	35C3	C79	CAP	20H4	C160	CAP	25C6	C241	CAP	52C6														
	C2	CAP	35D3	C80	CAP	20H2	C161	CAP	19C4	C242	CAP	16C3														
	C3	CAP	37B1	C81	CAP	20H7	C162	CAP	24C6	C243	CAP	16C2														
	C4	CAP	38B6	C82	CAP	20H4	C163	CAP	24C6	C244	CAP	16C3														
C	C5	CAP_P	35A5	C83	CAP	20H3	C164	CAP	24C7	C245	CAP_P	46D5														
	C6	CAP	35B3	C84	CAP	20H8	C165	CAP	22B3	C246	CAP_P	53B3														
	C7	CAP_P	35A5	C85	CAP_P	41C6	C166	CAP	22B3	C247	CAP	10D4														
	C8	CAP	23A4	C86	CAP	20H8	C167	CAP	20H5	C248	CAP	41B4														
	C9	CAP	23A4	C87	CAP	19B3	C168	CAP	19C4	C249	CAP	33D1														
	C10	CAP_P	38D7	C88	CAP_P	50B1	C169	CAP	19C3	C250	CAP	33D2														
	C11	CAP	23A5	C89	CAP	20H2	C170	CAP	24C7	C251	CAP	10D7														
	C12	CAP_P	38D7	C90	CAP	20H7	C171	CAP_P	48A6	C252	CAP	53B2														
	C13	CAP_P	43C4	C91	CAP	19B4	C172	CAP	22A4	C253	CAP	10D7														
	C14	CAP_P	44C7	C92	CAP	36B6	C173	CAP	22B2	C254	CAP	53B1														
B	C15	CAP	23D2	C93	CAP	19C4	C174	CAP	22B4	C255	CAP	16C2														
	C16	CAP	23B4	C94	CAP	19C3	C175	CAP	20H3	C256	CAP	16C1														
	C17	CAP	23D5	C95	CAP	20H5	C176	CAP	19D2	C257	CAP	53B2														
	C18	CAP	53D2	C96	CAP	20H2	C177	CAP	19B4	C258	CAP	53B2														
	C19	CAP_P	53D3	C97	CAP	19B3	C178	CAP	19D4	C259	CAP	33D1														
	C20	CAP	23B2	C98	CAP	20H6	C179	CAP	19B3	C260	CAP	33D2														
	C21	CAP	23D5	C99	CAP	19D2	C180	CAP	19D3	C261	CAP	16C1														
	C22	CAP	23B4	C100	CAP	19D3	C181	CAP	19B2	C262	CAP	49B4														
	C23	CAP	23B4	C101	CAP	19D3	C182	CAP	19B2	C263	CAP	16C3														
	C24	CAP_P	42B4	C102	CAP	19C3	C183	CAP	19D2	C264	CAP	13C4														
A	C25	CAP_P	42C4	C103	CAP	19D3	C184	CAP	19B2	C265	CAP	49B3														
	C26	CAP_P	43D4	C104	CAP	19D4	C185	CAP	19D3	C266	CAP	49B8														
	C27	CAP	38D8	C105	CAP	19B3	C186	CAP	19B4	C267	CAP	46D6														
	C28	CAP	23B1	C106	CAP_P	50B2	C187	CAP	19B3	C268	CAP	10B8														
	C29	CAP	23B3	C107	CAP	44B8	C188	CAP	19B1	C269	CAP_P	53D6														
	C30	CAP	44B4	C108	CAP	44B8	C189	CAP	25C5	C270	CAP	49A5														
	C31	CAP	23B2	C109	CAP	19D3	C190	CAP	24C7	C271	CAP	13B8														
	C32	CAP	23D4	C110	CAP	19D2	C191	CAP	22A5	C272	CAP	49B4														
	C33	CAP	23B2	C111	CAP	19B4	C192	CAP	19D5	C273	CAP	49C5														
	C34	CAP	23B3	C112	CAP	19B2	C193	CAP	19B5	C274	CAP	10B8														
																								APPLE COMPUTER INC. SIZE D DRAWING NUMBER 051-6423 REV. D SCALE NONE SHT 67 OF 74		
C35	CAP	45C4	C113	CAP_P	48A4	C194	CAP	22B4	C275	CAP	16C2															
C36	CAP	37D2	C114	CAP	34D3	C195	CAP	22B1	C276	CAP	16C2															
C37	CAP	37D7	C115	CAP	20H3	C196	CAP	22B2	C277	CAP	13B8															
C38	CAP	23D2	C116	CAP	19B3	C197	CAP	22B2	C278	CAP	13C6															
C39	CAP	23D5	C117	CAP	19D4	C198	CAP	22B4	C279	CAP	13C5															
C40	CAP	23B3	C118	CAP	19C3	C199	CAP	20H3	C280	CAP	13C6															
C41	CAP	37D7	C119	CAP	25B4	C200	CAP	19D4	C281	CAP	13C6															
C42	CAP	38B8	C120	CAP	19C3	C201	CAP	19D4	C282	CAP	49B6															
C43	CAP	43D6	C121	CAP	41C4	C202	CAP	19D3	C283	CAP	49C7															
																								APPLE COMPUTER INC. SIZE D DRAWING NUMBER 051-6423 REV. D SCALE NONE SHT 67 OF 74		
C44	CAP	37D3	C122	CAP	44C8	C203	CAP	19D3	C284	CAP	49B4															
C45	CAP	37D2	C123	CAP	32C2	C204	CAP	42C5	C285	CAP	49B7															
C46	CAP	37D4	C124	CAP	20H1	C205	CAP	22D2	C286	CAP	49B7															
C47	CAP_P	44D7	C125	CAP	25B5	C206	CAP	24C2	C287	CAP	52A1															
C48	CAP	44D7	C126	CAP_P	41C7	C207	CAP	24C7	C288	CAP	13D8															
C49	CAP	44C7	C127	CAP_P	41B2	C208	CAP	43A5	C289	CAP	13D8															
C50	CAP_P	44D7	C128	CAP	24D3	C209	CAP	24B6	C290	CAP	13D8															
C51	CAP	37B3	C129	CAP	19B3	C210	CAP	16A3	C291	CAP	13D8															
C52	CAP	37D2	C130	CAP	25B5	C211	CAP	16A2	C292	CAP	13D4															
																								APPLE COMPUTER INC. SIZE D DRAWING NUMBER 051-6423 REV. D SCALE NONE SHT 67 OF 74		
C53	CAP	37D4	C131	CAP	25B4	C212	CAP	24D6	C293	CAP	52A1															
C54	CAP	20B8	C132	CAP_P	50B2	C213	CAP	24D6	C294	CAP	52A1															
C55	CAP	37D3	C133	CAP	20H6	C214	CAP	19A3	C295	CAP_P	52C6															
C56	CAP	37D3	C134	CAP	19D4	C215	CAP	19A3	C296	CAP_P	49B3															
C57	CAP	37D5	C135	CAP	19C3	C216	CAP	22D2	C297	CAP	49C4															
C58	CAP	37D5	C136	CAP	19B3	C217	CAP	19C1	C298	CAP	49C5															
C59	CAP_P	50B2	C137	CAP	19B4	C218	CAP_P	51B3	C299	CAP	49C4															
C60	CAP	43C6	C138	CAP_P	48A5	C219	CAP	19C2	C300	CAP_P	49C3															
C61	CAP	20B6	C139	CAP	19D2	C220	CAP_P	50C8	C301	CAP_P	49C4															
																								APPLE COMPUTER INC. SIZE D DRAWING NUMBER 051-6423 REV. D SCALE NONE SHT 67 OF 74		
C62	CAP	20D6	C140	CAP	19C2	C221	CAP	16B3	C302	CAP_P	52A2															
C63	CAP	42B2	C141	CAP	22B3	C222	CAP	18A8	C303	CAP	52C3															
C64	CAP	42C2	C142	CAP	22D5	C223	CAP	19C2	C304	CAP	16C2															
C65	CAP	37D6	C143	CAP	22D4	C224	CAP	24C2	C305	CAP	49C4															
C66	CAP	37D7	C144	CAP	22D5	C225	CAP	32C2	C306	CAP_P	52A2															
C67	CAP	37D4	C145	CAP	20H2	C226	CAP	24D6	C307	CAP_P	52A2															
C68	CAP	37D6	C146	CAP	19D1	C227	CAP	24C3	C308	CAP_P	52A3															
C69	CAP	20B5	C147	CAP	19C3	C228	CAP	24C3	C309	CAP_P	52A3															
C70	CAP	20B6	C148	CAP	19C4	C229	CAP	30C6	C310	CAP_P	52A2															
C71	CAP_P	50B1	C149	CAP	19C4	C230	CAP	15D7	C311	CAP	16C1															
C72	CAP	20H7	C150	CAP	19C4	C231	CAP	13C8	C312	CAP	31D3															
C73	CAP	20H7	C151	CAP	19C3	C232	CAP	13C8	C313	CAP	16C3															
																								APPLE COMPUTER INC. SIZE D DRAWING NUMBER 051-6423 REV. D SCALE NONE SHT 67 OF 74		
			C152	CAP	25B5	C233	CAP	30D6	C314	CAP	31D3															
			C153	CAP	24C7	C234	CAP	30D7	C315	CAP	52A8															
			C154	CAP	10B6	C235	CAP_P	51B3	C316	CAP	52A8															

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D	C317	CAP	52B8				C399	CAP	38A7				C480	CAP_P	42B4				C561	CAP	44B6		
	C318	CAP	52B8				C400	CAP	37C1				C481	CAP	38B3				C562	CAP	17C2		
	C319	CAP	16A8				C401	CAP	37D1				C482	CAP	38B3				C563	CAP	50B6		
	C320	CAP	16C2				C402	CAP	44C3				C483	CAP	38B3				C564	CAP	50C2		
	C321	CAP	52B7				C403	CAP	35D4				C484	CAP	38B5				C565	CAP	22B5		
	C322	CAP	52A7				C404	CAP	35D4				C485	CAP	38B1				C566	CAP	22B7		
	C323	CAP_P	51B3				C405	CAP	35D3				C486	CAP	23B6				C567	CAP	22B7		
	C324	CAP_P	51B3				C406	CAP	38B6				C487	CAP	23D7				C568	CAP	22B8		
	C325	CAP	49A4				C407	CAP	38A6				C488	CAP	23B7				C569	CAP	22B8		
	C326	CAP_P	52B2				C408	CAP	43B3				C489	CAP	23B5				C570	CAP	34C8		
C	C327	CAP_P	52B2				C409	CAP	27B3				C490	CAP	38B6				C571	CAP	44B5		
	C328	CAP_P	52B2				C410	CAP	38D4				C491	CAP	44B4				C572	CAP	50B7		
	C329	CAP	51B3				C411	CAP	38A7				C492	CAP	23D2				C573	CAP	34C8		
	C330	CAP_P	51B3				C412	CAP	43C3				C493	CAP	23D7				C574	CAP	34C8		
	C331	CAP_P	51B2				C413	CAP	35D3				C494	CAP	23D7				C575	CAP	44B7		
	C332	CAP_P	51B2				C414	CAP	27B4				C495	CAP	38B2				C576	CAP	50C3		
	C333	CAP	52B6				C415	CAP	27A4				C496	CAP	38B2				C577	CAP	44B5		
	C334	CAP_P	47C1				C416	CAP	27A4				C497	CAP	44B4				C578	CAP	41C5		
	C335	CAP_P	47C2				C417	CAP	35B3				C498	CAP	44B4				C579	CAP	41C5		
	C336	CAP_P	47C2				C418	CAP	38D4				C499	CAP	43D6				C580	CAP_P	41B2		
B	C337	CAP_P	47C2				C419	CAP	44B3				C500	CAP	38C7				C581	CAP	41B2		
	C338	CAP_P	52D7				C420	CAP	43D3				C501	CAP	44C5				C582	CAP	44C3		
	C339	CAP_P	52D7				C421	CAP	27D4				C502	CAP	44C5				C583	CAP	48A5		
	C340	CAP_P	47C2				C422	CAP	27C4				C503	CAP	43C6				C584	CAP	41D3		
	C341	CAP	52B7				C423	CAP	27C3				C504	CAP	43B7				C585	CAP	50B5		
	C342	CAP_P	51C3				C424	CAP	38B6				C505	CAP	42D3				C586	CAP	50A4		
	C343	CAP_P	47D3				C425	CAP	44B3				C506	CAP	42C3				C587	CAP	41B2		
	C344	CAP	52B4				C426	CAP	44B2				C507	CAP	42B3				C588	CAP	41B5		
	C345	CAP	4B1				C427	CAP	43B3				C508	CAP	43C6				C589	CAP	50C5		
	C346	CAP_P	47B1				C428	CAP	38B7				C509	CAP	20B8				C590	CAP	53C4		
A	C347	CAP_P	47B2				C429	CAP	44B2				C510	CAP	20B8				C591	CAP_P	41D2		
	C348	CAP_P	47B2				C430	CAP	44B2				C511	CAP	34D5				C592	CAP	41B5		
	C349	CAP_P	52D7				C431	CAP	42B6				C512	CAP_P	43B7				C593	CAP	17C1		
	C350	CAP	9D4				C432	CAP	27B3				C513	CAP	42B3				C594	CAP	41B5		
	C351	CAP_P	51C3				C433	CAP	35C3				C514	CAP	42C3				C595	CAP	41B5		
	C352	CAP	4B1				C434	CAP	37D1				C515	CAP	34C8				C596	CAP	50C6		
	C353	CAP	9D4				C435	CAP	44B2				C516	CAP	34D8				C597	CAP	41C4		
	C354	CAP_P	52D4				C436	CAP	43A4				C517	CAP	34C8				C598	CAP	9B2		
	C355	CAP_P	52D4				C437	CAP	42C6				C518	CAP	43B7				C599	CAP	41D4		
	C356	CAP_P	52D4				C438	CAP	37D2				C519	CAP	34D8				C600	CAP_P	41D4		
A	C357	CAP_P	47D3				C439	CAP	38B4				C520	CAP	22D2				C601	CAP	31A5		
	C358	CAP	51C4				C440	CAP	38B4				C521	CAP	22D2				C602	CAP	17C1		

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D	C642	CAP	45C7	C723	CAP	13A6	C806	CAP	13C2	C887	CAP	13D7	D										
	C643	CAP	13C2	C724	CAP	13A5	C807	CAP	13B2	C888	CAP	13D6											
	C644	CAP	13B2	C725	CAP	13B7	C808	CAP	13D2	C889	CAP	13D6											
	C645	CAP	13B1	C726	CAP	17C2	C809	CAP	13C3	C890	CAP	13D7											
	C646	CAP	13B1	C727	CAP	13D4	C810	CAP	13D2	C891	CAP	13D6											
	C647	CAP	13C7	C728	CAP	13C3	C811	CAP	13D2	C892	CAP	13C4											
	C648	CAP	13B7	C729	CAP	13B3	C812	CAP	13C2	C893	CAP	13C1											
	C649	CAP	13C6	C730	CAP	13D3	C813	CAP	13A5	C894	CAP	13C2											
	C650	CAP	13C7	C731	CAP	13D1	C814	CAP	13A6	C895	CAP	13D5											
	C651	CAP	13C5	C732	CAP	13D1	C815	CAP	13B6	C896	CAP	13D5											
C	C652	CAP	26B2	C733	CAP	13C7	C816	CAP	13A6	C897	CAP	13D6	C										
	C653	CAP	53B4	C734	CAP	13D2	C817	CAP	13B6	C898	CAP	13D5											
	C654	CAP	53B4	C735	CAP	13A7	C818	CAP	13A5	C899	CAP	13D5											
	C655	CAP	13C4	C736	CAP	13A5	C819	CAP	17C2	C900	CAP	13D7											
	C656	CAP	13C2	C737	CAP	10D6	C820	CAP	30C3	C901	CAP	13D7											
	C657	CAP	13B3	C738	CAP	10D7	C821	CAP	13C1	C902	CAP	13C7											
	C658	CAP	13B2	C739	CAP	10D6	C822	CAP	13C1	C903	CAP	13B2											
	C659	CAP	13B3	C740	CAP	10B5	C823	CAP	13D7	C904	CAP	13B6											
	C660	CAP	13B1	C741	CAP	17C2	C824	CAP	13D6	C905	CAP	10B7											
	C661	CAP	13B7	C742	CAP	53C6	C825	CAP	13A7	C906	CAP	10B7											
B	C662	CAP	13B7	C743	CAP	13B3	C826	CAP	13A7	C907	CAP	10B8	B										
	C663	CAP	13B6	C744	CAP	13B1	C827	CAP	13B5	C908	CAP	15C2											
	C664	CAP	13C5	C745	CAP	13C3	C828	CAP	13C1	C909	CAP	15C2											
	C665	CAP	13C6	C746	CAP	13D3	C829	CAP	13C2	C910	CAP	15C1											
	C666	CAP	13C5	C747	CAP	13D1	C830	CAP	13C3	C911	CAP	39C2											
	C667	CAP	13C6	C748	CAP	13D2	C831	CAP	13D3	C912	CAP	10B8											
	C668	CAP	13A5	C749	CAP	13D3	C832	CAP	13D7	C913	CAP	10B7											
	C669	CAP	15C5	C750	CAP	13A6	C833	CAP	13D3	C914	CAP	17C2											
	C670	CAP	15C6	C751	CAP	13A7	C834	CAP	13D6	C915	CAP	53D7											
	C671	CAP	15C5	C752	CAP	13A6	C835	CAP	13B3	C916	CAP	14A8											
A	C672	CAP	17C1	C753	CAP	13A6	C836	CAP	13B6	C917	CAP	39C5	A										
	C673	CAP	13B2	C754	CAP	13A5	C837	CAP	13B7	C918	CAP	53D6											
	C674	CAP	13B6	C755	CAP	13A6	C838	CAP	13A7	C919	CAP	40C7											
	C675	CAP	45B7	C756	CAP	10D5	C839	CAP	13B6	C920	CAP	40C1											
	C676	CAP	13B3	C757	CAP	10D6	C840	CAP	30A6	C921	CAP	40C5											
	C677	CAP	13B1	C758	CAP	10D4	C841	CAP	13D4	C922	CAP	17C1											
	C678	CAP	13B3	C759	CAP	46D5	C842	CAP	13D6	C923	CAP	40B5											
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	C680	CAP	13C3	C761	CAP	13B3	C844	CAP	13D6	C925	CAP	17C2											
	C681	CAP	13B3	C762	CAP	13C1	C845	CAP	13D6	C926	CAP	52C1											
C682	CAP	13B3	C763	CAP	13D2	C846	CAP	13D7	C927	CAP	52B1												
C683	CAP	13C6	C764	CAP	13D3	C847	CAP	14A7	C928	CAP_P	49B4												
C684	CAP	13C7	C765	CAP	13D3	C848	CAP	32D5	C929	CAP	16A7												
C685	CAP	13C6	C766	CAP	13D1	C849	CAP	13C6	C930	CAP	52B8												
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C691	CAP	13C2	C772	CAP	10C6	C855	CAP	13B5	C936	CAP	17C2												
C692	CAP	30D6	C773	CAP	53C5	C856	CAP	13A7	C937	CAP	52C7												
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C697	CAP	13C7	C779	CAP	13D1	C861	CAP	46A1	C942	CAP	8C3												
C698	CAP	13B5	C780	CAP	13D2	C862	CAP	46B7	C943	CAP	17A3												
C699	CAP	13A7	C781	CAP	13A5	C863	CAP	13C2	C944	CAP	47D3												
C700	CAP	13B7	C782	CAP	13A7	C864	CAP	17C2	C945	CAP	17A2												
C701	CAP	46B2	C783	CAP	46D5	C865	CAP	13B1	C946	CAP	8A6												
C702	CAP	13D4	C784	CAP	30A6	C866	CAP	13C3	C947	CAP_P	47C1												
C703	CAP	30D6	C785	CAP	30C3	C867	CAP	13C3	C948	CAP	51B4												
C704	CAP	30D5	C787	CAP	13D3	C868	CAP	13C2	C949	CAP	47C5												
C705	CAP	46A6	C788	CAP	13D3	C869	CAP	13D5	C950	CAP	47D4												
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C707	CAP	13B3	C790	CAP	13D1	C871	CAP	13D6	C952	CAP	47D4												
C708	CAP	13B3	C791	CAP	13A6	C872	CAP	13D5	C953	CAP	47D4												
C709	CAP	13C6	C792	CAP	13A6	C873	CAP	13D5	C954	CAP	4C2												
C710	CAP	13C6	C793	CAP	13B6	C874	CAP	13D5	C955	CAP	52D4												
C711	CAP	13B6	C794	CAP	13B5	C875	CAP	13B3	C956	CAP	52D5												
C712	CAP	13B1	C795	CAP	30C3	C876	CAP	13B5	C957	CAP_P	47B1												
C713	CAP	30D5	C796	CAP	13C3	C877	CAP	13B7	C958	CAP	9D4												
C714	CAP	13A5	C797	CAP	13D3	C878	CAP	13B7	C959	CAP	51C4												
C715	CAP	46B2	C798	CAP	13D1	C879	CAP	13B2	C960	CAP	47B2												
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C718	CAP	13B1	C801	CAP	13A7	C882	CAP	13B1	C963	CAP	47D4												
C719	CAP	13D2	C802	CAP	13A5	C883	CAP	13B2	C964	CAP	47D4												
C720	CAP	13C3	C803	CAP	13A6	C884	CAP	13D6	C965	CAP	47D4												
C721	CAP	13D3	C804	CAP	10D3	C885	CAP	13D7	C966	CAP	47D4												
C722	CAP	13A7	C805	CAP	13B2	C886	CAP	13D5	C967	CAP	9C1												
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8			7			6			5			4			3			2			1								
D	C968 CAP 51C4			C1049 CAP 47B7			D39 DIODE_SCHOT 52B4			L21 IND_3P 51B4			D			L22 IND_3P 52A3			C			L23 IND 52C3							
	C969 CAP 9D4			C1050 CAP 47C7			D40 DIODE_SCHOT 52A4			L24 IND 47B3						L25 IND 44C3													
	C970 CAP 52D5			C1051 CAP 47B6			D41 DIODE_SCHOT 47C4			L26 IND 44B1						L27 IND 43C3													
	C971 CAP 52D5			C1052 CAP 47C8			D43 DIODE_SCHOT 47B3			L28 IND 35C5						L29 IND 35C5													
	C972 CAP 9C2			C1053 CAP 48A8			D44 DIODE_SCHOT 47D2			L30 IND 35B5						L31 IND 35B5													
	C973 CAP 9C1			C1054 CAP 9B2			D45 DIODE_SCHOT 47D6			L32 IND 38B6						L33 IND 38B6													
	C974 CAP 9D1			C1055 CAP 9B2			D46 DIODE_SCHOT 51B2			L34 IND 43D3						L35 IND 43B3													
	C975 CAP 51C4			C1056 CAP 9B1			D4901 DIODE_SCHOT 49B2			L36 IND 42B6						L37 IND 35D5													
	C976 CAP 9C2			C1057 CAP 9B1			DS1 LED 37A2			L38 IND 35D5						L39 IND 43A3													
	C977 CAP 9D1			C1058 CAP 9B1			DS2 LED 37A1			L40 IND 44B1						L41 IND 44C3													
C	C978 CAP 52D5			C1101 CAP 11D3			DS3 LED 37A1			L42 IND 43B3						L43 IND 43A4													
	C979 CAP 52D5			C1601 CAP 16D7			DS4 LED 32A3			L44 IND 43C3						L45 IND 43D3													
	C980 CAP 9C2			C1602 CAP 16D3			DS5 LED 53A5			L46 IND 42C6						L47 IND 43B3													
	C981 CAP 9C2			C1603 CAP 16D3			DS6 LED 53A6			L48 IND 42C6						L49 IND 42B5													
	C982 CAP 9D2			C1701 CAP 17D7			DS7 LED 40B2			L50 IND 42C5						L51 IND 42D6													
	C983 CAP 9D3			C1702 CAP 17D7			DS8 LED 40B6			L52 IND 42C5						L53 IND 42D5													
	C984 CAP 51C3			C1801 CAP 18D5			DS9 LED 52D6			L54 IND 44B5						L55 IND 44B5													
	C985 CAP 52D4			C1802 CAP 18A7			DS10 LED 53A3			L56 IND 46A1						L57 IND 46A1													
	C986 CAP 52D5			C1901 CAP 9A6			F1 FUSE 35A7			L58 IND 34D6						L59 IND 45B5													
	C987 CAP 9D2			C1902 CAP 9A5			F2 FUSE 38D5			L60 IND 45C5						L61 IND 44D7													
B	C988 CAP 9D3			C1903 CAP 9A5			F3 FUSE 38D5			L62 IND 44D7						L63 IND 41D4													
	C989 CAP 51C3			C1904 CAP 9A5			FL1 FILTER_12P 44B3			L64 IND 26A5						L65 IND 31A7													
	C990 CAP 9C1			C1905 CAP 9A5			FL2 FILTER_LC 27C6			L66 IND 31A3						L67 FILTER_4P 26A5													
	C991 CAP 9D1			C1910 CAP 9A5			FL3 FILTER_LC 27C6			L68 IND 24C2						L69 IND 31A3													
	C992 CAP 9D3			C1911 CAP 9B2			FL4 FILTER_LC 27B6			L70 IND 31A7						L71 FILTER_4P 26B5													
	C993 CAP 9C1			C1912 CAP 9B2			J1 CON_RJ45 37C1			L72 IND 31A7						L73 IND 31A3													
	C994 CAP 9C2			C1913 CAP 9B1			J2 CON_FWVERT_SKT 38C1			L74 FILTER_4P 26C5						L75 IND 31A7													
	C995 CAP 9D2			C1914 CAP 9B1			J3 CON_F8RT_S_TH1 44B1			L76 FILTER_4P 26D5						L77 IND 31B7													
	C996 CAP 9A6			C2201 CAP 22C8			J4 CON_F4RT_USB_UPRIGHT 35C3			L78 IND 31A3						L79 IND 45C7													
	C997 CAP 9A5			C2202 CAP 22C4			J5 CON_FWVERT_SKT 38D1			L80 IND 31B3						L81 IND 45B7													
A	C998 CAP 9D2			C2301 CAP 23C8			J6 CON_F4RT_USB_UPRIGHT 35B3			L82 IND 26D5						L83 IND 45B7													
	C999 CAP 9D1			C2302 CAP 23C4			J7 CON_F4RT_USB_UPRIGHT 35D3			L84 IND 52D7						L85 IND 52D7													
	C1000 CAP 51B6			C2501 CAP_P 25A8			J8 CON_F4RT_S4MT_TH1 43C1			L86 IND 52D7						L2401 IND 24C7													
	C1001 CAP 9A5			C3001 CAP 30C5			J9 CON_F14RT_D4MT_TH1 27C5			L2501 IND 25A6						Q1 TRA_2N7002 53C2													
	C1002 CAP 9A6			C3002 CAP 30D5			J10 CON_F4RT_S4MT_TH1 42C7			Q2 TRA_2N7002 44A7						Q3 TRA_2N7002 44B7													
	C1003 CAP 9D1			C3003 CAP 30D5			J11 CON_F184ST_DDRDIMM 17D5			Q4 TRA_2N7002 44C4						Q5 TRA_2N7002 43A5													
	C1004 CAP 51B6			C3004 CAP 30D4			J12 CON_F21ST_D2MT_SM 26C3			Q6 TRA_2N7002 53C4						Q7 TRA_IRLR8203 50B4													
	C1005 CAP 51C4			C3005 CAP 30D4			J13 CON_M40SM_635 31D6			Q8 TRA_IRLR8503 50B4						Q9 TRA_IRLR8503 50C4													
	C1006 CAP 47C2			C3201 CAP 32D4			J14 CON_M16ST_D_TH 31B5			Q10 TRA_2N7002 24B7						Q11 TRA_FDC602P 53C2													
	C1007 CAP 9C2			C3901 CAP 39D1			J15 CON_M40ST_NC20 40D6			Q12 TRA_2N3904 46D7						Q13 TRA_2N7002 53B2													
A	C1008 CAP 9B5			C4081 CAP 48C7			J16 CON_M40ST_NC20 40D2			Q14 TRA_2N7002 53C7																			
	C1009 CAP 9A6			C4301 CAP 43A6			J17 CON_M16ST_MICROFIT 52D8																						
	C1010 CAP 9B5			C4502 CAP_P 45C5																									

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D	Q15	TRA_2N7002	53B7	R43	RES	35D6	R124	RES	25C4	R205	RES	18A8	C										
	Q16	TRA_2N7002	53A7	R44	RES	35D6	R125	RES	28D5	R206	RES	24D7		B									
	Q17	TRA_2N7002	53B6	R45	RES	35C6	R126	RES	28C6	R207	RES	25D2			A								
	Q18	TRA_2N7002	53A7	R46	RES	35C6	R127	RES	34B8	R208	RES	25D2				A							
	Q19	TRA_2N7002	53A6	R47	RES	35B6	R128	RES	34B8	R209	RES	16B3					A						
	Q20	TRA_FDC602P	53D7	R48	RES	35B6	R129	RES	34D3	R210	RES	19B8						A					
	Q21	TRA_2N7002	52C3	R49	RES	20B7	R130	RES	20D2	R211	RES	18C7							A				
	Q22	TRA_2N7002	52D3	R50	RES	20B7	R131	RES	20D3	R212	RES	32B3								A			
	Q23	TRA_2N7002	52C4	R51	RES	44C3	R132	RES	21D2	R213	RES	25C4									A		
	Q24	TRA_2N7002	52C3	R52	RES	20C5	R133	RES	24D3	R214	RES	29B5										A	
Q25	TRA_IRF7811W	49B5	R53	RES	21A4	R134	RES	28B8	R215	RES	18C7	A											
Q26	TRA_IRF7805	49B5	R54	RES	20D3	R135	RES	28A8	R216	RES	18B7		A										
Q27	TRA_IRLR8203	51B4	R55	RES	21A4	R136	RES	28C5	R217	RES	18D3			A									
Q28	TRA_IRLR8503	51B4	R56	RES	20D2	R137	RES	44B7	R218	RES	32D6				A								
Q29	TRA_2N7002	47D8	R57	RES	21A4	R138	RES	32B3	R219	RES	32D6					A							
Q30	TRA_2N3904	38C7	R58	RES	20D3	R139	RES	32B4	R220	RES	18C7						A						
Q31	TRA_2N7002	43A8	R59	RES	20C2	R140	RES	21A7	R221	RES	18D1							A					
Q32	TRA_2N7002	41C7	R60	RES	21A4	R141	RES	25B4	R222	RES	18D3								A				
Q33	TRA_2N3904	45B2	R61	RES	20A3	R142	RES	28C5	R223	RES	18D1									A			
Q34	TRA_IRLR8203	50A4	R62	RES	20A2	R143	RES	20G2	R224	RES	18C7										A		
Q35	TRA_2N7002	42D4	R63	RES	21A4	R144	RES	25B2	R225	RES	18D7	A											
Q37	TRA_FDC602P	53C6	R64	RES	20D3	R145	RES	24C3	R226	RES	18C7		A										
Q38	TRA_2N7002	53B6	R65	RES	21A4	R146	RES	21A7	R227	RES	32C6			A									
Q39	TRA_2N3904	52C7	R66	RES	20D2	R147	RES	21A7	R228	RES	30C7				A								
Q40	TRA_IRLR8203	47C4	R67	RES	20D2	R148	RES	21C2	R229	RES	36C1					A							
Q41	TRA_2N7002	8D5	R68	RES	21A4	R149	RES	25B5	R230	RES	32B6						A						
Q42	TRA_2N7002	8C6	R69	RES	21A4	R150	RES	50C5	R231	RES	30C8							A					
Q43	TRA_IRLR8203	47C4	R70	RES	20D3	R151	RES	22A4	R232	RES	30C6								A				
Q44	TRA_IRLR8503	47C4	R71	RES	20D6	R152	RES	22A5	R233	RES	18B3									A			
Q45	TRA_IRF7811W	52B4	R72	RES	19A5	R153	RES	21A7	R234	RES	18D1										A		
Q46	TRA_IRF7805	52C4	R73	RES	28C2	R154	RES	20G3	R235	RES	29B5	A											
Q47	TRA_IRLR8203	52A4	R74	RES	28D2	R155	RES	20G2	R236	RES	29C5		A										
Q48	TRA_IRLR8503	52A4	R75	RES	37B8	R156	RES	21C2	R237	RES	25C4			A									
Q49	TRA_IRLR8203	47B4	R76	RES	37B8	R157	RES	25B2	R238	RES	29C6				A								
Q50	TRA_IRLR8503	47B4	R77	RES	28D2	R158	RES	21D2	R239	RES	29B6					A							
Q51	TRA_IRLR8203	47B4	R78	RES	34A7	R159	RES	21A7	R240	RES	29B5						A						
Q52	TRA_2N7002	48A7	R79	RES	28C2	R160	RES	19A5	R241	RES	29C5							A					
Q53	TRA_FDC602P	48B7	R80	RES	19A5	R161	RES	18D1	R242	RES	29B6								A				
Q4801	TRA_FDC602P	48D7	R81	RES	21A7	R162	RES	43A5	R243	RES	29C6									A			
R1	RES	38D4	R82	RES	28B3	R163	RES	22A4	R244	RES	29C4										A		
R2	RES	27B4	R83	RES	20A5	R164	RES	20G3	R245	RES	29B4	A											
R3	RES	27B4	R84	RES	20G2	R165	RES	37C4	R246	RES	25B7		A										
R4	RES	23A4	R85	RES	25D6	R166	RES	22A4	R247	RES	29B7			A									
R5	RES	23A4	R86	RES	25D6	R167	RES	21A7	R248	RES	29C7				A								
R6	RES	23A4	R87	RES	28B3	R168	RES	19A7	R249	RES	29B7					A							
R7	RES	27B5	R88	RES	20A5	R169	RES	19A4	R250	RES	29C7						A						
R8	RES	38C4	R89	RES	34D3	R170	RES	24B5	R251	RES	53B3							A					
R9	RES	38C4	R90	RES	20G3	R171	RES	24A6	R252	RES	32C7								A				
R10	RES	38C4	R91	RES	25D5	R172	RES	24B6	R253	RES	32C7									A			
R11	RES	38C4	R92	RES	25C5	R173	RES	24C7	R254	RES	36B4										A		
R12	RES	38C3	R93	RES	28A3	R174	RES	24B6	R255	RES	32C6	A											
R13	RES	38C3	R94	RES	28A3	R175	RES	42D4	R256	RES	32C7		A										
R14	RES	38C3	R95	RES	20A5	R176	RES	22A4	R257	RES	36B3			A									
R15	RES	38C3	R96	RES	37B1	R177	RES	19A5	R258	RES	39D5				A								
R16	RES	38B3	R97	RES	21A7	R178	RES	24C7	R259	RES	32C7					A							
R17	RES	23A4	R98	RES	25D5	R179	RES	24C7	R260	RES	14D4						A						
R18	RES	23A5	R99	RES	25D6	R180	RES	42D5	R261	RES	14D4							A					
R19	RES	27B5	R100	RES	28A7	R181	RES	43A4	R262	RES	32C7								A				
R20	RES	21C2	R101	RES	21A7	R182	RES	43A4	R263	RES	39D4									A			
R21	RES	53D2	R102	RES	25C4	R183	RES	24C3	R264	RES	30D1										A		
R22	RES	53C3	R103	RES	21B2	R184	RES	25C5	R265	RES	40B4	A											
R23	RES	53D2	R104	RES	21B2	R185	RES	24B6	R266	RES	39C6		A										
R24	RES	21A3	R105	RES	21C2	R186	RES	21A6	R267	RES	30B1			A									
R25	RES	21A3	R106	RES	21C2	R187	RES	21A6	R268	RES	40B4				A								
R26	RES	37B4	R107	RES	25C4	R188	RES	24B3	R269	RES	36C4					A							
R27	RES	37C4	R108	RES	25C6	R189	RES	16A3	R270	RES	39D2						A						
R28	RES	37B7	R109	RES	28B7	R190	RES	16B2	R271	RES	30B3							A					
R29	RES	37B7	R110	RES	28D6	R191	RES	24B6	R272	RES	30B3								A				
R30	RES	37C3	R111	RES	20G2	R192	RES	19A3	R273	RES	39D2									A			
R31	RES	21A3	R112	RES	25D6	R193	RES	19A3	R274	RES	30C7										A		
R32	RES	21A3	R113	RES	25C4	R194	RES	24B6	R275	RES	36C4	A											
R33	RES	37C4	R114	RES	25C4	R195	RES	19A3	R276	RES	30C8		A										
R34	RES	37C7	R115	RES	25C4	R196	RES	19A3	R277	RES	53B2			A									
R35	RES	38D6	R116	RES	28C6	R197	RES	18A8	R278	RES	53B3				A								
R36	RES	20B8	R117	RES	20G3	R198	RES	19C1	R279	RES	53B2					A							
R37	RES	37C4	R118	RES	24D3	R199	RES	24D6	R280	RES	46D8						A						
R38	RES	37C7	R119	RES	25C4	R200	RES	24C6	R281	RES	46D7							A					
R39	RES	38D6	R120	RES	28C5	R201	RES	16B3	R282	RES	46D7								A				
R40	RES	37C3	R121	RES	28D6	R202	RES	16B2	R283	RES	53B3									A			
R41	RES	20B8	R122	RES	21A7	R203	RES	18C8	R284	RES	53B3										A		
R42	RES	37B4	R123	RES	21D2	R204	RES	18B8	R285	RES	33C1	A											
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D	R286	RES	14A3	R367	RES	11C5	R449	RES	28A6	R530	RES	53C5	NOTICE OF PROPRIETARY PROPERTY THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING I TO MAINTAIN THE DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART	D									
	R287	RES	46D5	R368	RES	11C6	R450	RES	35B5	R531	RES	41C2											
	R288	RES	14D4	R369	RES	51C6	R451	RES	35B5	R532	RES	41C4											
	R289	RES	30C8	R370	RES	52D6	R452	RES	35C5	R533	RES	28D6											
	R290	RES	46D8	R371	RES	51B6	R453	RES	35C5	R534	RES	28C6											
	R291	RES	30C7	R372	RES	48D4	R454	RES	35D5	R535	RES	29C5											
	R292	RES	30C7	R374	RES	6C6	R455	RES	35D5	R536	RES	29B5											
	R293	RES	33C4	R375	RES	6C6	R456	RES	35B7	R537	RES	29C4											
	R294	RES	30C1	R376	RES	6C6	R457	RES	35B7	R538	RES	29B4											
	R295	RES	49B3	R377	RES	6C6	R458	RES	35C7	R539	RES	29B6											
R296	RES	49B7	R378	RES	6C7	R459	RES	35C7	R540	RES	26A6												
R297	RES	46D6	R379	RES	6C7	R460	RES	35D7	R541	RES	28A6												
R298	RES	46B1	R380	RES	6C7	R461	RES	35D7	R542	RES	28B6												
R299	RES	49B3	R381	RES	6C7	R462	RES	42B3	R543	RES	29C6												
R300	RES	53A5	R382	RES	6C8	R463	RES	20B8	R544	RES	29B7												
R301	RES	49B5	R383	RES	6C8	R464	RES	34C3	R545	RES	18A8												
R302	RES	53A6	R384	RES	47C5	R465	RES	34C3	R546	RES	26A5												
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R306	RES	30B8	R388	RES	47B6	R469	RES	34C3	R550	RES	26A5												
R307	RES	36D7	R389	RES	47B6	R470	RES	34B3	R551	RES	26A5												
R308	RES	9C3	R390	RES	37C1	R471	RES	44B4	R552	RES	28C8												
R309	RES	49B7	R391	RES	37C2	R472	RES	43C7	R553	RES	28D8												
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R311	RES	4B8	R393	RES	37C2	R474	RES	42C3	R555	RES	24C3												
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R313	RES	9B4	R395	RES	38B7	R476	RES	34B3	R557	RES	24C2												
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R316	RES	49B7	R398	RES	35A6	R479	RES	21A6	R560	RES	28C7												
R317	RES	49C5	R399	RES	28B6	R480	RES	34C3	R561	RES	28D7												
R318	RES	49C6	R400	RES	43C4	R481	RES	41B6	R562	RES	24B6												
R319	RES	11A4	R401	RES	28B5	R482	RES	41C6	R563	RES	29C5												
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R321	RES	11A4	R403	RES	21B2	R484	RES	34A7	R565	RES	30D5												
R322	RES	52C3	R404	RES	38D8	R485	RES	37B2	R566	RES	32D6												
R323	RES	52D4	R405	RES	44B4	R486	RES	37B2	R567	RES	26B5												
R324	RES	10C6	R406	RES	43D4	R487	RES	45B5	R568	RES	26B5												
R325	RES	10C6	R407	RES	42C5	R488	RES	45C4	R569	RES	24B3												
R326	RES	10C6	R408	RES	28A5	R489	RES	45C3	R570	RES	26B6												
R327	RES	10C6	R409	RES	38D7	R490	RES	34B3	R571	RES	30D8												
R328	RES	31D3	R410	RES	42B5	R491	RES	37B2	R572	RES	30D8												
R329	RES	31D3	R411	RES	38C5	R492	RES	37B1	R573	RES	30C6												
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R336	RES	52C5	R418	RES	38C6	R499	RES	50C2	R580	RES	18B3												
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R339	RES	11B5	R421	RES	45C5	R502	RES	41D7	R583	RES	28D3												
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R346	RES	7C7	R428	RES	21A3	R509	RES	21A7	R591	RES	26C6												
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R348	RES	7C7	R430	RES	38B6	R511	RES	32A3	R593	RES	26C5												
R349	RES	7B7	R431	RES	38C7	R512	RES	44B8	R594	RES	26D5												
R350	RES	7A7	R432	RES	38C7	R513	RES	44C7	R595	RES	28D5												
R351	RES	11A5	R433	RES	38B6	R514	RES	44B7	R596	RES	28D5												
R352	RES	11B6	R434	RES	42C4	R515	RES	50B5	R597	RES	28C3												
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R354	RES	11A6	R436	RES	42B4	R517	RES	41C5	R600	RES	26D6												
R355	RES	11B7	R437	RES	42B4	R518	RES	48B5	R601	RES	53B3												
R356	RES	11C5	R438	RES	38C8	R519	RES	48A5	R602	RES	25A7												
R357	RES	11C6	R439	RES	28B4	R520	RES	44B8	R603	RES	26D5												
R358	RES	11A7	R440	RES	43A8	R521	RES	41C5	R604	RES	46A3												
R359	RES	11A7	R441	RES	43D6	R522	RES	44C4	R605	RES	46A5												
R360	RES	11B6	R442	RES	42C4	R523	RES	50C6	R606	RES	46A5												
R361	RES	47B3	R443	RES	28A4	R524	RES	41B4	R607	RES	53A5												
R362	RES	47B3	R444	RES	43C6	R525	RES	44D4	R608	RES	53A4												
R363	RES	11C6	R445	RES	28B6	R526	RES	44C4	R609	RES	53A4												
R364	RES	11C6	R446	RES	43C6	R527	RES	50B7	R610	RES	48B4												
R365	RES	11C5	R447	RES	43D6	R528	RES	41C4	R611	RES	30A6												
R366	RES	11A7	R448	RES	42B3	R529	RES	50C6	R612	RES	53A4												
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8			7			6			5			4			3			2			1		

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D	R613	RES	31A8	R694	RES	30B3	R775	RES	40C7	R856	RES	7C5	D 										

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D	R937 RES 9B3			R938 RES 9B3			RP46 RPAK4P 19B7			U1 SWI_TPS2023 35A6			XW30 SHORT8L25_WITH_ALTS 47A3			A			NOTICE OF PROPRIETARY PROPERTY THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING I TO MAINTAIN THE DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART								
	R939 RES 9A3			R940 RES 9A3			RP47 RPAK4P 19A7			U2 SHNTREG_TLV431A 23A5			XW31 SHORT8L25_WITH_ALTS 47B3														
	R941 RES 39D1			R942 RES 39D2			RP48 RPAK4P 18B3			U3 VREG_LP2951 38D7			XW32 SHORT 41B7														
	R943 RES 39D2			R944 RES 39D2			RP49 RPAK4P 18B1 18B1 18C1			U4 FW802A 38C5			XW33 SHORT 43B7														
	R945 RES 48B7			R946 RES 48B7			RP50 RPAK4P 18B1			U5 SDRAM_DDR_4MX32 23D6 23D7			XW34 SHORT 41A7														
	R947 RES 48B7			R948 RES 33D4			RP51 RPAK4P 14B1 14C1			U6 OPAMP_TLV2362 42B3 42C3			XW35 SHORT 44A7														
	R949 RES 33D4			R950 RES 33D3			RP52 RPAK4P 14A1 14B1			U7 SHNTREG_TLV431A 20B8			XW36 SHORT 44A7														
	R951 RES 33C2			R952 RES 48B7			RP53 RPAK4P 10C2			U8 AMP_TPA6112A2 43D5			XW37 SHORT 41B7														
	R3001 RES 30A2			R3002 RES 30A2			RP54 RPAK4P 33B7			U10 AMP_TA2024 44B6			XW38 SHORT 41A7														
	R3801 RES 38B1			R4301 RES 43A7			RP55 RPAK4P 10C2			U11 UPD720101_FBG4 34C5			XW39 SHORT8L25_WITH_ALTS 50B5														
C	R4401 RES 44A8			R4501 RES 45C6			RP56 RPAK4P 33B7			U12 SDRAM_DDR_4MX32 22D6 22D7			XW40 SHORT 44D7														
	R4502 RES 45B6			R4503 RES 45B6			RP57 RPAK4P 10C2			U13 OPAMP_TLV2362 45B3			XW41 SHORT8L25_WITH_ALTS 51B5														
	R4504 RES 45C6			R4505 RES 45B6			RP58 RPAK4P 33C7			U14 VREG_LP2951 41D6			XW42 SHORT 30A5														
	R4506 RES 45B6			R4507 RES 45D5			RP59 RPAK4P 33C7			U15 TAS3004 41C3			XW43 SHORT 30A5														
	R4701 RES 47B6			R4702 RES 47B6			RP60 RPAK4P 30C2			U16 SHNTREG_TLV431A 22A5			XW44 SHORT 30A5														
	R4703 RES 47B3			R4704 RES 47B3			RP61 RPAK4P 33B7			U17 SHNTREG_TLV431A 16B3			XW45 SHORT 30A4														
	R4705 RES 47B3			R4706 RES 47D3			RP62 RPAK4P 14B3 14C3			U18 CBTV4020 15D7			XW46 SHORT 30A4														
	R4707 RES 47D3			R4708 RES 47B5			RP63 RPAK4P 32B8			U19 VREG_LT1962 30D7			XW47 SHORT 11A2														
	R4801 RES 48C7			R5301 RES 53C1			RP64 RPAK4P 33B7			U20 VREG_LM1117 25A8			XW48 SHORT 18A5														
	RP1 RPAK4P 37C8			RP2 RPAK4P 38C7			RP65 RPAK4P 36C4			U21 VDET_MC33465N_22ATR 46A4			XW49 SHORT 32B5														
B	RP3 RPAK4P 38B7			RP4 RPAK4P 21B5 21C5			RP66 RPAK4P 30A8			U22 CBTV4020 15B6			Y1 CRYSTAL 38C6														
	RP5 RPAK4P 21B5			RP6 RPAK4P 21B5			RP67 RPAK4P 33B7			U23 MAX6328 46A5			Y2 CRYSTAL_4PIN 24A3														
	RP7 RPAK4P 21B5			RP8 RPAK4P 20A2 20A3 20B2			RP68 RPAK4P 14C3 14D3			U24 CLK_DR_PCK2059 10C4			Y3 CRYSTAL 46A6														
	RP9 RPAK4P 20C2 20C2 20C3 20C3			RP10 RPAK4P 21C7			RP69 RPAK4P 32A8			U25 INTREPID 11D2 12D5 12D7 14D7 18D5 30C4 32D5			Y4 CRYSTAL_4PIN 46B2														
	RP11 RPAK4P 21B7			RP12 RPAK4P 21B7			RP70 RPAK4P 36C7			U26 M16C62 46C5			Y5 CRYSTAL 30A6														
	RP13 RPAK4P 21B7			RP14 RPAK4P 21C7			RP71 RPAK4P 14C1 14D1			U27 CBTV4020 15D4			Y6 CRYSTAL 37B7														
	RP15 RPAK4P 37B7 37B8			RP16 RPAK4P 37A4			RP72 RPAK4P 33C7			U28 VREG_TL431 46D8			Y7 CRYSTAL 34D3														
	RP17 RPAK4P 21C7			RP18 RPAK4P 21D7			RP73 RPAK4P 33C7			U29 CBTV4020 15C2			ZH3 MTGHOLE 41A7														
	RP19 RPAK4P 21D7			RP20 RPAK4P 21D7			RP74 RPAK4P 30A8			U30 CLK_GEN_CY28512 10A6			ZH4 MTGHOLE 4B2														
	RP21 RPAK4P 21D7			RP22 RPAK4P 20F2 20F3 20G2 20G3			RP75 RPAK4P 33C7			U31 DCDC_SC2602 49B6			ZH5 SLOT 4B1														
A	RP23 RPAK4P 20E2 20E3			RP24 RPAK4P 21C5			RP76 RPAK4P 36C7			U32 SHNTREG_TLV431A 14A8			ZH6 SLOT 4B2														
	RP25 RPAK4P 21C5 21D5			RP26 RPAK4P 21C5			RP77 RPAK4P 33C7			U33 LTC3707 52B6			ZH7 MTGHOLE 4B1														
	RP27 RPAK4P 21D5			RP28 RPAK4P 21D5			RP78 RPAK4P 7A7			U34 APOLLO_MPC7445_360 4C5 5D3 5D6			ZT1 HOLE_VIA 20C2														
	RP29 RPAK4P 21D5			RP30 RPAK4P 19D7			RP79 RPAK4P 7A5 7C5			U35 TRA_IRF7410 53D2			ZT2 HOLE_VIA 20C2														
	RP31 RPAK4P 19C7			RP32 RPAK4P 19D7			RP80 RPAK4P 21B7			U36 SDRAM_DDR_4MX32 23D2 23D4			ZT3 HOLE_VIA 20C2														
	RP33 RPAK4P 19B7			RP34 RPAK4P 19C7			RP81 RPAK4P 21B7			U37 TRANSCIVER_BCM5221 37C5			ZT4 HOLE_VIA 20A2														
	RP35 RPAK4P 19B7			RP36 RPAK4P 19A7			RP82 RPAK4P 21B7 21C7 21C7			U38 SN74AUC1G04 32A3			ZT5 HOLE_VIA 20C2														
	RP37 RPAK4P 18B3 18C3 18C3			RP38 RPAK4P 32A6			RP83 RPAK4P 20B2 20B3 20B3			U39 NV18B 19D6 20G5 20G7 24D4 25D3			ZT6 HOLE_VIA 20C2														
	RP39 RPAK4P 30D2			RP40 RPAK4P 19D7			RP84 RPAK4P 20B2 20B2 20B3			U40 DCDC_SC2602 50C6			ZT7 HOLE_VIA 20A2														
	RP41 RPAK4P 19C7			RP42 RPAK4P 19D7			RP85 RPAK4P 20B3 20C2 20C3			U41 SDRAM_DDR_4MX32 22D2 22D4			ZT8 HOLE_VIA 20B2														
	RP43 RPAK4P 19C7			RP44 RPAK4P 19C7			RP86 RPAK4P 21B5			U42 FEPR_1MX8 32C2			ZT9 HOLE_VIA 20B2														
8			7			6			5			4			3			2			1						
8			7																								