

	8	7	6	5	4	3	2	1
	1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%. 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS. 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.							REV ZONE ECN DESCRIPTION OF CHANGE CK APPD ENG APPD DATE DATE
							D	26343 PRODUCTION RELEASE 02/19/03
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	1	COVER PAGE						
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	4,5	MPC7450 MAXBUS						
	6,7	CPU SPEED & CONFIG OPTIONS						
	8	BOOTBANGER						
	9	CPU LA CONNECTORS, ESP, CPU BYPASS						
	10	CLOCKS						
	11	INTREPID MAX IF (SECTION 1)						
	12,13	INTREPID POWER & BYPASS (SECTION 8 & 9)						
	14	INTREPID DDR CONTROL						
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	31	MODEM, BLUETOOTH, KITCHEN SINK & SERIAL DOWNLOAD						
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	37	ETHERNET PHY						
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	40	ATA CD/HD CONNECTORS						
	41	AUDIO CODEC & VOLTAGE REGS						
	42,43	LINE IN/OUT BUFFERS						
	44,45	SPEAKER/MIC AMPS						
	46	POWER MANAGER UNIT						
	47-53	+5V/+12V, AUDIO, FW & TMDS POWER CONVERTERS						
	54-61	CONSTRAINT TABLES						
	62-66	NET TABLES						
	67-74	PART TABLES						
	8	7	6	5	4	3	2	1

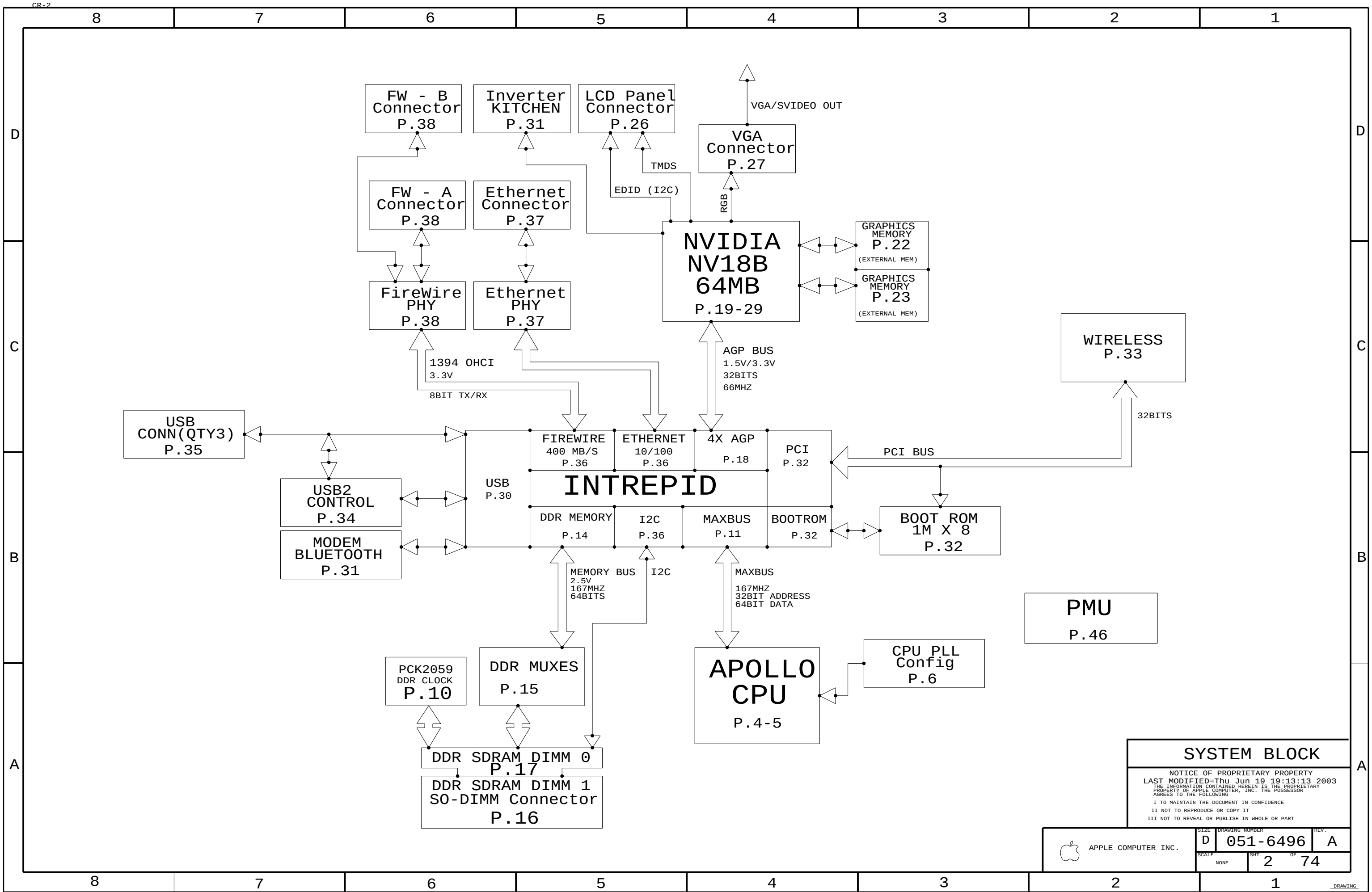
Q26C MLB

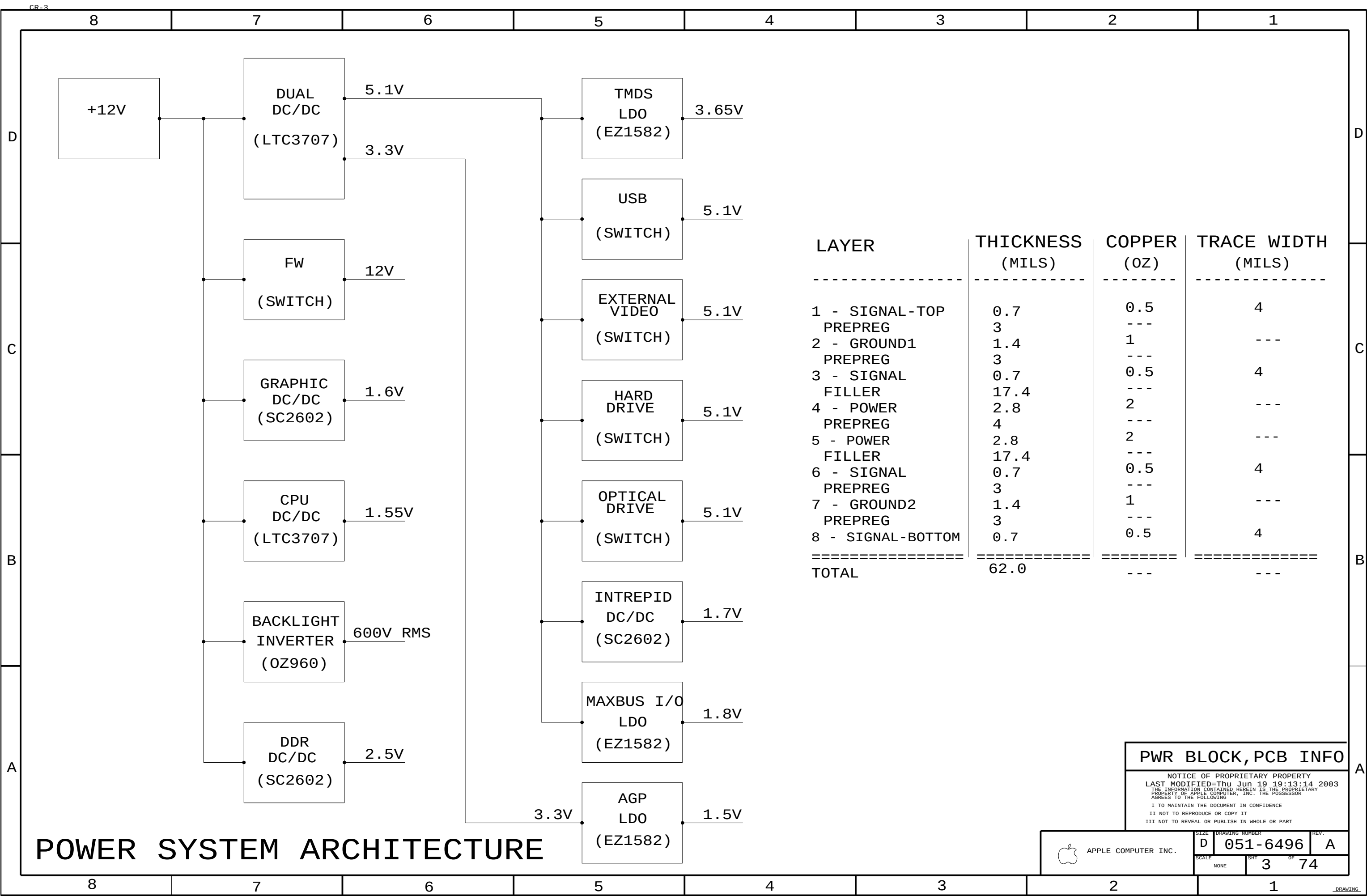
GEN X

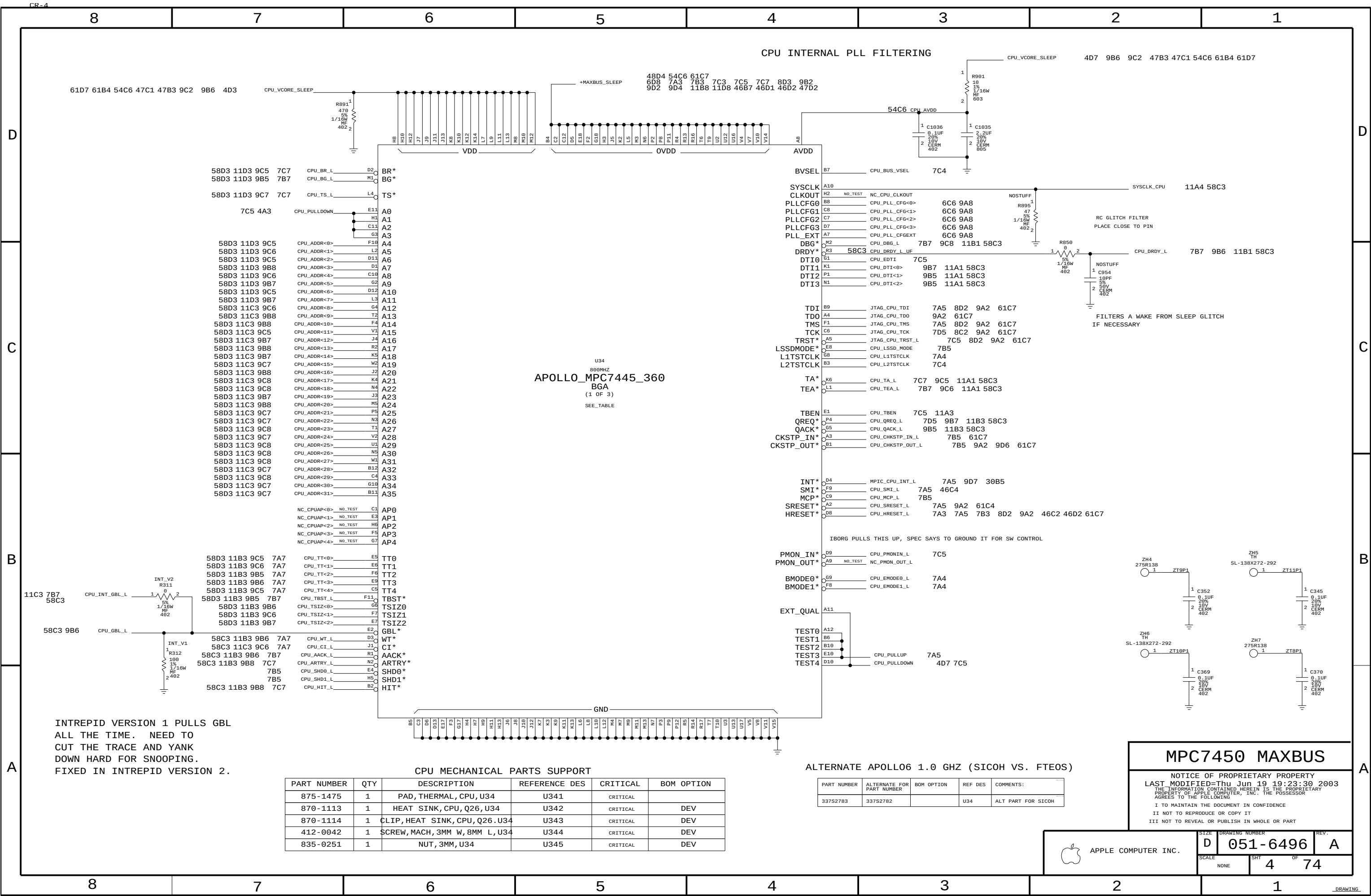
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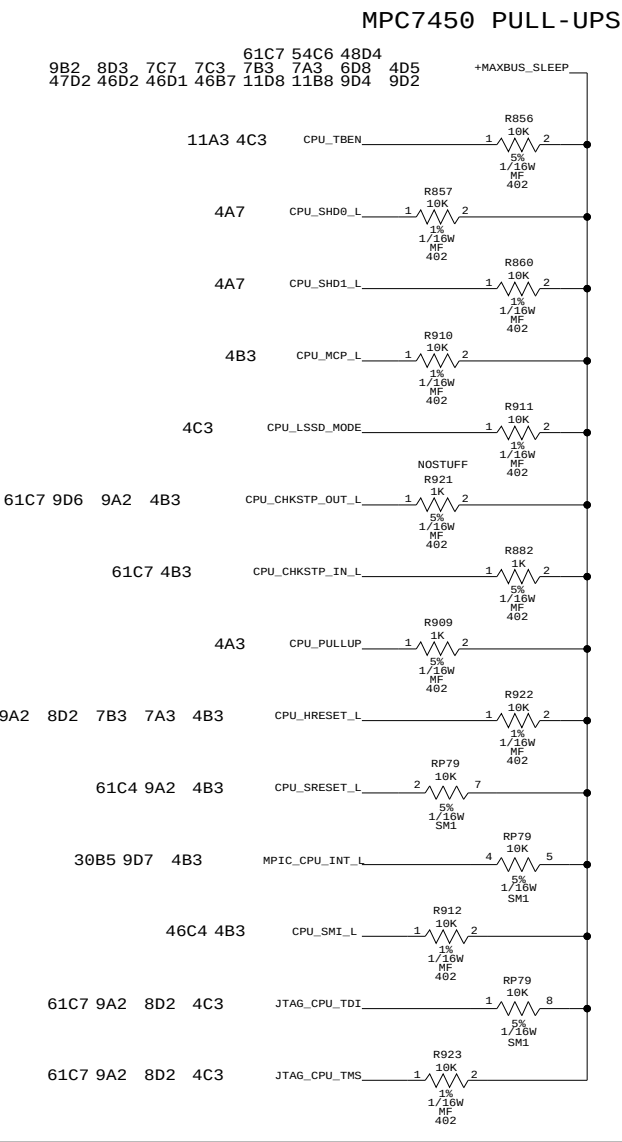
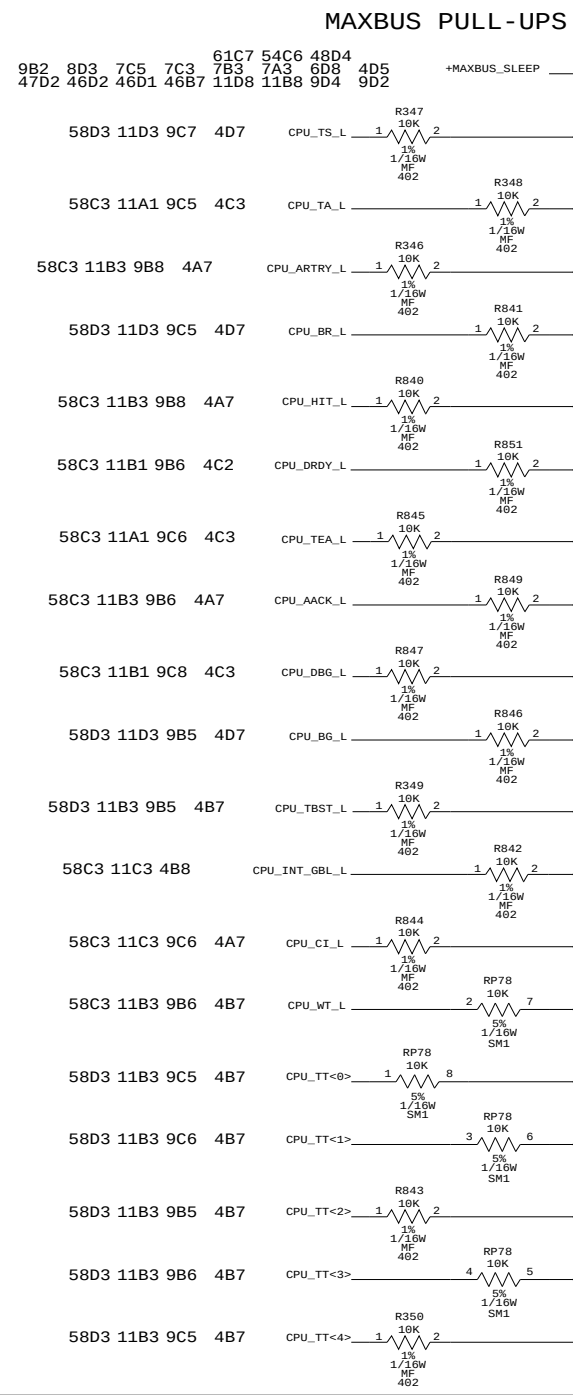
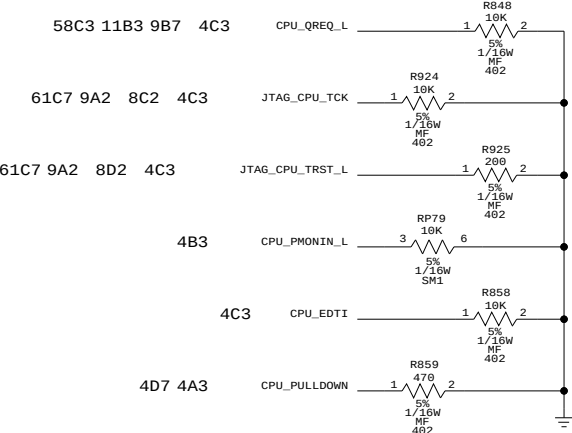
POWER RAIL DEFINITIONS



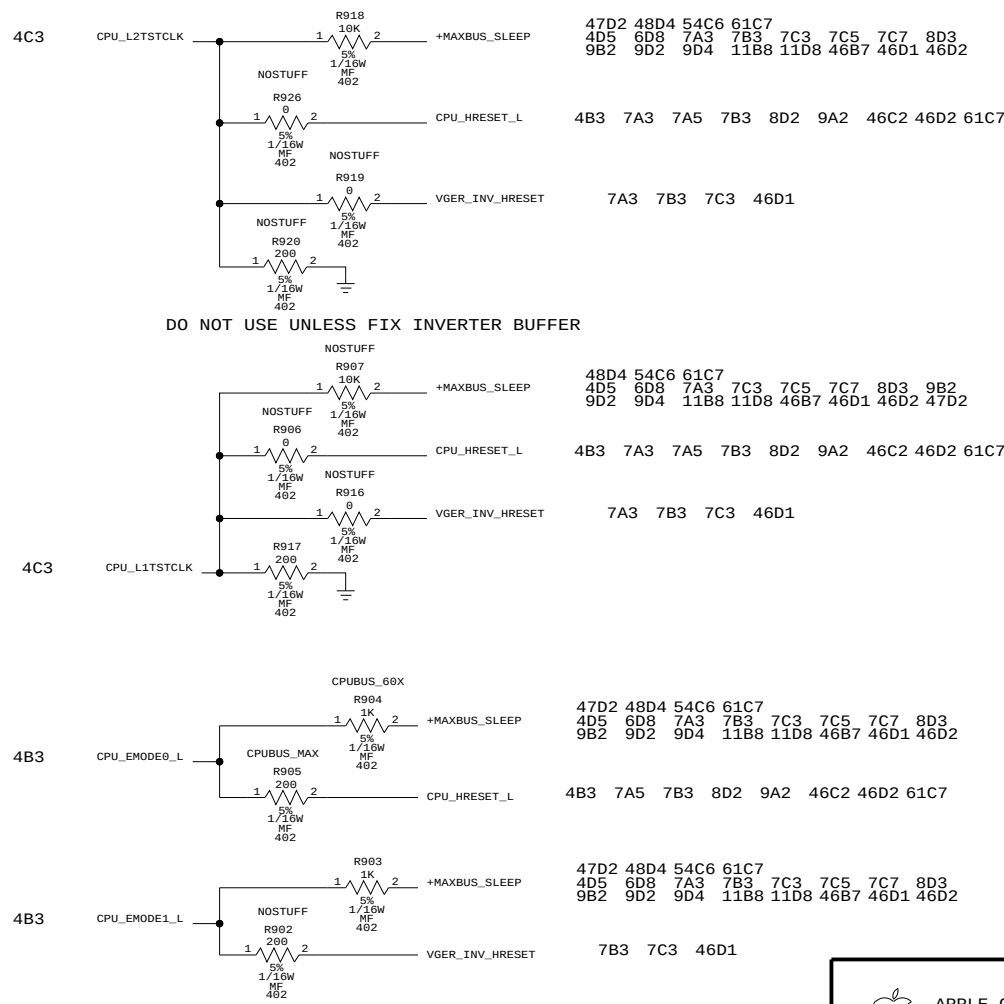
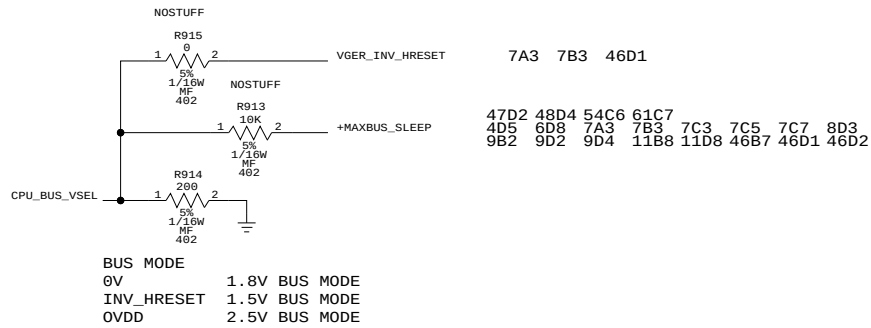




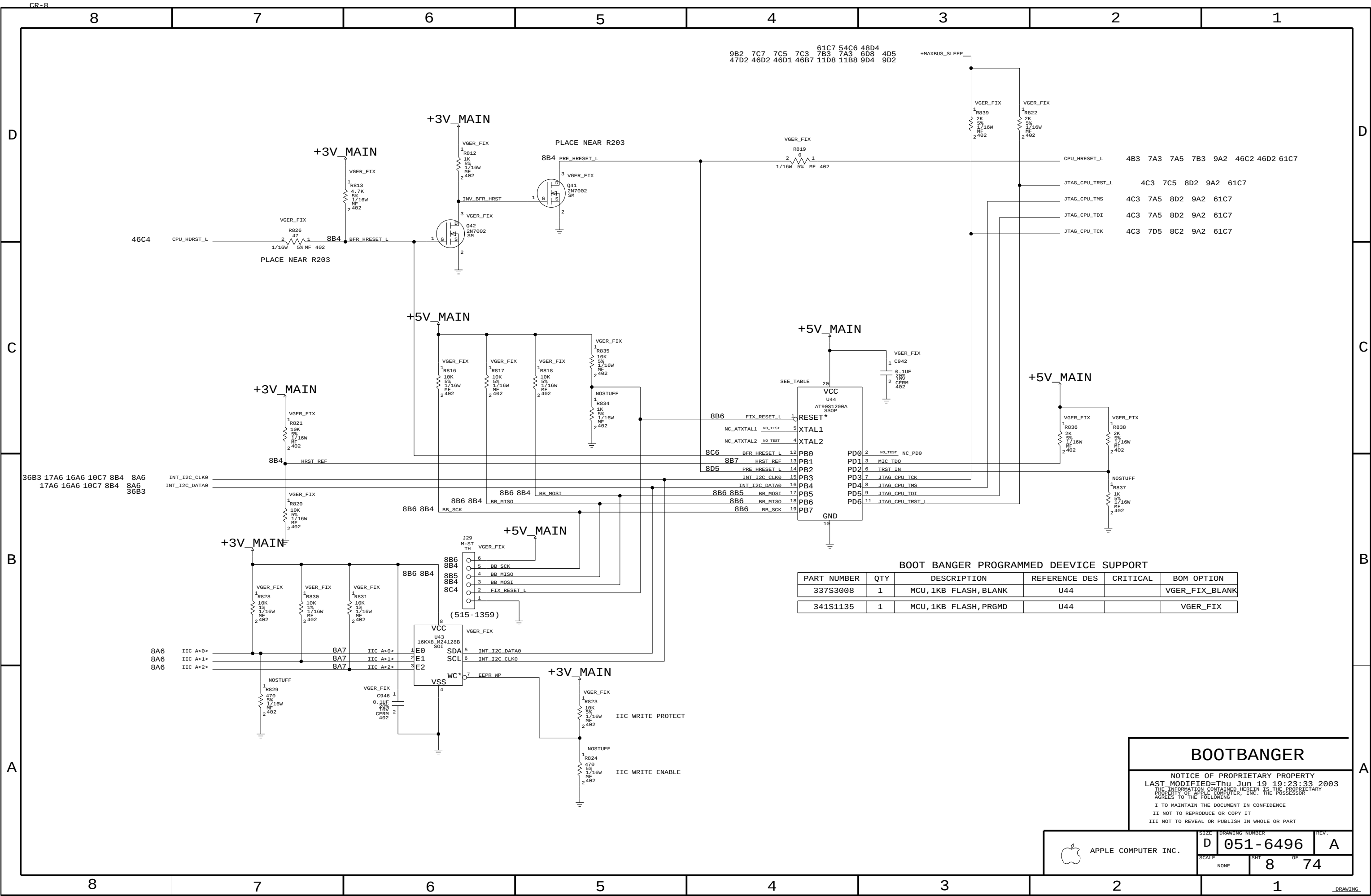
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L	L	1	1	???	01	yes	unavail
L	!hr	1	0	Max	01	yes	unavail
L	hr	1	1	???	00	yes	unavail
L	H	1	0	Max	00	yes	unavail
!hr	L	0	1	MB+	01	yes	unavail
!hr	!hr	0	0	60x	01	yes	unavail
!hr	hr	0	1	MB+	00	yes	unavail
!hr	H	0	0	60x	00	yes	unavail
hr	L	1	1	???	01	norm	unavail
hr	!hr	1	0	Max	01	norm	
hr	hr	1	1	???	00	norm	unavail
HR	H	1	0	MAX	00	NORM	<- DEFAULT
H	L	0	1	MB+	01	norm	unavail
H	!hr	0	0	60x	01	norm	
H	hr	0	1	MB+	00	norm	unavail
H	H	0	0	60x	00	norm	



SIGNAL	TIED	APPLICATION
CPU_EMODE0_L	HIGH	60X BUS MODE
	CPU_HRESET_L	MAX BUS MODE
CPU_BUS_VSEL	CPU_HRESET_L	2.5V INTERFACE
	LOW	1.8V INTERFACE
	CPU_HRESET_H	1.5V INTERFACE
CPU_L3_VSEL	CPU_HRESET_L or L3_OVDD	2.5V INTERFACE
	LOW	1.8V INTERFACE
	CPU_HRESET_H	1.5V INTERFACE



C5 46D2 61C7		CPU CONFIG OPTIONS	
C5 7C7 8D3 8B7 46D1 46D2		NOTICE OF PROPRIETARY PROPERTY LAST MODIFIED: Thu Jun 19 19:23:32 2003 THE INFORMATION CONTAINED HEREIN IS PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING I TO MAINTAIN THE DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART	
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SCALE	SHT	OF	
NONE	7	74	



BOOT BANGER PROGRAMMED DEEVICE SUPPORT					
PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
337S3008	1	MCU, 1KB FLASH, BLANK	U44		VGER_FIX_BLANK
341S1135	1	MCU, 1KB FLASH, PRGMD	U44		VGER_FIX

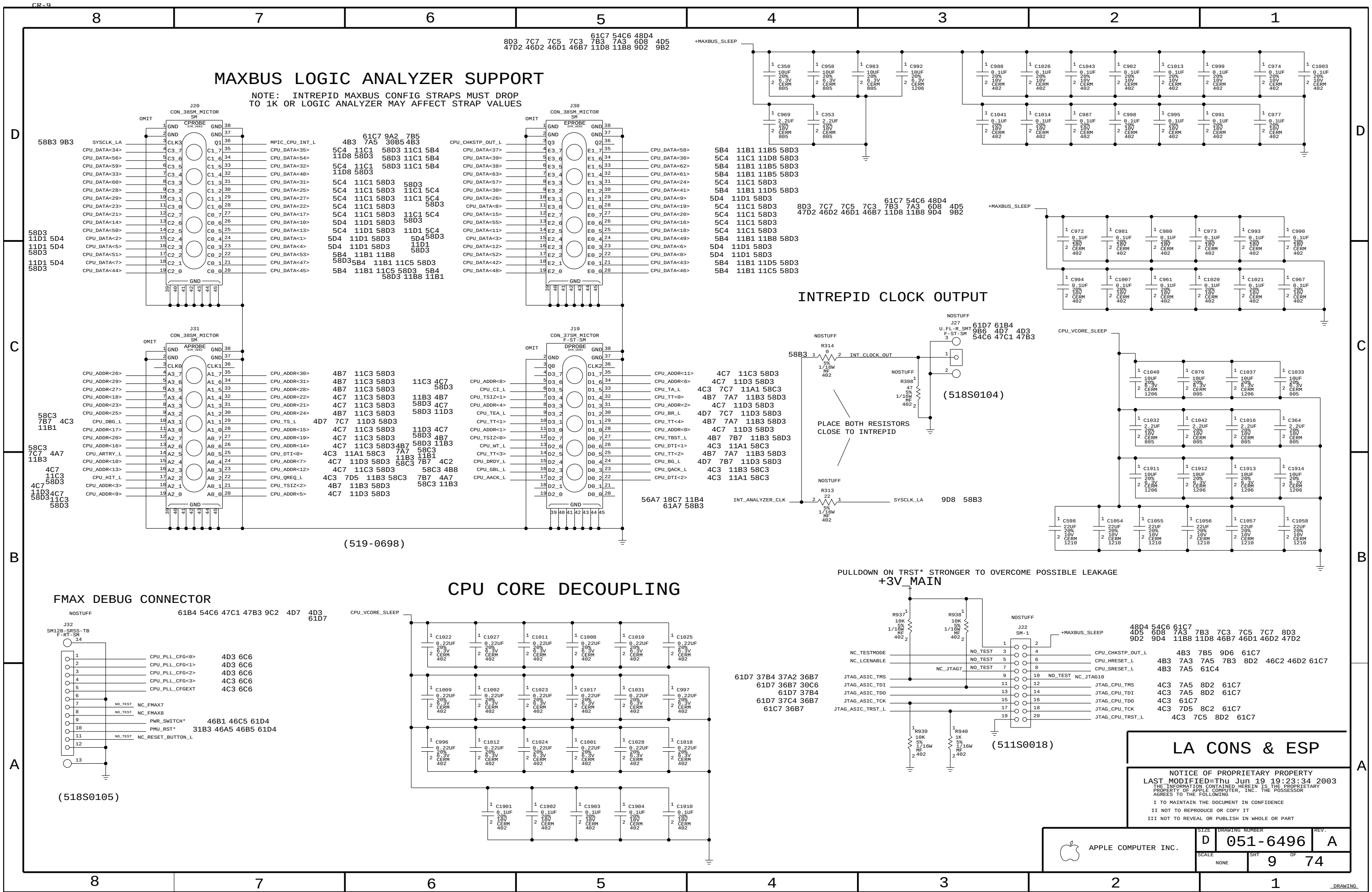
BOOTBANGER

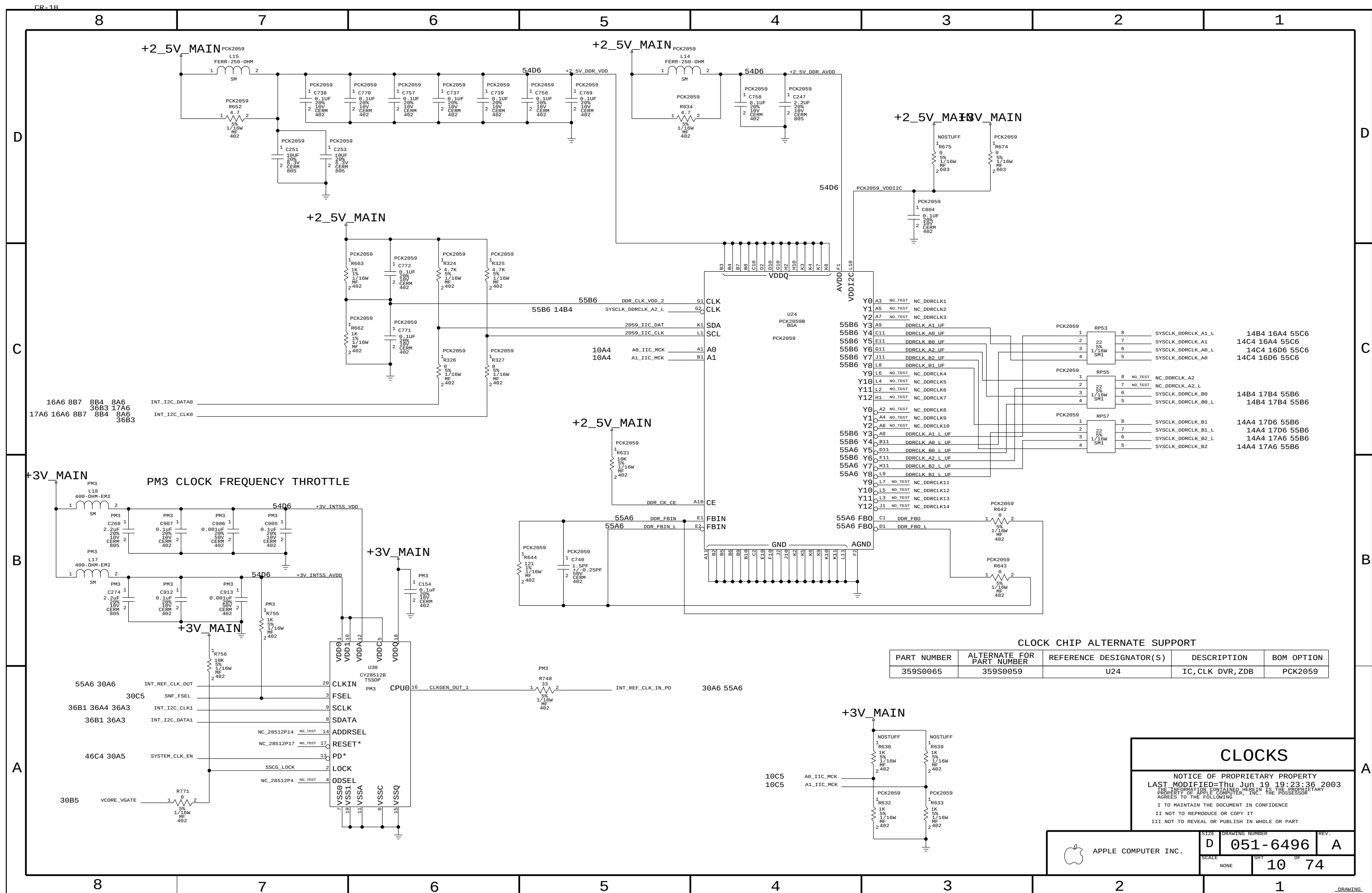
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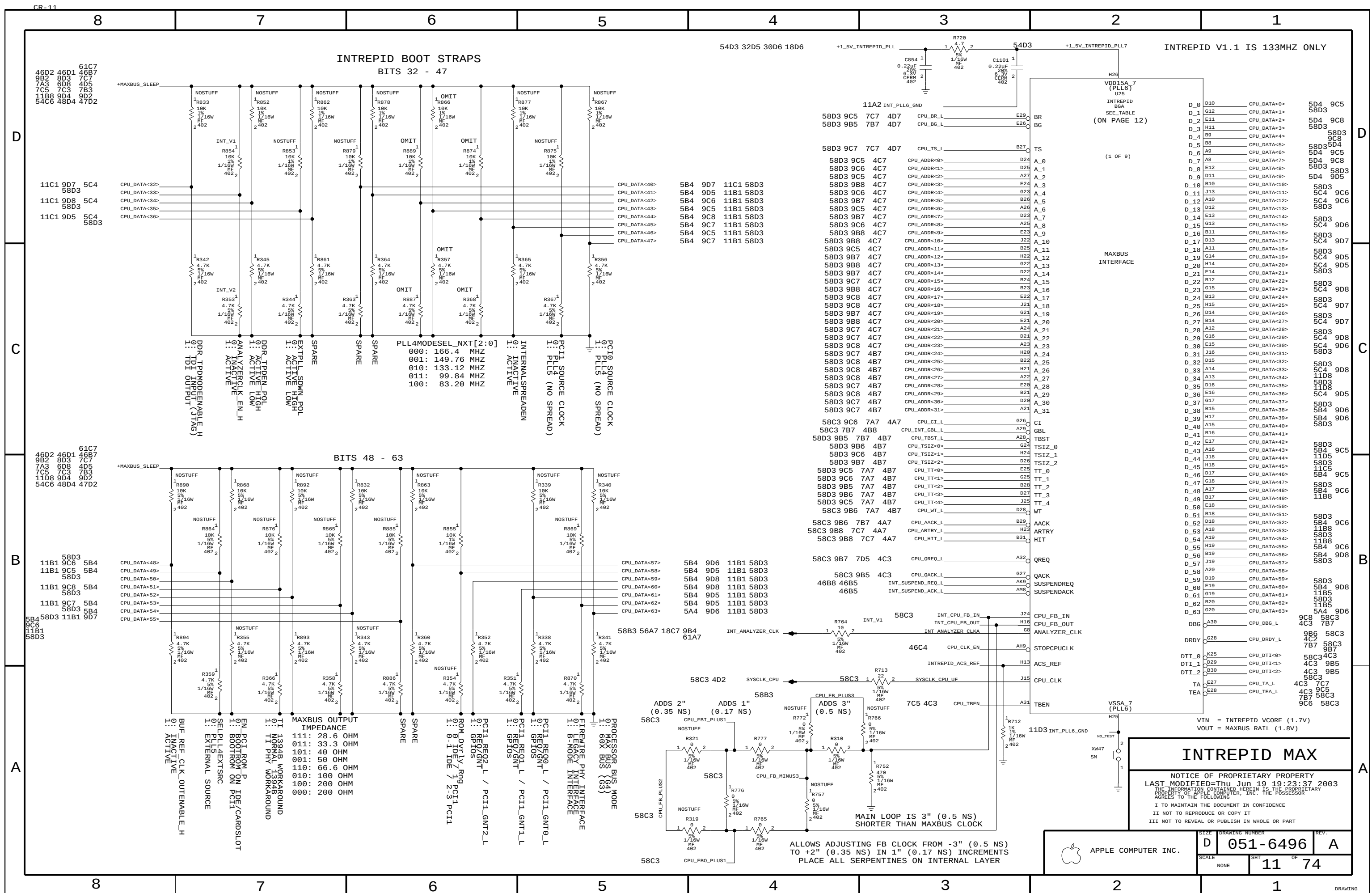


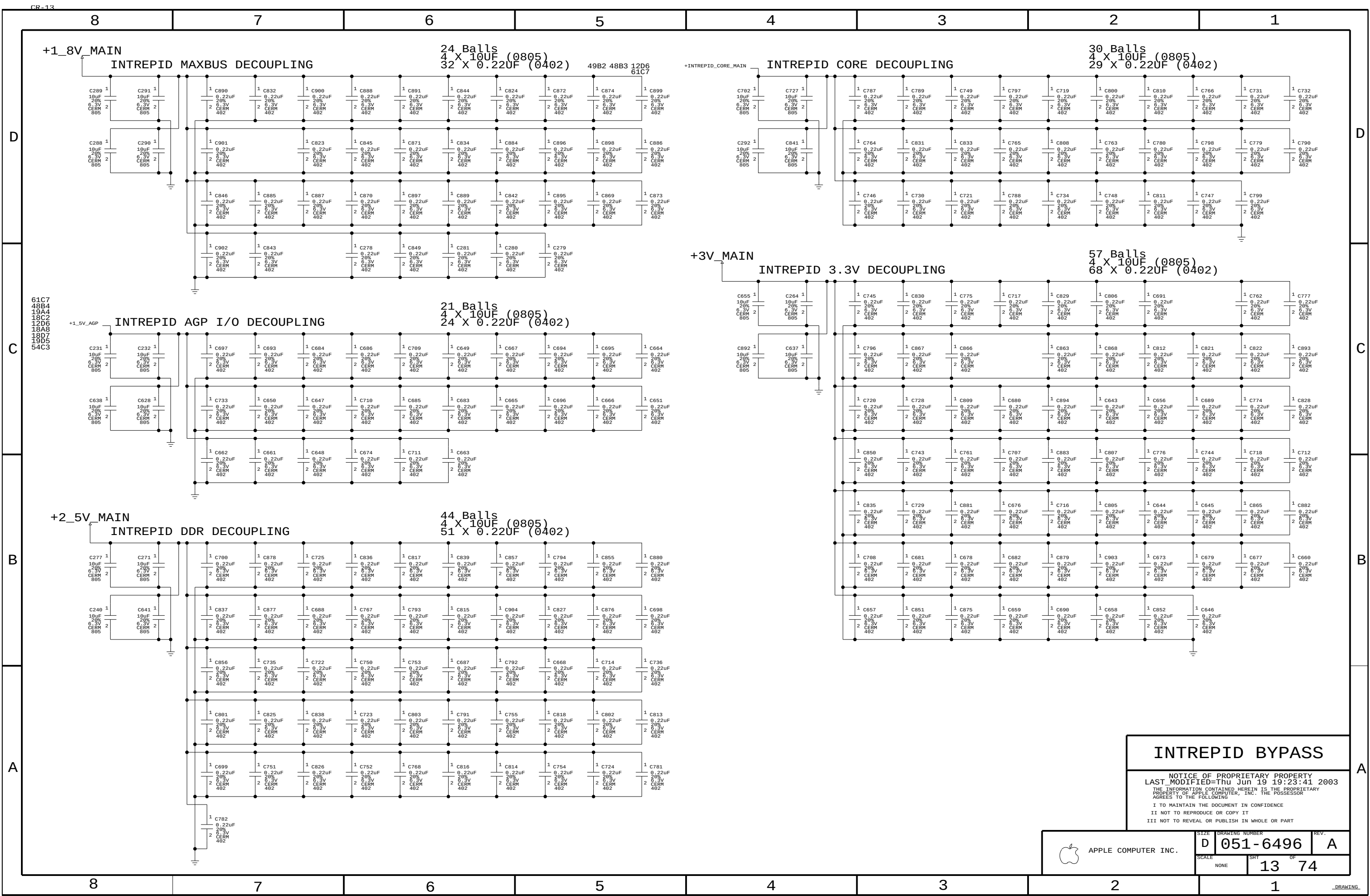
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NONE	8	74









0-ohm resistors to allow rewiring if necessary

SERIES RESISTORS FOR CONTROL SIGNALS

U25
INTREPID
BGA
(2 OF 9)
SEE_TABLE
(ON PAGE 12)

DDR MEMORY INTERFACE

+2_5V_MAIN

INTREPID DDR MEMORY REFERENCE SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
116S1103	1	RES, 1K-OHM, 5%, 1/16W, 0402	R778		MEMREFI_ACT
116S1000	1	RES, 0-OHM, 5%, 1/16W, 0402	R778		MEMREFI_PAS

INTREPID DDR CNTRL

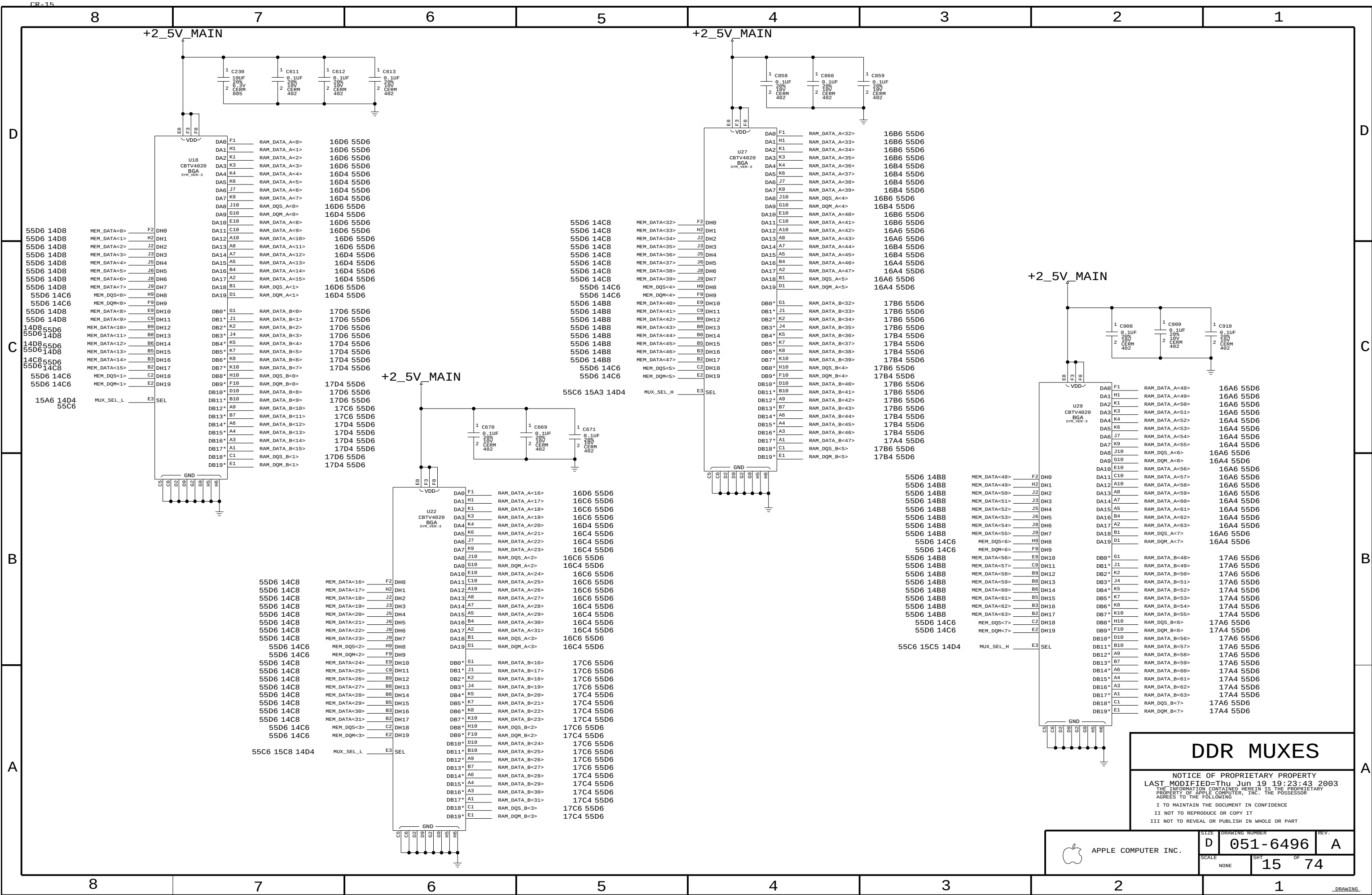
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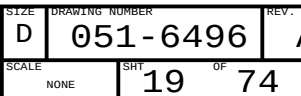
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SCALE NONE	SHT 14	OF 74

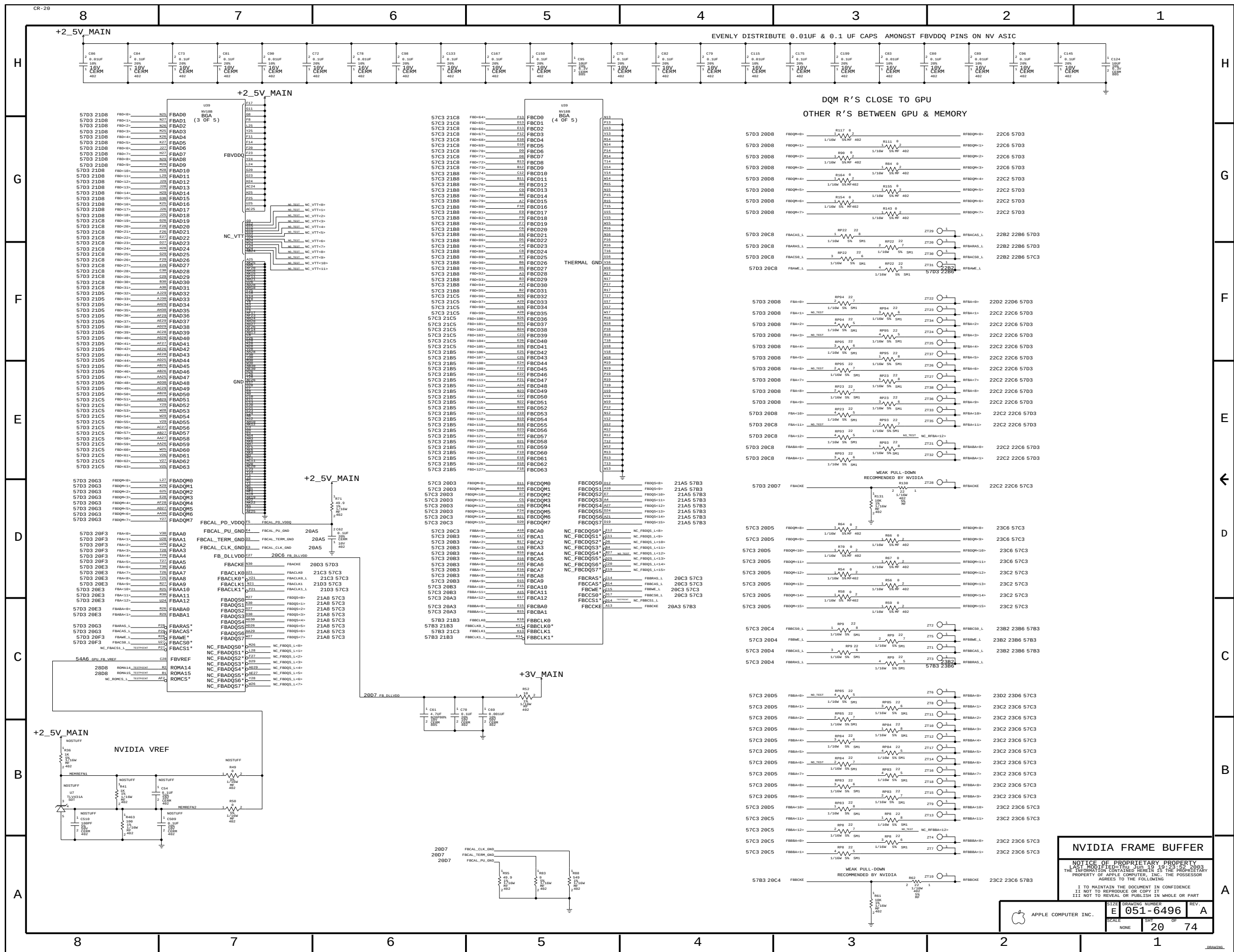




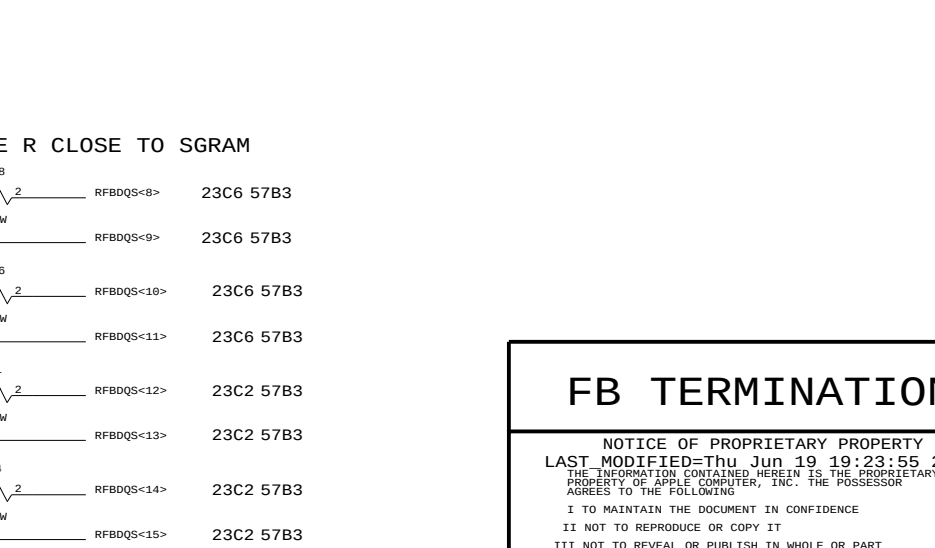
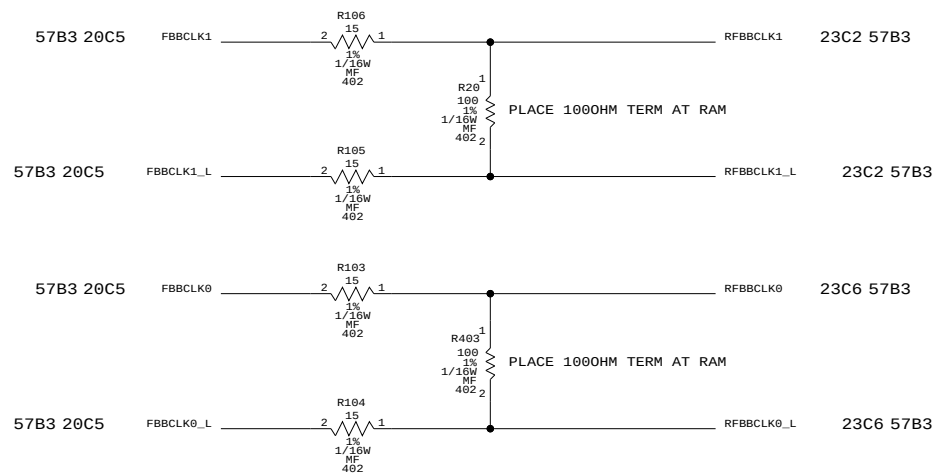
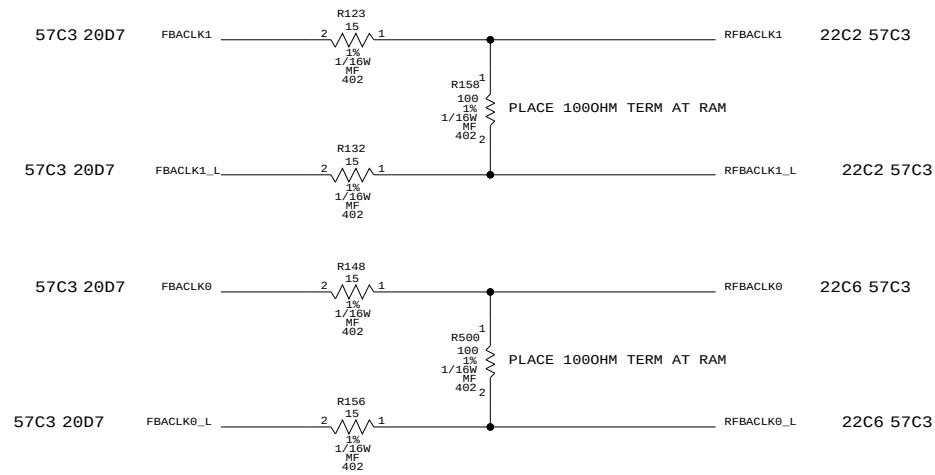


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PLACE R'S CLOSE TO GPU

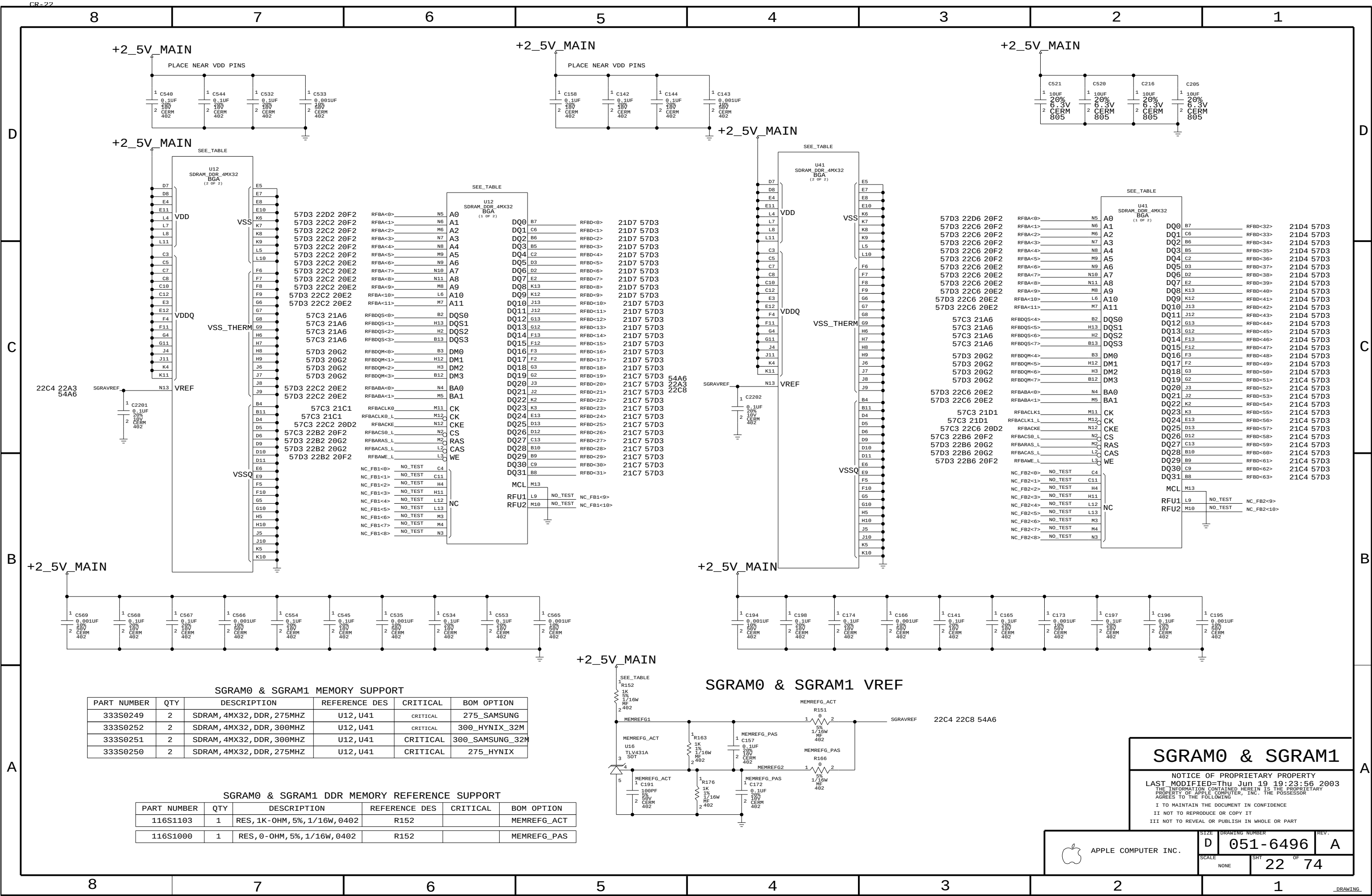


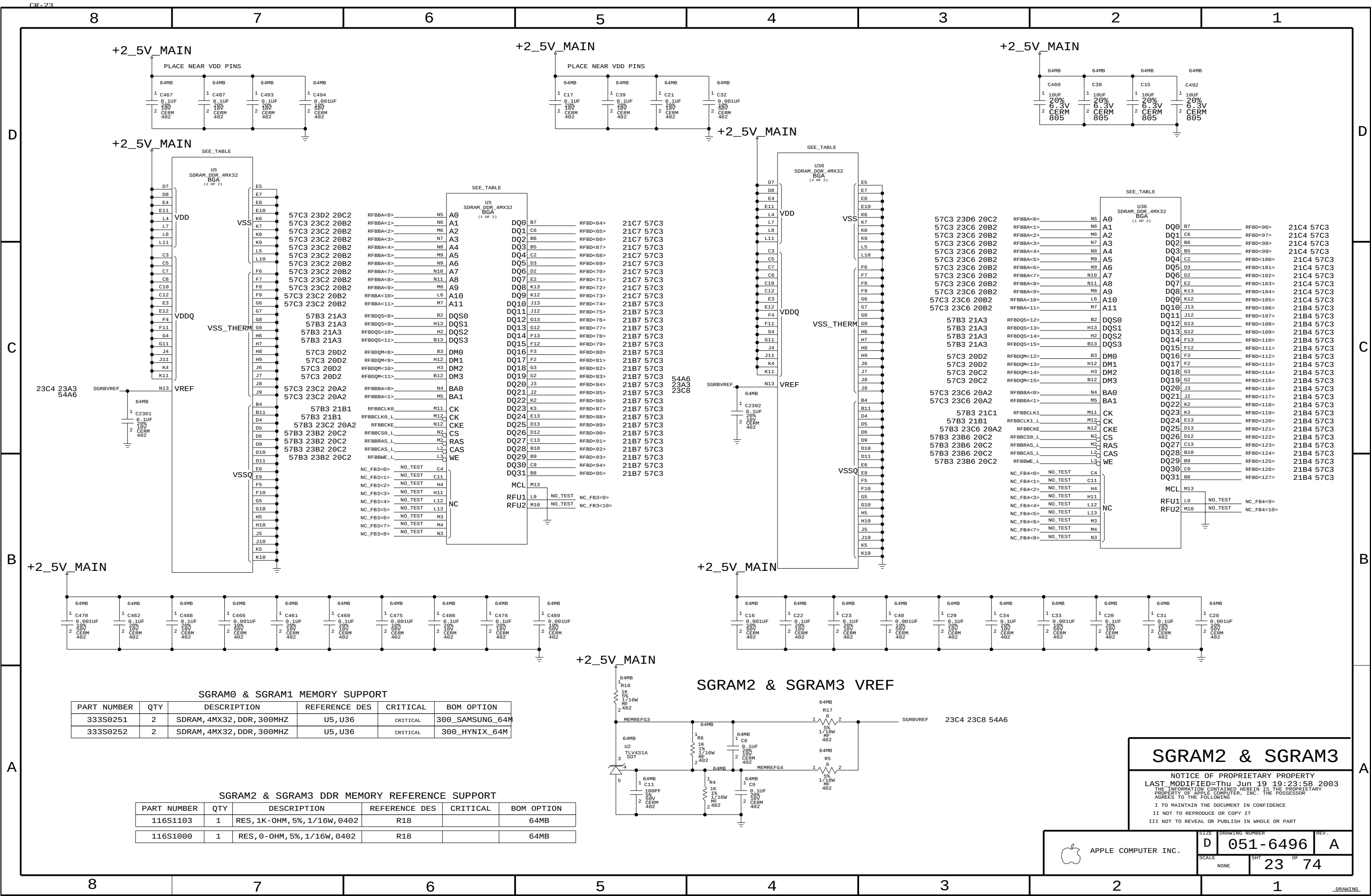
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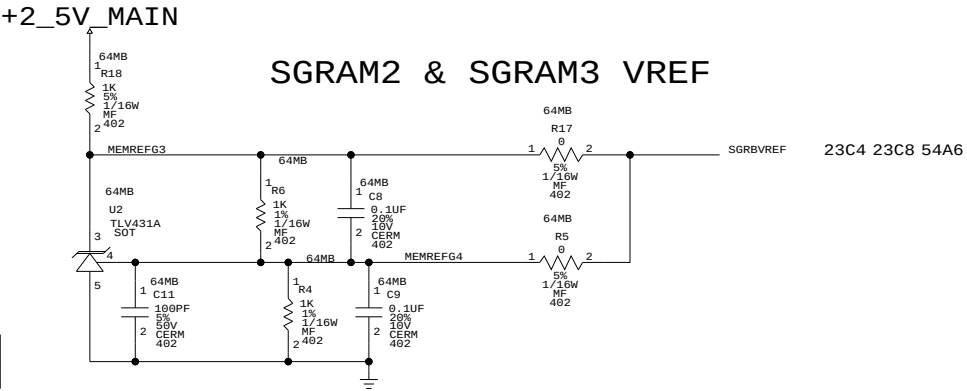
SIZE D	DRAWING NUMBER 051-6496	REV. A
SCALE NONE	SHT 21 OF 74	





SGRAM0 & SGRAM1 MEMORY SUPPORT					
PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
333S0251	2	SDRAM, 4MX32, DDR, 300MHZ	U5, U36	CRITICAL	300_SAMSUNG_64M
333S0252	2	SDRAM, 4MX32, DDR, 300MHZ	U5, U36	CRITICAL	300_HYNIX_64M

SGRAM2 & SGRAM3 DDR MEMORY REFERENCE SUPPORT					
PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
116S1103	1	RES, 1K-0HM, 5%, 1/16W, 0402	R18		64MB
116S1000	1	RES, 0-0HM, 5%, 1/16W, 0402	R18		64MB



SGRAM2 & SGRAM3

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SIZE

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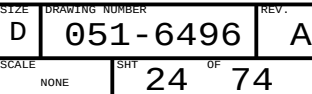
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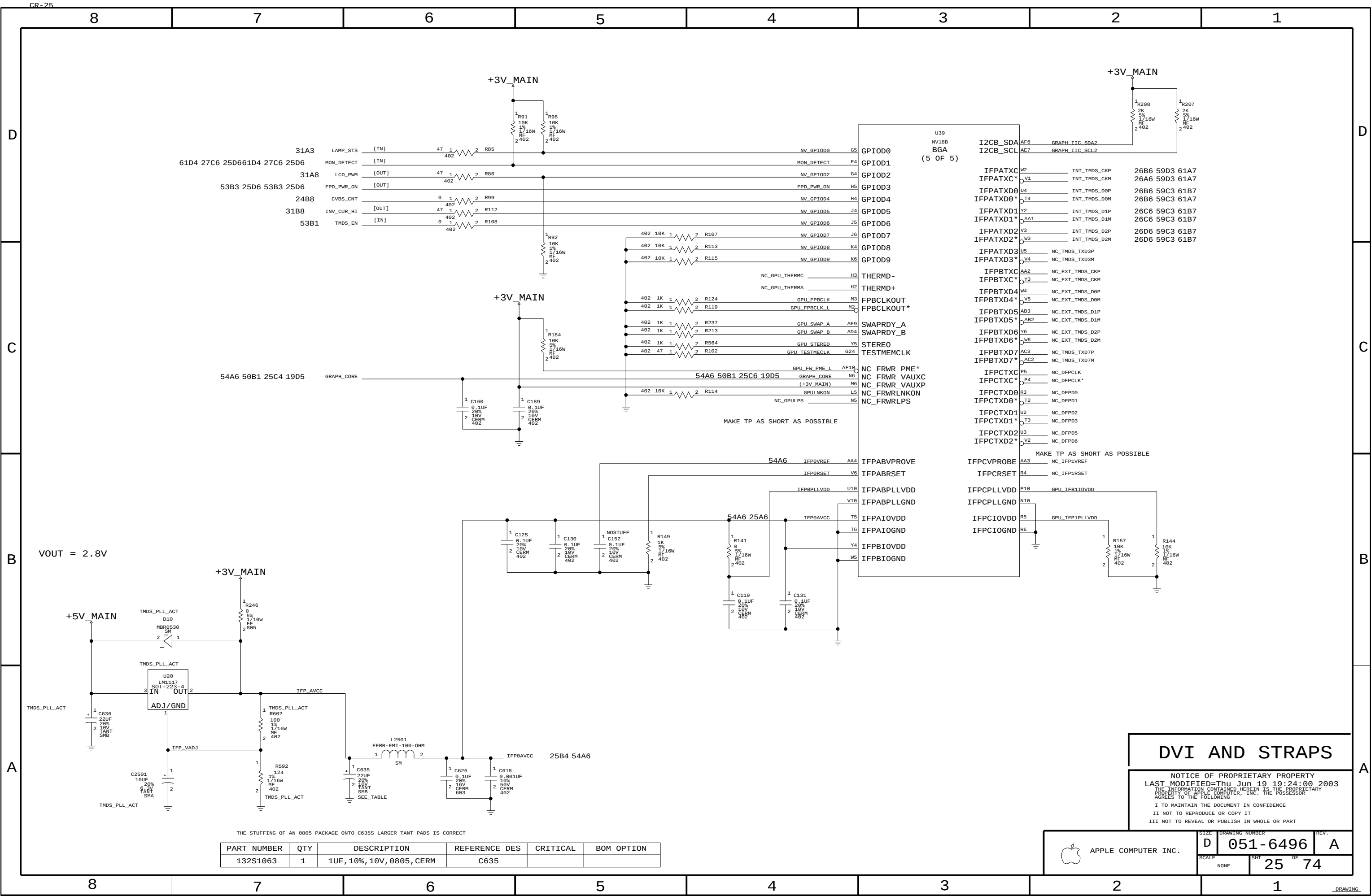
23

OF

74



NVIDIA ASIC SUPPORT					
PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
338S0119	1	IC, NV18B, GRPHCS CTLR, A03	U39		NV18B
338S0112	1	IC, NV31, GRPHCS CTLR	U39		NV31
338S0113	1	IC, NV34, GRPHCS CTLR	U39		NV34
155S0141	1	FLTR, EMI, 600 OHMS, .2A, 0603	L2401		NOSTUFF
155S0143	2	FLTR, EMI, 1000 OHMS, .2A, 0805	L13, L68		



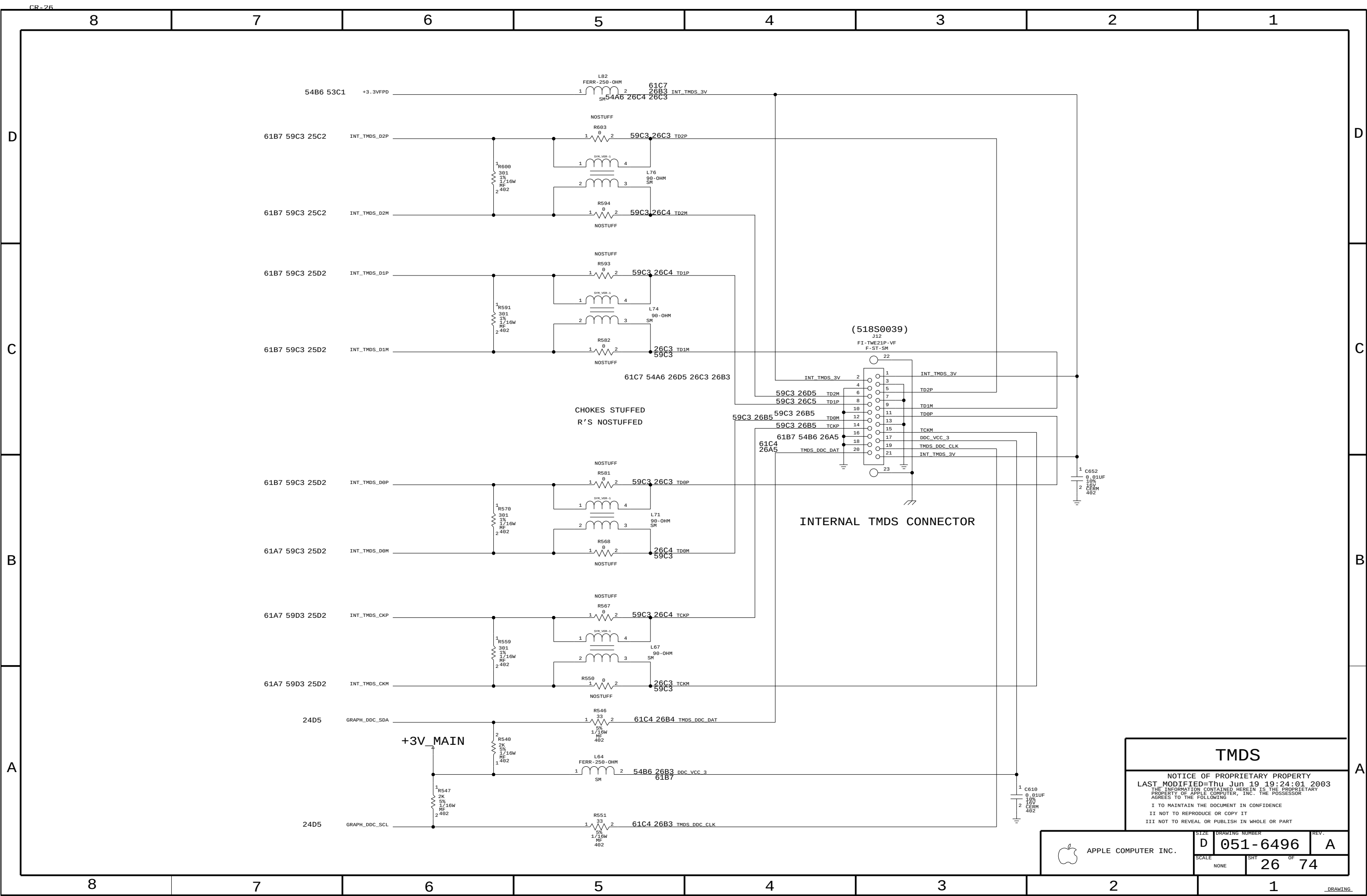
THE STUFFING OF AN 0805 PACKAGE ONTO C635S LARGER TANT PADS IS CORRECT

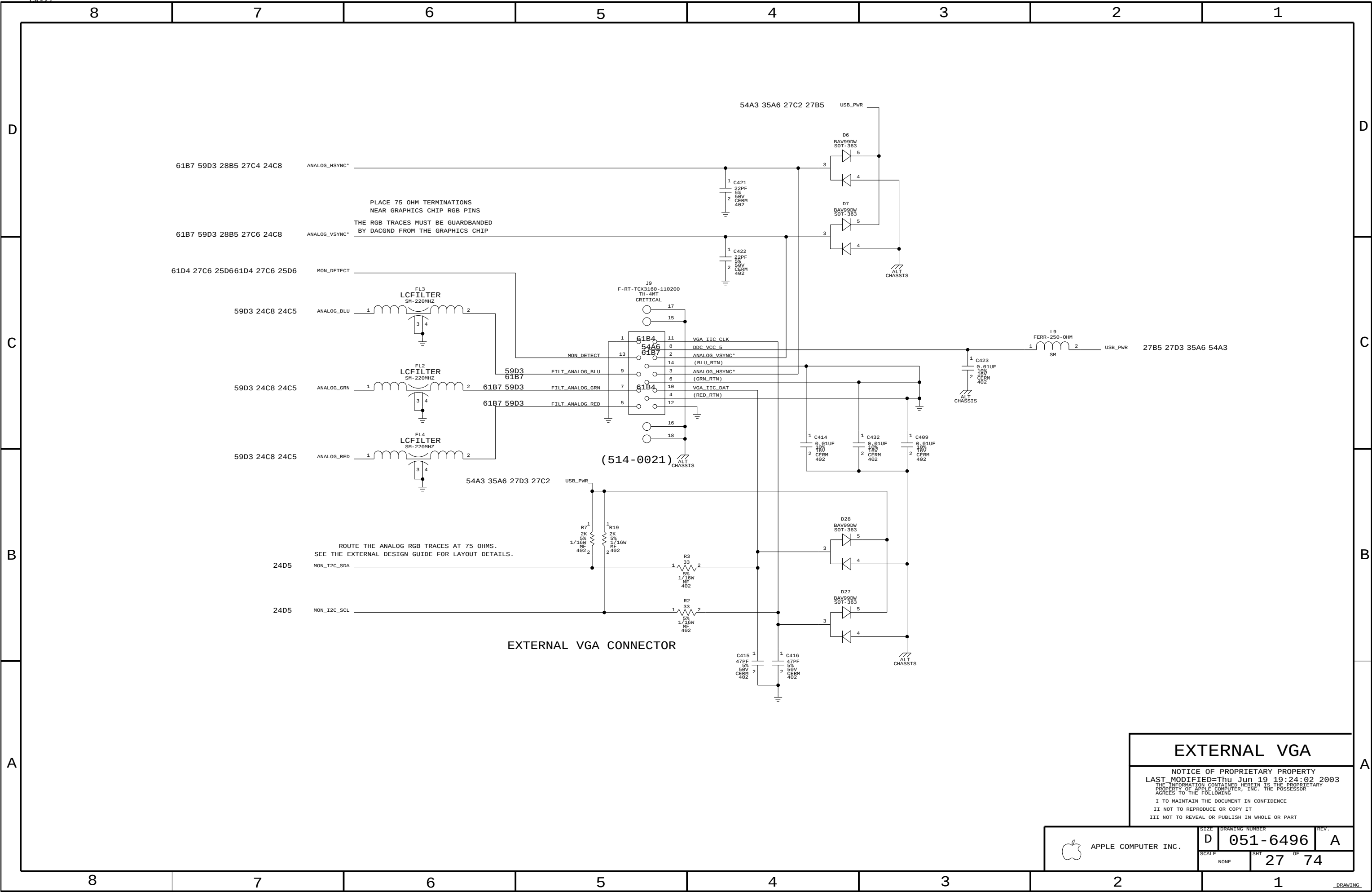
PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
132S1063	1	1UF, 10%, 10V, 0805, CERM	C635		

DVI AND STRAPS

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EXTERNAL VGA

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SCALE	NONE	SHT	27 OF 74

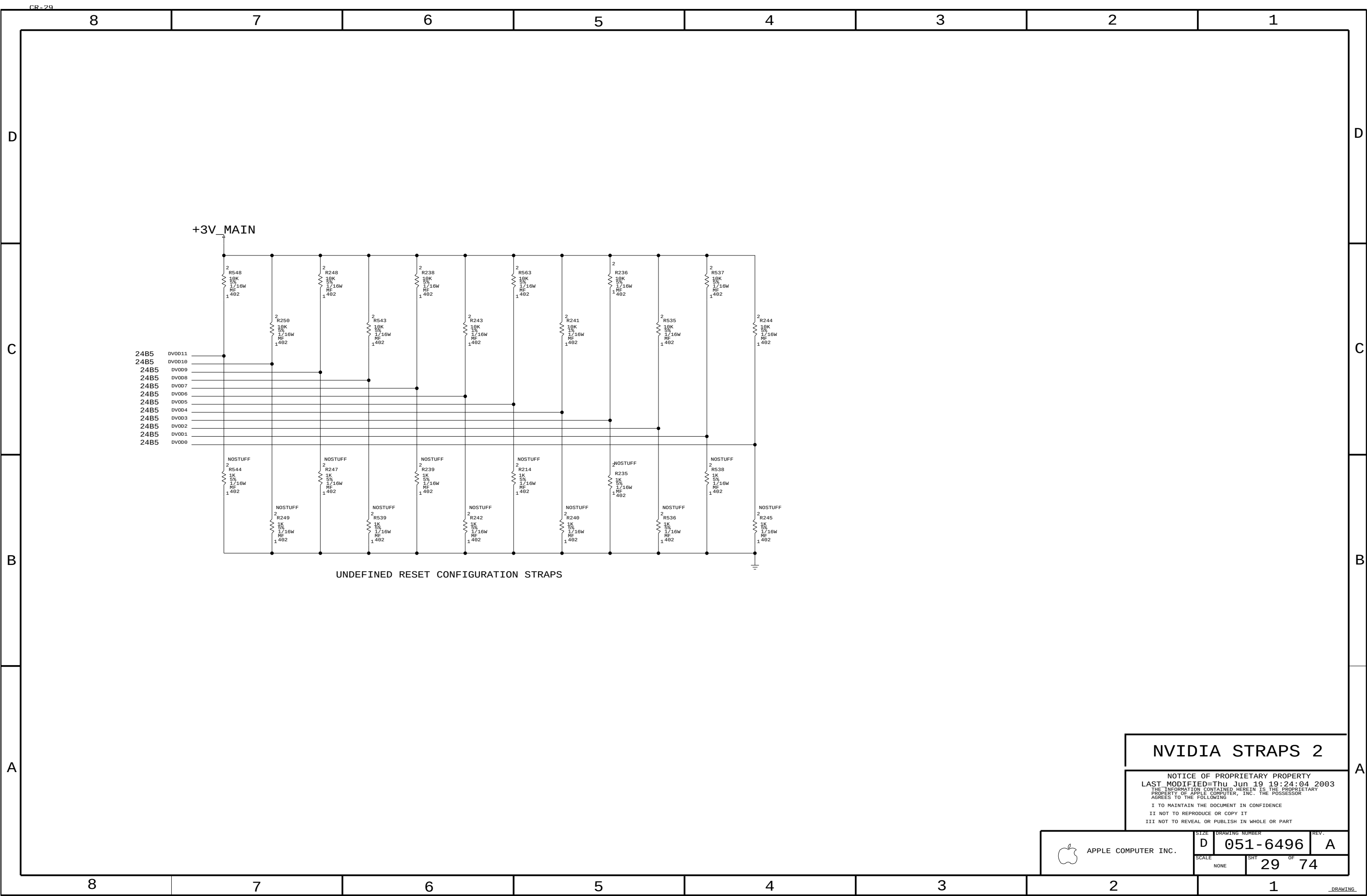
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8 7 6 5 4 3 2 1

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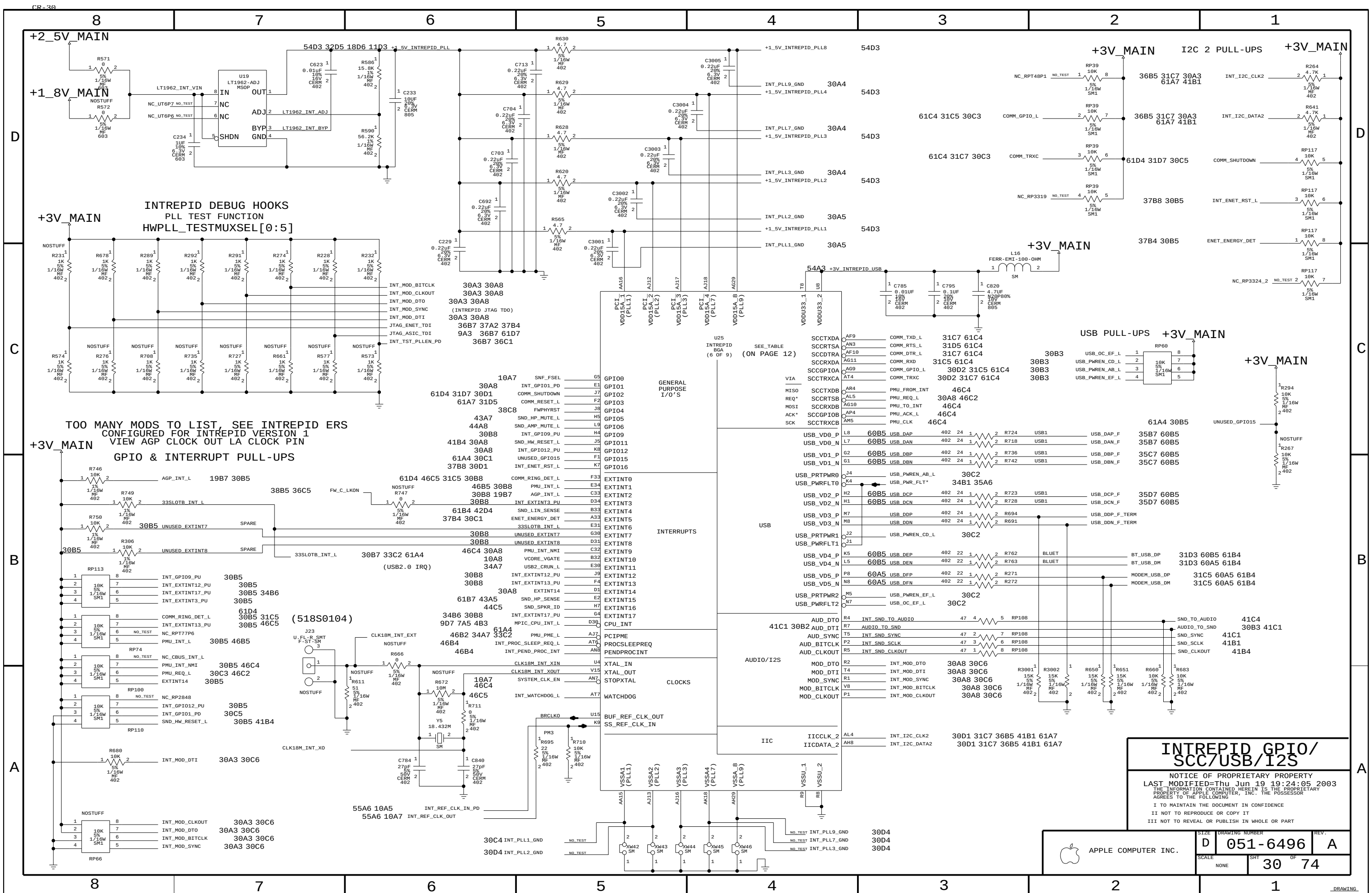
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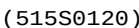
NVIDIA STRAPS 2

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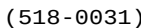
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NONE		29	74



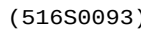
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MODEM STANDOFF SUPPORT

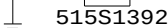
(MICROPHONE, INTERNAL SPEAKER CONNECTIONS
INVERTER, LCD, LED & FAN POWER)



+3V_MAIN



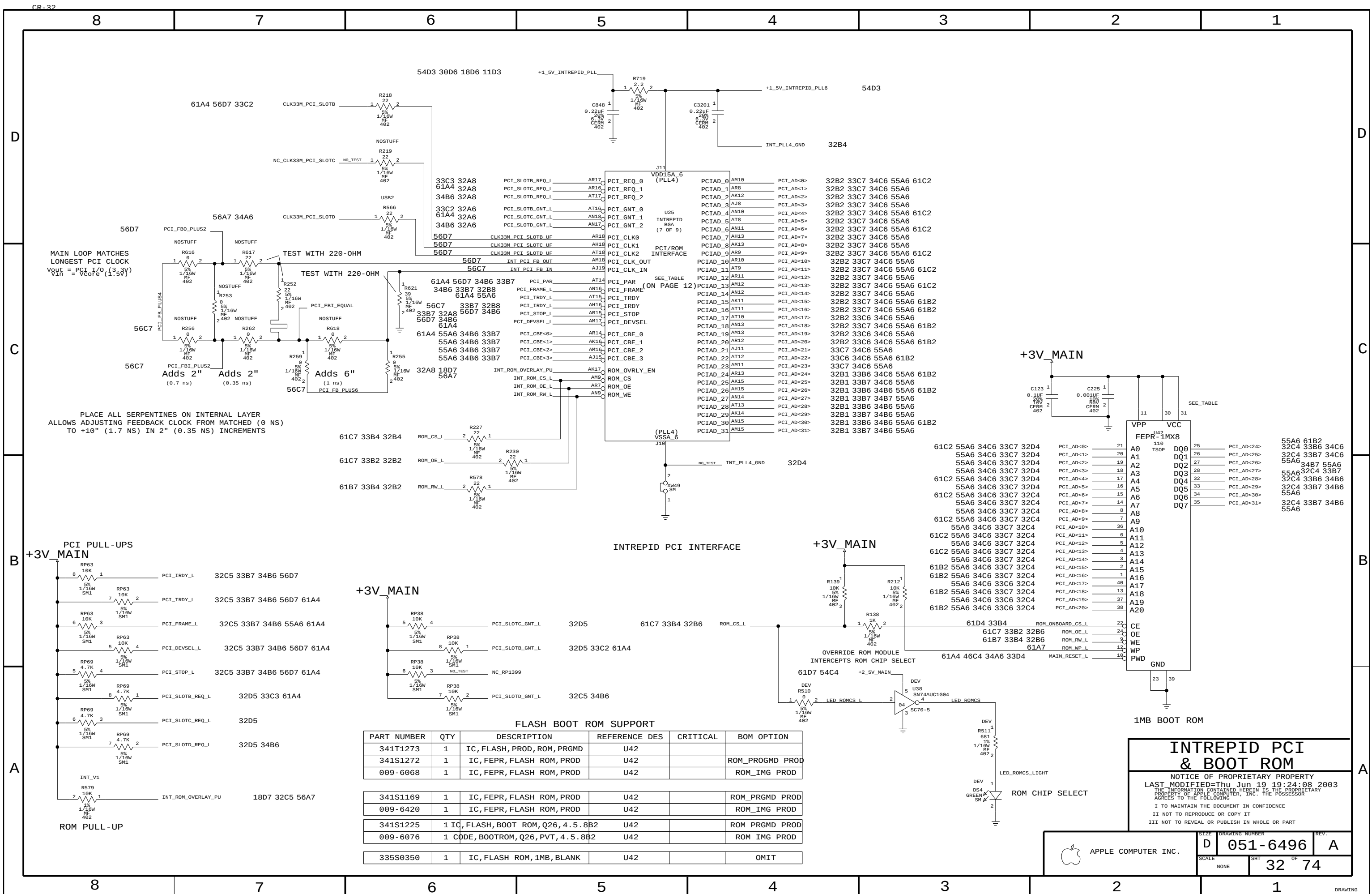
BLUETOOTH CARD MOUNTING HARDWARE SUPPORT

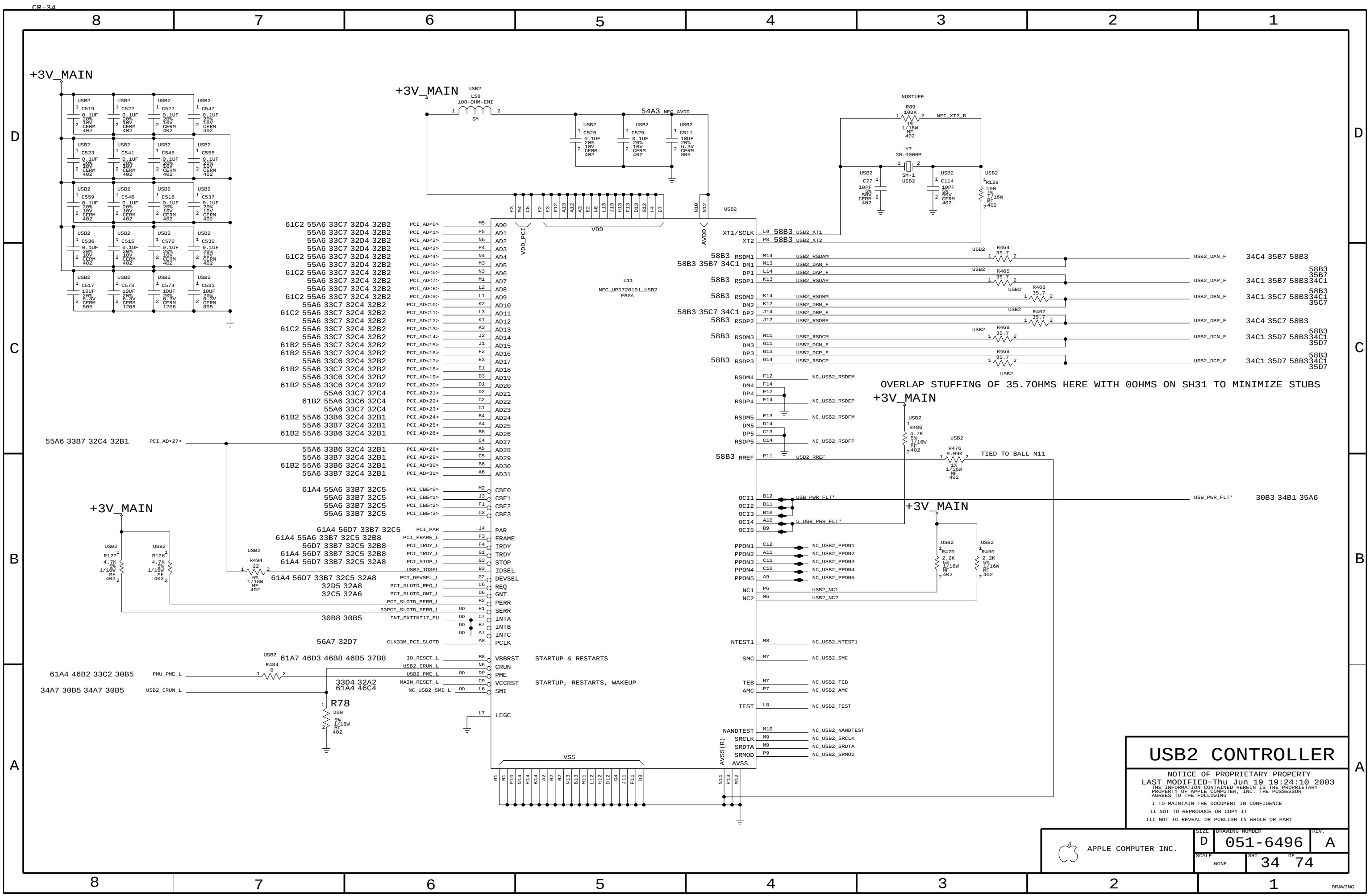


SERIAL DOWNLOAD CONNECTOR

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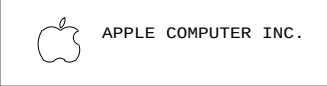


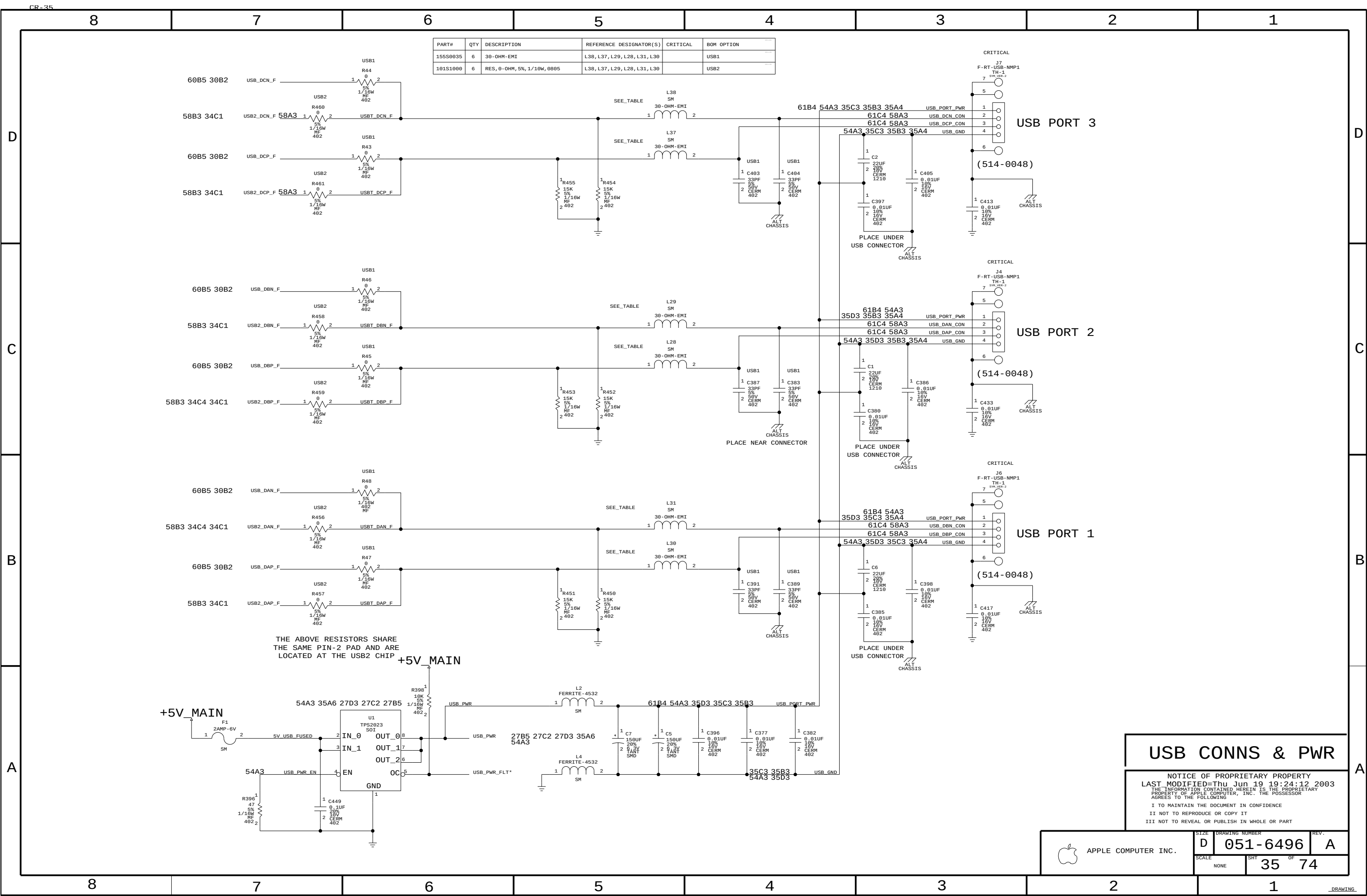


USB2 CONTROLLER

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NONE		SHT	OF
		34	74





PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
155S0035	6	30-OHM-EMI	L38, L37, L29, L28, L31, L30		USB1
101S1000	6	RES, 0-OHM, 5%, 1/10W, 0805	L38, L37, L29, L28, L31, L30		USB2

USB CONNS & PWR

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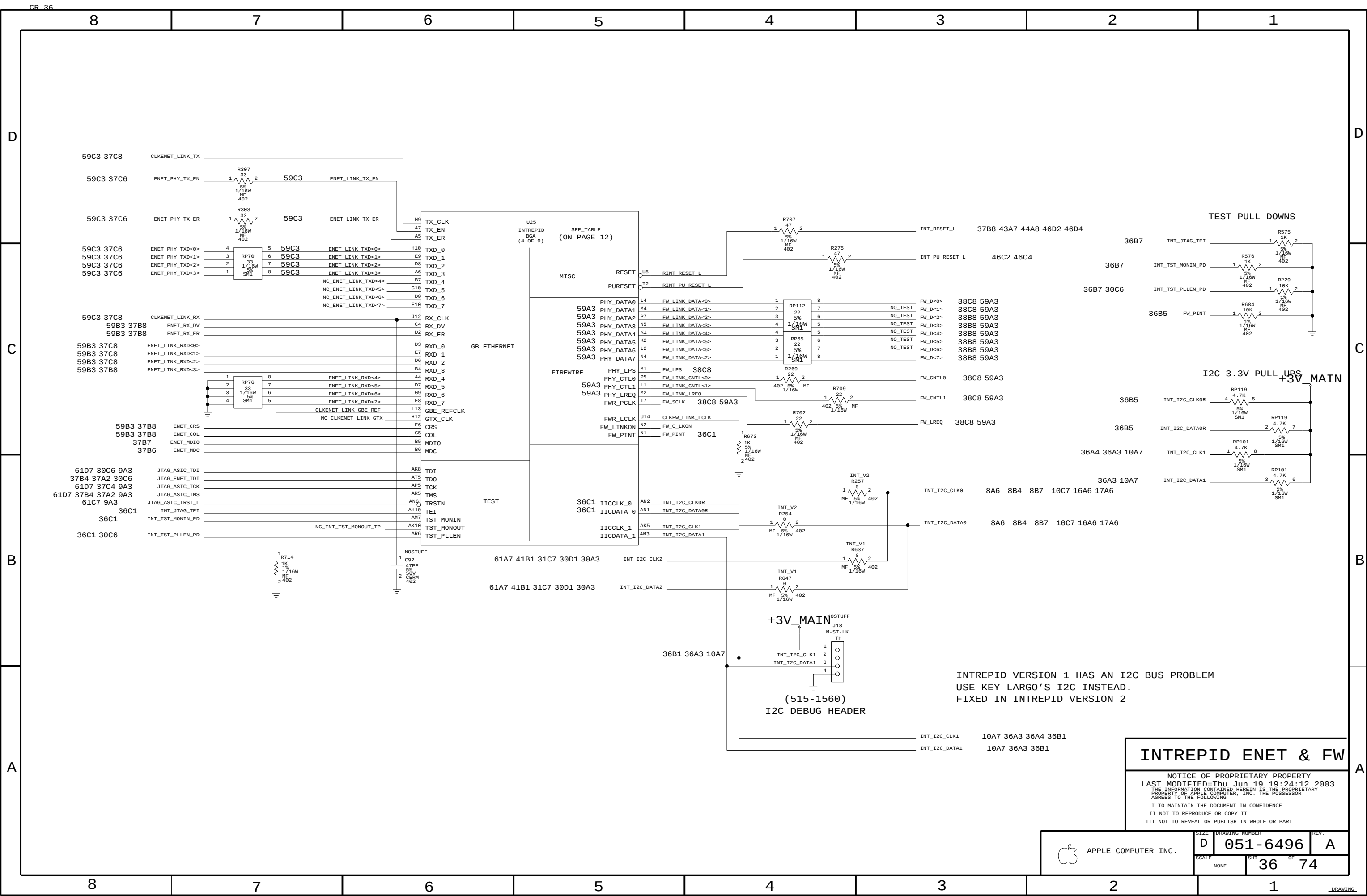
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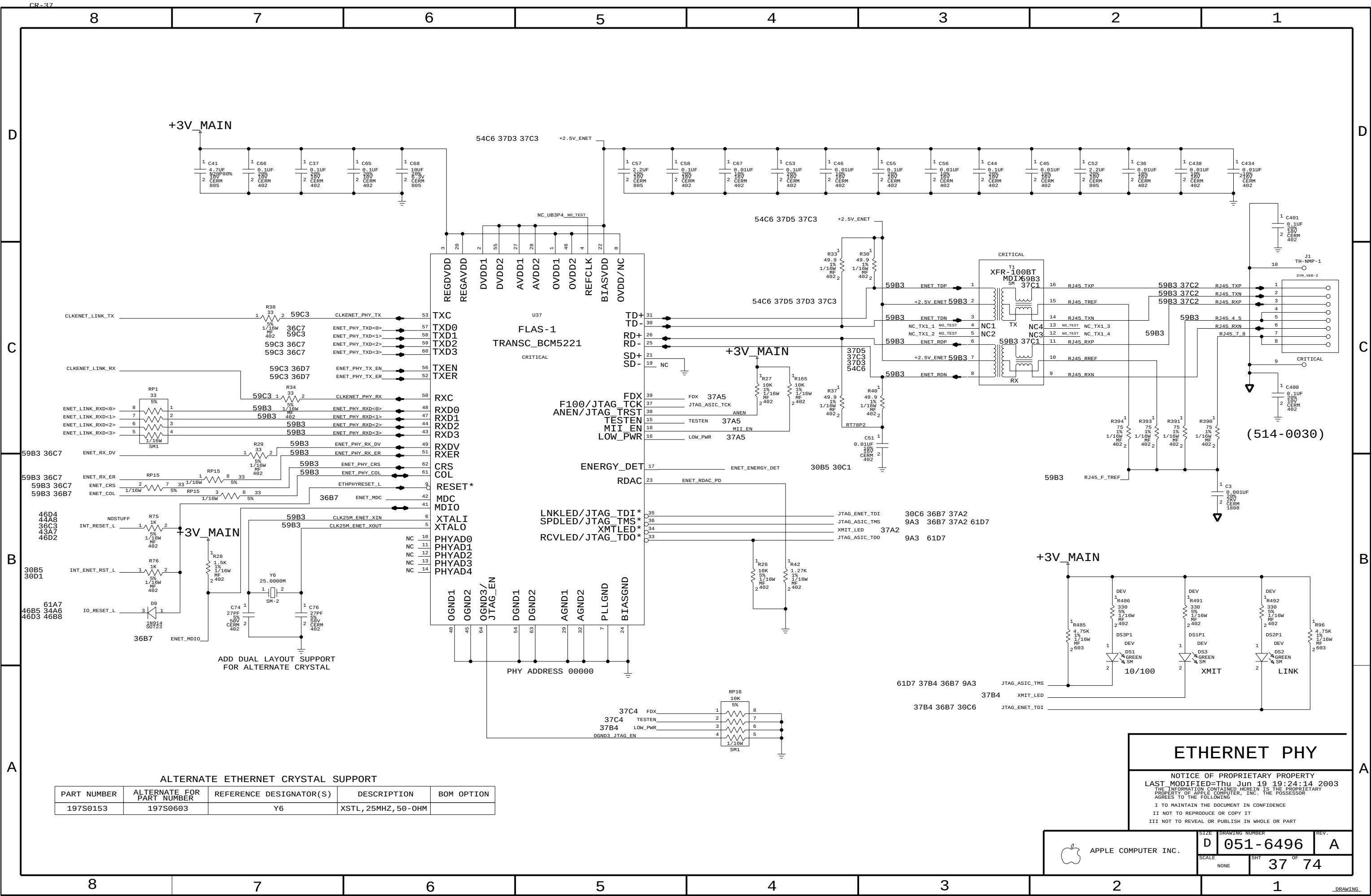
DRAWING NUMBER 051-6496

REV. A

SCALE NONE

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ALTERNATE ETHERNET CRYSTAL SUPPORT

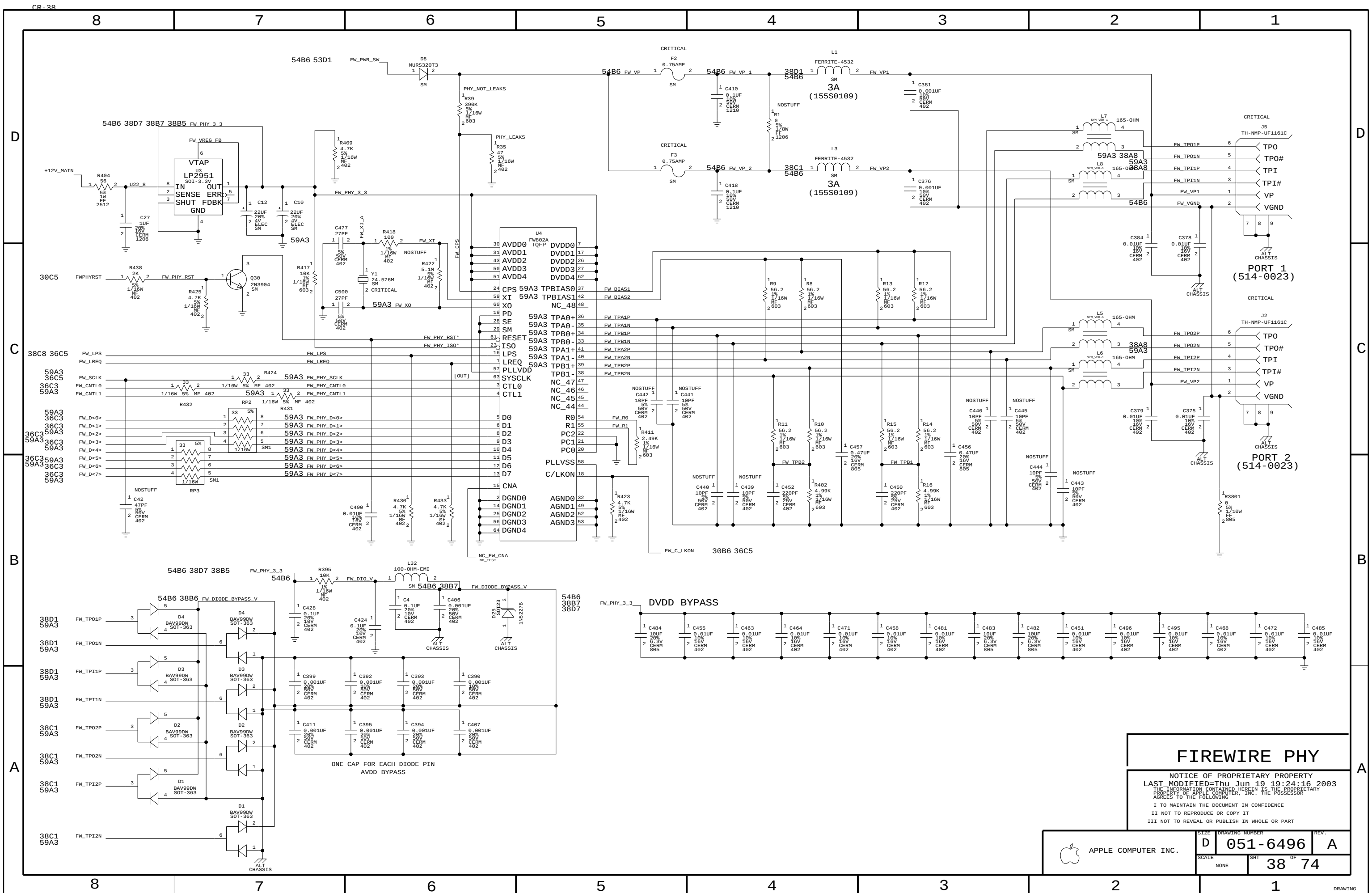
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197S0153	197S0603	Y6	XSTL, 25MHZ, 50-OHM	

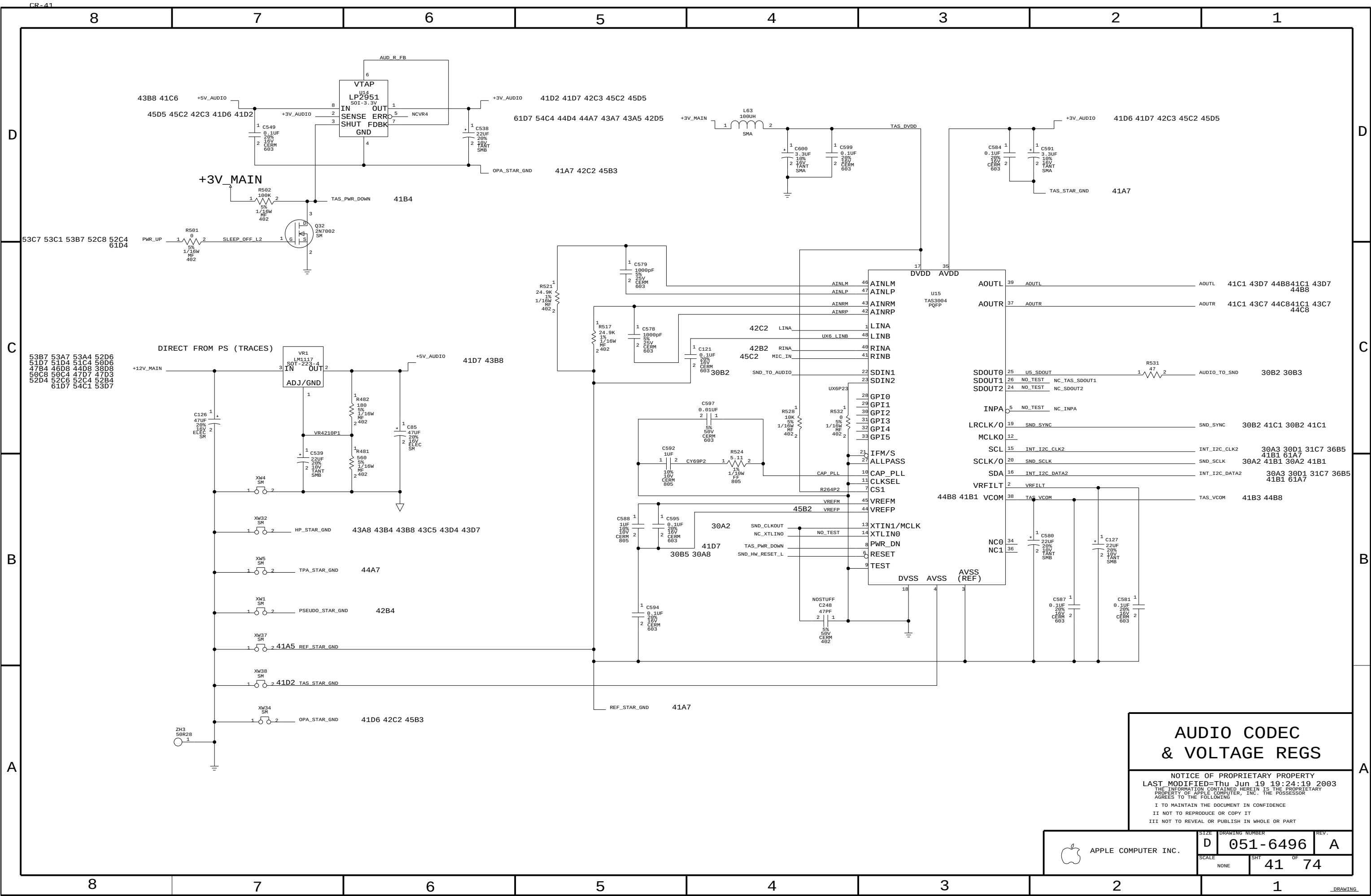
ETHERNET PHY

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AUDIO CODEC
& VOLTAGE REGS

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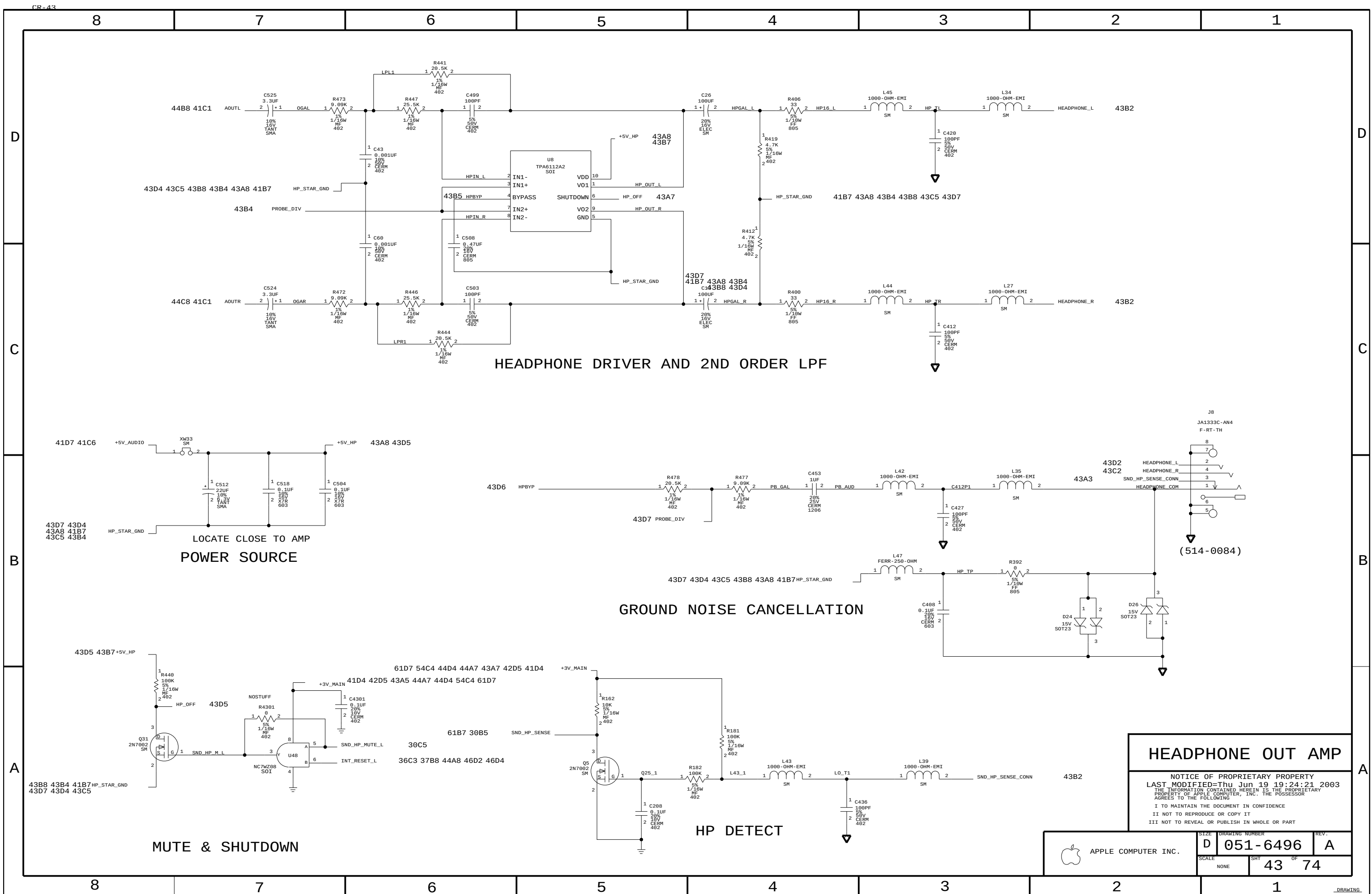
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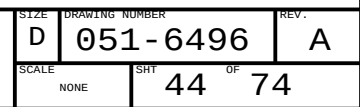
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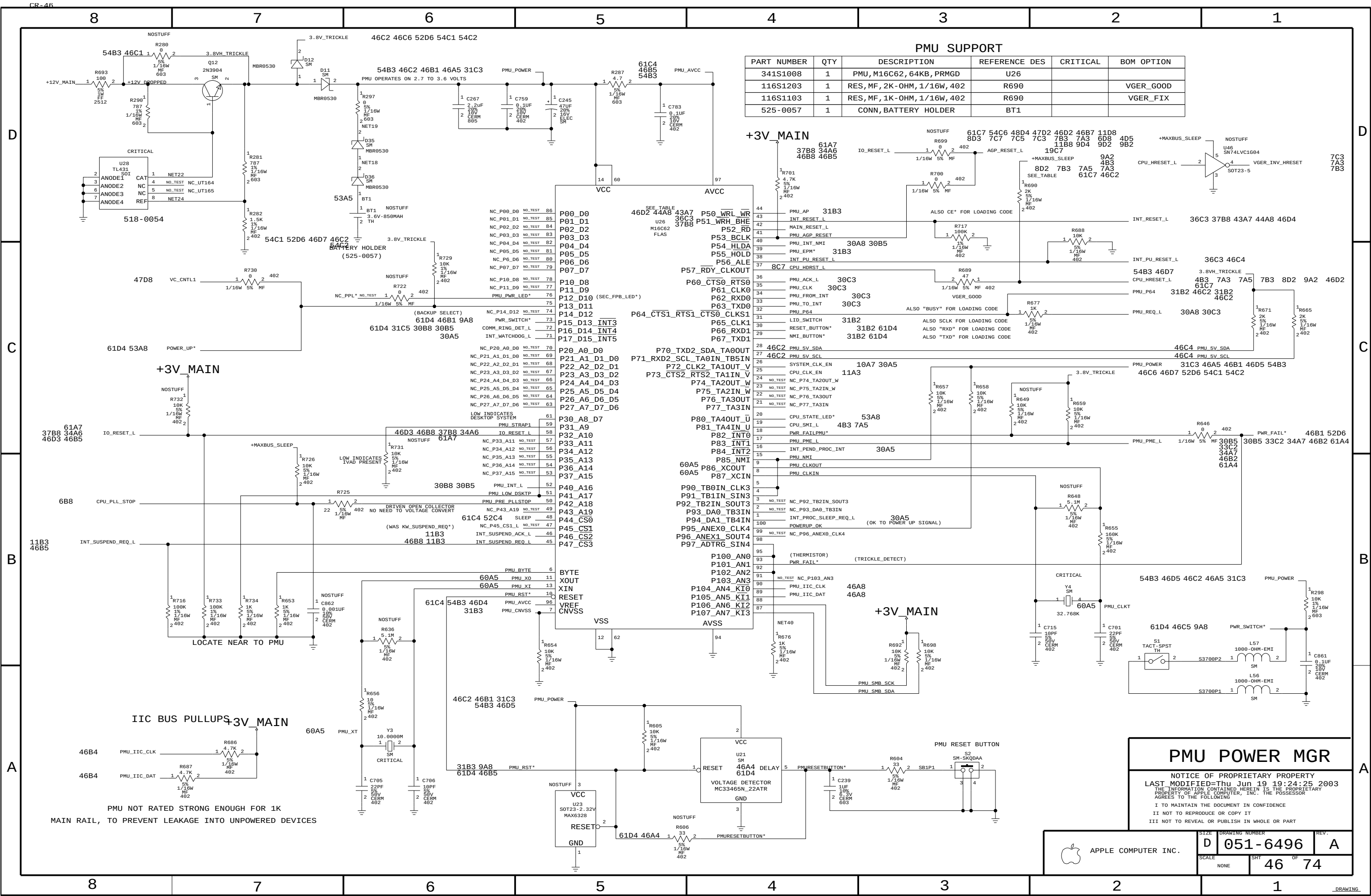
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PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
341S1008	1	PMU, M16C62, 64KB, PRMGD	U26		
116S1203	1	RES, MF, 2K-0HM, 1/16W, 402	R690		VGER_GOOD
116S1103	1	RES, MF, 1K-0HM, 1/16W, 402	R690		VGER_FIX
525-0057	1	CONN, BATTERY HOLDER	BT1		

PMU POWER MGR

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APPLE COMPUTER INC.

SIZE

D

DRAWING NUMBER

051-6496

REV.

A

SCALE

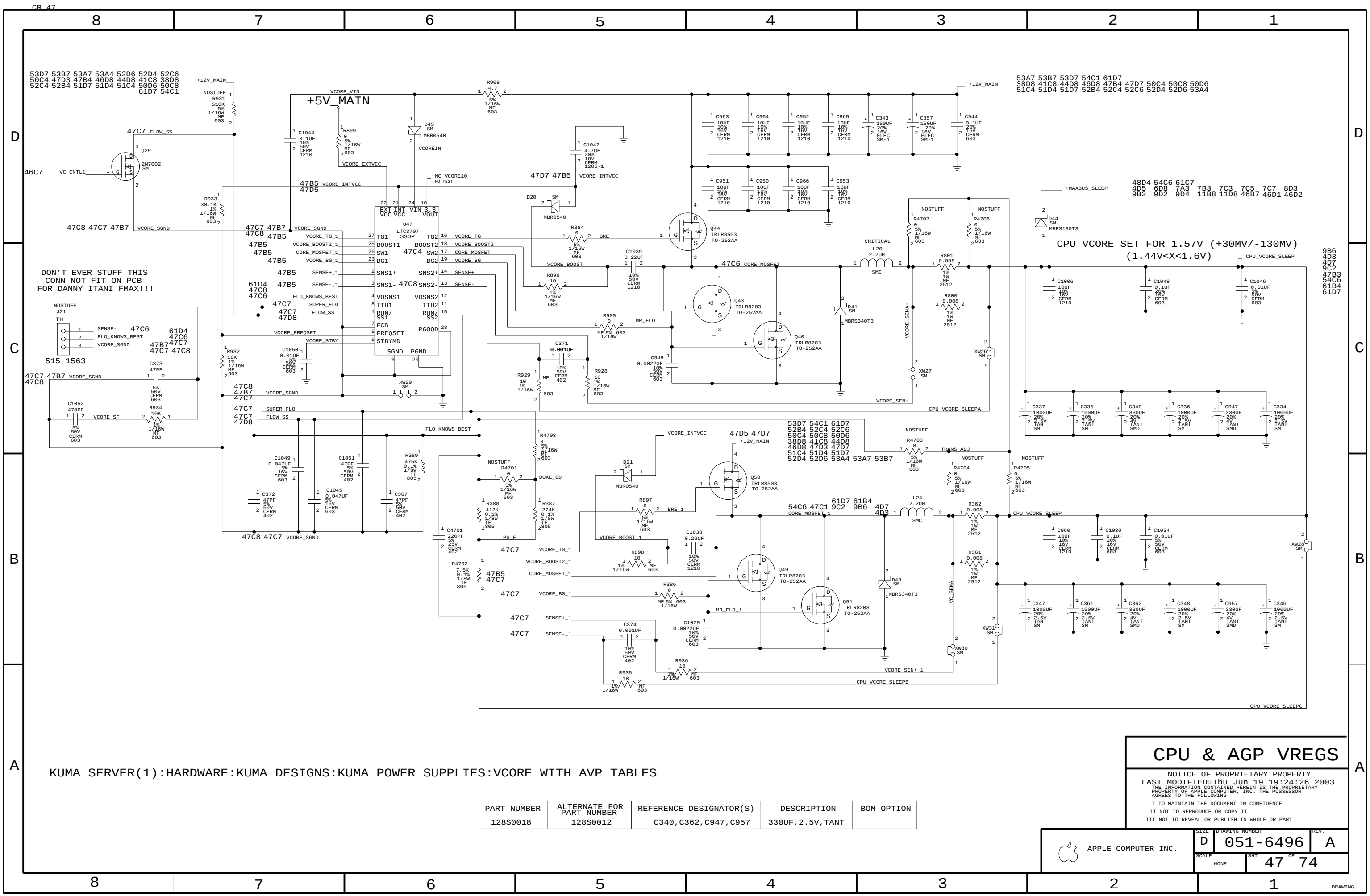
NONE

SHT

46

OF

74



KUMA SERVER(1):HARDWARE:KUMA DESIGNS:KUMA POWER SUPPLIES:Vcore WITH AVP TABLES

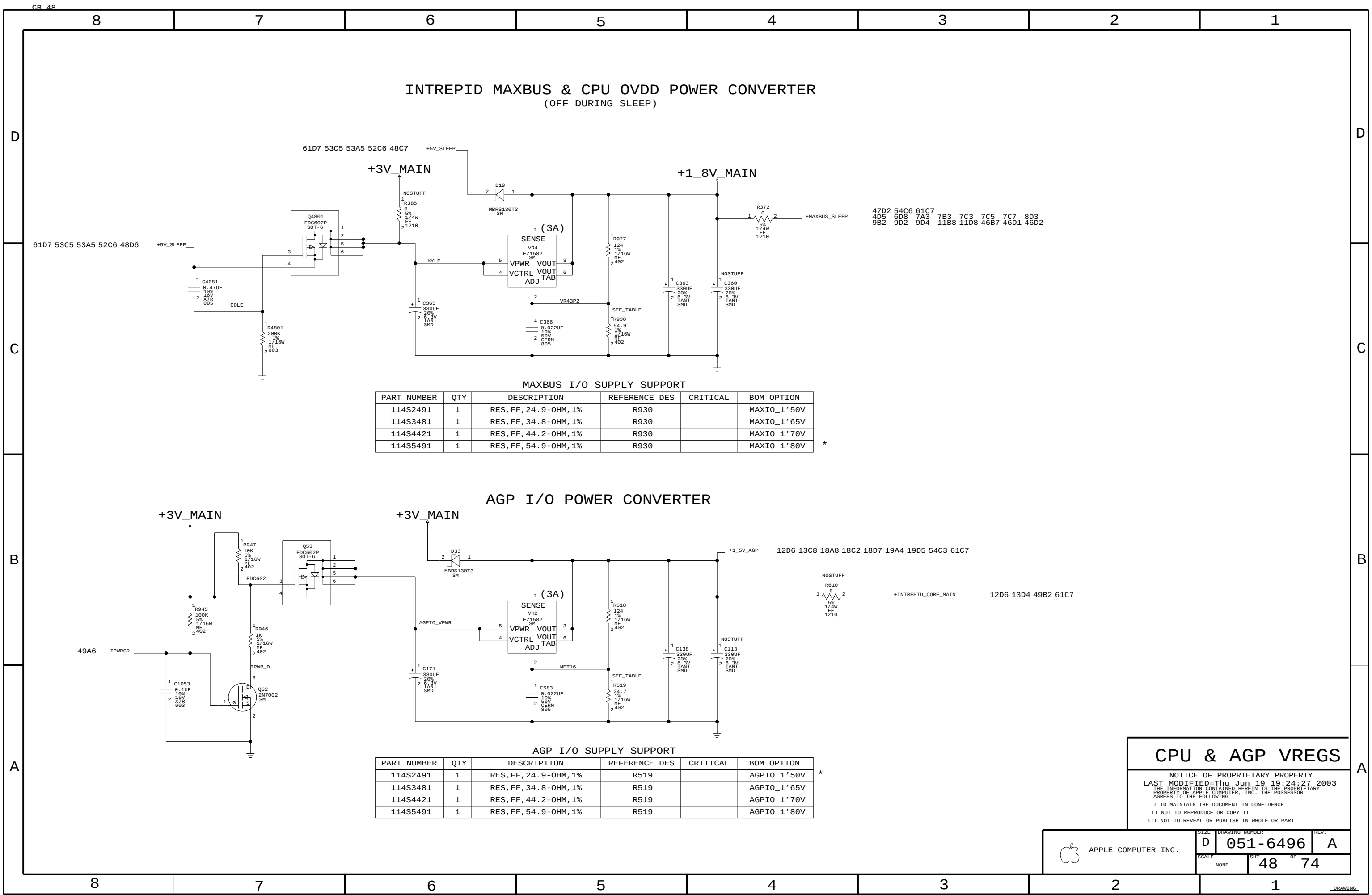
PART NUMBER	ALTERNATE FOR PART NUMBER	REFERENCE DESIGNATOR(S)	DESCRIPTION	BOM OPTION
128S0018	128S0012	C340, C362, C947, C957	330UF, 2.5V, TANT	

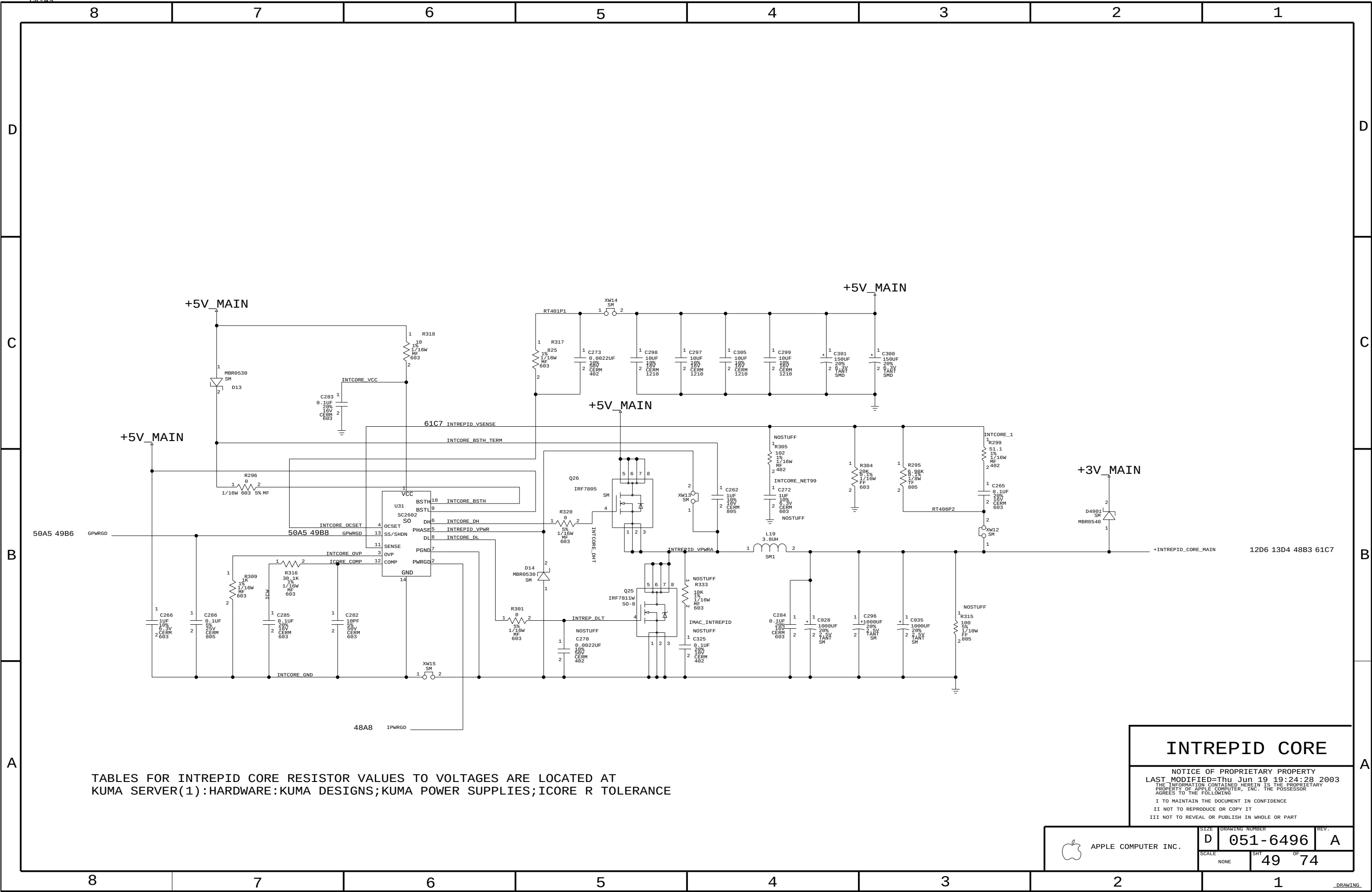
CPU & AGP VREGS

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APPLE COMPUTER INC.

SIZE	D	DRAWING NUMBER	051-6496	REV.	A
SCALE	NONE	SHT	47	OF	74





8 7 6 5 4 3 2 1

D

C

B

A

8 7 6 5 4 3 2 1

DRAWING

8 7 6 5 4 3 2 1

D

C

B

A

8 7 6 5 4 3 2 1

DRAWING

8 7 6 5 4 3 2 1

D

C

B

A

8 7 6 5 4 3 2 1

DRAWING

8 7 6 5 4 3 2 1

D

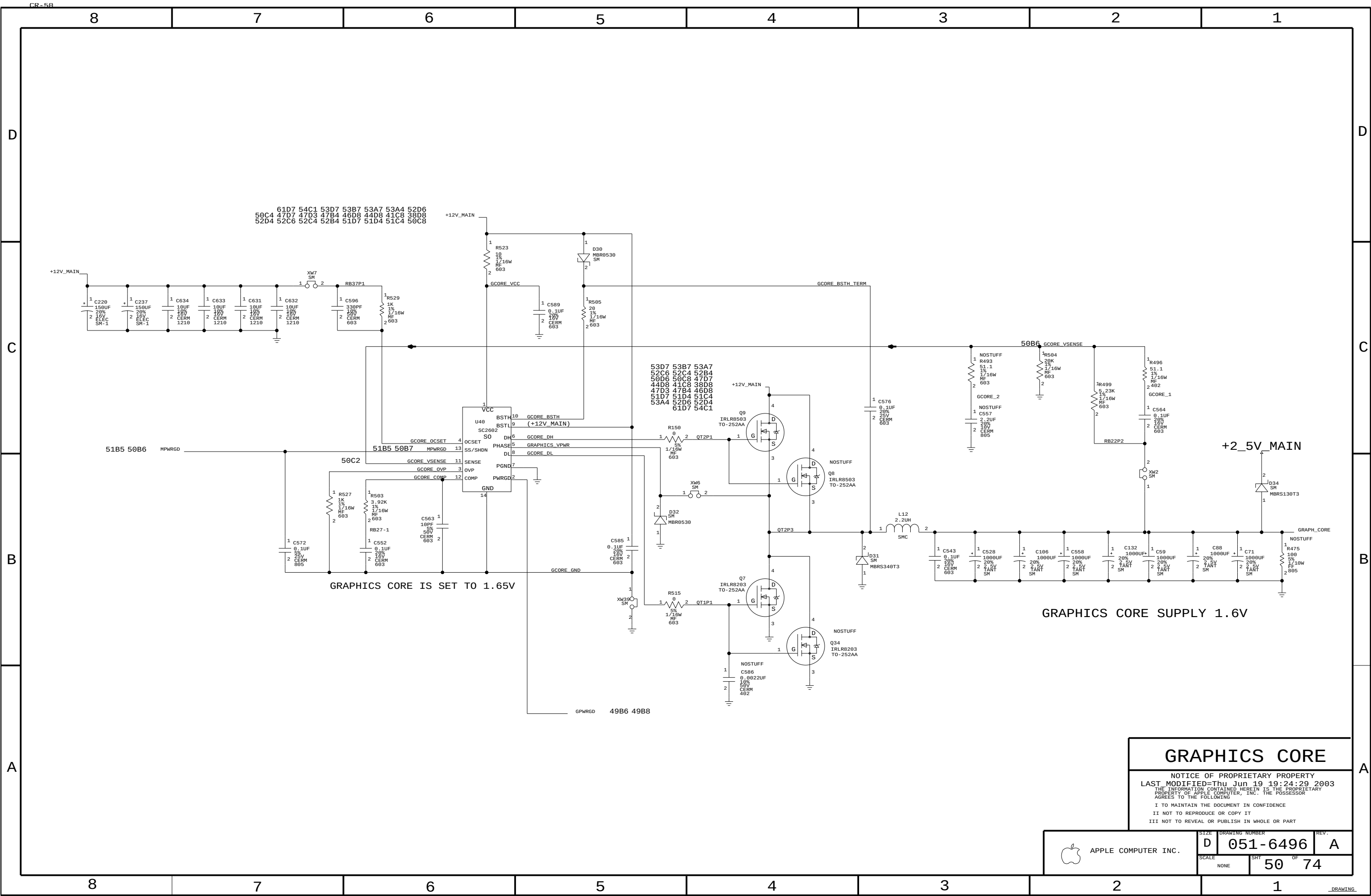
C

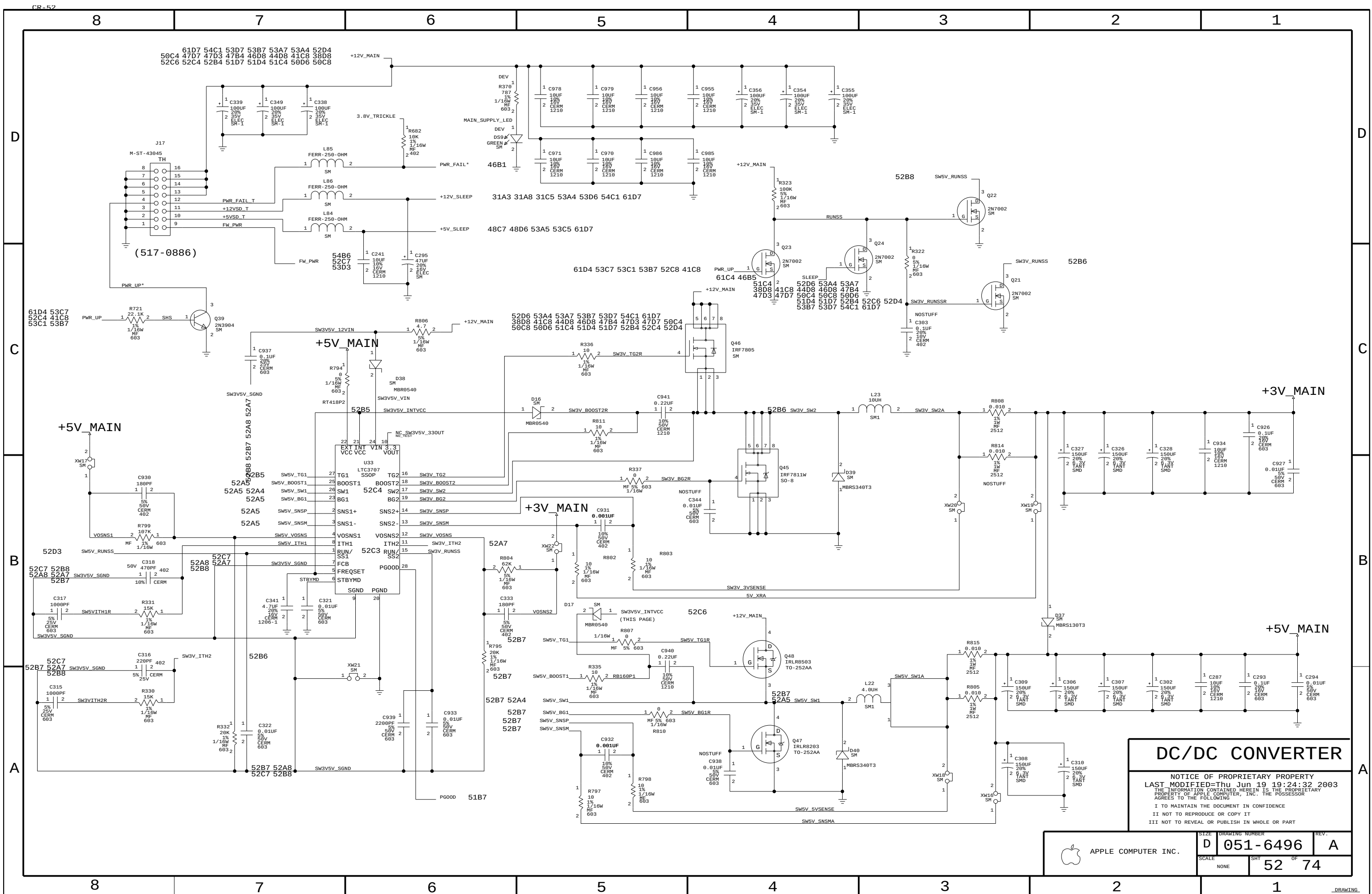
B

A

8 7 6 5 4 3 2 1

DRAWING

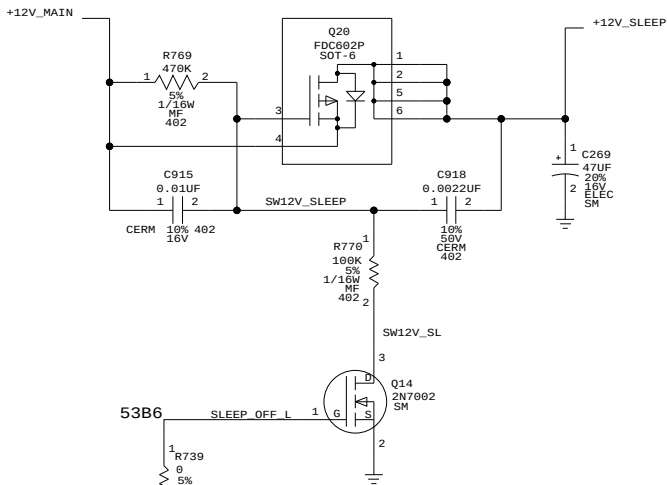




8 7 6 5 4 3 2 1

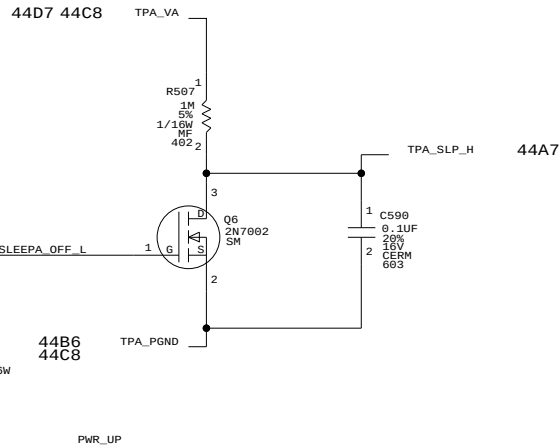
+12V MAIN POWER SWITCH
(OFF DURING SLEEP)

52C4 52B4 61D7 54C1 53B7
47B4 46D8 51D7 51D4 51C4
50D6 50C8 50C4 47D7 47D3
53A7 53A4 52D6 52D4 52C6



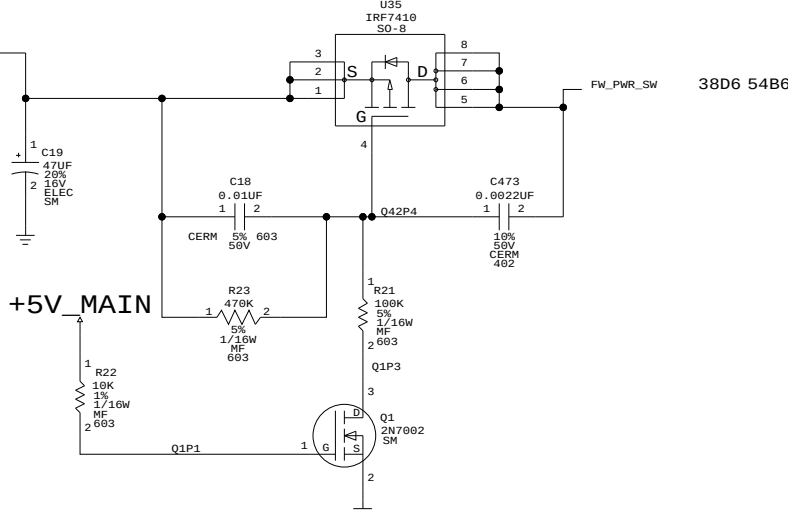
AUDIO SLEEP SWITCH
(OFF DURING SLEEP)

31A3 31A8 31C5 52D6 53A4 54C1 61D7



FIREWIRE POWER SWITCH

54B6 52C7



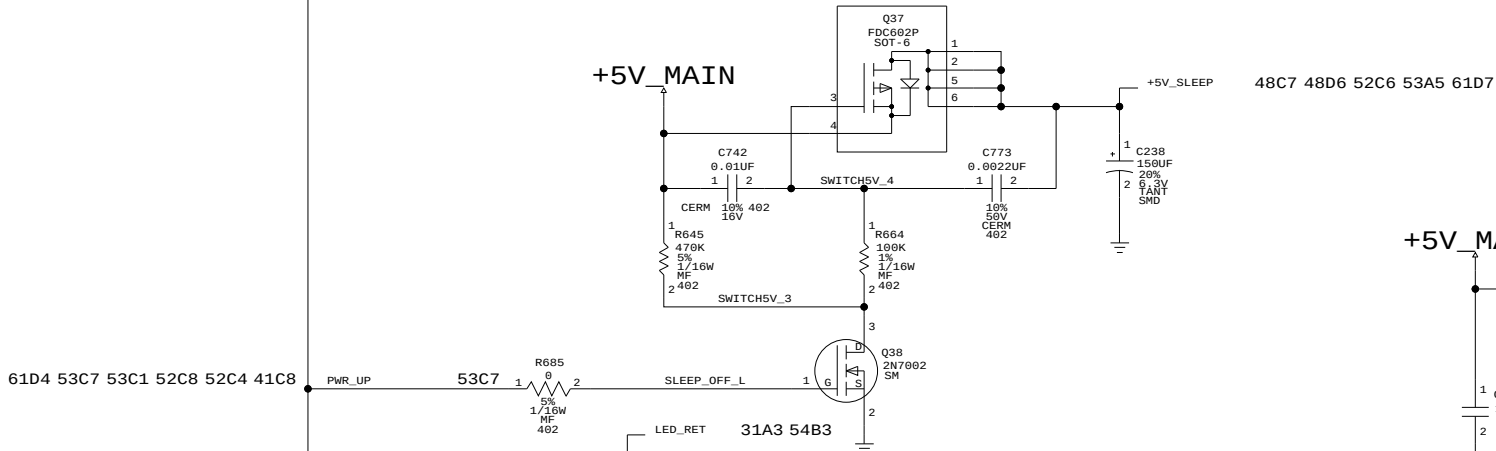
+5V MAIN

61D4 53C1 53B7 52C8 52C4 41C8

61D4 41C8 52C4 52C8 53B7 53C1 53C7

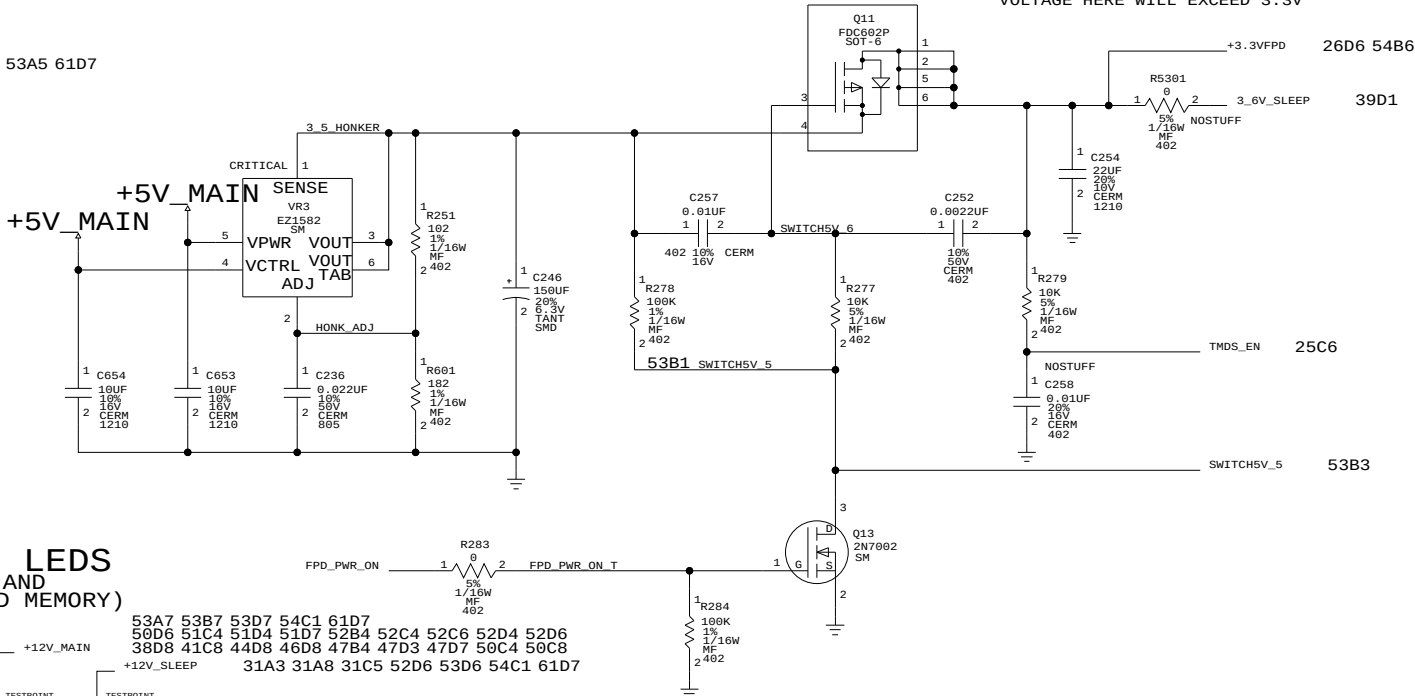
+5V POWER SWITCH
(OFF DURING SLEEP)

+5V MAIN



TMDS POWER CONVERTER & SWITCH
(OFF DURING SLEEP)

VOLTAGE TO SUPPORT 3.3V AT PANEL
VOLTAGE HERE WILL EXCEED 3.3V



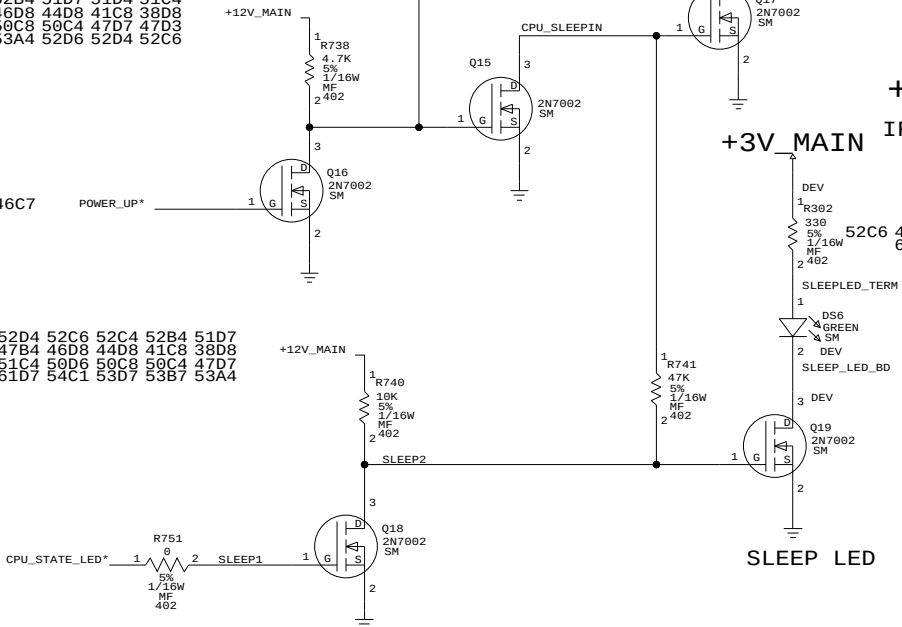
+5V POWER, SLEEP & TESTPOINT LEDES
(TESTPOINT LED IS USED FOR SERVICE AND
IF NOT ILLUMINATED, TELLS USER ITS OK TO ADD MEMORY)

+3V MAIN

+5V MAIN

52C4 52B4 61D7 54C1 53D7
47B4 46D8 51D7 51D4 51C4
50D6 50C8 50C4 47D7 47D3
53A7 53A4 52D6 52D4 52C6

52D6 52D4 52C6 52C4 52B4 51D7
47D3 47B4 46D8 44D8 41C8 38D8
51D4 51C4 50D6 50C8 50C4 47D7
61D7 54C1 53D7 53B7 53A4



GENERAL POWER LED
(NOT ILLUMINATED = OK TO ADD MEMORY)

**+5V/+12V, AUDIO
FW & TMDS PWR**

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APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6496	A
SCALE	SHEET	OF
NONE	53	74

8 7 6 5 4 3 2 1

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+2_5V_DDR_AVDD	10	2.5	20
+2_5V_DDR_VDD	10	2.5	20
+3V_INTSS_AVDD	10	3.3	20
+3V_INTSS_VDD	10	3.3	20
PCK2059_VDDI2C	10	2.5	20

OUT	+2_5V_DDR_AVDD	10	2.5	20	10D4
OUT	+2_5V_DDR_VDD	10	2.5	20	10D5
OUT	+3V_INTSS_AVDD	10	3.3	20	10B7
OUT	+3V_INTSS_VDD	10	3.3	20	10B6
OUT	PCK2059_VDDI2C	10	2.5	20	10D4

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+MAXBUS_SLEEP	10	1.8	20
CPU_AVDD	10	1.85	20
CPU_VCORE_SLEEP	10	1.85	20

OUT	+MAXBUS_SLEEP	10	1.8	20	4D5	6D8	7A3	7B3	7C3	7C5	7C7	8D3	9B2	9D2	9D4
OUT	CPU_AVD	10	1.85	20	4D3	11B8	11D8	46B7	46D1	46D2	47D2	48D4	61C7		
OUT	CPU_VCORE_SLEEP	10	1.85	20	4D3	4D7	9B6	9C2	47B3	47C1	61B4	61D7			

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+2.5V_ENET	10	2.5	20

OUT	+2.5V_ENET	10	2.5	20	37C3 37D3 37D5
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SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
FW_DIO_V	10	3.3	20
FW_DIODE_BYPASS_V	10	3.3	20
FW_PWR	10	24	20
FW_PWR_SW	10	24	20
FW_PHY_3_3	10	3.3	20
FW_VGND	10	0	20
FW_VP	10	12	20
FW_VP1	10	12	20
FW_VP2	10	12	20
FW_VP_1	10	12	20
FW_VP_2	10	12	20

OUT	FW_DIO_V	10	3.3	20	38B6
OUT	FW_DIODE_BYPASS_V	10	3.3	20	38B6 38B7
OUT	FW_PwR	10	24	20	52C7 53D3
OUT	FW_PwR_SW	10	24	20	38D6 53D1
OUT	FW_PHY_3_3	10	3.3	20	38B5 38B7 38D7
OUT	FW_VGND	10	0	20	38D1
OUT	FW_VP	10	12	20	38D5
OUT	FW_VP1	10	12	20	38D1 38D3
OUT	FW_VP2	10	12	20	38C1 38D3
OUT	FW_VP_1	10	12	20	38D4
OUT	FW_VP_2	10	12	20	38D4

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+3.3VFPD	10	3.6	20
DAC2VDD	10	3.3	20
DACVDD	10	3.3	20
DDC_VCC_3	10	3.3	20
DDC_VCC_5	10	5	20
DDR_VREF	10	1.25	20
IFP9AVCC	10	3.8	20
IFP9VREF	10	3.8	20
INT_TMSD_3V	10	3.6	20
GPU_AGP_VREF	10	0.75	20
GPU_FB_VREF	10	1.25	20
GRAPH_CORE	10	1.6	20
NVPLLVD	10	3.3	20
SGRAVREF	10	1.25	20
SGRBVREF	10	1.25	20

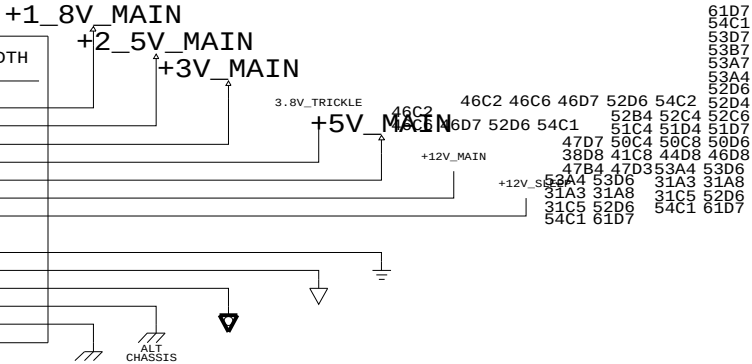
QNT	+3.3VFPD	10	3.6	20	26D6 53C1
QNT	DAC2VDD	10	3.3	20	24C5
QNT	DACVDD	10	3.3	20	24C4
QNT	DDC_VCC_3	10	3.3	20	26A5 26B3 61B7
QNT	DDC_VCC_5	10	5	20	27C4 61B7
QNT	DDR_VREF	10	1.25	20	16B2 16D1 16D8 17D8
QNT	IFPBAVCC	10	3.8	20	25A6 25B4
QNT	IFPBVREF	10	3.8	20	25B4
QNT	INT_TMSD_3V	10	3.6	20	26B3 26C3 26C4 26D5 61C7
QNT	GPU_AGP_VREF	10	0.75	20	19A3 19A8
QNT	GPU_FB_VREF	10	1.25	20	20C8
QNT	GRAPH_CORE	10	1.6	20	19D5 25C4 25C6 50B1
QNT	NVPLLVD	10	3.3	20	24D5
QNT	SGRAVREF	10	1.25	20	22A3 22C4 22C8
QNT	SRGBVREF	10	1.25	20	23A3 23C4 23C8

GPU_XTALSSIN	0	24B4
VIPCLK	0	24D4
GLOT_IOWAIT_L	3.3	39B7
EIDE_CSELP_L	0	40C6
EIDE_TOCS16_L	5	40C6
IUIDE_CSELP_L	0	40C2
UNUSED_ATAIOCS16_L	5	40C2

	SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
OUT	+1_5V_INTREPID_PLL	10	1.5	20
OUT	+1_5V_INTREPID_PLL1	10	1.5	20
OUT	+1_5V_INTREPID_PLL2	10	1.5	20
OUT	+1_5V_INTREPID_PLL3	10	1.5	20
OUT	+1_5V_INTREPID_PLL4	10	1.5	20
OUT	+1_5V_INTREPID_PLL5	10	1.5	20
OUT	+1_5V_INTREPID_PLL6	10	1.5	20
OUT	+1_5V_INTREPID_PLL7	10	1.5	20
OUT	+1_5V_INTREPID_PLL8	10	1.5	20
OUT	+1_5V_AGP	10	1.5	20
OUT	INT_AGP_VREF	10	0.75	20

OUT	+1_5V_INTREPID_PLL	10	1.5	20	1103 18D6 30D6 32D5
OUT	+1_5V_INTREPID_PLL1	10	1.5	20	30C4
OUT	+1_5V_INTREPID_PLL2	10	1.5	20	30D4
OUT	+1_5V_INTREPID_PLL3	10	1.5	20	30D4
OUT	+1_5V_INTREPID_PLL4	10	1.5	20	30D4
OUT	+1_5V_INTREPID_PLL5	10	1.5	20	18D5
OUT	+1_5V_INTREPID_PLL6	10	1.5	20	32D4
OUT	+1_5V_INTREPID_PLL7	10	1.5	20	11D2
OUT	+1_5V_INTREPID_PLL8	10	1.5	20	30D4
OUT	+1_5V_AGP	10	1.5	20	12D6 13C8 18A8 18C2 18D7 19A4 19D5 48B4 61C7
OUT	INT_AGP_VREF	10	0.75	20	18A7 18C6

	SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
UNIT	+1_8V_MAIN	10	1.8	20
UNIT	+2_5V_MAIN	10	2.5	20
UNIT	+3V_MAIN	10	3.3	20
UNIT	3.8V_TRICKLE	10	3.8	20
UNIT	+5V_MAIN	10	5	20
UNIT	+12V_MAIN	10	12	20
UNIT	+12V_SLEEP	10	12	20
UNIT	GND	10	0	20
UNIT	AGND	10	0	20
UNIT	ANALOGGND	10	0	20
UNIT	ALTCGND	10	0	20
UNIT	CHGND	10	0	20



	SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
QNT1	3.8VH_TRICKLE	10	3.8	20
QNT1	PMU_AVCC	10	3.5	20
QNT1	PMU_POWER	10	3.5	20

3.8VH_TRICKLE	10	3.8	20	46C1 46D7
PMU_AVCC	10	3.5	20	46B5 46D4 61C4
PMU_POWER	10	3.5	20	31C3 46A5 46B1 46C2 46D5

	SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
OUT	+12VSD_FILT	10	12	20
OUT	FAN_12V_FILT	10	12	20
OUT	KS5VSD	10	5	20
OUT	LED_5V	10	5	20
OUT	LED_5V_FILT	10	5	20
OUT	LED_RET	10	0	20
OUT	LED_RET_FILT	10	0	20

OUT	+12VSD_FILT	10	12	20	31A5
OUT	FAN_12V_FILT	10	12	20	31A5 61C7
OUT	K5VSD	10	5	20	31A5 61A7
OUT	LED_5V	10	5	20	31A8
OUT	LED_5V_FILT	10	5	20	31A5 61A7
OUT	LED_RET	10	0	20	31A3 53B6
OUT	LED_RET_FILT	10	0	20	31A5 61A7

	SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
⑩	+3V_INTREPID_USB	10	3.3	20
⑪	NEC_AVDD	10	3.3	20
⑫	USB_GND	10	0	20
⑬	USB_PORT_PWR	10	5	20
⑭	USB_PWR	10	5	20
⑮	USB_PWR_EN	10	0	20

OUT	+3V_INTREPID_USB	10	3.3	20	30C4
OUT	NEC_AVDD	10	3.3	20	34D5
OUT	USB_GND	10	0	20	35A4 35B3 35C3 35D3
OUT	USB_PORT_PWR	10	5	20	35A4 35B3 35C3 35D3
OUT	USB_PWR	10	5	20	61B4 27B5 27C2 27D3
OUT	USB_PWR_EN	10	0	20	35A6

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SIZE	DRAWING NUMBER	REV.
D	051-6496	
SCALE	SHT	OF
NONE	54	74

SIGNAL CONSTRAINTS

SIZE D	DRAWING NUMBER 051-6496	REV. A
SCALE NONE	SHT 55 OF 74	



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SIZE D	DRAWING NUMBER 051-6496	REV. A
SCALE NONE	SHT 56	OF 74

D

C

B

A

1

8		7		6		5		4		3		2		1	
DIGITAL SIGNALS															
GROUP		SIG_NAME	MATCHED_DELAY	MAX_VIAS	DELAY_RULE	STUB_LENGTH	NET_SPACING_TYPE	MIN_LINE_WIDTH	MAX_EXPOSED_LENGTH	PULSE_PARAM					
NEW STUFF HERE		VSYNC*		3	:::1000					24C5					
		ANALOG_VSYNC*		4	:::3500					24C8 27C6 28B5 61B7					
		HSYNC*		3	:::1000					24C5					
		ANALOG_HSYNC*		5	:::3500					24C8 27C4 27D6 28B5 61B7					
		ANALOG_BLU		4	:::4000	200	10 MIL SPACING	5.8		24C5 24C8 27C6					
		ANALOG_GRN		4	:::4000	200	10 MIL SPACING	5.8		24C5 24C8 27C6					
		ANALOG_RED		4	:::4000	200	10 MIL SPACING	5.8		24C5 24C8 27B6					
		FILT_ANALOG_RED		2	:::500		10 MIL SPACING	5.8		27C5 61B7					
		FILT_ANALOG_GRN		2	:::500		10 MIL SPACING	5.8		27C5 61B7					
		FILT_ANALOG_BLU		2	:::500		10 MIL SPACING	5.8		27C5 61B7					
		DAC2RSET			:::1000		10 MIL SPACING			24C6					
		DAC2VREF			:::1000		10 MIL SPACING			24C5					
		NV11_XTALIN		4	:::1000	100	8 MIL SPACING		27 MHZ	24B4					
		NV11_XTALOUT		4	:::1000	100	8 MIL SPACING		27 MHZ	24B4					
		INT_TMDS_CKP	TMDS:::120	3		50	8 MIL SPACING	TMDS_CLK		25D2 26B6 61A7					
		INT_TMDS_CKM	TMDS:::120	3		50	8 MIL SPACING	TMDS_CLK		25D2 26A6 61A7					
		INT_TMDS_D0P	TMDS:::120	3		50	8 MIL SPACING	TMDS_D0		25D2 26B6 61B7					
		INT_TMDS_D0M	TMDS:::120	3		50	8 MIL SPACING	TMDS_D0		25D2 26B6 61A7					
		INT_TMDS_D1P	TMDS:::120	3		50	8 MIL SPACING	TMDS_D1		25D2 26C6 61B7					
		INT_TMDS_D1M	TMDS:::120	3		50	8 MIL SPACING	TMDS_D1		25D2 26C6 61B7					
		INT_TMDS_D2P	TMDS:::120	3		50	8 MIL SPACING	TMDS_D2		25C2 26D6 61B7					
		INT_TMDS_D2M	TMDS:::120	3		50	8 MIL SPACING	TMDS_D2		25C2 26D6 61B7					
		TCKP	TMDSFILT:::50	2		20	8 MIL SPACING	TMDSFILT_CLK		26B5 26C4					
		TCKM	TMDSFILT:::50	2		20	8 MIL SPACING	TMDSFILT_CLK		26A5 26C3					
		TD0P	TMDSFILT:::50	2		20	8 MIL SPACING	TMDSFILT_D0		26B5 26C3					
		TD0M	TMDSFILT:::50	2		20	8 MIL SPACING	TMDSFILT_D0		26B5 26C4					
		TD1P	TMDSFILT:::50	2		20	8 MIL SPACING	TMDSFILT_D1		26C4 26C5					
		TD1M	TMDSFILT:::50	2		20	8 MIL SPACING	TMDSFILT_D1		26C3 26C5					
	TD2P	TMDSFILT:::50	2		20	8 MIL SPACING	TMDSFILT_D2		26C3 26D5						
	TD2M	TMDSFILT:::50	2		20	8 MIL SPACING	TMDSFILT_D2		26C4 26D5						
		ENET_LINK_TX_EN		4	:::1000					25 MHZ	36D6				
		ENET_LINK_TX_ER		4	:::1000					25 MHZ	36D6				
		ENET_LINK_TXD<0..3>		4	:::1000					25 MHZ	36C6				
		ENET_PHY_TX_EN		4	:::5600					25 MHZ	36D7 37C6				
		ENET_PHY_TX_ER		4	:::5600					25 MHZ	36D7 37C6				
		ENET_PHY_TXD<0..3>		4	:::4600:5600					25 MHZ	36C7 37C6				
		CLKENET_LINK_TX		4	:::4600:5600					25 MHZ	36D7 37C8				
		CLKENET_PHY_TX		4	:::1000					25 MHZ	37C6				
		CLKENET_LINK_RX		4	:::4600:5600					25 MHZ	36C7 37C8				
		CLKENET_PHY_RX		4	:::1000					25 MHZ	37C6				
		ENET_PHY_RXD<0..3>		4	:::1000					25 MHZ	37B6 37C6				
		ENET_PHY_RX_DV		4	:::1000					25 MHZ	37B6				
		ENET_PHY_RX_ER		4	:::1000					25 MHZ	37B6				
		ENET_PHY_CRS		4	:::1000					25 MHZ	37B6				
	ENET_PHY_COL		4	:::1000					25 MHZ	37B6					
		ENET_LINK_RXD<0..3>		4	:::4600:5600					25 MHZ	36C7 37B8 37C8				
		ENET_CRS		4	:::4600:5600					25 MHZ	36C7 37B8				
		ENET_COL		4	:::4600:5600					25 MHZ	36B7 37B8				
		ENET_RX_DV		4	:::4600:5600					25 MHZ	36C7 37B8				
		ENET_RX_ER		4	:::4600:5600					25 MHZ	36C7 37B8				
		CLK25M_ENET_XIN		3	:::1000	100	8 MIL SPACING			25 MHZ	37B6				
		CLK25M_ENET_XOUT		3	:::1000	100	8 MIL SPACING			25 MHZ	37B6				
		ENET_TDP	ETHTD:::70	3	:::4000	3150	10 MIL SPACING	ETH_TXD	100 MHZ	37C3					
		ENET_TDN	ETHTD:::70	3	:::4000	3150	10 MIL SPACING	ETH_TXD	100 MHZ	37C3					
		ENET_RDP	ETHRD:::70	3	:::4000	3150	10 MIL SPACING	ETH_RXD	100 MHZ	37C3					
		ENET_RDN	ETHRD:::70	3	:::4000	3150	10 MIL SPACING	ETH_RXD	100 MHZ	37C3					
		RJ45_TXP	RJTXD:::70	2	:::750		2KV ISO	RJ45_TXD	100 MHZ	37C1 37C2					
		RJ45_TXN	RJTXD:::70	2	:::750		2KV ISO	RJ45_TXD	100 MHZ	37C1 37C2					
		RJ45_RXP	RJRXD:::70	2	:::750		2KV ISO	RJ45_RXD	100 MHZ	37C1 37C2					
	RJ45_RXN	RJRXD:::70	2	:::750		2KV ISO	RJ45_RXD	100 MHZ	37C1 37C2						
		RJ45_TREF					2KV ISO			37C2					
		RJ45_RREF					2KV ISO			37C2					
		RJ45_4_5					2KV ISO			37C1					
		RJ45_7_8					2KV ISO			37C1					
		RJ45_F_TREF					2KV ISO			37B2					
		FW_LINK_DATA<0..7>		4	:::1000					36C5					
		FW_LINK_CNTL<0..1>		4	:::1000					49.152 MHZ	36C5				
		FW_LINK_LREQ		4	:::1000					49.152 MHZ	36C5				
		FW_SCLK		4	:::3500:4500					49.152 MHZ	36C5 38C8				
		FW_D<0..7>		4	:::3700:4700					49.152 MHZ	36C3 38B8 38C8				
		FW_CNTL0		4	:::3700:4700					49.152 MHZ	36C3 38C8				
		FW_CNTL1		4	:::3700:4700					49.152 MHZ	36C3 38C8				
		FW_LREQ		4	:::3700:4700					49.152 MHZ	36C3 38C8				
		FW_PHY_SCLK		4	:::500					49.152 MHZ	38C7				
	FW_PHY_CNTL0		4	:::1000					49.152 MHZ	38C7					
	FW_PHY_CNTL1		4	:::1000					49.152 MHZ	38C7					
	FW_PHY_D<0..7>		4	:::1000					49.152 MHZ	38B7 38C7					
	FW_XI		3	:::1000	100	8 MIL SPACING			24.576 MHZ	38C6					
	FW_X0		3	:::1000	100	8 MIL SPACING			24.576 MHZ	38C6					
	FW_BIAS1									38C5					
	FW_BIAS2									38C5					
		FW_TPA1P	FWTPA1:::50	3	:::1220	5000		FW_TPA1	400 MHZ	38C5					
		FW_TPA1N	FWTPA1:::50	3	:::1220	5000		FW_TPA1	400 MHZ	38C5					
		FW_TPB1P	FWTPB1:::50	3	:::1220	5000		FW_TPB1	400 MHZ	38C5					
		FW_TPB1N	FWTPB1:::50	3	:::1220	5000		FW_TPB1	400 MHZ	38C5					
		FW_TPA2P	FWTPA2:::50	3	:::1220	5000		FW_TPA2	400 MHZ	38C5					
		FW_TPA2N	FWTPA2:::50	3	:::1220	5000		FW_TPA2	400 MHZ	38C5					
		FW_TPB2P	FWTPB2:::50	3	:::1220	5000		FW_TPB2	400 MHZ	38C5					
		FW_TPB2N	FWTPB2:::50	3	:::1220	5000		FW_TPB2	400 MHZ	38C5					
		FW_TPO1P	FWTPO1:::50	3	:::1220	5000		FW_TPO1	400 MHZ	38B8 38D1					
		FW_TPO1N	FWTPO1:::50	3	:::1220	5000		FW_TPO1	400 MHZ	38A8 38D1					
		FW_TPI1P	FWTPL1:::50	3	:::1220	5000		FW_TPL1	400 MHZ	38A8 38D1					
		FW_TPI1N	FWTPL1:::50	3	:::1220	5000		FW_TPL1	400 MHZ	38A8 38D1					
		FW_TPO2P	FWTPO2:::50	3	:::1220	5000		FW_TPO2	400 MHZ	38A8 38C1					
		FW_TPO2N	FWTPO2:::50	3	:::1220	5000		FW_TPO2	400 MHZ	38A8 38C1					
	FW_TPI2P	FWTPL2:::50	3	:::1220	5000		FW_TPL2	400 MHZ	38A8 38C1						
	FW_TPI2N	FWTPL2:::50	3	:::1220	5000		FW_TPL2	400 MHZ	38A8 38C1						

SIGNAL CONSTRAINTS

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<table><tr><th>GROUP</th><th>SIG_NAME</th><th>MATCHED_DELAY</th><th>MAX_VIAS</th><th>DELAY_RULE</th><th>STUB_LENGTH</th><th>NET_SPACING_TYPE</th><th>MAX_EXPOSED_LENGTH</th><th>PULSE_PARAM</th></tr><tr><td rowspan="38">CD DRIVE BUS</td><td>EIDE_RST_L</td><td></td><td></td><td>:::3500:5500</td><td></td><td></td><td></td><td>33 MHZ 39A7 39D5</td></tr><tr><td>EIDE_DMACK_L</td><td></td><td></td><td>:::3500:5500</td><td></td><td></td><td></td><td>33 MHZ 39A7 39D5</td></tr><tr><td>EIDE_STOP</td><td></td><td></td><td>:::5500</td><td></td><td></td><td></td><td>33 MHZ 39A7 39D5</td></tr><tr><td>EIDE_HSTB_RDY</td><td></td><td></td><td>:::5500</td><td></td><td></td><td></td><td>33 MHZ 39A7 39C5</td></tr><tr><td>EIDE_DSTB_RDY</td><td></td><td></td><td>:::3500:5500</td><td></td><td></td><td></td><td>33 MHZ 39A7 39C5</td></tr><tr><td>EIDE_DATA<0..15></td><td></td><td></td><td>:::3500:5500</td><td></td><td></td><td></td><td>33 MHZ 39A5 39B5 39B7 39C5</td></tr><tr><td>CD_RESET_L</td><td></td><td></td><td>:::1000</td><td></td><td></td><td></td><td>33 MHZ 39D4 40C6</td></tr><tr><td>CD_DMACK_L</td><td></td><td></td><td>:::4000</td><td></td><td></td><td></td><td>33 MHZ 39D4 40C6</td></tr><tr><td>CD_STOP</td><td></td><td></td><td>:::5000</td><td></td><td></td><td></td><td>33 MHZ 39D4 40C6</td></tr><tr><td>CD_HSTB_RDY</td><td></td><td></td><td>:::5000</td><td></td><td></td><td></td><td>33 MHZ 39C4 40C6</td></tr><tr><td>CD_DSTB_RDY</td><td></td><td></td><td>:::1000</td><td></td><td></td><td></td><td>33 MHZ 39C4 40C6</td></tr><tr><td>UATA0<0..15></td><td></td><td></td><td>:::1000</td><td></td><td></td><td></td><td>33 MHZ 39A4 39B4 39C4 40C6</td></tr><tr><td>CD_DMARQ</td><td></td><td></td><td>:::1000</td><td></td><td></td><td></td><td>33 MHZ 40C6</td></tr><tr><td>EIDE_DMARQ</td><td></td><td></td><td>:::3500:5500</td><td></td><td></td><td></td><td>33 MHZ 39A7 40C8</td></tr><tr><td>UATA0IRQ</td><td></td><td></td><td>:::1000</td><td></td><td></td><td></td><td>33 MHZ 40C6</td></tr><tr><td>EIDE_INTRQ</td><td></td><td></td><td>:::3500:5500</td><td></td><td></td><td></td><td>33 MHZ 39A7 40C8</td></tr><tr><td>CD_EIDE_ADDR<0..2></td><td></td><td></td><td>:::1000</td><td></td><td></td><td></td><td>33 MHZ 40C6</td></tr><tr><td>EIDE_ADDR<0..2></td><td></td><td></td><td>:::3500:5500</td><td></td><td></td><td></td><td>33 MHZ 39B7 40A8 40B8</td></tr><tr><td>CD_CS1FX_L</td><td></td><td></td><td>:::1000</td><td></td><td></td><td></td><td>33 MHZ 40C6</td></tr><tr><td>EIDE_CS1FX_L</td><td></td><td></td><td>:::3500:5500</td><td></td><td></td><td></td><td>33 MHZ 39A7 40B8</td></tr><tr><td>CD_CS3FX_L</td><td></td><td></td><td>:::1000</td><td></td><td></td><td></td><td>33 MHZ 40C6</td></tr><tr><td>EIDE_CS3FX_L</td><td></td><td></td><td>:::3500:5500</td><td></td><td></td><td></td><td>33 MHZ 39A7 40B8</td></tr><tr><td rowspan="55">HD DRIVE BUS</td><td>UIDE_RST_L</td><td>HD_DATA:::5500</td><td></td><td>:::100:6000</td><td>500</td><td></td><td></td><td>100 MHZ 39C7 39D3</td></tr><tr><td>UIDE_DMACK_L</td><td>HD_DATA:::5500</td><td></td><td>:::100:6000</td><td>500</td><td></td><td></td><td>100 MHZ 39C7 39D3</td></tr><tr><td>UIDE_DIOR_L</td><td>HD_DATA:::5500</td><td></td><td>:::100:6000</td><td></td><td></td><td></td><td>100 MHZ 39C7 39D3</td></tr><tr><td>UIDE_DIOW_L</td><td>HD_DATA:::5500</td><td></td><td>:::6000</td><td></td><td></td><td></td><td>100 MHZ 39C3 39C7</td></tr><tr><td>UIDE_IOCHRDY</td><td>HD_DATA:::5500</td><td></td><td>:::100:6000</td><td>500</td><td></td><td></td><td>100 MHZ 39C3 39C7</td></tr><tr><td>UIDE_DATA<0..15></td><td>HD_DATA:::5500</td><td></td><td>:::100:6000</td><td></td><td></td><td></td><td>100 MHZ 39A3 39B3 39C3 39C7 39D7</td></tr><tr><td>HD_RESET_L</td><td></td><td></td><td>:::1000</td><td></td><td></td><td></td><td>100 MHZ 39D1 40C3</td></tr><tr><td>HD_DMACK_L</td><td></td><td></td><td>:::1000</td><td></td><td></td><td></td><td>100 MHZ 39D1 40C3</td></tr><tr><td>HD_DIOR_L</td><td></td><td></td><td>:::5500</td><td></td><td></td><td></td><td>100 MHZ 39D1 40C3</td></tr><tr><td>HD_DIOW_L</td><td></td><td></td><td>:::55000</td><td></td><td></td><td></td><td>100 MHZ 39C1 40C3</td></tr><tr><td>HD_IOCHRDY</td><td></td><td></td><td>:::1000</td><td></td><td></td><td></td><td>100 MHZ 39C1 40C3</td></tr><tr><td>HD_DMARQ</td><td>HD_DATA:::5500</td><td></td><td>:::100:6000</td><td></td><td></td><td></td><td>100 MHZ 40C3</td></tr><tr><td>UIDE_DMARQ</td><td>HD_DATA:::5500</td><td></td><td>:::100:6000</td><td></td><td></td><td></td><td>100 MHZ 39C7 40C4</td></tr><tr><td>HD_INTRQ</td><td></td><td></td><td>:::1000</td><td>500</td><td></td><td></td><td>100 MHZ 40C3</td></tr><tr><td>UIDE_INTRQ</td><td>HD_DATA:::5500</td><td></td><td>:::100:6000</td><td></td><td></td><td></td><td>100 MHZ 39C7 40C4</td></tr><tr><td>HD_UIDE_ADDR<0..2></td><td></td><td></td><td>:::1000</td><td></td><td></td><td></td><td>100 MHZ 40C2 40C3</td></tr><tr><td>UIDE_ADDR<0..2></td><td>HD_DATA:::5500</td><td></td><td>:::100:6000</td><td></td><td></td><td></td><td>100 MHZ 39C7 40A4 40B4</td></tr><tr><td>HD_UIDE_CS1FX_L</td><td></td><td></td><td>:::6000</td><td></td><td></td><td></td><td>100 MHZ 40C3</td></tr><tr><td>UIDE_CS1FX_L</td><td>HD_DATA:::5500</td><td></td><td>:::6000</td><td></td><td></td><td></td><td>100 MHZ 39C7 40B4</td></tr><tr><td>HD_UIDE_CS3FX_L</td><td></td><td></td><td>:::6000</td><td></td><td></td><td></td><td>100 MHZ 40C2</td></tr><tr><td>UIDE_CS3FX_L</td><td>HD_DATA:::5500</td><td></td><td>:::6000</td><td></td><td></td><td></td><td>100 MHZ 39C7 40B4</td></tr><tr><td>CLK_18M_INT_XOUT</td><td></td><td>3</td><td>:::1000</td><td>100</td><td>8 MIL SPACING</td><td></td><td>18.432 MHZ 60B5</td></tr><tr><td>CLK_18M_INT_XOUT</td><td></td><td>3</td><td>:::1000</td><td>100</td><td>8 MIL SPACING</td><td></td><td>18.432 MHZ 60B5</td></tr><tr><td>CLK_18M_INT_XOUT</td><td></td><td>3</td><td>:::200</td><td>50</td><td>8 MIL SPACING</td><td></td><td>18.432 MHZ 60B5</td></tr><tr><td>USB_DAP</td><td>USBA:::500</td><td></td><td></td><td></td><td></td><td></td><td>30B3</td></tr><tr><td>USB_DAN</td><td>USBA:::500</td><td></td><td></td><td></td><td></td><td></td><td>30B3</td></tr><tr><td>USB_DAP_F</td><td>USBA_F:::500</td><td></td><td></td><td>100</td><td></td><td></td><td>30B2 35B7</td></tr><tr><td>USB_DAN_F</td><td>USBA_F:::500</td><td></td><td></td><td>100</td><td></td><td></td><td>30B2 35B7</td></tr><tr><td>USB_DBP</td><td>USBB:::500</td><td></td><td></td><td></td><td></td><td></td><td>30B3</td></tr><tr><td>USB_DBN</td><td>USBB:::500</td><td></td><td></td><td></td><td></td><td></td><td>30B3</td></tr><tr><td>USB_DBP_F</td><td>USBB_F:::500</td><td></td><td></td><td>100</td><td></td><td></td><td>30B2 35C7</td></tr><tr><td>USB_DBN_F</td><td>USBB_F:::500</td><td></td><td></td><td>100</td><td></td><td></td><td>30B2 35C7</td></tr><tr><td>USB_DCP</td><td>USBC:::500</td><td></td><td></td><td></td><td></td><td></td><td>30B3</td></tr><tr><td>USB_DCN</td><td>USBC:::500</td><td></td><td></td><td></td><td></td><td></td><td>30B3</td></tr><tr><td>USB_DCP_F</td><td>USBC_F:::500</td><td></td><td></td><td>100</td><td></td><td></td><td>30B2 35D7</td></tr><tr><td>USB_DCN_F</td><td>USBC_F:::500</td><td></td><td></td><td>100</td><td></td><td></td><td>30B2 35D7</td></tr><tr><td>USB_DEP</td><td>USBE:::500</td><td></td><td></td><td></td><td></td><td></td><td>30B3</td></tr><tr><td>USB_DEN</td><td>USBE:::500</td><td></td><td></td><td></td><td></td><td></td><td>30B3</td></tr><tr><td>BT_USB_DP</td><td>USBE_F:::500</td><td></td><td></td><td>100</td><td></td><td></td><td>30B2 31D3 61B4</td></tr><tr><td>BT_USB_DM</td><td>USBE_F:::500</td><td></td><td></td><td>100</td><td></td><td></td><td>30B2 31D3 61B4</td></tr><tr><td>USB_DFP</td><td>USBF:::500</td><td></td><td></td><td></td><td></td><td></td><td>30B3</td></tr><tr><td>USB_DFN</td><td>USBF:::500</td><td></td><td></td><td></td><td></td><td></td><td>30B3</td></tr><tr><td>MODEM_USB_DP</td><td>USBF_F:::500</td><td></td><td></td><td>100</td><td></td><td></td><td>30B2 31C5 61B4</td></tr><tr><td>MODEM_USB_DM</td><td>USFE_F:::500</td><td></td><td></td><td>100</td><td></td><td></td><td>30B2 31C5 61B4</td></tr><tr><td>PMU_X0</td><td></td><td>3</td><td>:::1000</td><td>100</td><td>8 MIL SPACING</td><td>10 MHZ</td><td>46B5</td></tr><tr><td>PMU_XI</td><td></td><td>3</td><td>:::1000</td><td>100</td><td>8 MIL SPACING</td><td>10 MHZ</td><td>46B5</td></tr><tr><td>PMU_XT</td><td></td><td>3</td><td>:::300</td><td>50</td><td>8 MIL SPACING</td><td>10 MHZ</td><td>46A6</td></tr><tr><td>PMU_CLKOUT</td><td></td><td>3</td><td>:::1000</td><td>100</td><td>8 MIL SPACING</td><td>32.768 MHZ</td><td>46B4</td></tr><tr><td>PMU_CLKIN</td><td></td><td>3</td><td>:::1000</td><td>100</td><td>8 MIL SPACING</td><td>32.768 MHZ</td><td>46B4</td></tr><tr><td>PMU_CLKT</td><td></td><td>3</td><td>:::300</td><td>50</td><td>8 MIL SPACING</td><td>32.768 MHZ</td><td>46B2</td></tr><tr><td>MICSHLD</td><td></td><td></td><td></td><td></td><td>10 MIL SPACING</td><td></td><td>31A5 45B8 61A7</td></tr><tr><td>MICHIGH</td><td></td><td></td><td></td><td></td><td>10 MIL SPACING</td><td></td><td>31A5 45C8 61A7</td></tr><tr><td>MICLOW</td><td></td><td></td><td></td><td></td><td>10 MIL SPACING</td><td></td><td>31A5 45B8 61A7</td></tr><tr><td>KS_INT_SPKR+</td><td></td><td></td><td></td><td></td><td>10 MIL SPACING</td><td></td><td>31A3 44C3 61B7</td></tr><tr><td>KS_INT_SPKR-</td><td></td><td></td><td></td><td></td><td>10 MIL SPACING</td><td></td><td>31A3 44C3 61B7</td></tr></table>																GROUP	SIG_NAME	MATCHED_DELAY	MAX_VIAS	DELAY_RULE	STUB_LENGTH	NET_SPACING_TYPE	MAX_EXPOSED_LENGTH	PULSE_PARAM	CD DRIVE BUS	EIDE_RST_L			:::3500:5500				33 MHZ 39A7 39D5	EIDE_DMACK_L			:::3500:5500				33 MHZ 39A7 39D5	EIDE_STOP			:::5500				33 MHZ 39A7 39D5	EIDE_HSTB_RDY			:::5500				33 MHZ 39A7 39C5	EIDE_DSTB_RDY			:::3500:5500				33 MHZ 39A7 39C5	EIDE_DATA<0..15>			:::3500:5500				33 MHZ 39A5 39B5 39B7 39C5	CD_RESET_L			:::1000				33 MHZ 39D4 40C6	CD_DMACK_L			:::4000				33 MHZ 39D4 40C6	CD_STOP			:::5000				33 MHZ 39D4 40C6	CD_HSTB_RDY			:::5000				33 MHZ 39C4 40C6	CD_DSTB_RDY			:::1000				33 MHZ 39C4 40C6	UATA0<0..15>			:::1000				33 MHZ 39A4 39B4 39C4 40C6	CD_DMARQ			:::1000				33 MHZ 40C6	EIDE_DMARQ			:::3500:5500				33 MHZ 39A7 40C8	UATA0IRQ			:::1000				33 MHZ 40C6	EIDE_INTRQ			:::3500:5500				33 MHZ 39A7 40C8	CD_EIDE_ADDR<0..2>			:::1000				33 MHZ 40C6	EIDE_ADDR<0..2>			:::3500:5500				33 MHZ 39B7 40A8 40B8	CD_CS1FX_L			:::1000				33 MHZ 40C6	EIDE_CS1FX_L			:::3500:5500				33 MHZ 39A7 40B8	CD_CS3FX_L			:::1000				33 MHZ 40C6	EIDE_CS3FX_L			:::3500:5500				33 MHZ 39A7 40B8	HD DRIVE BUS	UIDE_RST_L	HD_DATA:::5500		:::100:6000	500			100 MHZ 39C7 39D3	UIDE_DMACK_L	HD_DATA:::5500		:::100:6000	500			100 MHZ 39C7 39D3	UIDE_DIOR_L	HD_DATA:::5500		:::100:6000				100 MHZ 39C7 39D3	UIDE_DIOW_L	HD_DATA:::5500		:::6000				100 MHZ 39C3 39C7	UIDE_IOCHRDY	HD_DATA:::5500		:::100:6000	500			100 MHZ 39C3 39C7	UIDE_DATA<0..15>	HD_DATA:::5500		:::100:6000				100 MHZ 39A3 39B3 39C3 39C7 39D7	HD_RESET_L			:::1000				100 MHZ 39D1 40C3	HD_DMACK_L			:::1000				100 MHZ 39D1 40C3	HD_DIOR_L			:::5500				100 MHZ 39D1 40C3	HD_DIOW_L			:::55000				100 MHZ 39C1 40C3	HD_IOCHRDY			:::1000				100 MHZ 39C1 40C3	HD_DMARQ	HD_DATA:::5500		:::100:6000				100 MHZ 40C3	UIDE_DMARQ	HD_DATA:::5500		:::100:6000				100 MHZ 39C7 40C4	HD_INTRQ			:::1000	500			100 MHZ 40C3	UIDE_INTRQ	HD_DATA:::5500		:::100:6000				100 MHZ 39C7 40C4	HD_UIDE_ADDR<0..2>			:::1000				100 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D	EIDE_RST_L	39A7 39D5 60D5		FW_LINK_CNTL<1..0>	36C5 59A3	GPU_FB_VREF	20C8 54A6	INT_AGP_VREF	18A7 18C6 54C3						
	EIDE_STOP	39A7 39D5 60D5		FW_LINK_DATA<7..0>	36C5 59A3	GPU_FPBCLK	25C4	INT_ANALYZER_CLK	9B4 11B4 18C7 56A7 58B3 61A7						
	ENET_COL	36B7 37B8 59B3		FW_LINK_LREQ	36C5 59A3	GPU_FPBCLK_L	25C4	INT_ANALYZER_CLKA	11B3						
	ENET_CRS	36C7 37B8 59B3		FW_LPS	36C5 38C8	GPU_FW_PME_L	25C4	INT_CLOCK_OUT	9C4 58B3						
	ENET_ENERGY_DET	30B5 30C1 37B4		FW_LREQ	36C3 38C8 59A3	GPU_IFB1IOVDD	25B2	INT_CPU_FB_IN	11B3 58C3						
	ENET_LINK_RXD<7..0>	36C6 36C7 37B8 37C8 59B3		FW_PHY_3_3	38B5 38B7 38D7 54B6	GPU_IFP1PLLVDD	25B2	INT_CPU_FB_OUT	11B3 58C3						
	ENET_LINK_TXD<3..0>	36C6 59C3		FW_PHY_CNTL0	38C7 59A3	GPU_MBDL_L	19A7	INT_ENET_RST_L	30B5 30D1 37B8						
	ENET_LINK_TX_EN	36D6 59C3		FW_PHY_CNTL1	38C7 59A3	GPU_STEREO	25C4	INT_EXTINT3_PU	30B5 30B8						
	ENET_LINK_TX_ER	36D6 59C3		FW_PHY_D<7..0>	38B7 38C7 59A3	GPU_STRAP<3..0>	24B4 28A3 28B3 28D3	INT_EXTINT12_PU	30B5 30B8						
	ENET_MDC	36B7 37B6		FW_PHY_ISO*	38C6	GPU_SWAP_A	25C4	INT_EXTINT13_PU	30B5 30B8						
C	ENET_MDIO	36B7 37B7		FW_PHY_RST	38C8	GPU_SWAP_B	25C4	INT_EXTINT17_PU	30B5 30B8 34B6						
	ENET_PHY_COL	37B6 59B3		FW_PHY_RST*	38C6	GPU_TESTMECLK	25C4	INT_GPIO1_PD	30A8 30C5						
	ENET_PHY_CRS	37B6 59B3		FW_PHY_SCLK	38C7 59A3	GPU_TMODE	19A5 54A6	INT_GPIO9_PU	30B5 30B8						
	ENET_PHY_RXD<3..0>	37B6 37C6 59B3		FW_PINT	36B5 36C1	GPU_XTALSSIN	24B4 54A6	INT_GPIO12_PU	30A8 30B5						
	ENET_PHY_RX_DV	37B6 59B3		FW_PWR	52C7 53D3 54B6	GPWRGD	49B6 49B8 50A5	INT_I2C_CLK0	8A6 8B4 8B7 10C7 16A6 17A6 36B3						
	ENET_PHY_RX_ER	37B6 59B3		FW_PWR_SW	38D6 53D1 54B6	GRAPHICS_VPWR	50B5	INT_I2C_CLK0R	36B5 36C1						
	ENET_PHY_TXD<3..0>	36C7 37C6 59C3		FW_R0	38C5	GRAPH_CORE	19D5 25C4 25C6 50B1 54A6	INT_I2C_CLK1	10A7 36A3 36A4 36B1						
	ENET_PHY_TX_EN	36D7 37C6 59C3		FW_R1	38C5	GRAPH_DDC_SCL	24D5 26A6	INT_I2C_CLK2	30A3 30D1 31C7 36B5 41B1 61A7						
	ENET_PHY_TX_ER	36D7 37C6 59C3		FW_SCLK	36C5 38C8 59A3	GRAPH_DDC_SDA	24D5 26A6	INT_I2C_DATA0	8A6 8B4 8B7 10C7 16A6 17A6 36B3						
	ENET_RDAC_PD	37B5		FW_TPA1N	38C5 59A3	GRAPH_IIC_SCL2	25D2	INT_I2C_DATA0R	36B5 36C1						
B	ENET_RDN	37C3 59B3		FW_TPA1P	38C5 59A3	GRAPH_IIC_SDA2	25D2	INT_I2C_DATA1	10A7 36A3 36B1						
	ENET_RDP	37C3 59B3		FW_TPA2N	38C5 59A3	HD_DIOR_L	39D1 40C3 60C5	INT_I2C_DATA2	30A3 30D1 31C7 36B5 41B1 61A7						
	ENET_RX_DV	36C7 37B8 59B3		FW_TPA2P	38C5 59A3	HD_DIOW_L	39C1 40C3 60C5	INT_JTAG_TEI	36B7 36C1						
	ENET_RX_ER	36C7 37B8 59B3		FW_TPB1	38B3	HD_DMACK_L	39D1 40C3 60C5	INT_MEM_REF	14B6						
	ENET_TDN	37C3 59B3		FW_TPB1N	38C5 59A3	HD_DMARQ	40C3 60C5	INT_MOD_BITCLK	30A3 30A8 30C6						
	ENET_TDP	37C3 59B3		FW_TPB1P	38C5 59A3	HD_INTRQ	40C3 60C5	INT_MOD_CLKOUT	30A3 30A8 30C6						
	ETHPHYRESET_L	37B6		FW_TPB2	38B4	HD_IOCHRDY	39C1 40C3 60C5	INT_MOD_DTI	30A3 30A8 30C6						
	EXTINT14	30A8 30B5		FW_TPB2N	38C5 59A3	HD_RESET_L	39D1 40C3 60C5	INT_MOD_DTO	30A3 30A8 30C6						
	FAN_12V_FILT	31A5 54B3 61C7		FW_TPB2P	38C5 59A3	HD_UIDE_ADDR<2..0>	40C2 40C3 60C5	INT_MOD_SYNC	30A3 30A8 30C6						
	FBA<12..0>	20C8 20D8 20E3 20F3 57D3		FW_TPI1N	38A8 38D1 59A3	HD_UIDE_CS1FX_L	40C3 60B5	INT_PCI_FB_IN	32C5 56C7						
A	FBABA<1..0>	20C8 20E3 57D3		FW_TPI1P	38A8 38D1 59A3	HD_UIDE_CS3FX_L	40C2 60B5	INT_PCI_FB_OUT	32C5 56D7						
	FBACAS_L	20C8 20G3 57D3		FW_TPI2N	38A8 38C1 59A3	HEADPHONE_COM	43B2	INT_PEND_PROC_INT	30A5 46B4						
	FBACKE	20D3 20D7 57D3		FW_TPI2P	38A8 38C1 59A3	HEADPHONE_L	43B2 43D2	INT_PLL1_GND	30A5 30C4						
	FBACLK0	20D7 21C3 57C3		FW_TP01N	38A8 38D1 59A3	HEADPHONE_R	43B2 43C2	INT_PLL2_GND	30A5 30D4						
	FBACLK0_L	20D7 21C3 57C3		FW_TP01P	38B8 38D1 59A3	HONK_ADJ	53B4	INT_PLL3_GND	30A4 30D4						
	FBACLK1	20D7 21D3 57C3		FW_TP02N	38A8 38C1 59A3	HP16_L	43D4	INT_PLL4_GND	32B4 32D4						
	FBACLK1_L	20D7 21D3 57C3		FW_TP02P	38A8 38C1 59A3	HP16_R	43C4	INT_PLL5_GND	18A5 18D5						
	FBACS0_L	20C8 20F3 57D3		FW_VGND	38D1 54B6	HPBYP	43B5 43D6	INT_PLL6_GND	11A2 11D3						
	FBARAS_L	20C8 20G3 57D3		FW_VP	38D5 54B6	HPGAL_L	43D4	INT_PLL7_GND	30A4 30D4						
	FBAWE_L	20C8 20F3 57D3		FW_VP1	38D1 38D3 54B6	HPGAL_R	43C4	INT_PLL9_GND	30A4 30D4						

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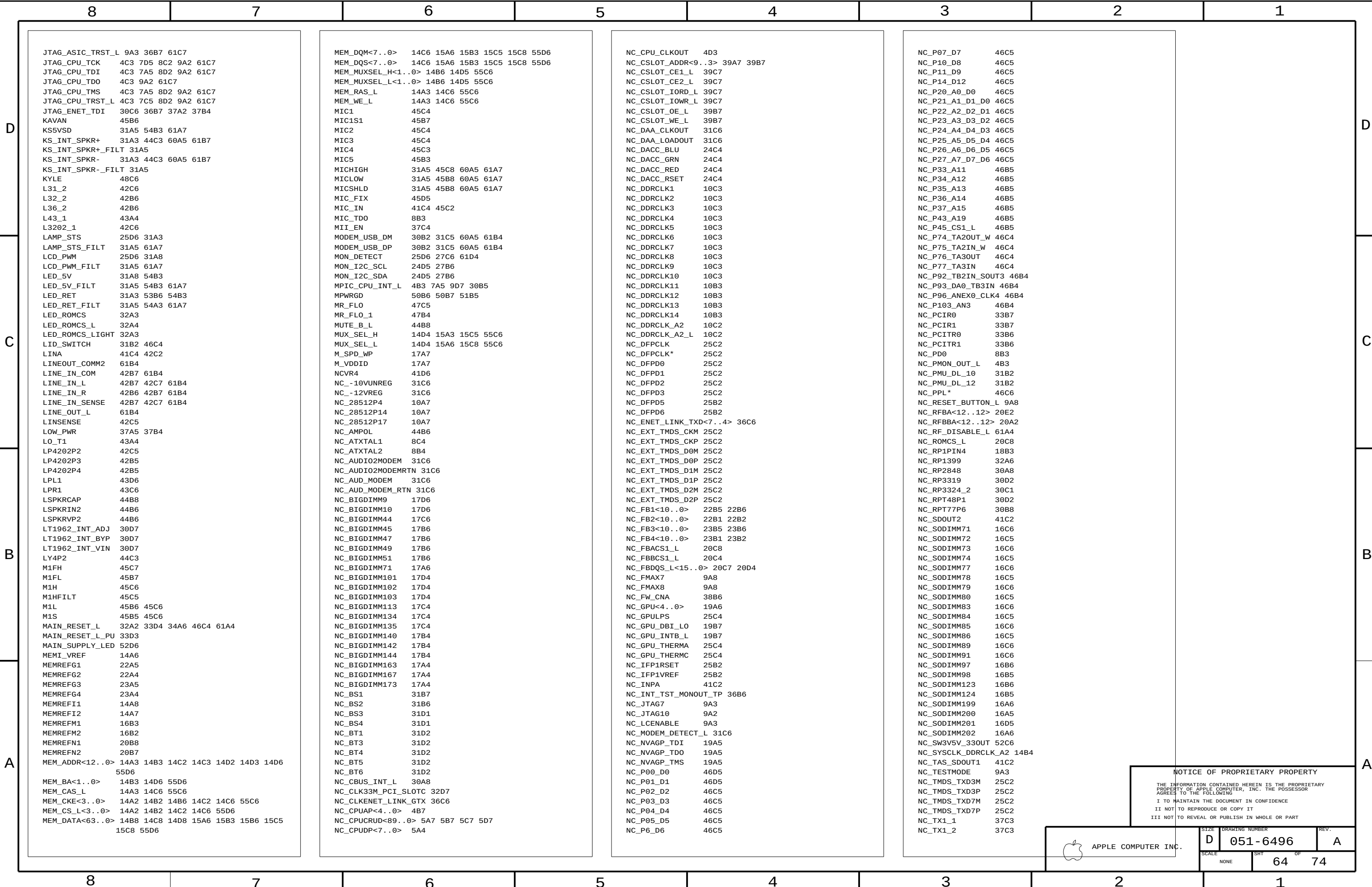
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D	NC_TX1_3	37C2			PB_AUD	43B4			PWR_UP	41C8 52C4 52C8 53B7 53C1 53C7 61D4			RJ45_RXP	37C1 37C2 59B3	
	NC_TX1_4	37C2			PB_GAL	43B4			PWR_UP*	52C8			RJ45_TREF	37C2 59B3	
	NC_UB3P4	37D5			PCIT_AD<31..0>	33B2 33B3 33B6 33B7 33C2 33C3 33C6			Q1P1	53C3			RJ45_TXN	37C1 37C2 59B3	
	NC_USB2_AMC	34A4				33C7 56C7 61B2 61C2			Q1P3	53C2			RJ45_TXP	37C1 37C2 59B3	
	NC_USB2_NANDTEST	34A4			PCIT_CBE<31..0>	33B2 33B6 33C3 56C7 61A4			Q25_1	43A5			ROMA14	20C8 28D8	
	NC_USB2_NTST1	34A4			PCIT_DEVSEL_L	33B6 33C2 56C7			Q42P4	53D2			ROMA15	20C8 28D8	
	NC_USB2_PPON1	34B4			PCIT_FRAME_L	33B6 33C2 56C7			QT1P1	50B4			ROM_CS_L	32B4 32B6 33B4 61C7	
	NC_USB2_PPON2	34B4			PCIT_IRDY_L	33B6 33C3 56C7 61B4			QT2P1	50B4			ROM_OE_L	32B2 32B6 33B2 61C7	
	NC_USB2_PPON3	34B4			PCIT_PAR	33B6 33C2 56C7			QT2P3	50B4			ROM_ONBOARD_CS_L	32B2 33B4 61D4	
	NC_USB2_PPON4	34B4			PCIT_STOP_L	33B6 33C2 56C7			R264P2	41B4			ROM_RW_L	32B2 32B6 33B4 61B7	
NC_USB2_PPON5	34B4			PCIT_TRDY_L	33B6 33C2 56C7			RAM_ADDR<12..0>	14A3 14B3 14C1 14C3 14D1 14D3 16B4			ROM_WP_L	32A2 61A7		
NC_USB2_RSDEM	34C4			PCI_AD<31..0>	32B1 32B2 32C4 32D4 33B6 33B7 33C6				16B6 17B4 17B6 17C4 17C6 55D6			RSPKRCAP	44C8		
NC_USB2_RSDEP	34C4				33C7 34B6 34B7 34C6 55A6 61B2 61C2			RAM_BA<1..0>	14B3 16B4 16B6 17B6 55D6			RSPKRIN1	44B6		
NC_USB2_RSDFM	34C4			PCI_CBE<3..0>	32C5 33B7 34B6 55A6 61A4			RAM_CAS_L	14A3 16B4 17B6 55C6			RSPKRV1	44B6		
NC_USB2_RSDFP	34B4			PCI_DEVSEL_L	32A8 32C5 33B7 34B6 56D7 61A4			RAM_CKE<3..0>	14A1 14B1 14C1 16B4 16B6 17C4 17C6			RT78P2	37C4		
NC_USB2_SMC	34A4			PCI_FBI_EQUAL	32C7 56C7				55C6			RT373P1	53A5		
NC_USB2_SMI_L	34A6			PCI_FBI_PLUS2	32C7 56C7			RAM_CS_L<3..0>	14A1 14B1 14C1 16B4 16B6 17B4 55C6			RT401P1	49C5		
NC_USB2_SRCLK	34A4			PCI_FBO_PLUS2	32C7 56D7			RAM_DATA_A<63..0>	15B2 15B6 15C2 15C4 15C7 15D4			RT406P2	49B3		
NC_USB2_SRDTA	34A4			PCI_FB_PLUS4	32C8 56C7				15D7 16A4 16A6 16B4 16B6 16C4 16C6			RT418P2	52C7		
NC_USB2_SRMOD	34A4			PCI_FB_PLUS6	32C7 56C7				16D4 16D6 55D6			RUNLED1	53A5		
NC_USB2_TEB	34A4			PCI_FRAME_L	32B8 32C5 33B7 34B6 55A6 61A4			RAM_DATA_B<63..0>	15A2 15A6 15B2 15B4 15B7 15C4			RUNSS	52D4		
NC_USB2_TEST	34A4			PCI_IRDY_L	32B8 32C5 33B7 34B6 56D7 61A4				15C7 17A4 17A6 17B4 17B6 17C4 17C6			S3700P1	46A1		
NC_USB_M	33B2			PCI_PAR	32C5 33B7 34B6 56D7 61A4				17D4 17D6 55D6			S3700P2	46A1		
NC_USB_P	33B2			PCI_SLOTB_GNT_L	32A6 32D5 33C2 61A4			RAM_DQM_A<7..0>	15A6 15B2 15B6 15C4 15C7 15D4 15D7			SB1P1	46A3		
NC_UT6P6	30D8			PCI_SLOTB_REQ_L	32A8 32D5 33C3 61A4				16A4 16B4 16C4 16D4 55D6			SENSE+	47C6		
NC_UT6P7	30D8			PCI_SLOTC_GNT_L	32A6 32D5			RAM_DQM_B<7..0>	15A2 15A6 15B2 15B4 15B7 15C4 15C7			SENSE+_1	47B5 47C7		
NC_UT164	46D7			PCI_SLOTC_REQ_L	32A8 32D5				17A4 17B4 17C4 17D4 55D6			SENSE-	47C6 47C8		
NC_UT165	46D7			PCI_SLOTD_GNT_L	32A6 32C5 34B6			RAM_DQS_A<7..0>	15B2 15B6 15C4 15C7 15D4 15D7 16A6			SENSE-_1	47B5 47C7		
NC_VCORE10	47D6			PCI_SLOTD_PERR_L	34B6				16B6 16C6 16D6 55D6			SGRAVREF	22A3 22C4 22C8 54A6		
NC_VIPHCLK	24D5			PCI_SLOTD_REQ_L	32A8 32D5 34B6			RAM_DQS_B<7..0>	15A2 15A6 15B2 15B4 15B7 15C4 15C7			SGRBVREF	23A3 23C4 23C8 54A6		
NC_VTT<11..0>	20F7 20G7			PCI_STOP_L	32A8 32C5 33B7 34B6 56D7 61A4				17A6 17B6 17C6 17D6 55D6			SHS	52C8		
NC_WL<23..1>	33B2 33B3			PCI_TRDY_L	32B8 32C5 33B7 34B6 56D7 61A4			RAM_RAS_L	14A3 16B4 17B4 55C6			SLEEP	46B5 52C4 61C4		
NC_XTALSOUT	24B4			PCK2059_VDDI2C	10D4 54D6			RAM_SA0	17A4			SLEEP1	53A8		
NC_XTLINO	41B4			PGOOD	51B7 52A6			RAM_WE_L	14A3 16B6 17B6 55C6			SLEEP2	53A7		
NEC_AVDD	34D5 54A3			PG_E	47B6			RB22P2	50B2			SLEEPA_OFF_L	53C5		
NEC_XT2_B	34D3			PMURESETBUTTON*	46A4 61D4			RB27-1	50B6			SLEEPLD_TERM	53A6		
NET16	48A5			PMU_5V_SCL	46C2 46C4			RB37P1	50C6			SLEEP_LED_BD	53A6		
NET18	46D6			PMU_5V_SDA	46C2 46C4			RB160P1	52A5			SLEEP_OFF_L	53B6 53C7		
NET19	46D6			PMU_ACK_L	30C3 46C4			RB213P2	51C3			SLEEP_OFF_L2	41C7		
NET22	46D8			PMU_AGP_RESET	46C4			RB227P1	51C5			SND_AMP_MUTE_L	30C5 44A8		
NET24	46D8			PMU_AP	31B3 46D4			REF_STAR_GND	41A5 41A7			SND_AMP_M_L	44A7 44B8		
NET32	44B1			PMU_AVCC	46B5 46D4 54B3 61C4			RESET_BUTTON*	31B2 46C4 61D4			SND_CLKOUT	30A2 41B4		
NET32_B	44B1			PMU_BYTE	46B5			RFBA<11..0>	20E2 20F2 22C2 22C6 22D2 22D6 57D3			SND_HP_MUTE_L	30C5 43A7		
NET40	46B4			PMU_CLK	30C3 46C4			RFBABA<1..0>	20E2 22C2 22C6 57D3			SND_HP_M_L	43A7		
NMI_BUTTON*	31B2 46C4 61D4			PMU_CLKIN	46B4 60A5			RFBACAS_L	20G2 22B2 22B6 57D3			SND_HP_SENSE	30B5 43A5 61B7		
NV11_HSYNC	24C4 28D3			PMU_CLKOUT	46B4 60A5			RFBACKE	20D2 22C2 22C6 57C3			SND_HP_SENSE_CONN	43A3 43B2		
NV11_VSYNC	24C4 28D3			PMU_CLKT	46B2 60A5			RFBACLK0	21C1 22C6 57C3			SND_HW_RESET_L	30A8 30B5 41B4		
NV11_XTALIN	24B4 59D3			PMU_CNVS	31B3 46B5			RFBACLK0_L	21C1 22C6 57C3			SND_LIN_SENSE	30B5 42D4 61B4		
NV11_XTALOUT	24B4 59D3			PMU_EPM*	31B3 46C4			RFBACLK1	21D1 22C2 57C3			SND_SCLK	30A2 41B1		
NVAGP_TCLK	19A5			PMU_FROM_INT	30C3 46C4			RFBACLK1_L	21D1 22C2 57C3			SND_SPKR_ID	30B5 44C5		
NVAGP_TRST_L	19A5			PMU_IIC_CLK	46A8 46B4			RFBACS0_L	20F2 22B2 22B6 57C3			SND_SYNC	30B2 41C1		
NVPLLVD	24D5 54A6			PMU_IIC_DAT	46A8 46B4			RFBARAS_L	20G2 22B2 22B6 57D3			SND_TO_AUDIO	30B2 41C4		
NV_BLUE2	24C1			PMU_INT_L	30B5 30B8 46B5			RFBawe_L	20F2 22B2 22B6 57D3			SNF_FSEL	10A7 30C5		
NV_GPIOD0	25D4			PMU_INT_NMI	30A8 30B5 46C4			RFBBA<11..0>	20B2 20C2 23C2 23C6 23D2 23D6 57C3			SPDA	44C3		
NV_GPIOD2	25D4			PMU_LOW_DSKTP	46B5			RFBBBA<1..0>	20A2 23C2 23C6 57C3			SPKR_JACK_DALLAS	44B1		
NV_GPIOD4	25D4			PMU_NMI	46B4			RFBBCAS_L	20C2 23B2 23B6 57B3			SPKR_L+	44B1		
NV_GPIOD5	25D4			PMU_P64	31B2 46C2			RFBBCKE	20A2 23C2 23C6 57B3			SPKR_L-	44B1		
NV_GPIOD6	25C4			PMU_PME_L	30B5 33C2 34A7 46B2 61A4			RFBBCLK0	21B1 23C6 57B3			SPKR_MUTE_T	44B6 44B7		
NV_GPIOD7	25C4			PMU_PME_LL	33C2			RFBBCLK0_L	21B1 23C6 57B3			SPKR_R+	44B1		
NV_GPIOD8	25C4			PMU_POWER	31C3 46A5 46B1 46C2 46D5 54B3			RFBBCLK1	21C1 23C2 57B3			SPKR_R-	44B1		
NV_GPIOD9	25C4			PMU_PRE_PLLSTOP	46B5			RFBBCLK1_L	21B1 23C2 57B3			SSCG_LOCK	10A7		
NV_GREEN2	24C1			PMU_PWR_LED*	46C5			RFBBCS0_L	20C2 23B2 23B6 57B3			STBYMD	52B7		
NV_RED2	24C1			PMU_REQ_L	30A8 30C3 46C2			RFBBRAS_L	20C2 23B2 23B6 57B3			STOP_AGP_L	18C6 18D3 19A8 56B7		
OGAL	43D7			PMU_RST*	9A8 31B3 46A5 46B5 61D4			RFBBWE_L	20C2 23B2 23B6 57B3			SUPER_FLO	47C7		
OGAR	43C7			PMU_SMB_SCK	46A3			RFBD<127..0>	21B4 21B7 21C4 21C7 21D4 21D7 22B1			SW3V5V_12VIN	52C7		
OGND3_JTAG_EN	37A5			PMU_SMB_SDA	46A3				22B5 22C1 22C5 23B1 23B5 23C1 23C5			SW3V5V_INTVCC	52B5 52C6		
OPA_STAR_GND	41A7 41D6 42C2 45B3			PMU_STRAP1	46C5				57C3 57D3			SW3V5V_SGND	52A7 52A8 52B7 52B8 52C7		
OPA_VREF	42B3 45B4			PMU_TO_INT	30C3 46C4			RFBDQM<15..0>	20C2 20D2 20G2 22C2 22C6 23C2 23C6			SW3V5V_VIN	52C6		
OUTM1	44B6			PMU_XI	46B5 60A5				57C3 57D3			SW3VITH2R	52A8		
OUTM1FILT	44B4			PMU_XO	46B5 60A5			RFBDQS<15..0>	21A3 21A6 22C2 22C6 23C2 23C6 57B3			SW3V_3VSENSE	52B4		
OUTM2	44B6			PMU_XT	46A6 60A5				57C3			SW3V_BG2	52B6		
OUTM2FILT	44A4			POWERUP_OK	46B4			RF_CLKRUN_L	33C3 61B4			SW3V_BG2R	52B5		
OUTP1	44B6			POWER_UP*	46C7 53A8 61D4			RF_DISABLE_L	33C3			SW3V_B00ST2	52B6		
OUTP1FILT	44B4			PRE_HRESET_L	8B4 8D5			RINA	41C4 42B2			SW3V_B00ST2R	52C5		
OUTP2	44B6			PROBE_DIV	43B4 43D7			RINT_PU_RESET_L	36C5			SW3V_ITH2	52A7 52B6		
OUTP2FILT	44B4			PSEUDO_STAR_GND	41B7 42B4			RINT_RESET_L	36C5			SW3V_RUNSS	52B6 52C3		
OUT_R	61B4			PWR_FAIL*	46B1 52D6			RJ45_4_5	37C1 59B3			SW3V_RUNSSR	52C3		
OVDD_ADJ	61C7			PWR_FAILPMU*	46B4			RJ45_7_8	37C1 59B3			SW3V_SNSM	52B6		
P1RC	44B4			PWR_FAIL_T	52D7			RJ45_F_TREF	37B2 59B3			SW3V_SNSP	52B6		
P2RC	44B4			PWR_LED	53A4			RJ45_RREF	37C2 59B3			SW3V_SW2	52B6 52C4		
PAT_M	44B8			PWR_SWITCH*	9A8 46B1 46C5 61D4			RJ45_RXN	37C1 37C2 59B3			SW3V_SW2A	52C3		
C															
B															
A															

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D	*** Unit Cross-Reference *** --- for the entire design ---														D	
	BS1	PCB_STANDOFF	31B5	C74	CAP	37B7	C155	CAP_P	44D7	C236	CAP	53B3	NOTICE OF PROPRIETARY PROPERTY THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING I TO MAINTAIN THE DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART			
	BS2	PCB_STANDOFF	31B6	C75	CAP	20H5	C156	CAP_P	44C7	C237	CAP_P	50C8				
	BS3	PCB_STANDOFF	31D1	C76	CAP	37B7	C157	CAP	22A4	C238	CAP_P	53C5				
	BS4	PCB_STANDOFF	31D1	C77	CAP	34D3	C158	CAP	22D5	C239	CAP	46A4				
	BT1	BATTERY	46D6	C78	CAP	20H6	C159	CAP	20H5	C240	CAP	13B8				
	C1	CAP	35C3	C79	CAP	20H4	C160	CAP	25C6	C241	CAP	52C6				
	C2	CAP	35D3	C80	CAP	20H2	C161	CAP	19C4	C242	CAP	16C3				
	C3	CAP	37B1	C81	CAP	20H7	C162	CAP	24C6	C243	CAP	16C2				
	C4	CAP	38B6	C82	CAP	20H4	C163	CAP	24C6	C244	CAP	16C3				
	C5	CAP_P	35A5	C83	CAP	20H3	C164	CAP	24C7	C245	CAP_P	46D5				
C	C6	CAP	35B3	C84	CAP	20H8	C165	CAP	22B3	C246	CAP_P	53B3	APPLE COMPUTER INC. SCALE: NONE SHY OF 67 74		A	
	C7	CAP_P	35A5	C85	CAP_P	41C6	C166	CAP	22B3	C247	CAP	10D4				
	C8	CAP	23A4	C86	CAP	20H8	C167	CAP	20H5	C248	CAP	41B4				
	C9	CAP	23A4	C87	CAP	19B3	C168	CAP	19C4	C249	CAP	33D1				
	C10	CAP_P	38D7	C88	CAP_P	50B1	C169	CAP	19C3	C250	CAP	33D2				
	C11	CAP	23A5	C89	CAP	20H2	C170	CAP	24C7	C251	CAP	10D7				
	C12	CAP_P	38D7	C90	CAP	20H7	C171	CAP_P	48A6	C252	CAP	53B2				
	C13	CAP_P	43C4	C91	CAP	19B4	C172	CAP	22A4	C253	CAP	10D7				
	C14	CAP_P	44C7	C92	CAP	36B6	C173	CAP	22B2	C254	CAP	53B1				
	C15	CAP	23D2	C93	CAP	19C4	C174	CAP	22B4	C255	CAP	16C2				
B	C16	CAP	23B4	C94	CAP	19C3	C175	CAP	20H3	C256	CAP	16C1	DRAWING NUMBER D 051-6496 REV. A			
	C17	CAP	23D5	C95	CAP	20H5	C176	CAP	19D2	C257	CAP	53B2				
	C18	CAP	53D2	C96	CAP	20H2	C177	CAP	19B4	C258	CAP	53B2				
	C19	CAP_P	53D3	C97	CAP	19B3	C178	CAP	19D4	C259	CAP	33D1				
	C20	CAP	23B2	C98	CAP	20H6	C179	CAP	19B3	C260	CAP	33D2				
	C21	CAP	23D5	C99	CAP	19D2	C180	CAP	19D3	C261	CAP	16C1				
	C22	CAP	23B4	C100	CAP	19D3	C181	CAP	19B2	C262	CAP	49B4				
	C23	CAP	23B4	C101	CAP	19D3	C182	CAP	19B2	C263	CAP	16C3				
	C24	CAP_P	42B4	C102	CAP	19C3	C183	CAP	19D2	C264	CAP	13C4				
	C25	CAP_P	42C4	C103	CAP	19D3	C184	CAP	19B2	C265	CAP	49B3				
A	C26	CAP_P	43D4	C104	CAP	19D4	C185	CAP	19D3	C266	CAP	49B8	DRAWING NUMBER D 051-6496 REV. A			
	C27	CAP	38D8	C105	CAP	19B3	C186	CAP	19B4	C267	CAP	46D6				
	C28	CAP	23B1	C106	CAP_P	50B2	C187	CAP	19B3	C268	CAP	10B8				
	C29	CAP	23B3	C107	CAP	44B8	C188	CAP	19B1	C269	CAP_P	53D6				
	C30	CAP	44B4	C108	CAP	44B8	C189	CAP	25C5	C270	CAP	49A5				
	C31	CAP	23B2	C109	CAP	19D3	C190	CAP	24C7	C271	CAP	13B8				
	C32	CAP	23D4	C110	CAP	19D2	C191	CAP	22A5	C272	CAP	49B4				
	C33	CAP	23B2	C111	CAP	19B4	C192	CAP	19D5	C273	CAP	49C5				
	C34	CAP	23B3	C112	CAP	19B2	C193	CAP	19B5	C274	CAP	10B8				
	C35	CAP	45C4	C113	CAP_P	48A4	C194	CAP	22B4	C275	CAP	16C2				

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D	C317	CAP	52B8				C399	CAP	38A7				C480	CAP_P	42B4			C561	CAP	44B6			
	C318	CAP	52B8				C400	CAP	37C1				C481	CAP	38B3			C562	CAP	17C2			
	C319	CAP	16A8				C401	CAP	37D1				C482	CAP	38B3			C563	CAP	50B6			
	C320	CAP	16C2				C402	CAP	44C3				C483	CAP	38B3			C564	CAP	50C2			
	C321	CAP	52B7				C403	CAP	35D4				C484	CAP	38B5			C565	CAP	22B5			
	C322	CAP	52A7				C404	CAP	35D4				C485	CAP	38B1			C566	CAP	22B7			
	C323	CAP_P	51B3				C405	CAP	35D3				C486	CAP	23B6			C567	CAP	22B7			
	C324	CAP_P	51B3				C406	CAP	38B6				C487	CAP	23D7			C568	CAP	22B8			
	C325	CAP	49A4				C407	CAP	38A6				C488	CAP	23B7			C569	CAP	22B8			
	C326	CAP_P	52B2				C408	CAP	43B3				C489	CAP	23B5			C570	CAP	34C8			
C	C327	CAP_P	52B2				C409	CAP	27B3				C490	CAP	38B6			C571	CAP	44B5			
	C328	CAP_P	52B2				C410	CAP	38D4				C491	CAP	44B4			C572	CAP	50B7			
	C329	CAP	51B3				C411	CAP	38A7				C492	CAP	23D2			C573	CAP	34C8			
	C330	CAP_P	51B3				C412	CAP	43C3				C493	CAP	23D7			C574	CAP	34C8			
	C331	CAP_P	51B2				C413	CAP	35D3				C494	CAP	23D7			C575	CAP	44B7			
	C332	CAP_P	51B2				C414	CAP	27B4				C495	CAP	38B2			C576	CAP	50C3			
	C333	CAP	52B6				C415	CAP	27A4				C496	CAP	38B2			C577	CAP	44B5			
	C334	CAP_P	47C1				C416	CAP	27A4				C497	CAP	44B4			C578	CAP	41C5			
	C335	CAP_P	47C2				C417	CAP	35B3				C498	CAP	44B4			C579	CAP	41C5			
	C336	CAP_P	47C2				C418	CAP	38D4				C499	CAP	43D6			C580	CAP_P	41B2			
B	C337	CAP_P	47C2				C419	CAP	44B3				C500	CAP	38C7			C581	CAP	41B2			
	C338	CAP_P	52D7				C420	CAP	43D3				C501	CAP	44C5			C582	CAP	44C3			
	C339	CAP_P	52D7				C421	CAP	27D4				C502	CAP	44C5			C583	CAP	48A5			
	C340	CAP_P	47C2				C422	CAP	27C4				C503	CAP	43C6			C584	CAP	41D3			
	C341	CAP	52B7				C423	CAP	27C3				C504	CAP	43B7			C585	CAP	50B5			
	C342	CAP_P	51C3				C424	CAP	38B6				C505	CAP	42D3			C586	CAP	50A4			
	C343	CAP_P	47D3				C425	CAP	44B3				C506	CAP	42C3			C587	CAP	41B2			
	C344	CAP	52B4				C426	CAP	44B2				C507	CAP	42B3			C588	CAP	41B5			
	C345	CAP	4B1				C427	CAP	43B3				C508	CAP	43C6			C589	CAP	50C5			
	C346	CAP_P	47B1				C428	CAP	38B7				C509	CAP	20B8			C590	CAP	53C4			
A	C347	CAP_P	47B2				C429	CAP	44B2				C510	CAP	20B8			C591	CAP_P	41D2			
	C348	CAP_P	47B2				C430	CAP	44B2				C511	CAP	34D5			C592	CAP	41B5			
	C349	CAP_P	52D7				C431	CAP	42B6				C512	CAP_P	43B7			C593	CAP	17C1			
	C350	CAP	9D4				C432	CAP	27B3				C513	CAP	42B3			C594	CAP	41B5			
	C351	CAP_P	51C3				C433	CAP	35C3				C514	CAP	42C3			C595	CAP	41B5			
	C352	CAP	4B1				C434	CAP	37D1				C515	CAP	34C8			C596	CAP	50C6			
	C353	CAP	9D4				C435	CAP	44B2				C516	CAP	34D8			C597	CAP	41C4			
	C354	CAP_P	52D4				C436	CAP	43A4				C517	CAP	34C8			C598	CAP	9B2			
	C355	CAP_P	52D4				C437	CAP	42C6				C518	CAP	43B7			C599	CAP	41D4			
	C356	CAP_P	52D4				C438	CAP	37D2				C519	CAP	34D8			C600	CAP_P	41D4			
A	C357	CAP_P	47D3				C439	CAP	38B4				C520	CAP	22D2			C601	CAP	31A5			
	C358	CAP	51C4				C440	CAP	38B4				C521	CAP	22D2			C602	CAP	17C1			
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	C360	CAP_P	48C4				C442	CAP	38C5				C523	CAP	34D8			C604	CAP_P	31A7			
	C361	CAP_P	47B2				C443	CAP	38B2				C524	CAP_P	43C7			C605	CAP	24C2			
	C362	CAP_P	47B2				C444	CAP	38B2				C525	CAP_P	43D7			C606	CAP	24C2			
	C363	CAP_P	48C5				C445	CAP	38C3				C526	CAP	34D5			C607	CAP	24C1			
	C364	CAP	9C1				C446	CAP	38C3				C527	CAP	34D8			C608	CAP	31A6			
	C365	CAP_P	48C6				C447	CAP	42B6				C528	CAP_P	50B3			C609	CAP	31A4			
	C366	CAP	48C5				C448	CAP	44B4				C529	CAP	34D5			C610	CAP	26A3			
A	C367	CAP	47B6				C449	CAP	35A7				C530	CAP	34C7			C611	CAP	15D7			
	C369	CAP	4A1				C450	CAP	38B3				C531	CAP	34C7			C612	CAP	15D7			
	C370	CAP	4A1				C451	CAP	38B2				C532	CAP	22D7			C613	CAP	15D6			
	C371	CAP	47C5				C452	CAP	38B4				C533	CAP	22D7			C614	CAP	31A6			
	C372	CAP	47B7				C453	CAP	43B4				C534	CAP	22B6			C615	CAP	31A4			
	C373	CAP	47C8				C454	CAP	42C6				C535	CAP	22B6			C616	CAP	24A3			
	C374	CAP	47B5				C455	CAP	38B4				C536	CAP	34C8			C617	CAP	24A3			
	C375	CAP	38C1				C456	CAP	38B3				C537	CAP	34D7			C618	CAP	25A6			
	C376	CAP	38D3				C457	CAP	38B4				C538	CAP_P	41D6			C619	CAP	31C7			
	C377	CAP	35A4				C458	CAP	38B3				C539	CAP_P	41B7			C620	CAP	31C7			
A	C378	CAP	38C2				C459	CAP	44B4				C540	CAP	22D8			C621	CAP	31D7			
	C379	CAP	38C2				C460	CAP	23D2				C541	CAP	34D8			C622	CAP	31D7			
	C380	CAP	35C3				C461	CAP	23B7				C542	CAP	45C3			C623	CAP	30D7			
	C381	CAP	38D3				C462	CAP	23B8				C543	CAP	50B3			C624	CAP	17C1			
	C382	CAP	35A4				C463	CAP	38B4				C544	CAP	22D7			C625	CAP	31A7			
	C383	CAP	35C4				C464	CAP	38B4				C545	CAP	22B7			C626	CAP	25A6			
	C384	CAP	38C2				C465	CAP	44B4				C546	CAP	34D8			C627	CAP	31A6			
	C385	CAP	35B3				C466	CAP	23B7				C547	CAP	34D7			C628	CAP	13C8			
	C386	CAP	35C3				C467	CAP	23D8				C548	CAP	34D8			C629	CAP_P	31A5			
	C387	CAP	35C4				C468	CAP	38B1				C549	CAP	41D7			C630	CAP	31A6			
A	C388	CAP	44A1				C469	CAP	23B7				C550	CAP	45B2			C631	CAP	50C7			
	C389	CAP	35B4				C470	CAP	23B8				C551	CAP	45B3			C632	CAP	50C7			
	C390	CAP	38A6				C471	CAP	38B4				C552	CAP	50B6			C633	CAP	50C7			
	C391	CAP	35B4				C472	CAP	38B1				C553	CAP	22B6			C634	CAP	50C7			
	C392	CAP	38A6				C473	CAP	53D2				C554	CAP	22B7			C635	CAP_P	25A6			
	C393	CAP	38A6				C474	CAP	44A4				C555	CAP	34D7			C636	CAP_P	25A8			
	C394	CAP	38A6				C475	CAP	23B6				C556	CAP	45C2			C637	CAP	13C4			
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	C396	CAP	35A4				C477	CAP	38C7				C558	CAP_P	50B2			C639	CAP	31A4			
	C397	CAP	35D3				C478	CAP	45B5				C559	CAP	34D8			C640	CAP	31A3			
C398	CAP	35B3				C479	CAP_P	42C4				C560	CAP	44B5			C641	CAP	13B8				
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APPLE COMPUTER INC.

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D	C642	CAP	45C7			C723	CAP	13A6			C806	CAP	13C2			C887	CAP	13D7					
	C643	CAP	13C2			C724	CAP	13A5			C807	CAP	13B2			C888	CAP	13D6					
	C644	CAP	13B2			C725	CAP	13B7			C808	CAP	13D2			C889	CAP	13D6					
	C645	CAP	13B1			C726	CAP	17C2			C809	CAP	13C3			C890	CAP	13D7					
	C646	CAP	13B1			C727	CAP	13D4			C810	CAP	13D2			C891	CAP	13D6					
	C647	CAP	13C7			C728	CAP	13C3			C811	CAP	13D2			C892	CAP	13C4					
	C648	CAP	13B7			C729	CAP	13B3			C812	CAP	13C2			C893	CAP	13C1					
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	C650	CAP	13C7			C731	CAP	13D1			C814	CAP	13A6			C895	CAP	13D5					
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	C653	CAP	53B4			C734	CAP	13D2			C817	CAP	13B6			C898	CAP	13D5					
	C654	CAP	53B4			C735	CAP	13A7			C818	CAP	13A5			C899	CAP	13D5					
	C655	CAP	13C4			C736	CAP	13A5			C819	CAP	17C2			C900	CAP	13D7					
	C656	CAP	13C2			C737	CAP	10D6			C820	CAP	30C3			C901	CAP	13D7					
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	C658	CAP	13B2			C739	CAP	10D6			C822	CAP	13C1			C903	CAP	13B2					
	C659	CAP	13B3			C740	CAP	10B5			C823	CAP	13D7			C904	CAP	13B6					
	C660	CAP	13B1			C741	CAP	17C2			C824	CAP	13D6			C905	CAP	10B7					
	C661	CAP	13B7			C742	CAP	53C6			C825	CAP	13A7			C906	CAP	10B7					
B	C662	CAP	13B7			C743	CAP	13B3			C826	CAP	13A7			C907	CAP	10B8					
	C663	CAP	13B6			C744	CAP	13B1			C827	CAP	13B5			C908	CAP	15C2					
	C664	CAP	13C5			C745	CAP	13C3			C828	CAP	13C1			C909	CAP	15C2					
	C665	CAP	13C6			C746	CAP	13D3			C829	CAP	13C2			C910	CAP	15C1					
	C666	CAP	13C5			C747	CAP	13D1			C830	CAP	13C3			C911	CAP	39C2					
	C667	CAP	13C6			C748	CAP	13D2			C831	CAP	13D3			C912	CAP	10B8					
	C668	CAP	13A5			C749	CAP	13D3			C832	CAP	13D7			C913	CAP	10B7					
	C669	CAP	15C5			C750	CAP	13A6			C833	CAP	13D3			C914	CAP	17C2					
	C670	CAP	15C6			C751	CAP	13A7			C834	CAP	13D6			C915	CAP	53D7					
	C671	CAP	15C5			C752	CAP	13A6			C835	CAP	13B3			C916	CAP	14A8					
A	C672	CAP	17C1			C753	CAP	13A6			C836	CAP	13B6			C917	CAP	39C5					
	C673	CAP	13B2			C754	CAP	13A5			C837	CAP	13B7			C918	CAP	53D6					
	C674	CAP	13B6			C755	CAP	13A6			C838	CAP	13A7			C919	CAP	40C7					
	C675	CAP	45B7			C756	CAP	10D5			C839	CAP	13B6			C920	CAP	40C1					
	C676	CAP	13B3			C757	CAP	10D6			C840	CAP	30A6			C921	CAP	40C5					
	C677	CAP	13B1			C758	CAP	10D4			C841	CAP	13D4			C922	CAP	17C1					
	C678	CAP	13B3			C759	CAP	46D5			C842	CAP	13D6			C923	CAP	40B5					
	C679	CAP	13B1			C760	CAP	18D6			C843	CAP	13C7			C924	CAP	40B7					
	C680	CAP	13C3			C761	CAP	13B3			C844	CAP	13D6			C925	CAP	17C2					
	C681	CAP	13B3			C762	CAP	13C1			C845	CAP	13D6			C926	CAP	52C1					
A	C682	CAP	13B3			C763	CAP	13D2			C846	CAP	13D7			C927	CAP	52B1					
	C683	CAP	13C6			C764	CAP	13D3			C847	CAP	14A7			C928	CAP_P	49B4					
	C684	CAP	13C7			C765	CAP	13D3			C848	CAP	32D5			C929	CAP	16A7					
	C685	CAP	13C6			C766	CAP	13D1			C849	CAP	13C6			C930	CAP	52B8					
	C686	CAP	13C6			C767	CAP	13B6			C850	CAP	13B3			C931	CAP	52B5					
	C687	CAP	13A6			C768	CAP	13A6			C851	CAP	13B3			C932	CAP	52A5					
	C688	CAP	13B7			C769	CAP	10D5			C852	CAP	13B2			C933	CAP	52A6					
	C689	CAP	13C1			C770	CAP	10D6			C853	CAP	14A7			C934	CAP	52B1					
	C690	CAP	13B2			C771	CAP	10C6			C854	CAP	11D3			C935	CAP_P	49B3					
	C691	CAP	13C2			C772	CAP	10C6			C855	CAP	13B5			C936	CAP	17C2					
A	C692	CAP	30D6			C773	CAP	53C5			C856	CAP	13A7			C937	CAP	52C7					
	C693	CAP	13C7			C774	CAP	13C1			C857	CAP	13B6			C938	CAP	52A4					
	C694	CAP	13C5			C775	CAP	13C3			C858	CAP	15D4			C939	CAP	52A6					
	C695	CAP	13C5			C776	CAP	13B2			C859	CAP	15D3			C940	CAP	52A5					
	C696	CAP	13C5			C777	CAP	13C1			C860	CAP	15D4			C941	CAP	52C5					
	C697	CAP	13C7			C779	CAP	13D1			C861	CAP	46A1			C942	CAP	8C3					
	C698	CAP	13B5			C780	CAP	13D2			C862	CAP	46B7			C943	CAP	17A3					
	C699	CAP	13A7			C781	CAP	13A5			C863	CAP	13C2			C944	CAP	47D3					
	C700	CAP	13B7			C782	CAP	13A7			C864	CAP	17C2			C945	CAP	17A2					
	C701	CAP	46B2			C783	CAP	46D5			C865	CAP	13B1			C946	CAP	8A6					
A	C702	CAP	13D4			C784	CAP	30A6			C866	CAP	13C3			C947	CAP_P	47C1					
	C703	CAP	30D6			C785	CAP	30C3			C867	CAP	13C3			C948	CAP	51B4					
	C704	CAP	30D5			C787	CAP	13D3			C868	CAP	13C2			C949	CAP	47C5					
	C705	CAP	46A6			C788	CAP	13D3			C869	CAP	13D5			C950	CAP	47D4					
	C706	CAP	46A6			C789	CAP	13D3			C870	CAP	13D6			C951	CAP	47D4					
	C707	CAP	13B3			C790	CAP	13D1			C871	CAP	13D6			C952	CAP	47D4					
	C708	CAP	13B3			C791	CAP	13A6			C872	CAP	13D5			C953	CAP	47D4					
	C709	CAP	13C6			C792	CAP	13A6			C873	CAP	13D5			C954	CAP	4C2					
	C710	CAP	13C6			C793	CAP	13B6			C874	CAP	13D5			C955	CAP	52D4					
	C711	CAP	13B6			C794	CAP	13B5			C875	CAP	13B3			C956	CAP	52D5					
A	C712	CAP	13B1			C795	CAP	30C3			C876	CAP	13B5			C957	CAP_P	47B1					
	C713	CAP	30D5			C796	CAP	13C3			C877	CAP	13B7			C958	CAP	9D4					
	C714	CAP	13A5			C797	CAP	13D3			C878	CAP	13B7			C959	CAP	51C4					
	C715	CAP	46B2			C798	CAP	13D1			C879	CAP	13B2			C960	CAP	47B2					
	C716	CAP	13B2			C799	CAP	13D1			C880	CAP	13B5			C961	CAP	9C2					
	C717	CAP	13C3			C800	CAP	13D2			C881	CAP	13B3			C962	CAP	51C3					
	C718	CAP	13B1			C801	CAP	13A7			C882	CAP	13B1			C963	CAP	47D4					
	C719	CAP	13D2			C802	CAP	13A5			C883	CAP	13B2			C964	CAP	47D4					
	C720	CAP	13C3			C803	CAP	13A6			C884	CAP	13D6			C965	CAP	47D4					
	C721	CAP	13D3			C804	CAP	10D3			C885	CAP	13D7			C966	CAP	47D4					
C722	CAP	13A7			C805	CAP	13B2			C886	CAP	13D5			C967	CAP	9C1						
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8			7			6			5			4			3			2			1		
D	C968	CAP	51C4	C1049	CAP	47B7	D39	DIODE_SCHOT	52B4	L21	IND_3P	51B4	D										
	C969	CAP	9D4	C1050	CAP	47C7	D40	DIODE_SCHOT	52A4	L22	IND_3P	52A3											
	C970	CAP	52D5	C1051	CAP	47B6	D41	DIODE_SCHOT	47C4	L23	IND	52C3											
	C971	CAP	52D5	C1052	CAP	47C8	D42	DIODE_SCHOT	51B5	L24	IND	47B3											
	C972	CAP	9C2	C1053	CAP	48A8	D43	DIODE_SCHOT	47B3	L25	IND	44C3											
	C973	CAP	9C1	C1054	CAP	9B2	D44	DIODE_SCHOT	47D2	L26	IND	44B1											
	C974	CAP	9D1	C1055	CAP	9B2	D45	DIODE_SCHOT	47D6	L27	IND	43C3											
	C975	CAP	51C4	C1056	CAP	9B1	D46	DIODE_SCHOT	51B2	L28	IND	35C5											
	C976	CAP	9C2	C1057	CAP	9B1	D4901	DIODE_SCHOT	49B2	L29	IND	35C5											
	C977	CAP	9D1	C1058	CAP	9B1	DS1	LED	37A2	L30	IND	35B5											
C	C978	CAP	52D5	C1101	CAP	11D3	DS2	LED	37A1	L31	IND	35B5	C										
	C979	CAP	52D5	C1601	CAP	16D7	DS3	LED	37A1	L32	IND	38B6											
	C980	CAP	9C2	C1602	CAP	16D3	DS4	LED	32A3	L34	IND	43D3											
	C981	CAP	9C2	C1603	CAP	16D3	DS5	LED	53A5	L35	IND	43B3											
	C982	CAP	9D2	C1701	CAP	17D7	DS6	LED	53A6	L36	IND	42B6											
	C983	CAP	9D3	C1702	CAP	17D7	DS7	LED	40B2	L37	IND	35D5											
	C984	CAP	51C3	C1801	CAP	18D5	DS8	LED	40B6	L38	IND	35D5											
	C985	CAP	52D4	C1802	CAP	18A7	DS9	LED	52D6	L39	IND	43A3											
	C986	CAP	52D5	C1901	CAP	9A6	DS10	LED	53A3	L40	IND	44B1											
	C987	CAP	9D2	C1902	CAP	9A5	F1	FUSE	35A7	L41	IND	44C3											
B	C988	CAP	9D3	C1903	CAP	9A5	F2	FUSE	38D5	L42	IND	43B3	B										
	C989	CAP	51C3	C1904	CAP	9A5	F3	FUSE	38D5	L43	IND	43A4											
	C990	CAP	9C1	C1910	CAP	9A5	FL1	FILTER_12P	44B3	L44	IND	43C3											
	C991	CAP	9D1	C1911	CAP	9B2	FL2	FILTER_LC	27C6	L45	IND	43D3											
	C992	CAP	9D3	C1912	CAP	9B2	FL3	FILTER_LC	27C6	L46	IND	42C6											
	C993	CAP	9C1	C1913	CAP	9B1	FL4	FILTER_LC	27B6	L47	IND	43B3											
	C994	CAP	9C2	C1914	CAP	9B1	J1	CON_RJ45	37C1	L48	IND	42C6											
	C995	CAP	9D2	C2201	CAP	22C8	J2	CON_FWVERT_SKT	38C1	L49	IND	42B5											
	C996	CAP	9A6	C2202	CAP	22C4	J3	CON_F8RT_S_TH1	44B1	L50	IND	42C5											
	C997	CAP	9A5	C2301	CAP	23C8	J4	CON_F4RT_USB_UPRIGHT	35C3	L51	IND	42D6											
A	C998	CAP	9D2	C2302	CAP	23C4	J5	CON_FWVERT_SKT	38D1	L52	IND	42C5	A										
	C999	CAP	9D1	C2501	CAP_P	25A8	J6	CON_F4RT_USB_UPRIGHT	35B3	L53	IND	42D5											
	C1000	CAP	51B6	C3001	CAP	30C5	J7	CON_F4RT_USB_UPRIGHT	35D3	L54	IND	44B5											
	C1001	CAP	9A5	C3002	CAP	30D5	J8	CON_F4RT_S4MT_TH1	43C1	L55	IND	44B5											
	C1002	CAP	9A6	C3003	CAP	30D5	J9	CON_F14RT_D4MT_TH1	27C5	L56	IND	46A1											
	C1003	CAP	9D1	C3004	CAP	30D4	J10	CON_F4RT_S4MT_TH1	42C7	L57	IND	46A1											
	C1004	CAP	51B6	C3005	CAP	30D4	J11	CON_F184ST_DDRDIMM	17D5	L58	IND	34D6											
	C1005	CAP	51C4	C3201	CAP	32D4	J12	CON_F21ST_D2MT_SM	26C3	L59	IND	45B5											
	C1006	CAP	47C2	C3901	CAP	39D1	J13	CON_M40SM_635	31D6	L60	IND	45C5											
	C1007	CAP	9C2	C4001	CAP	48C7	J14	CON_M16ST_D_TH	31B5	L61	IND	44D7											
A	C1008	CAP	9B5	C4301	CAP	43A6	J15	CON_M40ST_NC20	40D6	L62	IND	44D7	A										
	C1009	CAP	9A6	C4502	CAP_P	45C5	J16	CON_M40ST_NC20	40D2	L63	IND	41D4											
	C1010	CAP	9B5	C4701	CAP	47B6	J17	CON_M16ST_MICROFIT	52D8	L64	IND	26A5											
	C1011	CAP	9B5	D1	DIODE_DUAL_6P	38A7	J18	CON_M4ST_LCK	36B4	L65	IND	31A7											
	C1012	CAP	9A6	D2	DIODE_DUAL_6P	38A7	J19	CON_37SM_MT0R	9C5	L66	IND	31A3											
	C1013	CAP	9D2	D3	DIODE_DUAL_6P	38A7	J20	CON_38SM_MT0R	9D7	L67	FILTER_4P	26A5											
	C1014	CAP	9D2	D4	DIODE_DUAL_6P	38B7	J21	CON_M3ST_LCK	47C8	L68	IND	24C2											
	C1015	CAP	51B7	D5	ZENER_MMBZ15VDLT1	42B6	J22	CON_F20SM_KX	9B3	L69	IND	31A3											
	C1016	CAP	9C1	D6	DIODE_DUAL_6P	27D4	J23	CON_F1ST_S2MT_SM	30B7	L70	IND	31A7											
	C1017	CAP	9A5	D7	DIODE_DUAL_6P	27D4	J24	CON_M12ST_SM	31B3	L71	FILTER_4P	26B5											
A	C1018	CAP	9A5	D8	DIODE	38D6	J25	CON_F100RT_LP_SM	33D3	L72	IND	31A7	A										
	C1019	CAP	51C6	D9	DIODE	37B8	J26	CON_F200RT_DDRDIMM_SM2	16D5	L73	IND	31A3											
	C1020	CAP	9C1	D10	DIODE_SCHOT	25B8	J27	CON_F1ST_S2MT_SM	9C3	L74	FILTER_4P	26C5											
	C1021	CAP	9C1	D11	DIODE_SCHOT	46D7	J28	CON_F10ST_D_SMA	31D2	L75	IND	31A7											
	C1022	CAP	9B6	D12	DIODE_SCHOT	46D7	J29	CON_M6ST_BTRY	8B6	L76	FILTER_4P	26D5											
	C1023	CAP	9A5	D13	DIODE_SCHOT	49C7	J30	CON_38SM_MT0R	9D5	L77	IND	31B7											
	C1024	CAP	9A5	D14	DIODE_SCHOT	49B5	J31	CON_38SM_MT0R	9C7	L78	IND	31A3											
	C1025	CAP	9B5	D15	DIODE_SCHOT	51B4	J32	CON_F12RT_S2MT_SM	9B8	L79	IND	45C7											
	C1026	CAP	9D2	D16	DIODE_SCHOT	52C5	J4501	CON_F4ST_S2MT_SM	45C6	L80	IND	31B3											
	C1027	CAP	9B6	D17	DIODE_SCHOT	52B5	J4502	CON_F4ST_S2MT_SM	45C6	L81	IND	45B7											
A	C1028	CAP	9A5	D18	DIODE_SCHOT	51C6	JAZ1	TP	53A4	L82	IND	26D5	A										
	C1029	CAP	47B4	D19	DIODE_SCHOT	48D6	L1	IND	38D4	L83	IND	45B7											
	C1030	CAP	47B2	D20	DIODE_SCHOT	47D5	L2	IND	35A5	L84	IND	52D7											
	C1031	CAP	9A5	D21	DIODE_SCHOT	47B5	L3	IND	38D4	L85	IND	52D7											
	C1032	CAP	9C2	D22	ZENER_MMBZ15VDLT1	44C2	L4	IND	35A5	L86	IND	52D7											
	C1033	CAP	9C1	D23	DIODE_SCHOT_3P	44A1	L5	FILTER_4P	38C2	L2401	IND	24C7											
	C1034	CAP	47B2	D24	ZENER_MMBZ15VDLT1	43B2	L6	FILTER_4P	38C2	L2501	IND	25A6											
	C1035	CAP	4D3	D25	ZENER	38B6	L7	FILTER_4P	38D2	Q1	TRA_2N7002	53C2											
	C1036	CAP	4D3	D26	ZENER_MMBZ15VDLT1	43B2	L8	FILTER_4P	38D2	Q2	TRA_2N7002	44A7											
	C1037	CAP	9C1	D27	DIODE_DUAL_6P	27B4	L9	IND	27C2	Q3	TRA_2N7002	44B7											
A	C1038	CAP	47B4	D28	DIODE_DUAL_6P	27B4	L10	IND	44A5	Q4	TRA_2N7002	44C4	A										
	C1039	CAP	47C5	D29	ZENER_MMBZ15VDLT1	42B5	L11	IND	44B5	Q5	TRA_2N7002	43A5											
	C1040	CAP	9C2	D30	DIODE_SCHOT	50C5	L12	IND	50B3	Q6	TRA_2N7002	53C4											
	C1041	CAP	9D3	D31	DIODE_SCHOT	50B3	L13	IND	24C7	Q7	TRA_IRLR8503	50B4											
	C1042	CAP	9C2	D32	DIODE_SCHOT	50B5	L14	IND	10D5	Q8	TRA_IRLR8503	50B4											
	C1043	CAP	9D2	D33	DIODE_SCHOT	48B6	L15	IND	10D7	Q9	TRA_IRLR8503	50C4											
	C1044	CAP	47D7	D34	DIODE_SCHOT	50B1	L16	IND	30C3	Q10	TRA_2N7002	24B7											
	C1045	CAP	47B7	D35	DIODE_SCHOT	46D6	L17	IND	10B8	Q11	TRA_FDC602P	53C2											
	C1046	CAP	47C1	D36	DIODE_SCHOT	46D6	L18	IND	10B8	Q12	TRA_2N3904	46D7											
	C1047	CAP	47D5	D37	DIODE_SCHOT	52B2	L19	IND	49B4	Q13	TRA_2N7002	53B2											
A	C1048	CAP	47C2	D38	DIODE_SCHOT	52C6	L20	IND	47C3	Q14	TRA_2N7002	53C7	A										

		8		7		6		5		4		3		2		1			
D	Q15	TRA_2N7002	53B7	R43	RES	35D6	R124	RES	25C4	R205	RES	18A8	D						
	Q16	TRA_2N7002	53A7	R44	RES	35D6	R125	RES	28D5	R206	RES	24D7							
	Q17	TRA_2N7002	53B6	R45	RES	35C6	R126	RES	28C6	R207	RES	25D2							
	Q18	TRA_2N7002	53A7	R46	RES	35C6	R127	RES	34B8	R208	RES	25D2							
	Q19	TRA_2N7002	53A6	R47	RES	35B6	R128	RES	34B8	R209	RES	16B3							
	Q20	TRA_FDC602P	53D7	R48	RES	35B6	R129	RES	34D3	R210	RES	19B8							
	Q21	TRA_2N7002	52C3	R49	RES	20B7	R130	RES	20D2	R211	RES	18C7							
	Q22	TRA_2N7002	52D3	R50	RES	20B7	R131	RES	20D3	R212	RES	32B3							
	Q23	TRA_2N7002	52C4	R51	RES	44C3	R132	RES	21D2	R213	RES	25C4							
	Q24	TRA_2N7002	52C3	R52	RES	20C5	R133	RES	24D3	R214	RES	29B5							
C	Q25	TRA_IRF7811W	49B5	R53	RES	21A4	R134	RES	28B8	R215	RES	18C7	C						
	Q26	TRA_IRF7805	49B5	R54	RES	20D3	R135	RES	28A8	R216	RES	18B7							
	Q27	TRA_IRLR8203	51B4	R55	RES	21A4	R136	RES	28C5	R217	RES	18D3							
	Q28	TRA_IRLR8503	51B4	R56	RES	20D2	R137	RES	44B7	R218	RES	32D6							
	Q29	TRA_2N7002	47D8	R57	RES	21A4	R138	RES	32B3	R219	RES	32D6							
	Q30	TRA_2N3904	38C7	R58	RES	20D3	R139	RES	32B4	R220	RES	18C7							
	Q31	TRA_2N7002	43A8	R59	RES	20C2	R140	RES	21A7	R221	RES	18D1							
	Q32	TRA_2N7002	41C7	R60	RES	21A4	R141	RES	25B4	R222	RES	18D3							
	Q33	TRA_2N3904	45B2	R61	RES	20A3	R142	RES	28C5	R223	RES	18D1							
	Q34	TRA_IRLR8203	50A4	R62	RES	20A2	R143	RES	20G2	R224	RES	18C7							
B	Q35	TRA_2N7002	42D4	R63	RES	21A4	R144	RES	25B2	R225	RES	18D7	B						
	Q37	TRA_FDC602P	53C6	R64	RES	20D3	R145	RES	24C3	R226	RES	18C7							
	Q38	TRA_2N7002	53B6	R65	RES	21A4	R146	RES	21A7	R227	RES	32C6							
	Q39	TRA_2N3904	52C7	R66	RES	20D2	R147	RES	21A7	R228	RES	30C7							
	Q40	TRA_IRLR8203	47C4	R67	RES	20D2	R148	RES	21C2	R229	RES	36C1							
	Q41	TRA_2N7002	8D5	R68	RES	21A4	R149	RES	25B5	R230	RES	32B6							
	Q42	TRA_2N7002	8C6	R69	RES	21A4	R150	RES	50C5	R231	RES	30C8							
	Q43	TRA_IRLR8203	47C4	R70	RES	20D3	R151	RES	22A4	R232	RES	30C6							
	Q44	TRA_IRLR8503	47C4	R71	RES	20D6	R152	RES	22A5	R233	RES	18B3							
	Q45	TRA_IRF7811W	52B4	R72	RES	19A5	R153	RES	21A7	R234	RES	18D1							
A	Q46	TRA_IRF7805	52C4	R73	RES	28C2	R154	RES	20G3	R235	RES	29B5	A						
	Q47	TRA_IRLR8203	52A4	R74	RES	28D2	R155	RES	20G2	R236	RES	29C5							
	Q48	TRA_IRLR8503	52A4	R75	RES	37B8	R156	RES	21C2	R237	RES	25C4							
	Q49	TRA_IRLR8203	47B4	R76	RES	37B8	R157	RES	25B2	R238	RES	29C6							
	Q50	TRA_IRLR8503	47B4	R77	RES	28D2	R158	RES	21D2	R239	RES	29B6							
	Q51	TRA_IRLR8203	47B4	R78	RES	34A7	R159	RES	21A7	R240	RES	29B5							
	Q52	TRA_2N7002	48A7	R79	RES	28C2	R160	RES	19A5	R241	RES	29C5							
	Q53	TRA_FDC602P	48B7	R80	RES	19A5	R161	RES	18D1	R242	RES	29B6							
	Q4801	TRA_FDC602P	48D7	R81	RES	21A7	R162	RES	43A5	R243	RES	29C6							
	R1	RES	38D4	R82	RES	28B3	R163	RES	22A4	R244	RES	29C4							
R2	RES	27B4	R83	RES	20A5	R164	RES	20G3	R245	RES	29B4								
R3	RES	27B4	R84	RES	20G2	R165	RES	37C4	R246	RES	25B7								
R4	RES	23A4	R85	RES	25D6	R166	RES	22A4	R247	RES	29B7								
R5	RES	23A4	R86	RES	25D6	R167	RES	21A7	R248	RES	29C7								
R6	RES	23A4	R87	RES	28B3	R168	RES	19A7	R249	RES	29B7								
R7	RES	27B5	R88	RES	20A5	R169	RES	19A4	R250	RES	29C7								
R8	RES	38C4	R89	RES	34D3	R170	RES	24B5	R251	RES	53B3								
R9	RES	38C4	R90	RES	20G3	R171	RES	24A6	R252	RES	32C7								
R10	RES	38C4	R91	RES	25D5	R172	RES	24B6	R253	RES	32C7								
R11	RES	38C4	R92	RES	25C5	R173	RES	24C7	R254	RES	36B4								
R12	RES	38C3	R93	RES	28A3	R174	RES	24B6	R255	RES	32C6								
R13	RES	38C3	R94	RES	28A3	R175	RES	42D4	R256	RES	32C7								
R14	RES	38C3	R95	RES	20A5	R176	RES	22A4	R257	RES	36B3								
R15	RES	38C3	R96	RES	37B1	R177	RES	19A5	R258	RES	39D5								
R16	RES	38B3	R97	RES	21A7	R178	RES	24C7	R259	RES	32C7								
R17	RES	23A4	R98	RES	25D5	R179	RES	24C7	R260	RES	14D4								
R18	RES	23A5	R99	RES	25D6	R180	RES	42D5	R261	RES	14D4								
R19	RES	27B5	R100	RES	28A7	R181	RES	43A4	R262	RES	32C7								
R20	RES	21C2	R101	RES	21A7	R182	RES	43A4	R263	RES	39D4								
R21	RES	53D2	R102	RES	25C4	R183	RES	24C3	R264	RES	30D1								
R22	RES	53C3	R103	RES	21B2	R184	RES	25C5	R265	RES	40B4								
R23	RES	53D2	R104	RES	21B2	R185	RES	24B6	R266	RES	39C6								
R24	RES	21A3	R105	RES	21C2	R186	RES	21A6	R267	RES	30B1								
R25	RES	21A3	R106	RES	21C2	R187	RES	21A6	R268	RES	40B4								
R26	RES	37B4	R107	RES	25C4	R188	RES	24B3	R269	RES	36C4								
R27	RES	37C4	R108	RES	25C6	R189	RES	16A3	R270	RES	39D2								
R28	RES	37B7	R109	RES	28B7	R190	RES	16B2	R271	RES	30B3								
R29	RES	37B7	R110	RES	28D6	R191	RES	24B6	R272	RES	30B3								
R30	RES	37C3	R111	RES	20G2	R192	RES	19A3	R273	RES	39D2								
R31	RES	21A3	R112	RES	25D6	R193	RES	19A3	R274	RES	30C7								
R32	RES	21A3	R113	RES	25C4	R194	RES	24B6	R275	RES	36C4								
R33	RES	37C4	R114	RES	25C4	R195	RES	19A3	R276	RES	30C8								
R34	RES	37C7	R115	RES	25C4	R196	RES	19A3	R277	RES	53B2								
R35	RES	38D6	R116	RES	28C6	R197	RES	18A8	R278	RES	53B3								
R36	RES	20B8	R117	RES	20G3	R198	RES	19C1	R279	RES	53B2								
R37	RES	37C4	R118	RES	24D3	R199	RES	24D6	R280	RES	46D8								
R38	RES	37C7	R119	RES	25C4	R200	RES	24C6	R281	RES	46D7								
R39	RES	38D6	R120	RES	28C5	R201	RES	16B3	R282	RES	46D7								
R40	RES	37C3	R121	RES	28D6	R202	RES	16B2	R283	RES	53B3								
R41	RES	20B8	R122	RES	21A7	R203	RES	18C8	R284	RES	53B3								
R42	RES	37B4	R123	RES	21D2	R204	RES	18B8	R285	RES	33C1								
												NOTICE OF PROPRIETARY PROPERTY THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING I TO MAINTAIN THE DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART		SIZE D		DRAWING NUMBER 051-6496		REV. A	
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D	R286	RES	14A3			R367	RES	11C5			R449	RES	28A6			R530	RES	53C5					
	R287	RES	46D5			R368	RES	11C6			R450	RES	35B5			R531	RES	41C2					
	R288	RES	14D4			R369	RES	51C6			R451	RES	35B5			R532	RES	41C4					
	R289	RES	30C8			R370	RES	52D6			R452	RES	35C5			R533	RES	28D6					
	R290	RES	46D8			R371	RES	51B6			R453	RES	35C5			R534	RES	28C6					
	R291	RES	30C7			R372	RES	48D4			R454	RES	35D5			R535	RES	29C5					
	R292	RES	30C7			R374	RES	6C6			R455	RES	35D5			R536	RES	29B5					
	R293	RES	33C4			R375	RES	6C6			R456	RES	35B7			R537	RES	29C4					
	R294	RES	30C1			R376	RES	6C6			R457	RES	35B7			R538	RES	29B4					
	R295	RES	49B3			R377	RES	6C6			R458	RES	35C7			R539	RES	29B6					
C	R296	RES	49B7			R378	RES	6C7			R459	RES	35C7			R540	RES	26A6					
	R297	RES	46D6			R379	RES	6C7			R460	RES	35D7			R541	RES	28A6					
	R298	RES	46B1			R380	RES	6C7			R461	RES	35D7			R542	RES	28B6					
	R299	RES	49B3			R381	RES	6C7			R462	RES	42B3			R543	RES	29C6					
	R300	RES	53A5			R382	RES	6C8			R463	RES	20B8			R544	RES	29B7					
	R301	RES	49B5			R383	RES	6C8			R464	RES	34C3			R545	RES	18A8					
	R302	RES	53A6			R384	RES	47C5			R465	RES	34C3			R546	RES	26A5					
	R303	RES	36D7			R385	RES	48D6			R466	RES	34C2			R547	RES	26A6					
	R304	RES	49B3			R386	RES	47B5			R467	RES	34C2			R548	RES	29C7					
	R305	RES	49B4			R387	RES	47B5			R468	RES	34C3			R549	RES	18A8					
B	R306	RES	30B8			R388	RES	47B6			R469	RES	34C3			R550	RES	26A5					
	R307	RES	36D7			R389	RES	47B6			R470	RES	34B3			R551	RES	26A5					
	R308	RES	9C3			R390	RES	37C1			R471	RES	44B4			R552	RES	28C8					
	R309	RES	49B7			R391	RES	37C2			R472	RES	43C7			R553	RES	28D8					
	R310	RES	11A4			R392	RES	43B3			R473	RES	43D7			R554	RES	24B7					
	R311	RES	4B8			R393	RES	37C2			R474	RES	42C3			R555	RES	24C3					
	R312	RES	4A8			R394	RES	37C2			R475	RES	50B1			R556	RES	24C3					
	R313	RES	9B4			R395	RES	38B7			R476	RES	34B3			R557	RES	24C2					
	R314	RES	9C4			R396	RES	35A7			R477	RES	43B4			R558	RES	19A7					
	R315	RES	49B3			R397	RES	28A6			R478	RES	43B5			R559	RES	26B6					
A	R316	RES	49B7			R398	RES	35A6			R479	RES	21A6			R560	RES	28C7					
	R317	RES	49C5			R399	RES	28B6			R480	RES	34C3			R561	RES	28D7					
	R318	RES	49C6			R400	RES	43C4			R481	RES	41B6			R562	RES	24B6					
	R319	RES	11A4			R401	RES	28B5			R482	RES	41C6			R563	RES	29C5					
	R320	RES	49B5			R402	RES	38B4			R483	RES	21A6			R564	RES	25C4					
	R321	RES	11A4			R403	RES	21B2			R484	RES	34A7			R565	RES	30D5					
	R322	RES	52C3			R404	RES	38D8			R485	RES	37B2			R566	RES	32D6					
	R323	RES	52D4			R405	RES	44B4			R486	RES	37B2			R567	RES	26B5					
	R324	RES	10C6			R406	RES	43D4			R487	RES	45B5			R568	RES	26B5					
	R325	RES	10C6			R407	RES	42C5			R488	RES	45C4			R569	RES	24B3					

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D	R613	RES	31A8	R694	RES	30B3	R775	RES	40C7	R856	RES	7C5	C	R614	RES	53A4	R695	RES	30A5	R776	RES	11A4	R857	RES	7B5	B	R615	RES	53A5	R696	RES	14A7	R777	RES	11A4	R858	RES	7C5	A	R616	RES	32C7	R697	RES	14A3	R778	RES	14A8	R859	RES	7C5	R617	RES	32C7	R698	RES	46A3	R779	RES	40C1	R860	RES	7B5	R618	RES	32C7	R699	RES	46D3	R780	RES	40C3	R861	RES	11C7	R619	RES	18D6	R700	RES	46D3	R781	RES	40C5	R862	RES	11D7	R620	RES	30D5	R701	RES	46D4	R782	RES	40C7	R863	RES	11B6	R621	RES	32C6	R702	RES	36C4	R783	RES	40C4	R864	RES	11B7	R622	RES	18C6	R703	RES	14A8	R784	RES	40C7	R865	RES	11B7	R623	RES	39D5	R704	RES	14D4	R785	RES	40C4	R866	RES	11D6	R624	RES	39A7	R705	RES	31B3	R786	RES	40A4	R867	RES	11D5	R625	RES	14B6	R706	RES	31C3	R787	RES	40B4	R868	RES	11B7	R626	RES	39D2	R707	RES	36D4	R788	RES	40A7	R869	RES	11B5	R627	RES	39D5	R708	RES	30C8	R789	RES	40B7	R870	RES	11A5	R628	RES	30D5	R709	RES	36C4	R790	RES	40B7	R871	RES	51C3	R629	RES	30D5	R710	RES	30A5	R791	RES	40B7	R872	RES	51C3	R630	RES	30D5	R711	RES	30A6	R792	RES	40B2	R873	RES	51C5	R631	RES	10B5	R712	RES	11A3	R793	RES	40B6	R874	RES	11D6	R632	RES	10A3	R713	RES	11A3	R794	RES	52C7	R875	RES	11D5	R633	RES	10A3	R714	RES	36B7	R795	RES	52A6	R876	RES	11B7	R634	RES	10D5	R715	RES	14A7	R796	RES	17A8	R877	RES	11D5	R635	RES	39D3	R716	RES	46B8	R797	RES	52A5	R878	RES	11D6	R636	RES	46B6	R717	RES	46C3	R798	RES	52A5	R879	RES	11D6	R637	RES	36B3	R718	RES	30B3	R799	RES	52B8	R880	RES	51C3	R638	RES	10A3	R719	RES	32D5	R800	RES	47C3	R881	RES	51C3	R639	RES	10A3	R720	RES	11D3	R801	RES	47C3	R882	RES	7B5	R640	RES	39A5	R721	RES	52C8	R802	RES	52B5	R883	RES	51B6	R641	RES	30D1	R722	RES	46C6	R803	RES	52B5	R884	RES	51C5	R642	RES	10B3	R723	RES	30B3	R804	RES	52B6	R885	RES	11B6	R643	RES	10B3	R724	RES	30C3	R805	RES	52A3	R886	RES	11A6	R644	RES	10B5	R725	RES	46B6	R806	RES	52C6	R887	RES	11C6	R645	RES	53B6	R726	RES	46B7	R807	RES	52B5	R888	RES	51D5	R646	RES	46C1	R727	RES	30C7	R808	RES	52C3	R889	RES	11D6	R647	RES	36B4	R728	RES	30B3	R809	RES	17A8	R890	RES	11B8	R648	RES	46B2	R729	RES	46C6	R810	RES	52A5	R891	RES	4D6	R649	RES	46C3	R730	RES	46C7	R811	RES	52C5	R892	RES	11B7	R650	RES	30A2	R731	RES	46B6	R812	RES	8D6	R893	RES	11B7	R651	RES	30A2	R732	RES	46C7	R813	RES	8D6	R894	RES	11B8	R652	RES	10D7	R733	RES	46B7	R814	RES	52B3	R895	RES	4D2	R653	RES	46B7	R734	RES	46B7	R815	RES	52B3	R896	RES	47C5	R654	RES	46A5	R735	RES	30C7	R816	RES	8C6	R897	RES	47B5	R655	RES	46B2	R736	RES	30B3	R817	RES	8C6	R898	RES	47B5	R656	RES	46A6	R737	RES	40C7	R818	RES	8C5	R899	RES	47D6	R657	RES	46C3	R738	RES	53B7	R819	RES	8D4	R900	RES	47C5	R658	RES	46C3	R739	RES	53C7	R820	RES	8B7	R901	RES	4D3	R659	RES	46C2	R740	RES	53A7	R821	RES	8C7	R902	RES	7A4	R660	RES	30A2	R741	RES	53A6	R822	RES	8D3	R903	RES	7A4	R661	RES	30C7	R742	RES	30B3	R823	RES	8A5	R904	RES	7A4	R662	RES	10C6	R743	RES	40C4	R824	RES	8A5	R905	RES	7A4	R663	RES	10C6	R744	RES	40C7	R825	RES	17A3	R906	RES	7B4	R664	RES	53B6	R745	RES	40C3	R826	RES	8C7	R907	RES	7B4	R665	RES	46C1	R746	RES	30B8	R827	RES	51B5	R908	RES	47D6	R666	RES	30A6	R747	RES	30B6	R828	RES	8B7	R909	RES	7B5	R667	RES	18D6	R748	RES	10A5	R829	RES	8A7	R910	RES	7B5	R668	RES	31C3	R749	RES	30B8	R830	RES	8B7	R911	RES	7B5	R669	RES	31C2	R750	RES	30B8	R831	RES	8B6	R912	RES	7A5	R670	RES	31C2	R751	RES	53A8	R832	RES	11B6	R913	RES	7C4	R671	RES	46C1	R752	RES	11A3	R833	RES	11D7	R914	RES	7C4	R672	RES	30A6	R753	RES	39C2	R834	RES	8C5	R915	RES	7C4	R673	RES	36B4	R754	RES	39D4	R835	RES	8C5	R916	RES	7B4	R674	RES	10D3	R755	RES	10B7	R836	RES	8C2	R917	RES	7A4	R675	RES	10D3	R756	RES	10A7	R837	RES	8B2	R918	RES	7C4	R676	RES	46B4	R757	RES	11A4	R838	RES	8C2	R919	RES	7B4	R677	RES	46C2	R758	RES	40C1	R839	RES	8D3	R920	RES	7B4	R678	RES	30C8	R759	RES	39D2	R840	RES	7C7	R921	RES	7B5	R679	RES	39A2	R760	RES	40C5	R841	RES	7C7	R922	RES	7A5	R680	RES	30A8	R761	RES	39C5	R842	RES	7B7	R923	RES	7A5	R681	RES	14A7	R762	RES	30B3	R843	RES	7A7	R924	RES	7D5	R682	RES	52D6	R763	RES	30B3	R844	RES	7A7	R925	RES	7C5	R683	RES	30A2	R764	RES	11B4	R845	RES	7B7	R926	RES	7B4	R684	RES	36C1	R765	RES	11A4	R846	RES	7B7	R927	RES	48C5	R685	RES	53B6	R766	RES	11A3	R847	RES	7B7	R928	RES	47C5	R686	RES	46A7	R767	RES	39D2	R848	RES	7D5	R929	RES	47C5	R687	RES	46A7	R768	RES	39D5	R849	RES	7B7	R930	RES	48C5	R688	RES	46C2	R769	RES	53D7	R850	RES	4C2	R931	RES	47D7	R689	RES	46C3	R770	RES	53D7	R851	RES	7B7	R932	RES	47C7	R690	RES	46D3	R771	RES	10A7	R852	RES	11D7	R933	RES	47D7	R691	RES	30B3	R772	RES	11A4	R853	RES	11D7	R934	RES	47C8	R692	RES	46A3	R773	RES	39D2	R854	RES	11D7	R935	RES	47A5	R693	RES	46D8	R774	RES	39D4	R855	RES	11B6	R936	RES	47A5
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D	R937	RES	9B3			RP46	RPAK4P	19B7			U1	SWI_TPS2023	35A6		XW30	SHORT8L25_WITH_ALTS	47A3						
	R938	RES	9B3			RP47	RPAK4P	19A7			U2	SHNTREG_TLV431A	23A5		XW31	SHORT8L25_WITH_ALTS	47B3						
	R939	RES	9A3			RP48	RPAK4P	18B3			U3	VREG_LP2951	38D7		XW32	SHORT	41B7						
	R940	RES	9A3			RP49	RPAK4P	18B1 18B1 18C1			U4	FW802A	38C5		XW33	SHORT	43B7						
	R941	RES	39D1			RP50	RPAK4P	18B1			U5	SDRAM_DDR_4MX32	23D6 23D7		XW34	SHORT	41A7						
	R943	RES	39D2			RP51	RPAK4P	14B1 14C1			U6	OPAMP_TLV2362	42B3 42C3		XW35	SHORT	44A7						
	R944	RES	39D2			RP52	RPAK4P	14A1 14B1			U7	SHNTREG_TLV431A	20B8		XW36	SHORT	44A7						
	R945	RES	48B7			RP53	RPAK4P	10C2			U8	AMP_TPA6112A2	43D5		XW37	SHORT	41B7						
	R946	RES	48B7			RP54	RPAK4P	33B7			U10	AMP_TA2024	44B6		XW38	SHORT	41A7						
	R947	RES	48B7			RP55	RPAK4P	10C2			U11	UPD720101_FBG	A 34C5		XW39	SHORT8L25_WITH_ALTS	50B5						
C	R948	RES	33D4			RP56	RPAK4P	33B7			U12	SDRAM_DDR_4MX32	22D6 22D7		XW40	SHORT	44D7						
	R949	RES	33D4			RP57	RPAK4P	10C2			U13	OPAMP_TLV2362	45B3		XW41	SHORT8L25_WITH_ALTS	51B5						
	R950	RES	33D3			RP58	RPAK4P	33C7			U14	VREG_LP2951	41D6		XW42	SHORT	30A5						
	R951	RES	33C2			RP59	RPAK4P	33C7			U15	TAS3004	41C3		XW43	SHORT	30A5						
	R3001	RES	30A2			RP60	RPAK4P	30C2			U16	SHNTREG_TLV431A	22A5		XW44	SHORT	30A5						
	R3002	RES	30A2			RP61	RPAK4P	33B7			U17	SHNTREG_TLV431A	16B3		XW45	SHORT	30A4						
	R3801	RES	38B1			RP62	RPAK4P	14B3 14C3			U18	CBTV4020	15D7		XW46	SHORT	30A4						
	R4301	RES	43A7			RP63	RPAK4P	32B8			U19	VREG_LT1962	30D7		XW47	SHORT	11A2						
	R4401	RES	44A8			RP64	RPAK4P	33B7			U20	VREG_LM1117	25A8		XW48	SHORT	18A5						
	R4501	RES	45C6			RP65	RPAK4P	36C4			U21	VDET_MC33465N_22ATR	46A4		XW49	SHORT	32B5						
B	R4502	RES	45B6			RP66	RPAK4P	30A8			U22	CBTV4020	15B6		Y1	CRYSTAL	38C6						
	R4503	RES	45B6			RP67	RPAK4P	33B7			U23	MAX6328	46A5		Y2	CRYSTAL_4PIN	24A3						
	R4504	RES	45C6			RP68	RPAK4P	14C3 14D3			U24	CLK_DR_PCK2059	10C4		Y3	CRYSTAL	46A6						
	R4505	RES	45B6			RP69	RPAK4P	32A8			U25	INTREPID	11D2 12D5 12D7 14D7 18D5 30C4 32D5		Y4	CRYSTAL_4PIN	46B2						
	R4506	RES	45B6			RP70	RPAK4P	36C7				U26	M16C62	46C5		Y5	CRYSTAL	30A6					
	R4507	RES	45D5			RP71	RPAK4P	14C1 14D1				U27	CBTV4020	15D4		Y6	CRYSTAL	37B7					
	R4701	RES	47B6			RP72	RPAK4P	33C7			U28	VREG_TL431	46D8		Y7	CRYSTAL	34D3						
	R4702	RES	47B6			RP73	RPAK4P	33C7			U29	CBTV4020	15C2		ZH3	MTGHOLE	41A7						
	R4703	RES	47B3			RP74	RPAK4P	30A8			U30	CLK_GEN_CY28512	10A6		ZH4	MTGHOLE	4B2						
	R4704	RES	47B3			RP75	RPAK4P	33C7			U31	DCDC_SC2602	49B6		ZH5	SLOT	4B1						
A	R4705	RES	47B3			RP76	RPAK4P	36C7			U32	SHNTREG_TLV431A	14A8		ZH6	SLOT	4B2						
	R4706	RES	47D3			RP77	RPAK4P	33C7			U33	SHNTREG_TLV431A	14A8		ZH7	MTGHOLE	4B1						
	R4707	RES	47D3			RP78	RPAK4P	7A7			U34	LTC3707	52B6		ZT1	HOLE_VIA	20C2						
	R4708	RES	47B5			RP79	RPAK4P	7A5 7C5			U35	APOLLO_MPC7445_360	4C5 5D3 5D6		ZT2	HOLE_VIA	20C2						
	R4801	RES	48C7			RP80	RPAK4P	21B7			U36	TRA_IRF7410	53D2		ZT3	HOLE_VIA	20C2						
	R5301	RES	53C1			RP81	RPAK4P	21B7			U37	SDRAM_DDR_4MX32	23D2 23D4		ZT4	HOLE_VIA	20A2						
	RP1	RPAK4P	37C8			RP82	RPAK4P	21B7 21C7 21C7			U38	TRANSCIVER_BCM5221	37C5		ZT5	HOLE_VIA	20C2						
	RP2	RPAK4P	38C7			RP83	RPAK4P	20B2 20B3 20B3			U39	SN74AUC1G04	32A3		ZT6	HOLE_VIA	20C2						
	RP3	RPAK4P	38B7			RP84	RPAK4P	20B2 20B2 20B3			U40	NV18B	19D6 20G5 20G7 24D4 25D3		ZT7	HOLE_VIA	20A2						
	RP4	RPAK4P	21B5 21C5			RP85	RPAK4P	20B3 20C2 20C3			U41	DCDC_SC2602	50C6		ZT8	HOLE_VIA	20C2						
	RP5	RPAK4P	21B5			RP86	RPAK4P	21B5			U42	SDRAM_DDR_4MX32	22D2 22D4		ZT9	HOLE_VIA	20B2						
	RP6	RPAK4P	21B5			RP87	RPAK4P	21B5			U43	FEPR_1MX8	32C2		ZT10	HOLE_VIA	20B2						
	RP7	RPAK4P	21B5			RP88	RPAK4P	21C5			U44	EEPROM_16KX8_M24128B	8B6		ZT11	HOLE_VIA	20B2						
	RP8	RPAK4P	20A2 20A3 20B2			RP89	RPAK4P	21C5			U45	AT90S1200	8C4		ZT12	HOLE_VIA	20B2						
	RP9	RPAK4P	20C2 20C2 20C3 20C3			RP90	RPAK4P	21C7			U46	DCDC_SC2602	51C5		ZT13	HOLE_VIA	20B2						
	RP10	RPAK4P	21C7			RP91	RPAK4P	21C7 21D7			U47	SN74LVC1G04	46D1		ZT14	HOLE_VIA	20B2						
	RP11	RPAK4P	21B7			RP92	RPAK4P	21C7			U48	LTC3707	47C6		ZT15	HOLE_VIA	20B2						
	RP12	RPAK4P	21B7			RP93	RPAK4P	20E2 20E3			VR1	NC7WZ08	43A7 44A7		ZT1								