

Rogue-2 SHB SVT LOGIC SCHEMATICS

RG2SB-5
VER 5.05
Apr/11/2013

BASE LOGIC :
Rogue-2 SHB SIT-R VER 4.12 Mar/11/2013

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- 59.CHARGER SELECTOR
- 60.BATTERY MONITOR
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- 62.DC/DC VCCCPUCORE(VT1320M)

- 63.DC/DC VCCCPUCORE(VT1334S)
- 64.BLANK
- 65.VCCCPUCORE DECOUPLING
- 66.BLANK
- 67.DC/DC VCC1R05AMT(VT382B)
- 68.DC/DC VCC1R35A(VT387B)
- 69.DC/DC VCC0R675B(TPS51200)
- 70.BLANK
- 71.BLANK
- 72.DC/DC VCC1R5B(BU15TD2WNVX)
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- 74.LOAD SW LAN
- 75.LOAD SW B
- 76.LOAD SW WWAN & WLAN
- 77.PTH FOR SCREW HOLES

EC HISTORY

ROGUE-2 SHB SVT
(BASE LOGIC :RG2SB-4 VER 4.12 Mar/11/2013)

- VER.5.00 03/21/2013 APPLIED RG2SB_SVT_EC001-007
- VER.5.01 03/26/2013 APPLIED RG2SB_SVT_EC009
- VER.5.02 03/28/2013 APPLIED RG2SB_SVT_EC010
- VER.5.03 04/04/2013 APPLIED RG2SB_SVT_EC011
- VER.5.04 04/05/2013 APPLIED RG2SB_SVT_EC012-017
- VER.5.05 04/11/2013 APPLIED RG2SB_SVT_EC018

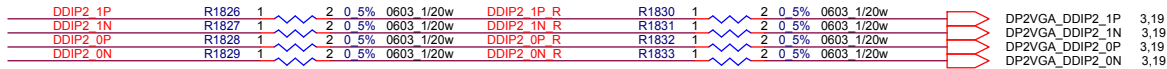
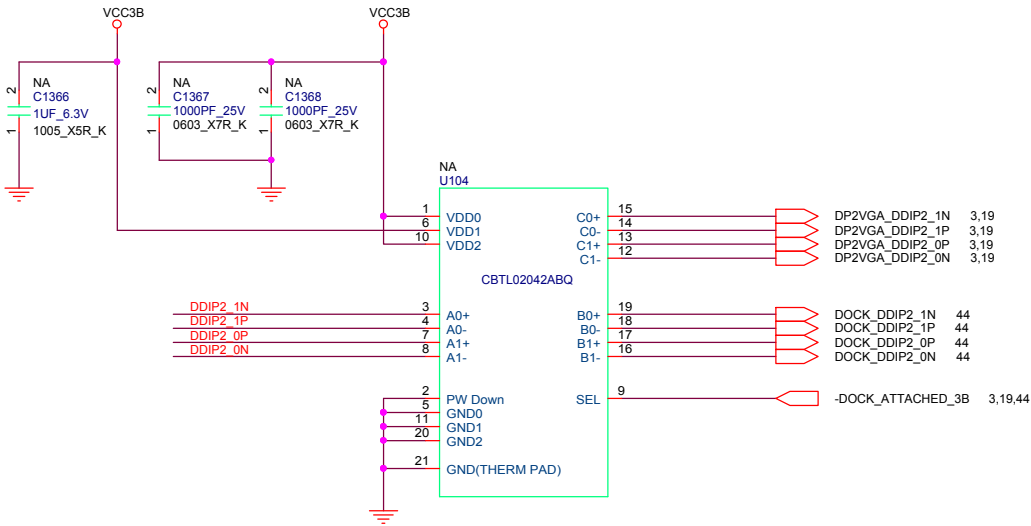
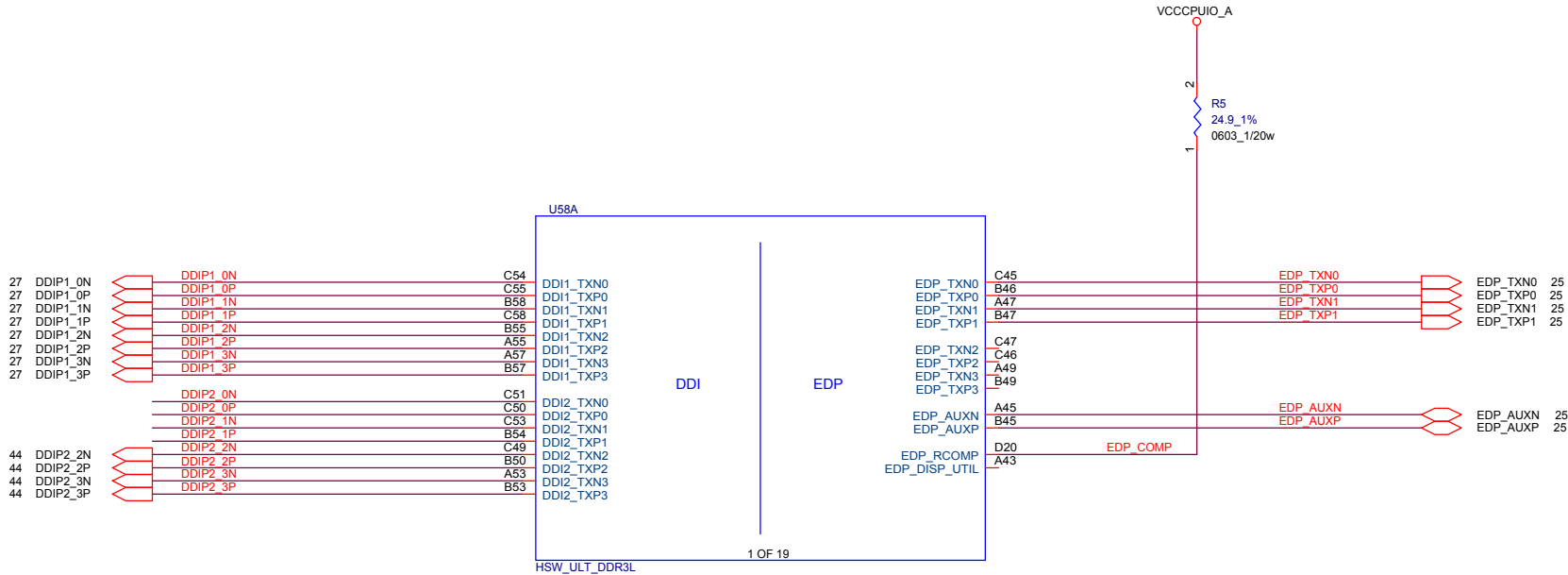


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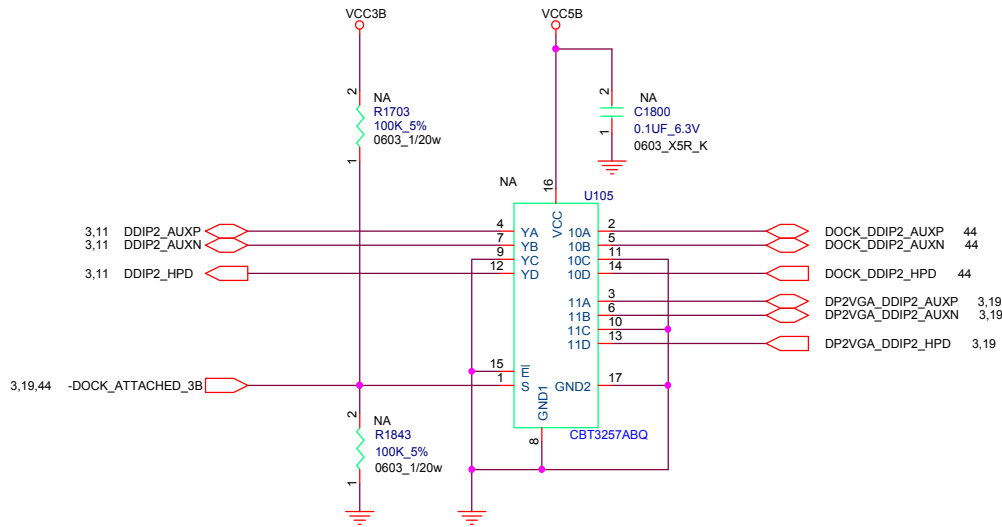
TABLE

ID	DOCKING SUPPORT	WIGIG SUPPORT	NON-SUPPORT
U104	ASM	ASM	NO ASM
C1366	ASM	ASM	NO ASM
C1367	ASM	ASM	NO ASM
C1368	ASM	ASM	NO ASM
U105	ASM	ASM	NO ASM
C1800	ASM	ASM	NO ASM
R1703	ASM	NO ASM	NO ASM
R1843	NO ASM	ASM	NO ASM
R1826	NO ASM	NO ASM	ASM
R1827	NO ASM	NO ASM	ASM
R1828	NO ASM	NO ASM	ASM
R1829	NO ASM	NO ASM	ASM
R1830	NO ASM	NO ASM	ASM
R1831	NO ASM	NO ASM	ASM
R1832	NO ASM	NO ASM	ASM
R1833	NO ASM	NO ASM	ASM
R1834	NO ASM	NO ASM	ASM
R1835	NO ASM	NO ASM	ASM
R1836	NO ASM	NO ASM	ASM

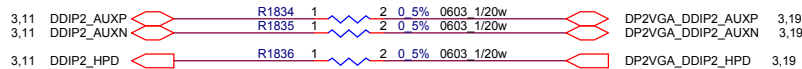
LOGIC
↓



Near U104 on the opposite side



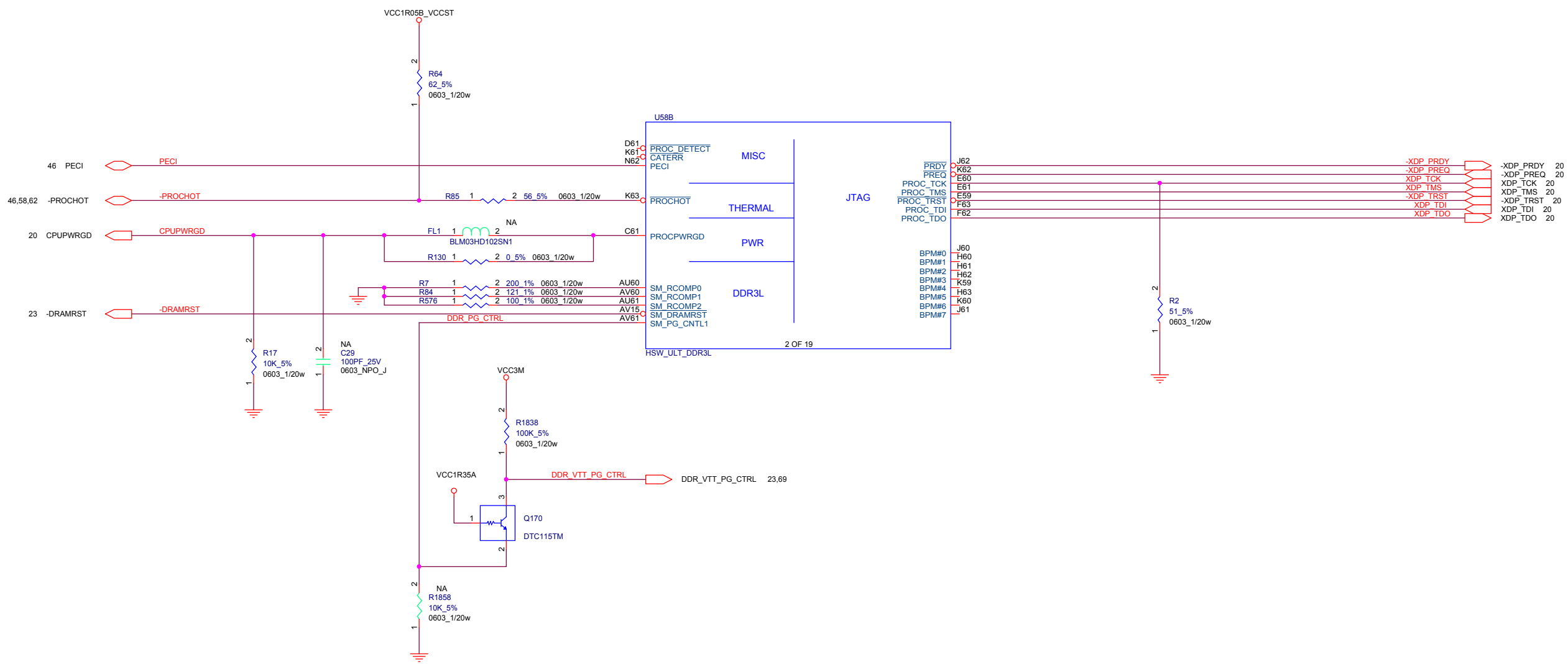
Near U105



Project Name : RG2 SB SV1 Title : CPU(1/16) : DDI/EDP

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23 M_A_DQ[63:0]

M_A DQ0	AH63	SA_DQ0
M_A DQ1	AH62	SA_DQ1
M_A DQ2	AK63	SA_DQ2
M_A DQ3	AK62	SA_DQ3
M_A DQ4	AH61	SA_DQ4
M_A DQ5	AH60	SA_DQ5
M_A DQ6	AK61	SA_DQ6
M_A DQ7	AK60	SA_DQ7
M_A DQ8	AM63	SA_DQ8
M_A DQ9	AM62	SA_DQ9
M_A DQ10	AP63	SA_DQ10
M_A DQ11	AP62	SA_DQ11
M_A DQ12	AM61	SA_DQ12
M_A DQ13	AM60	SA_DQ13
M_A DQ14	AP61	SA_DQ14
M_A DQ15	AP60	SA_DQ15
M_A DQ16	AP58	SA_DQ16
M_A DQ17	AR58	SA_DQ17
M_A DQ18	AM57	SA_DQ18
M_A DQ19	AK57	SA_DQ19
M_A DQ20	AL58	SA_DQ20
M_A DQ21	AK58	SA_DQ21
M_A DQ22	AR57	SA_DQ22
M_A DQ23	AN57	SA_DQ23
M_A DQ24	AP55	SA_DQ24
M_A DQ25	AR55	SA_DQ25
M_A DQ26	AM54	SA_DQ26
M_A DQ27	AK54	SA_DQ27
M_A DQ28	AL55	SA_DQ28
M_A DQ29	AK55	SA_DQ29
M_A DQ30	AR54	SA_DQ30
M_A DQ31	AN54	SA_DQ31
M_A DQ32	AY58	SA_DQ32
M_A DQ33	AW58	SA_DQ33
M_A DQ34	AY56	SA_DQ34
M_A DQ35	AW56	SA_DQ35
M_A DQ36	AV58	SA_DQ36
M_A DQ37	AU58	SA_DQ37
M_A DQ38	AV56	SA_DQ38
M_A DQ39	AU56	SA_DQ39
M_A DQ40	AY54	SA_DQ40
M_A DQ41	AW54	SA_DQ41
M_A DQ42	AY52	SA_DQ42
M_A DQ43	AW52	SA_DQ43
M_A DQ44	AV54	SA_DQ44
M_A DQ45	AU54	SA_DQ45
M_A DQ46	AV52	SA_DQ46
M_A DQ47	AU52	SA_DQ47
M_A DQ48	AK40	SA_DQ48
M_A DQ49	AK42	SA_DQ49
M_A DQ50	AM43	SA_DQ50
M_A DQ51	AM45	SA_DQ51
M_A DQ52	AK45	SA_DQ52
M_A DQ53	AK43	SA_DQ53
M_A DQ54	AM40	SA_DQ54
M_A DQ55	AM42	SA_DQ55
M_A DQ56	AM46	SA_DQ56
M_A DQ57	AK46	SA_DQ57
M_A DQ58	AM49	SA_DQ58
M_A DQ59	AK49	SA_DQ59
M_A DQ60	AM48	SA_DQ60
M_A DQ61	AK48	SA_DQ61
M_A DQ62	AM51	SA_DQ62
M_A DQ63	AK51	SA_DQ63

DDR CHANNEL A

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HSW_ULT_DDR3L

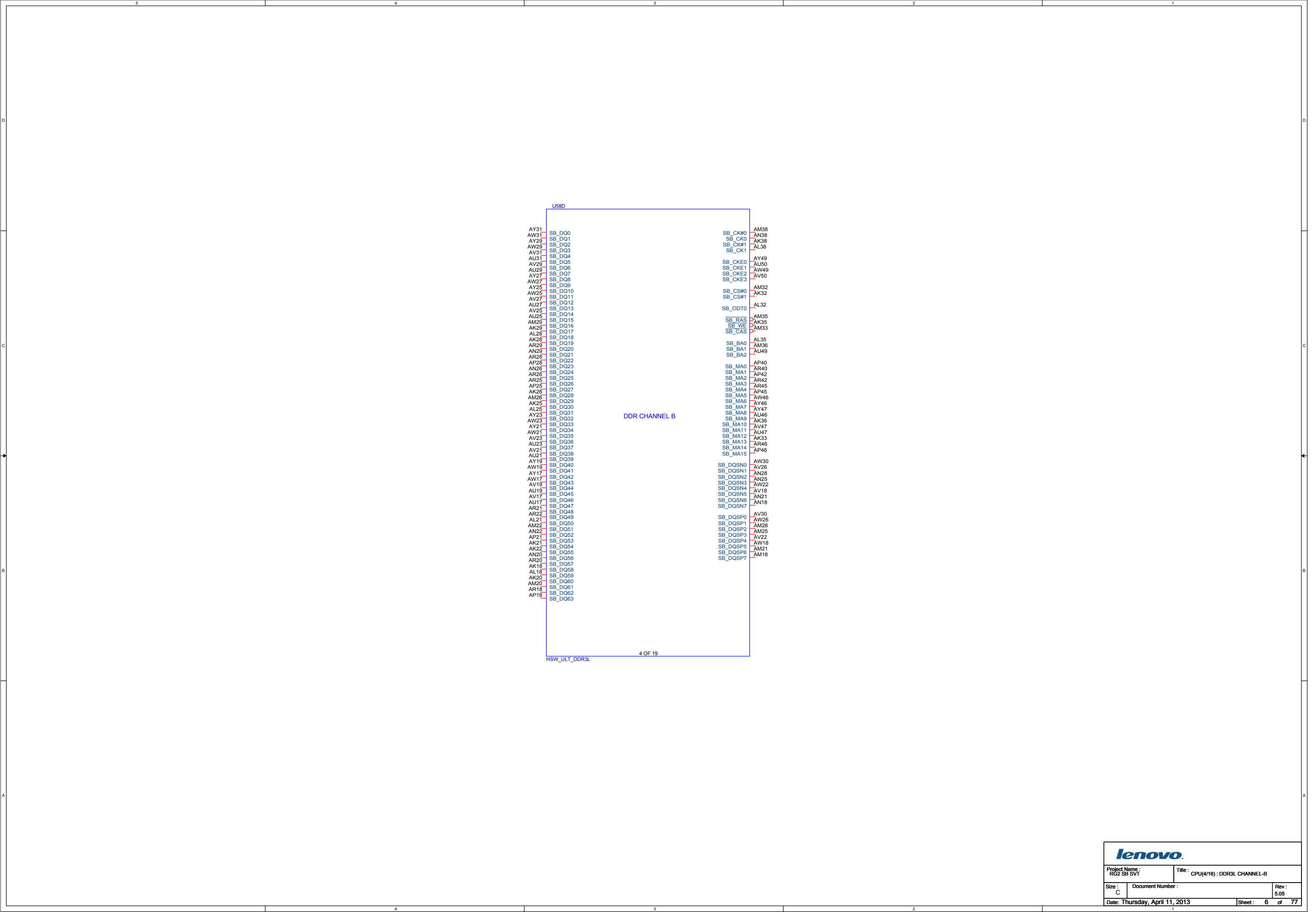
SA_CLK#0	AU37	-M_A DDRCLK0_800M	M_A DDRCLK0_800M	23
SA_CLK#1	AV37	M_A DDRCLK0_800M	M_A DDRCLK0_800M	23
SA_CLK#1	AW36	-M_A DDRCLK1_800M	M_A DDRCLK1_800M	23
SA_CLK#1	AY36	M_A DDRCLK1_800M	M_A DDRCLK1_800M	23
SA_CKE0	AU43	M_A CKE0	M_A_CKE0	23
SA_CKE1	AW43	M_A CKE1	M_A_CKE1	23
SA_CKE2	AY42			
SA_CKE3	AY43			
SA_CS#0	AP33	-M_A CS0	-M_A_CS0	23
SA_CS#1	AR32	-M_A CS1	-M_A_CS1	23
SA_ODT0	AP32			
SA_RAS	AY34	-M_A RAS	-M_A_RAS	23
SA_WE	AW34	-M_A WE	-M_A_WE	23
SA_CAS	AU34	-M_A CAS	-M_A_CAS	23
SA_BA0	AU35	M_A BS0	M_A_BS0	23
SA_BA1	AV35	M_A BS1	M_A_BS1	23
SA_BA2	AY41	M_A BS2	M_A_BS2	23
SA_MA0	AU36	M_A A0		
SA_MA1	AY37	M_A A1		
SA_MA2	AR38	M_A A2		
SA_MA3	AP36	M_A A3		
SA_MA4	AU39	M_A A4		
SA_MA5	AR36	M_A A5		
SA_MA6	AV40	M_A A6		
SA_MA7	AW39	M_A A7		
SA_MA8	AY39	M_A A8		
SA_MA9	AU40	M_A A9		
SA_MA10	AP35	M_A A10		
SA_MA11	AW41	M_A A11		
SA_MA12	AU41	M_A A12		
SA_MA13	AR35	M_A A13		
SA_MA14	AV42	M_A A14		
SA_MA15	AU42	M_A A15		
SA_DQS#0	AJ61	-M_A DQS0		
SA_DQS#1	AN62	-M_A DQS1		
SA_DQS#2	AM58	-M_A DQS2		
SA_DQS#3	AM55	-M_A DQS3		
SA_DQS#4	AV57	-M_A DQS4		
SA_DQS#5	AV53	-M_A DQS5		
SA_DQS#6	AL43	-M_A DQS6		
SA_DQS#7	AL48	-M_A DQS7		
SA_DQSP0	AJ62	M_A DQS0		
SA_DQSP1	AN61	M_A DQS1		
SA_DQSP2	AN58	M_A DQS2		
SA_DQSP3	AN55	M_A DQS3		
SA_DQSP4	AW57	M_A DQS4		
SA_DQSP5	AW53	M_A DQS5		
SA_DQSP6	AL42	M_A DQS6		
SA_DQSP7	AL49	M_A DQS7		
SM_VREF_CA	AP49	DDR3_VREF_CA_M3	23	
SM_VREF_DQ0	AR51	DDR3_VREF_DQ_SA_M3	23	
SM_VREF_DQ1	AP51			

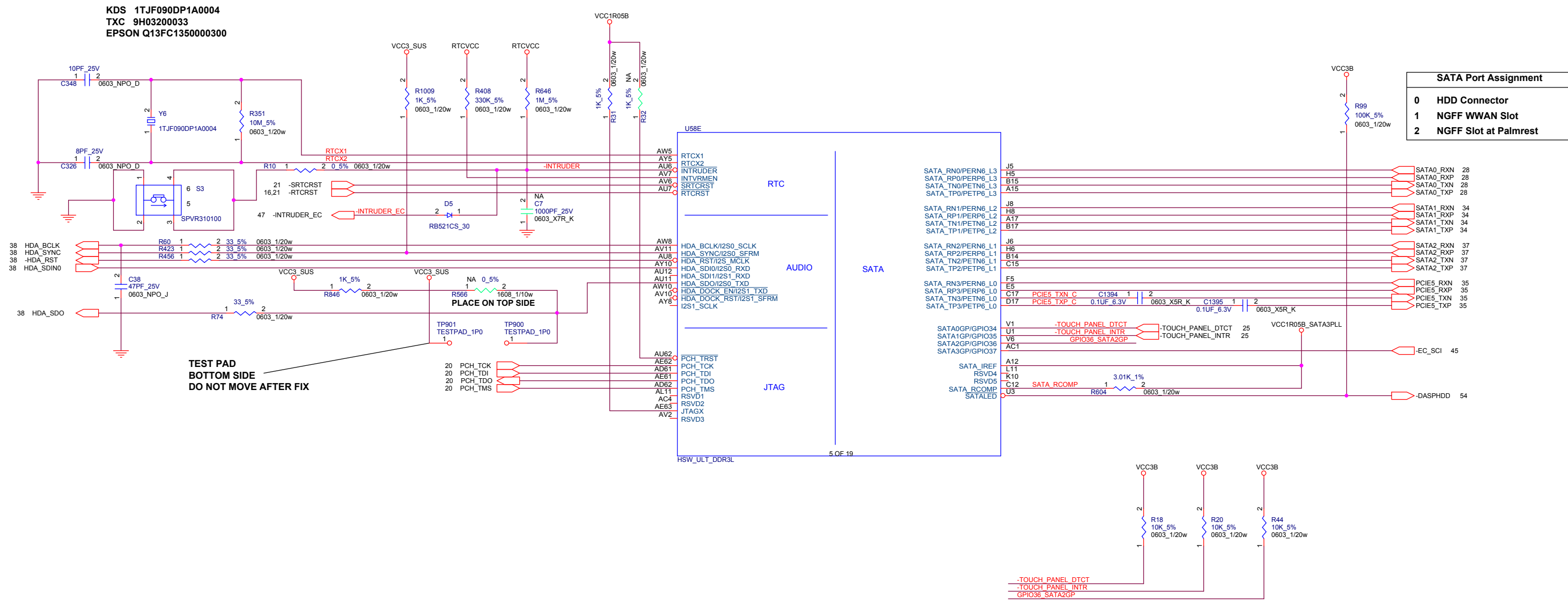
M_A_A[15:0] 23

-M_A_DQS[7:0] 23

M_A_DQS[7:0] 23



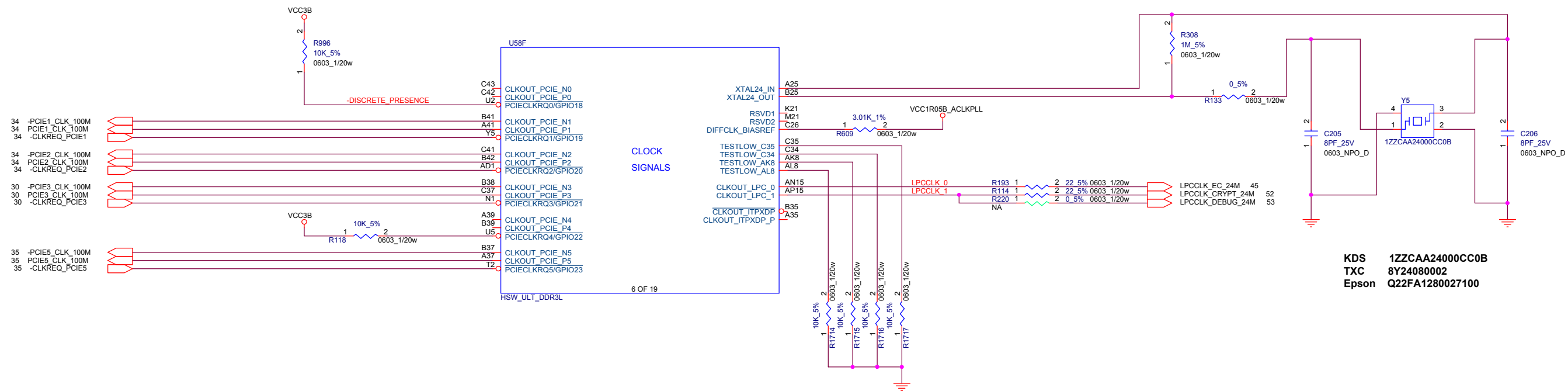




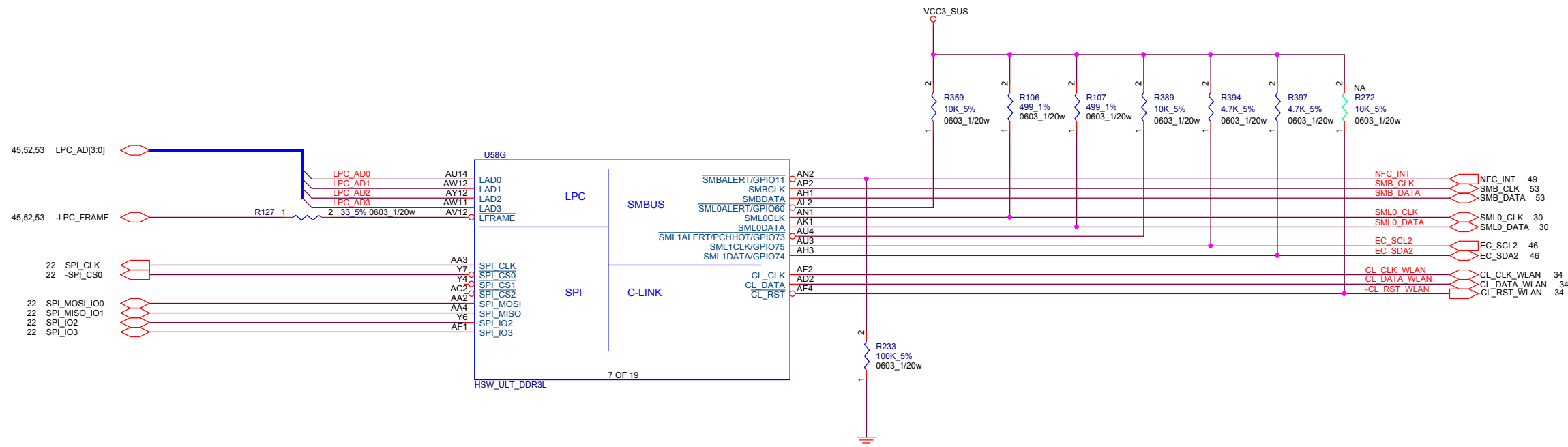
Project Name : RG2 SB SV1 Title : CPU(5/16) : RTC/HDA/SATA/JTAG

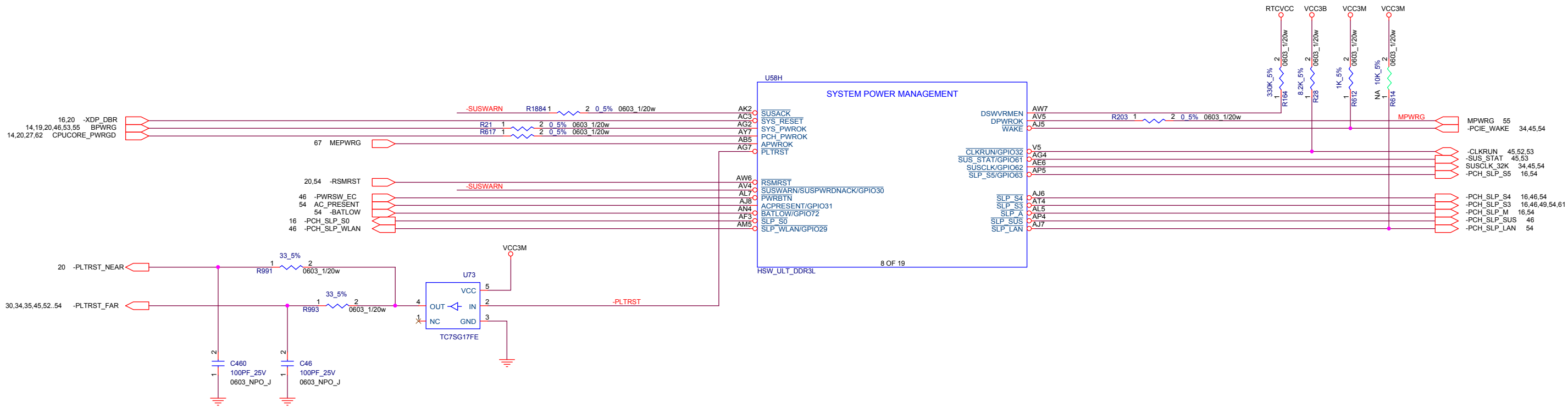
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KDS 1ZZCAA24000CC0B
TXC 8Y24080002
Epson Q22FA1280027100





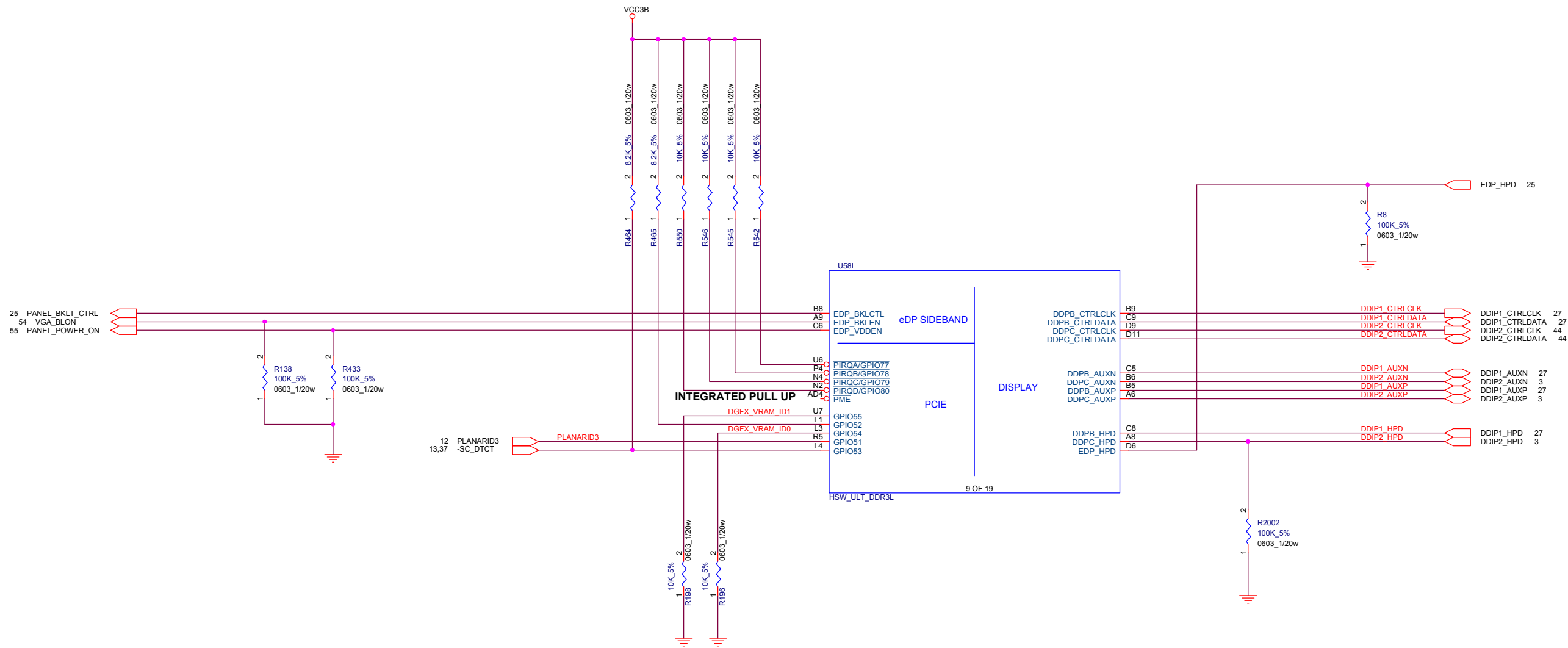


TABLE	
GPIO15	ME CRYPTO STRAP
HIGH	WITH CONFIDENTIALITY
LOW	NO CONFIDENTIALITY

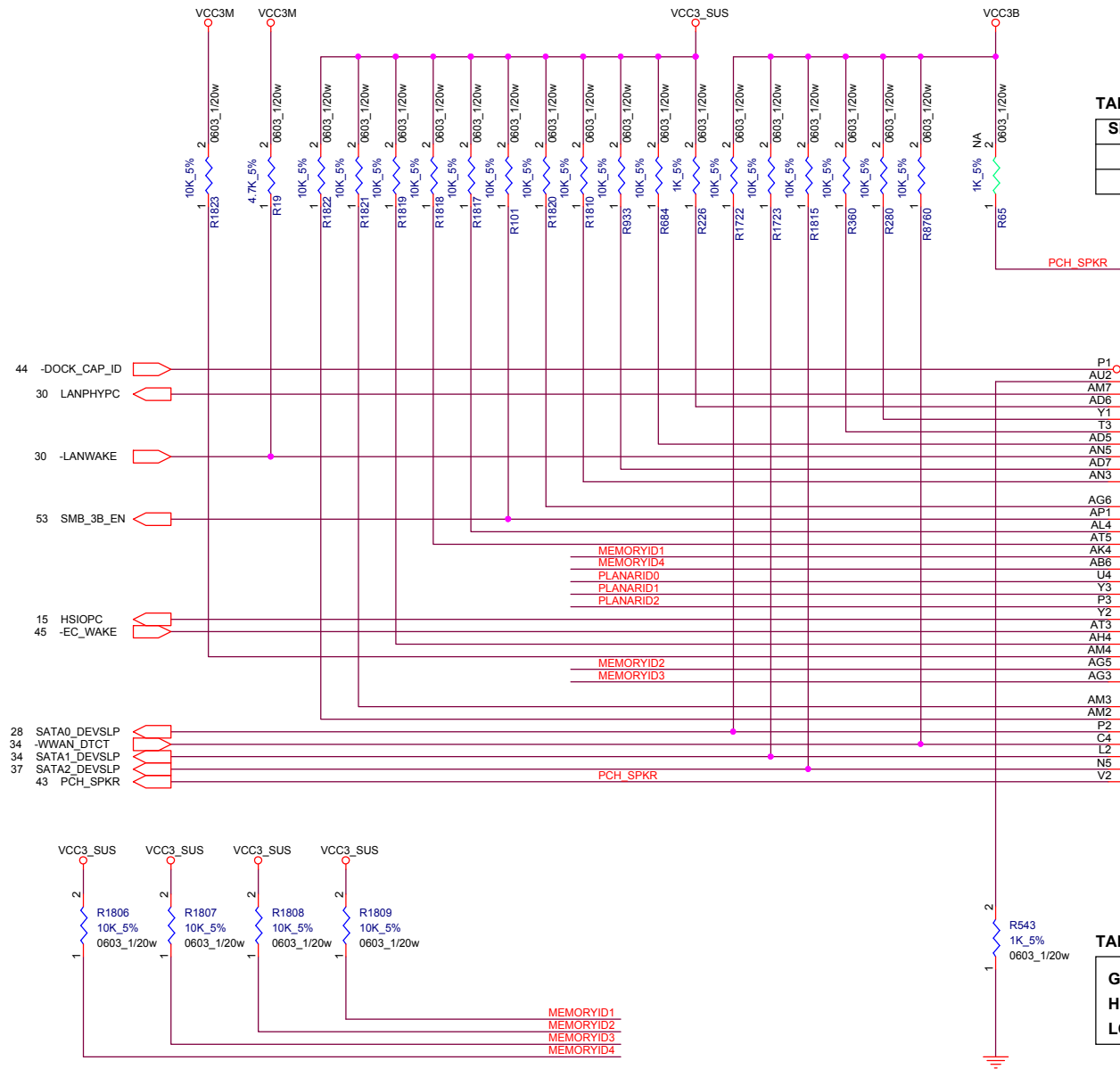


TABLE	
SPKR TCO TIMER SYSTEM REBOOT	
HIGH	DISABLED (NO REBOOT)
LOW	ENABLED

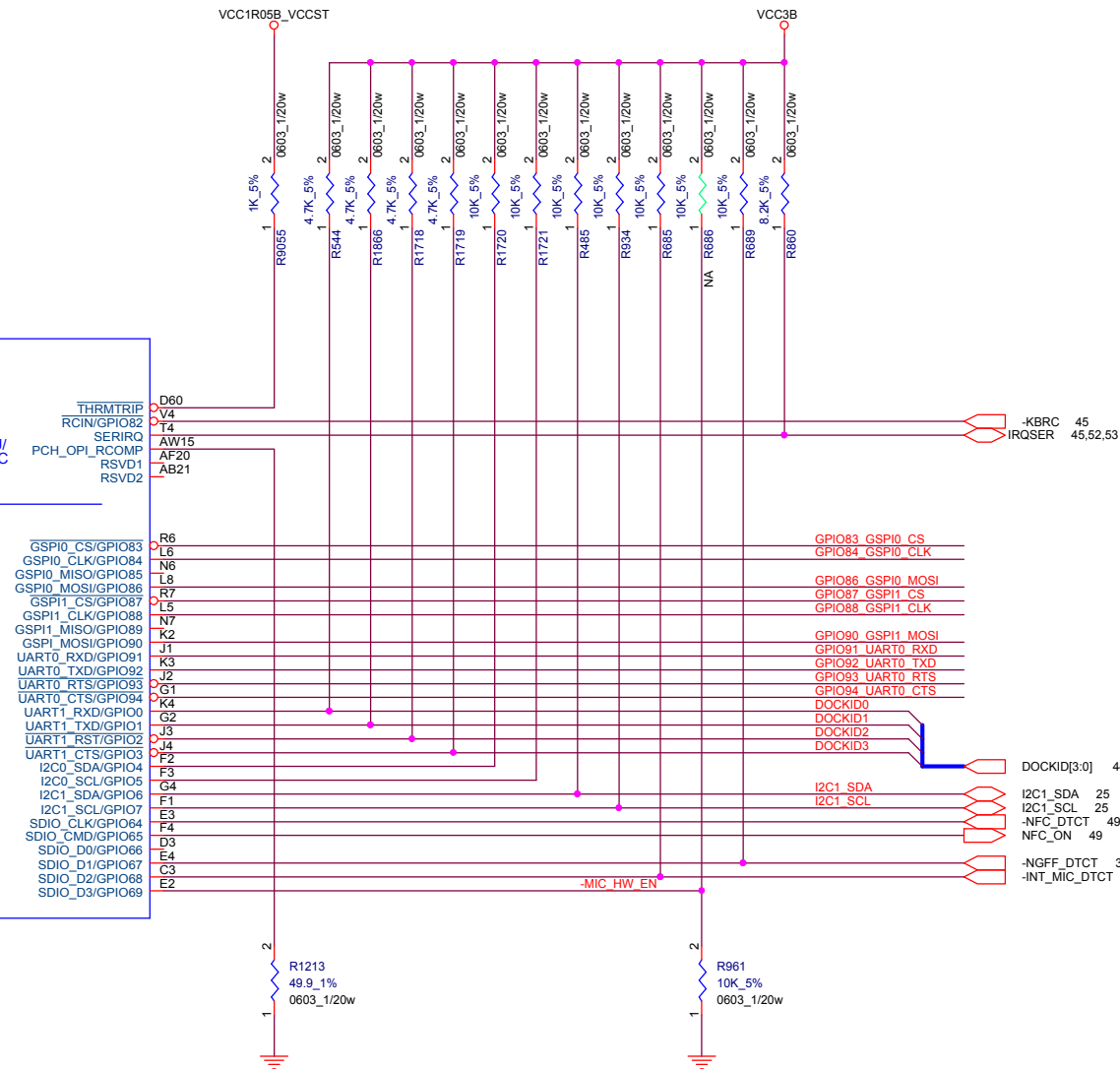


TABLE	
GPIO8 INTEGRATED CLOCKING	
HIGH	DISABLED(BTM)
LOW	ENABLED(FCIM)

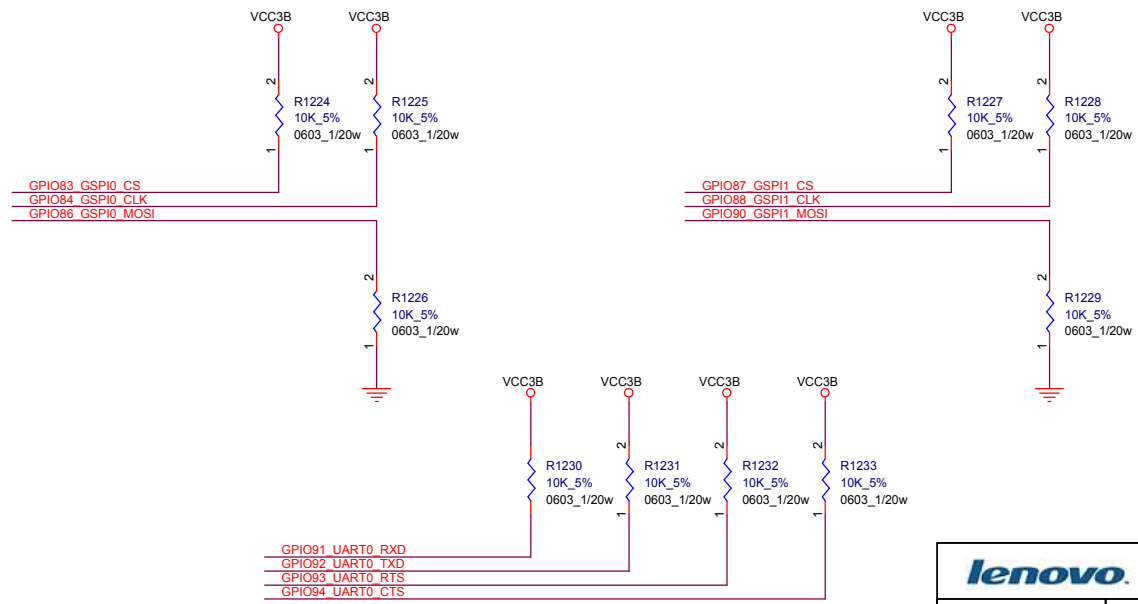
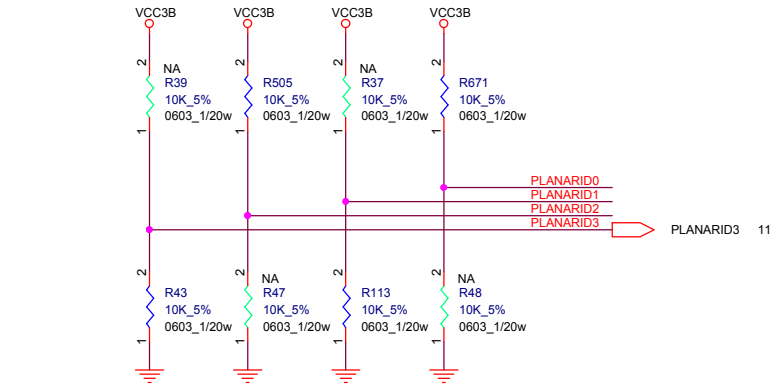


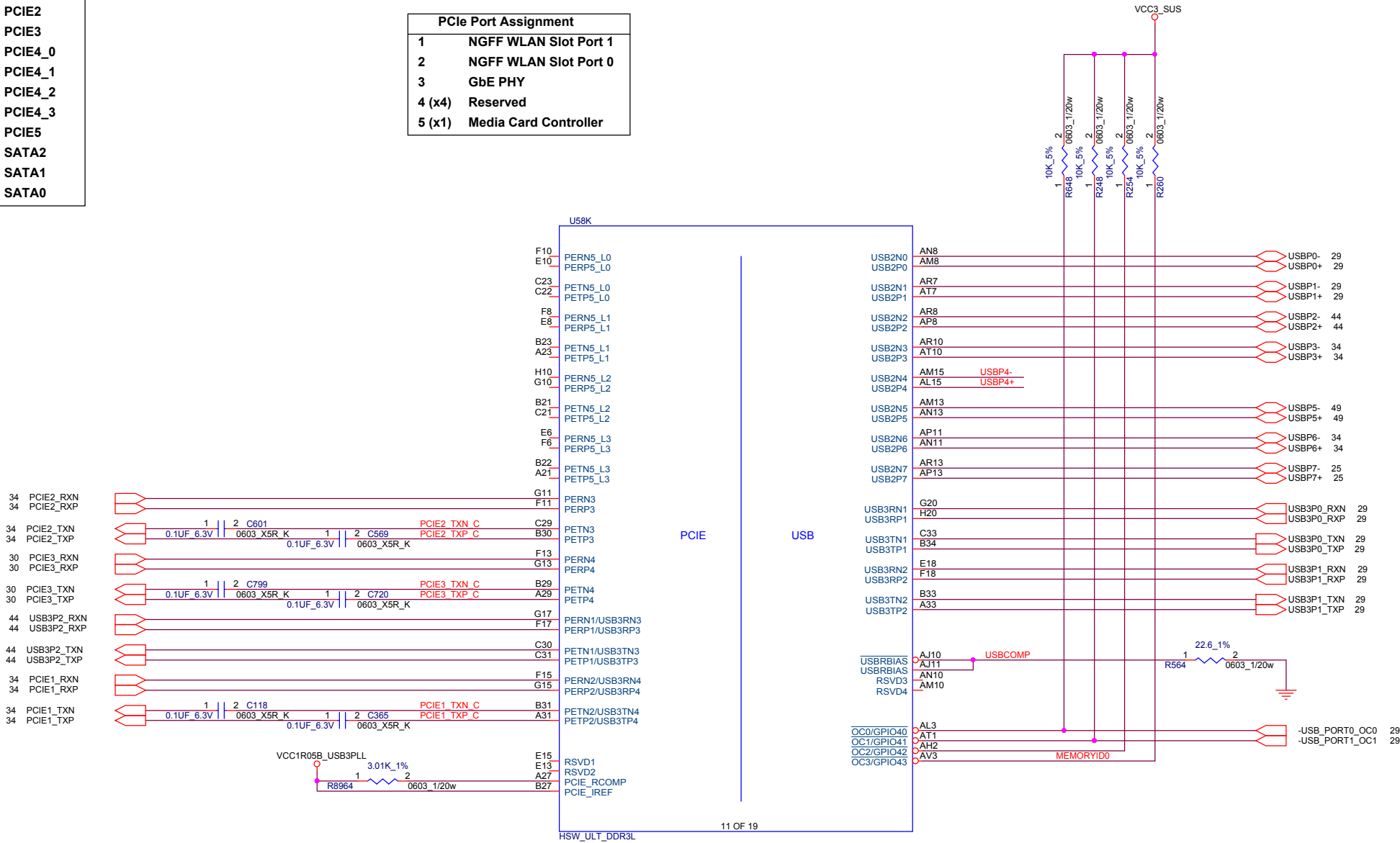
TABLE	
LEVEL	PLANAR ID
	3 2 1 0
1	R39 R505 R37 R671
0	R43 R47 R113 R48

TABLE	
LEVEL	PLANARID[3..0]
PDV	0000B
SDV	0001B
FVT	0010B
SIT	0011B
SIT-R	0100B
SVT	0101B



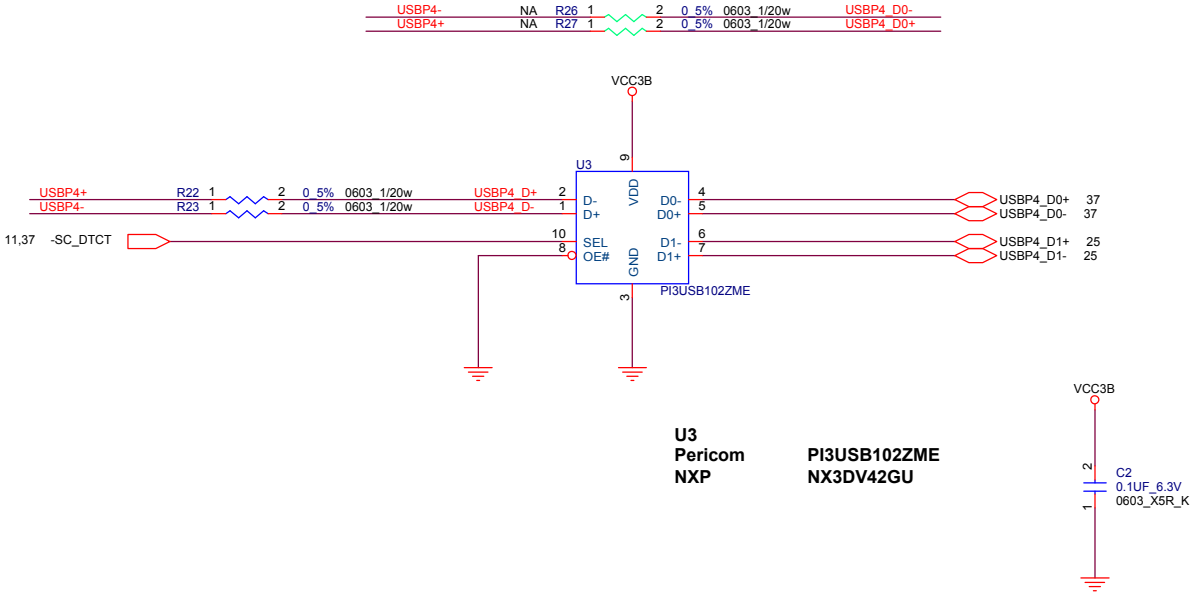
Flexible I/O Configuration			
I/O	High Speed Signals	Configuration	Net Name
Port 1	USB3 1	USB3 1	USB3P0
Port 2	USB3 2	USB3 2	USB3P1
Port 3	PCIE 1/USB3 3	USB3 3	USB3P2
Port 4	PCIE 2/USB3 4	PCIE 2	PCIE1
Port 5	PCIE 3	PCIE 3	PCIE2
Port 6	PCIE 4	PCIE 4	PCIE3
Port 7	PCIE 5 L0	PCIE 5 L0	PCIE4_0
Port 8	PCIE 5 L1	PCIE 5 L1	PCIE4_1
Port 9	PCIE 5 L2	PCIE 5 L2	PCIE4_2
Port 10	PCIE 5 L3	PCIE 5 L3	PCIE4_3
Port 11	PCIE 6 L0	PCIE 6 L0	PCIE5
Port 12	SATA 2/PCIE 6 L1	SATA 2	SATA2
Port 13	SATA 1/PCIE 6 L2	SATA 1	SATA1
Port 14	SATA 0/PCIE 6 L3	SATA 0	SATA0

PCIe Port Assignment	
1	NGFF WLAN Slot Port 1
2	NGFF WLAN Slot Port 0
3	GbE PHY
4 (x4)	Reserved
5 (x1)	Media Card Controller



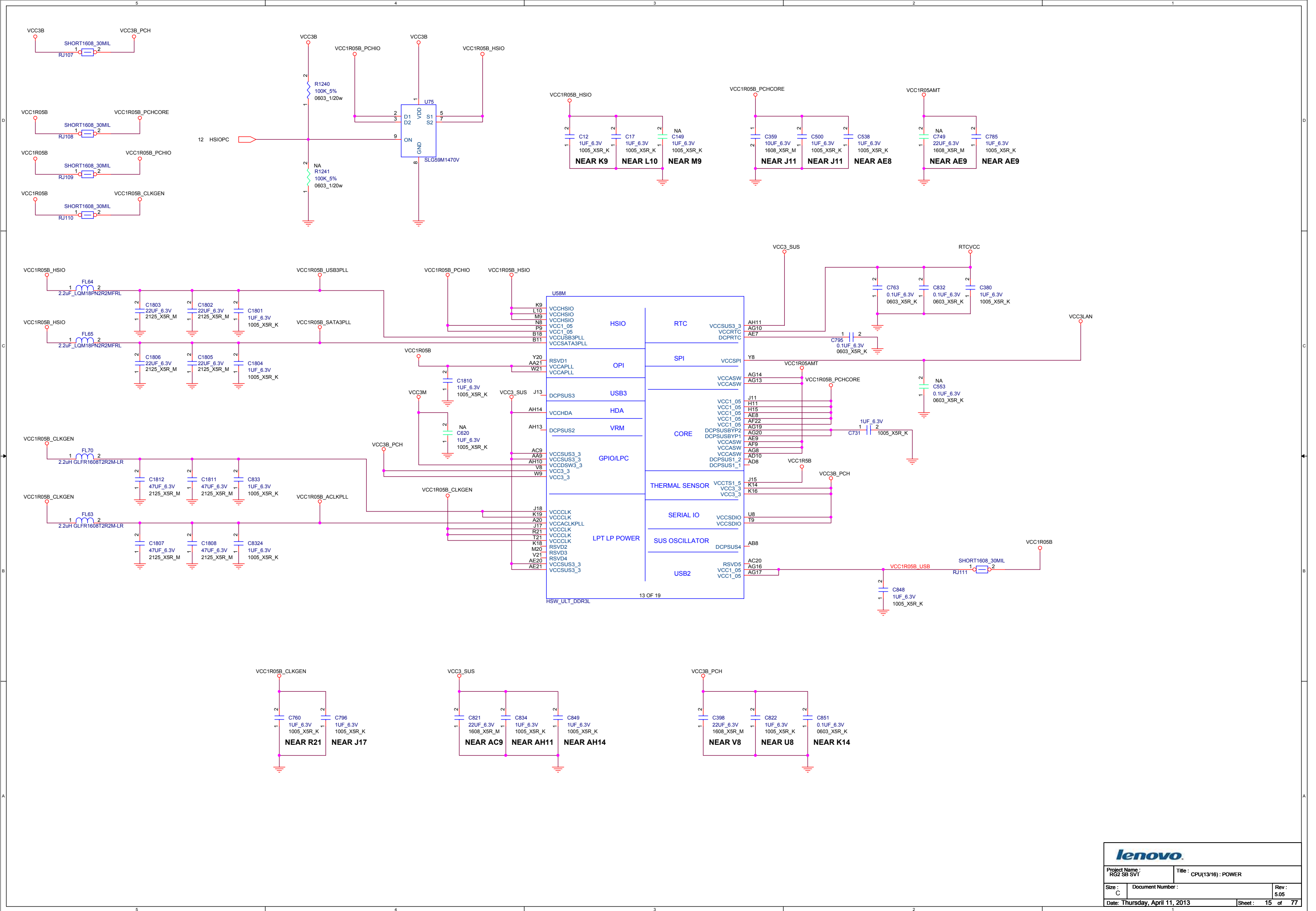
USB Port Assignment	
0	USB 3.0 System Port (AOU)
1	USB 3.0 System Port (Debug)
2	USB 3.0 Docking
3	NGFF WWAN Slot
4	Smart Card Slot or Touch Panel
5	Fingerprint Reader
6	NGFF WLAN Slot
7	USB Camera

USB 3.0 Port Assignment	
0	USB 3.0 System Port (AOU)
1	USB 3.0 System Port (Debug)
2	USB 3.0 Docking



Device Selection	
0	Smart Card Slot
1	Touch Panel





Project Name :
RG2 SB SV1

Title :
CPU(13/16) : POWER

Size :
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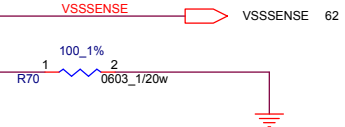
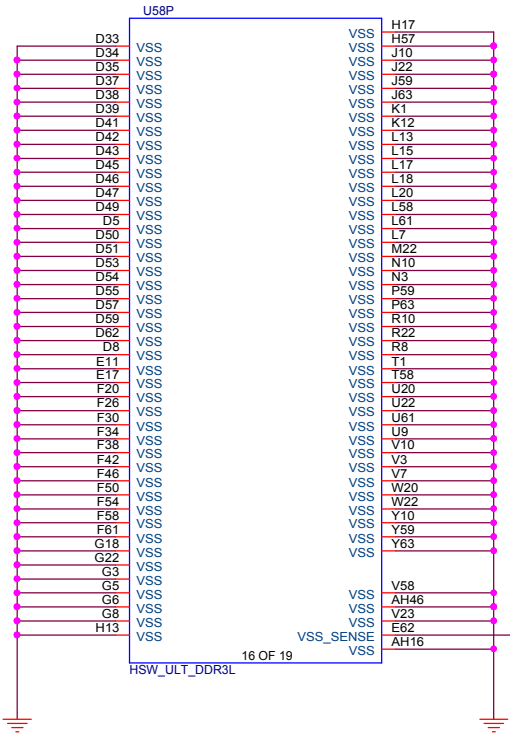
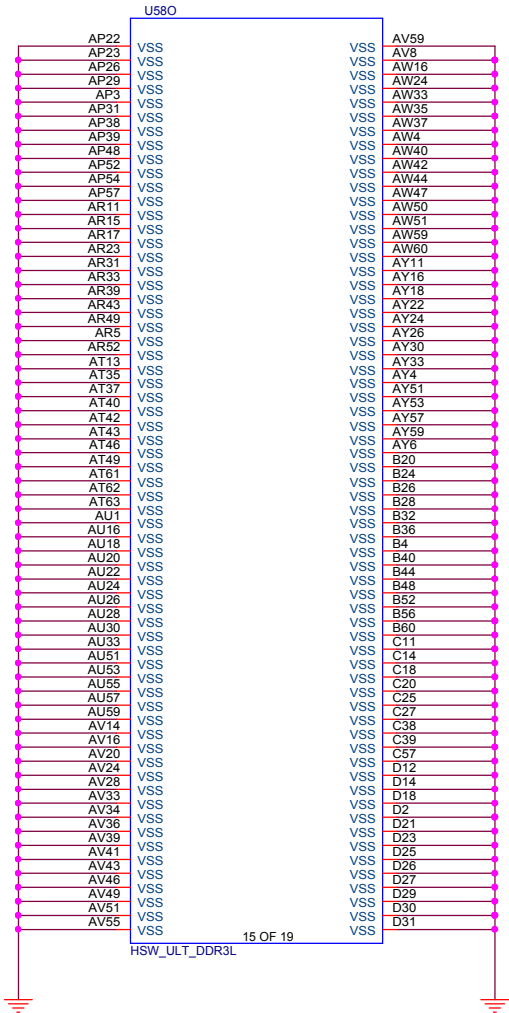
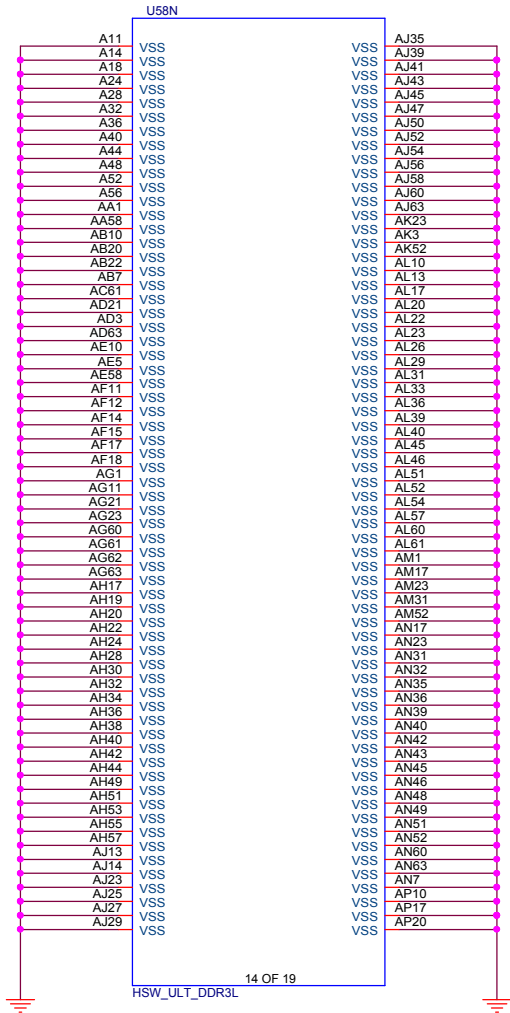
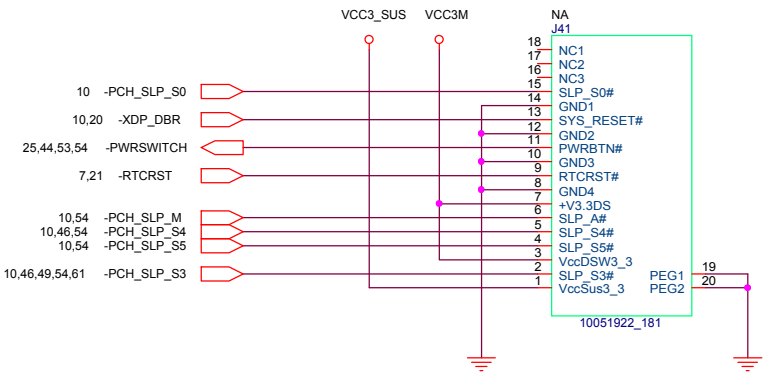
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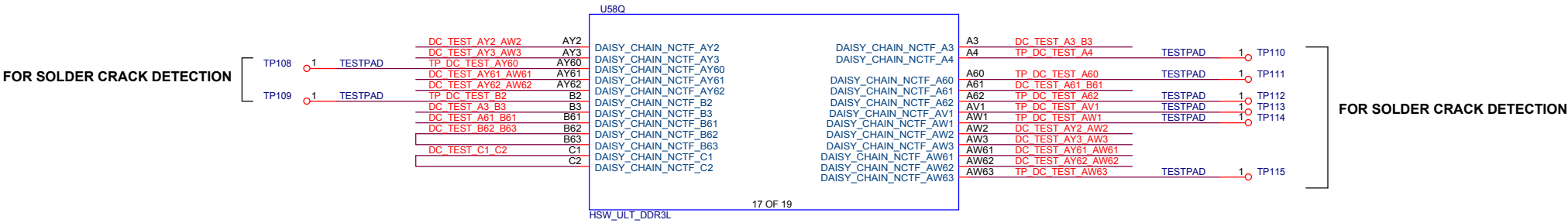
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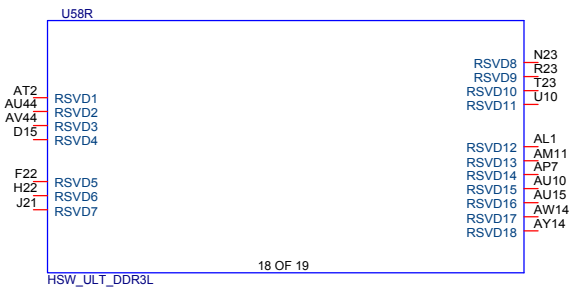
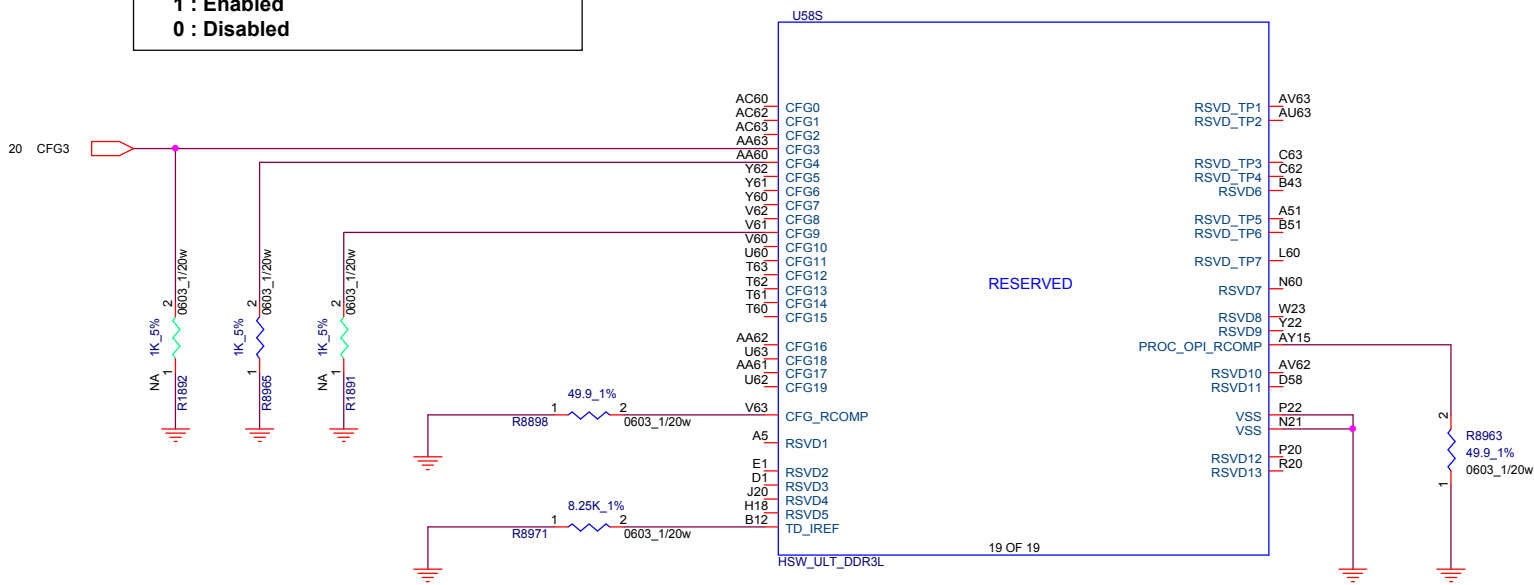
APS/PETS Interface

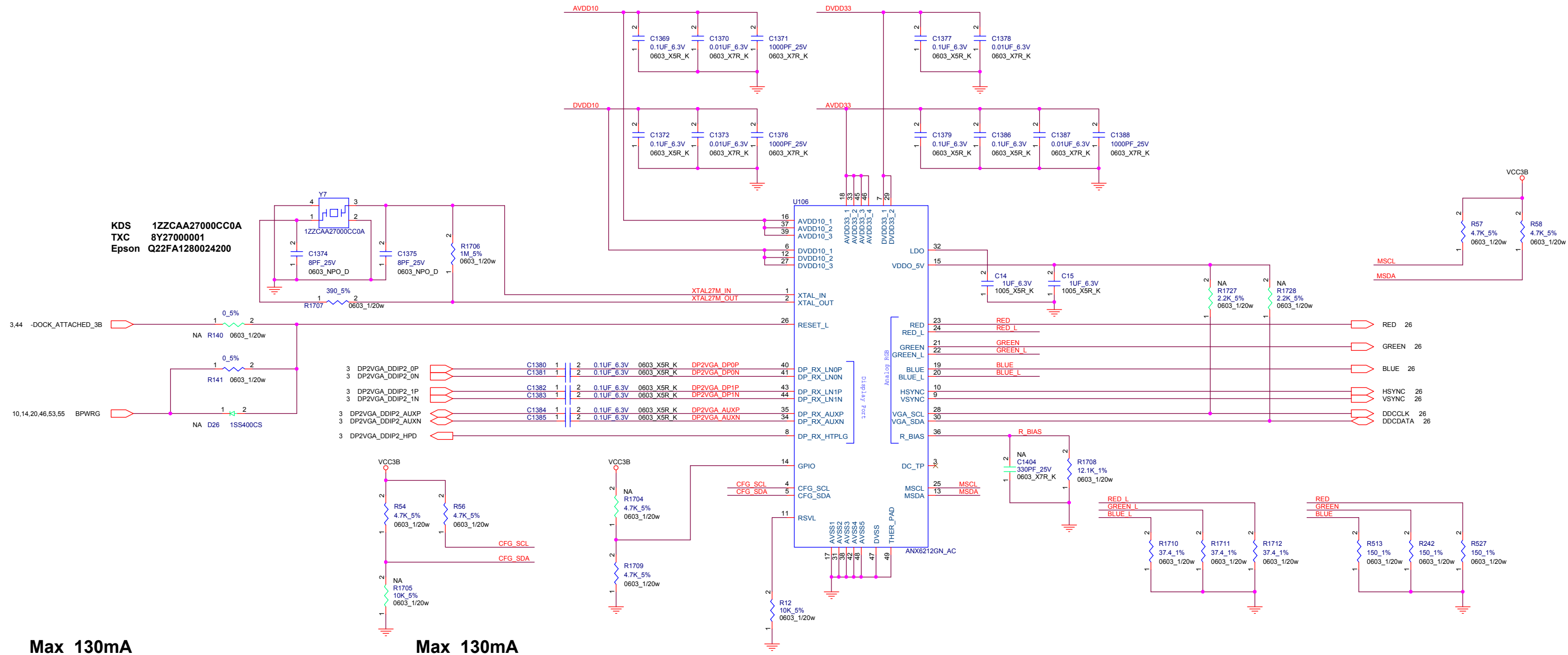




TABLE

CFG4 : Display Port Presence 1 : Disabled 0 : Enabled
CFG9 : SVID Bus Communication 1 : Enabled 0 : Disabled





2.2uF, 0.1uF, 1000pF : Near filter

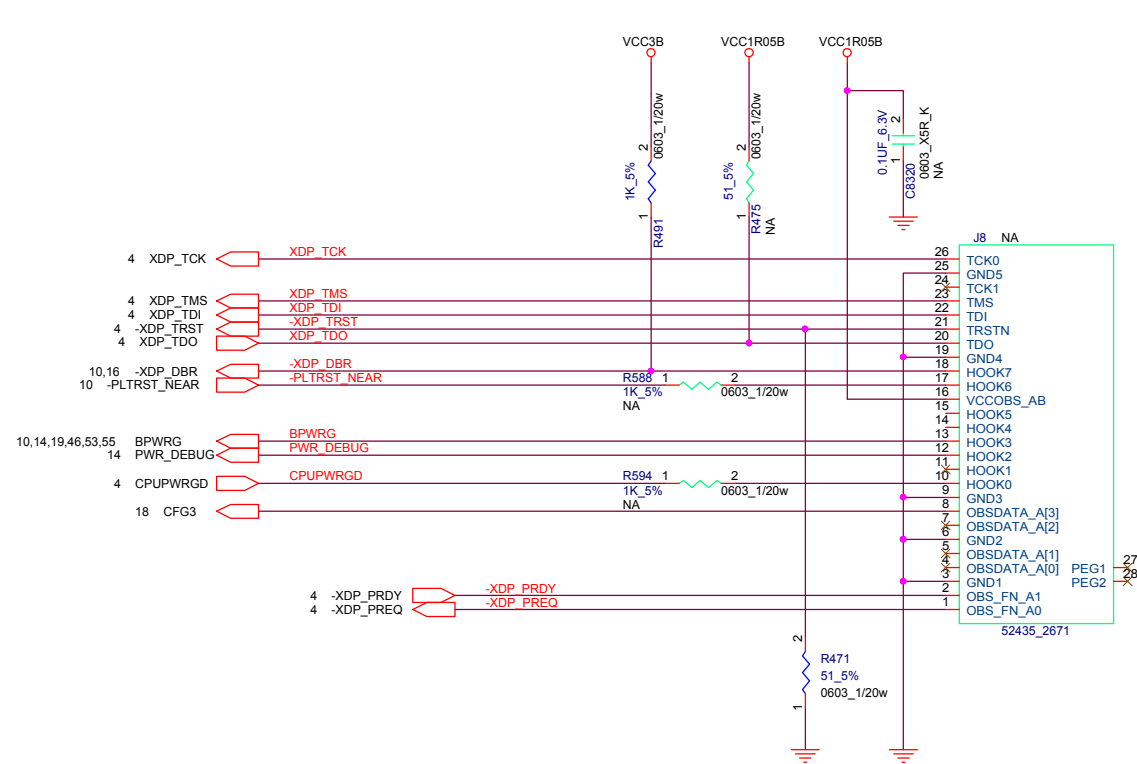


TABLE NOTE: J8 "ASM" FOR PDV ONLY.

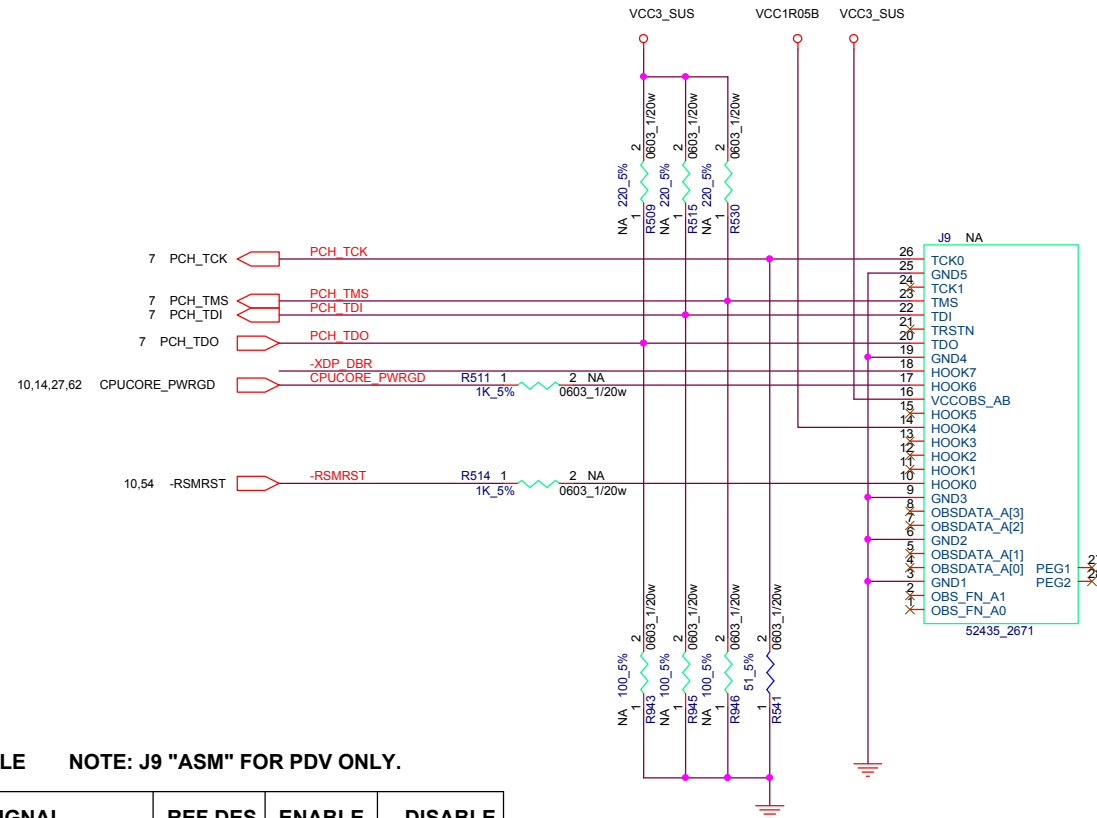
SIGNAL	REF DES	ENABLE	DISABLE
TDO	R475	ASM	NO ASM
TRST#	R471	ASM	ASM
DBRST#	R491	ASM	ASM
RESET#	R588	ASM	NO ASM
PWRGD	R594	ASM	NO ASM
	C8320	ASM	NO ASM
	J8	ASM	NO ASM

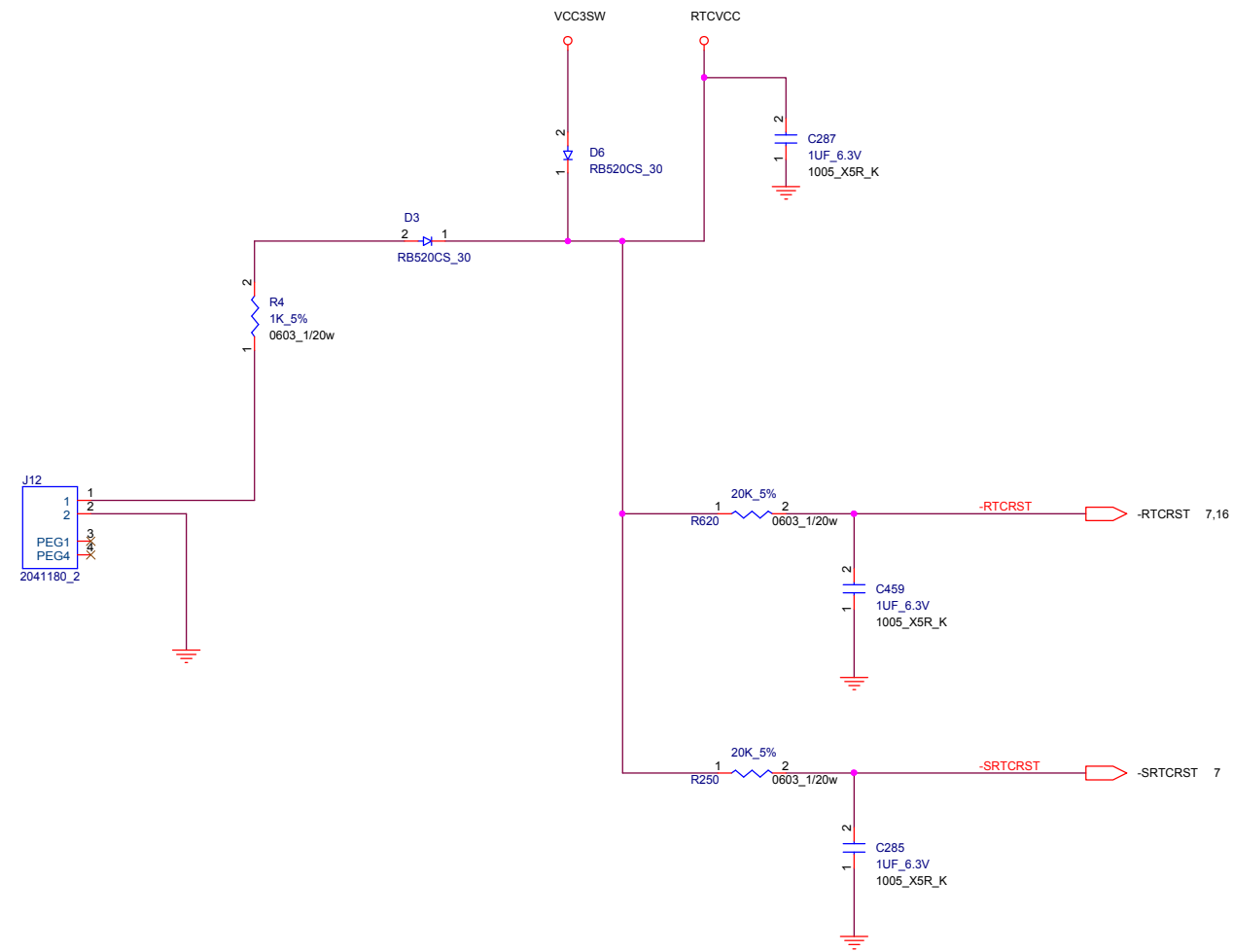
↑
LOGIC

TABLE NOTE: J9 "ASM" FOR PDV ONLY.

SIGNAL	REF DES	ENABLE	DISABLE
TDO	R509 R943	220 100	NO ASM NO ASM
TMS	R530 R946	220 100	NO ASM NO ASM
TDI	R515 R945	220 100	NO ASM NO ASM
TCK	R541	51	51
CPUCORE_PWRGD	R511	ASM	NO ASM
-RSMRST	R514	ASM	NO ASM
	J9	ASM	NO ASM

↑
LOGIC





TABLE

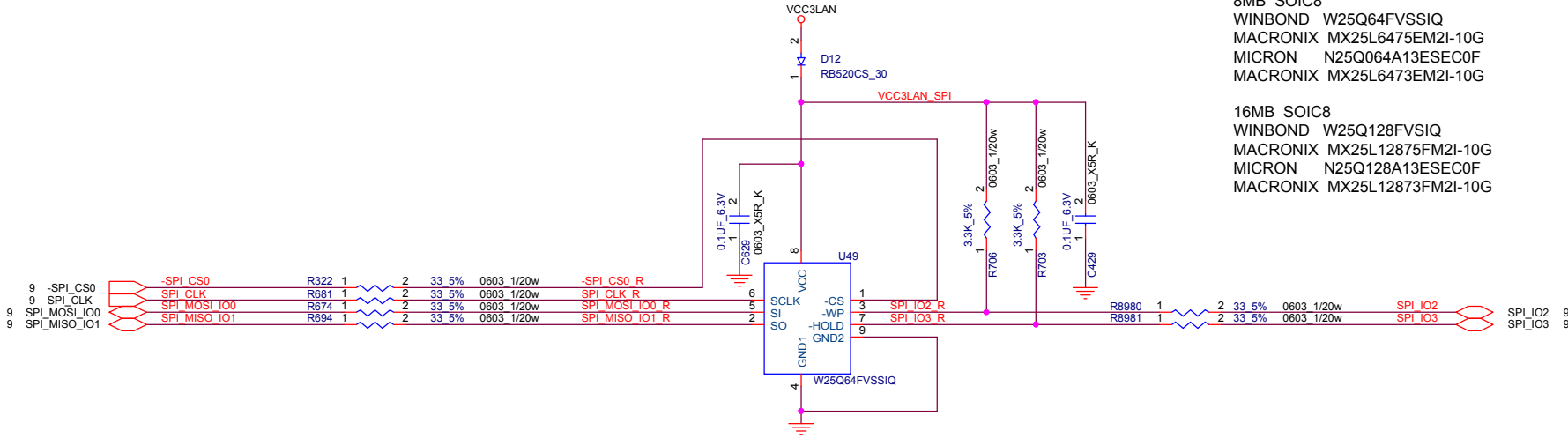
SPI	Wolverine-1 (Corporate)	Rogue-2 with Dock (Corporate)	Rogue-2 w/o Dock (Consumer)
U49	16MB	16MB	8MB



LOGIC

8MB SOIC8
WINBOND W25Q64FVSSIQ
MACRONIX MX25L6475EM2I-10G
MICRON N25Q064A13ESEC0F
MACRONIX MX25L6473EM2I-10G

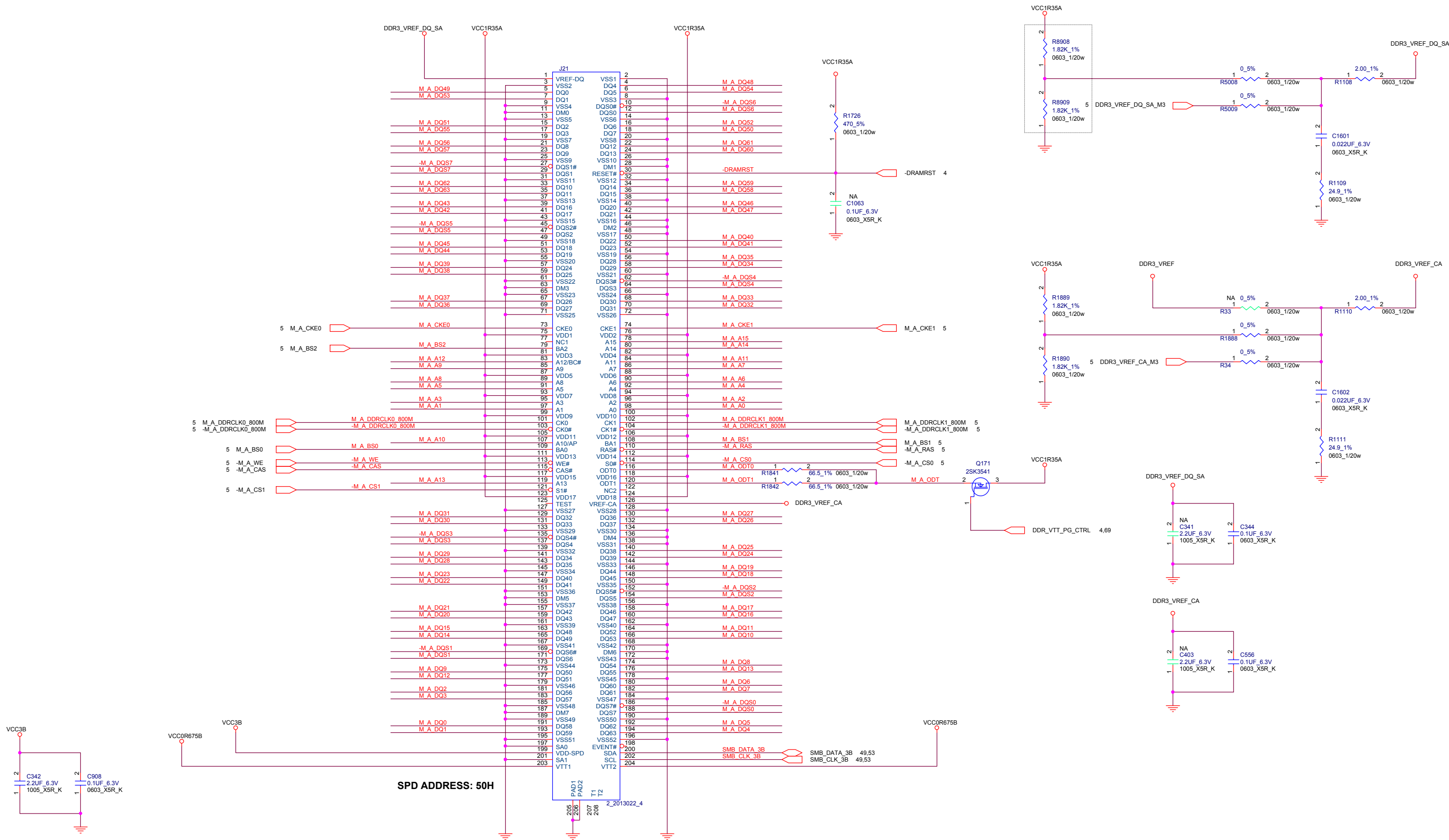
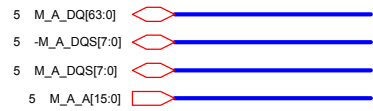
16MB SOIC8
WINBOND W25Q128FVSIQ
MACRONIX MX25L12875FM2I-10G
MICRON N25Q128A13ESEC0F
MACRONIX MX25L12873FM2I-10G

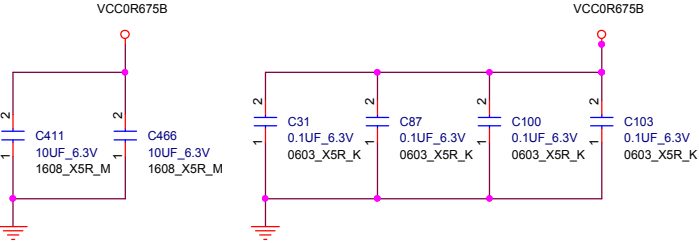
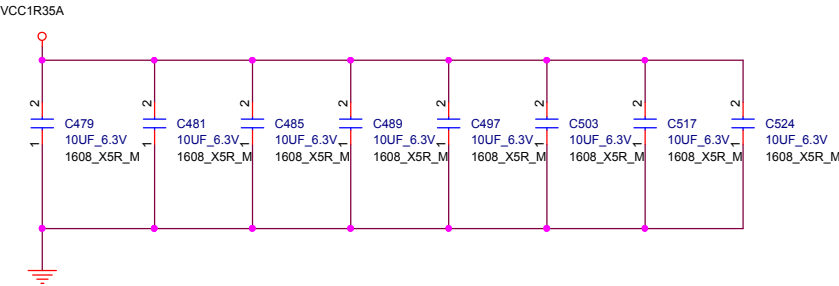
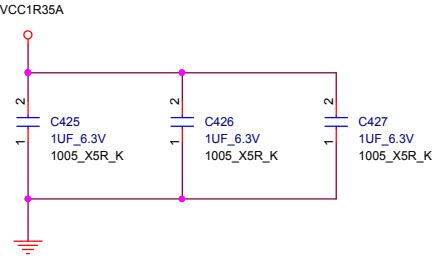
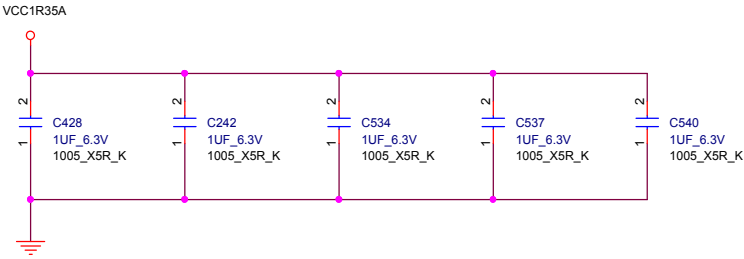


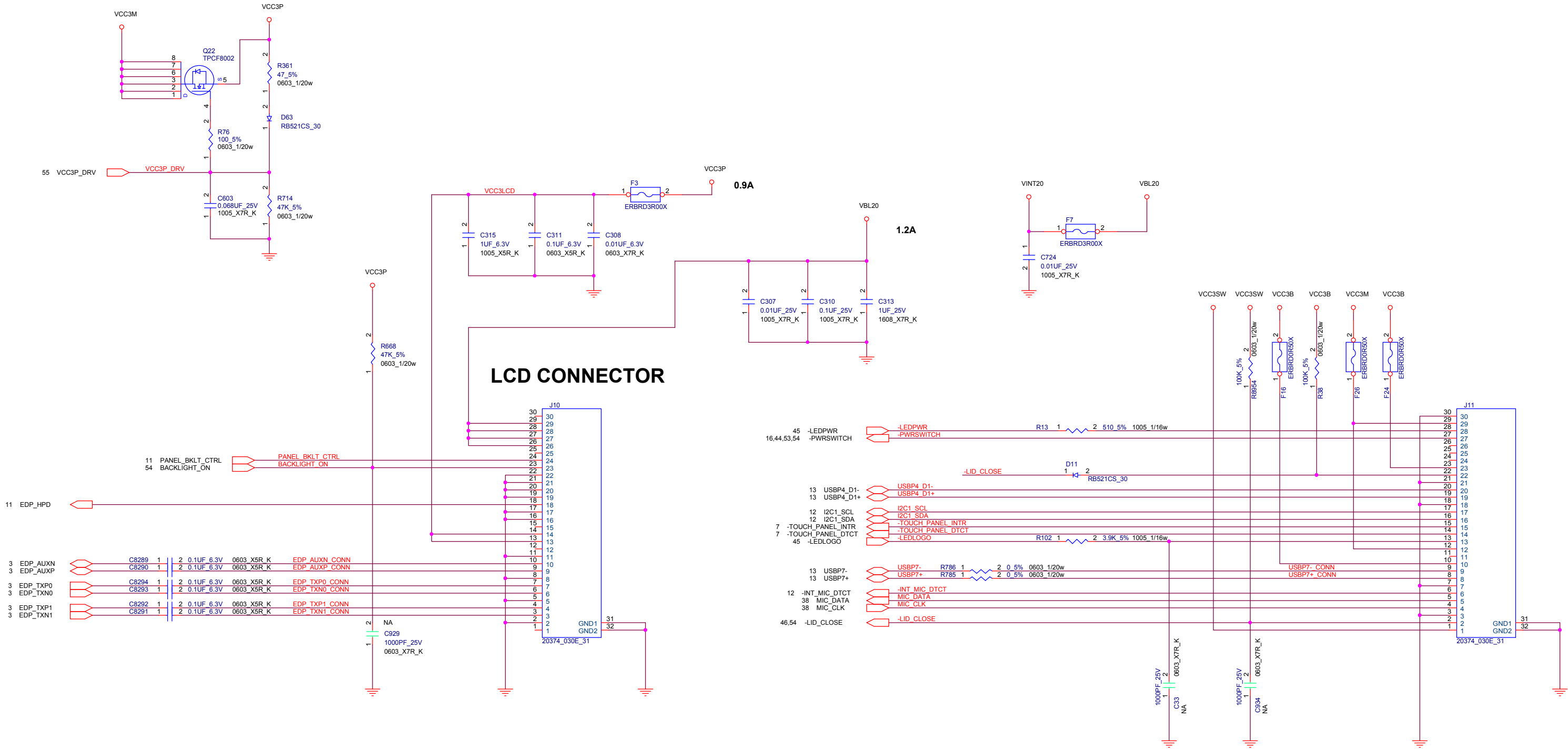
TABLE

SF100 PIN HEADER INTERFACE (TOP VIEW)					
1	VCC	D12.1	GND	GND	2
3	CS#	R322.2	R681.2	CLK	4
5	MISO	R694.2	R674.2	MOSI	6
7	(KEY)	N/A	N/A	(RESET)	8



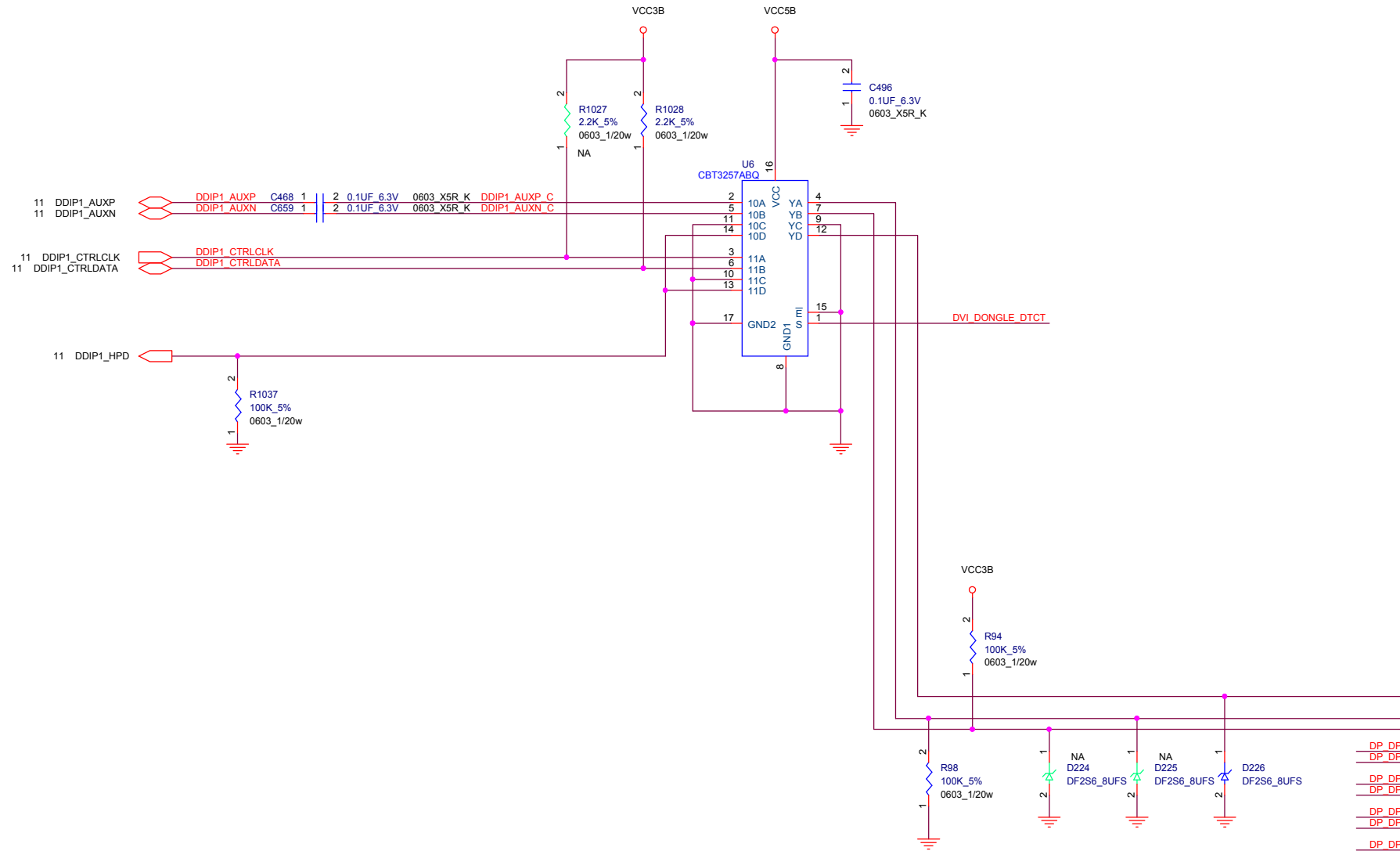




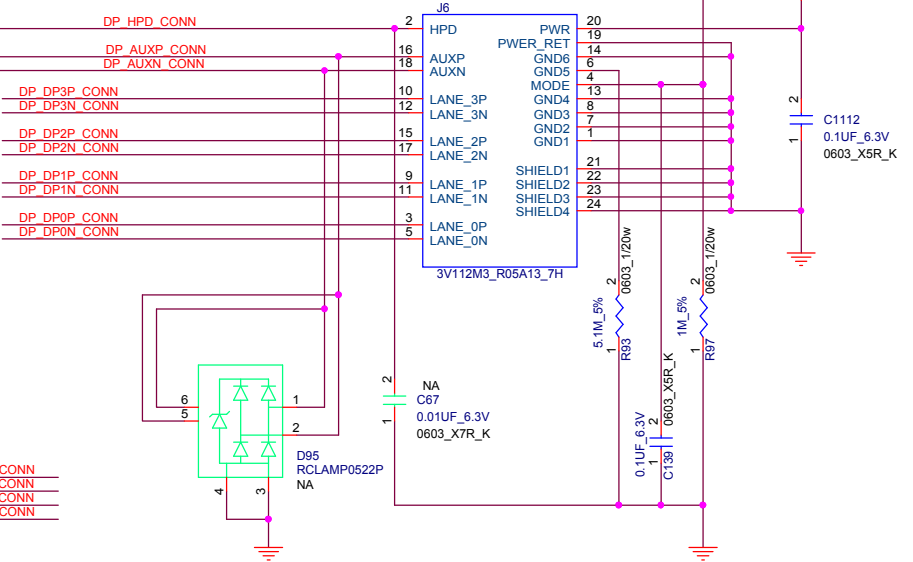


FOR SYSTEM DP NEAR DP CONN

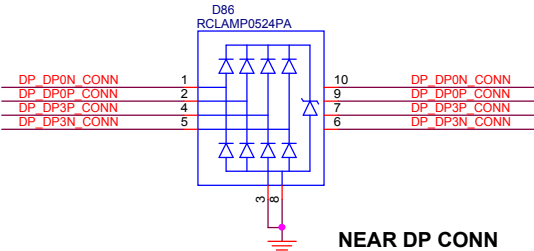
3	DDIP1_3N	C339	1	2	0.1UF_6.3V	0603_X5R_K	DP_DP3N_CONN
3	DDIP1_3P	C323	1	2	0.1UF_6.3V	0603_X5R_K	DP_DP3P_CONN
3	DDIP1_2N	C312	1	2	0.1UF_6.3V	0603_X5R_K	DP_DP2N_CONN
3	DDIP1_2P	C317	1	2	0.1UF_6.3V	0603_X5R_K	DP_DP2P_CONN
3	DDIP1_1N	C277	1	2	0.1UF_6.3V	0603_X5R_K	DP_DP1N_CONN
3	DDIP1_1P	C276	1	2	0.1UF_6.3V	0603_X5R_K	DP_DP1P_CONN
3	DDIP1_0N	C218	1	2	0.1UF_6.3V	0603_X5R_K	DP_DP0N_CONN
3	DDIP1_0P	C226	1	2	0.1UF_6.3V	0603_X5R_K	DP_DP0P_CONN



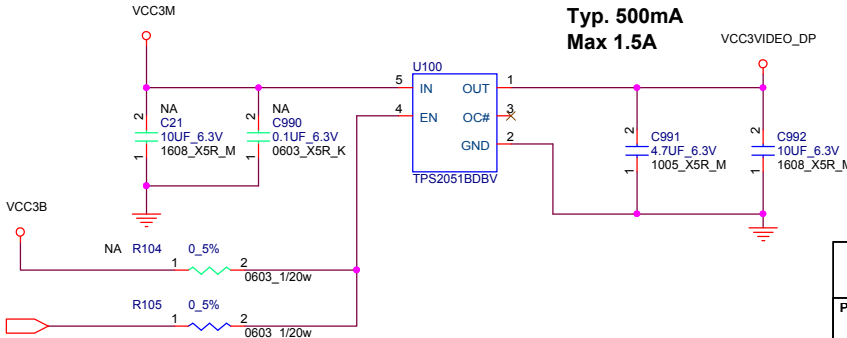
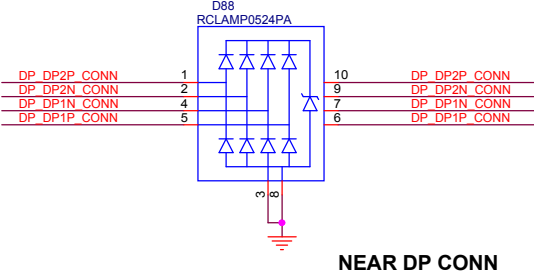
DP CONN

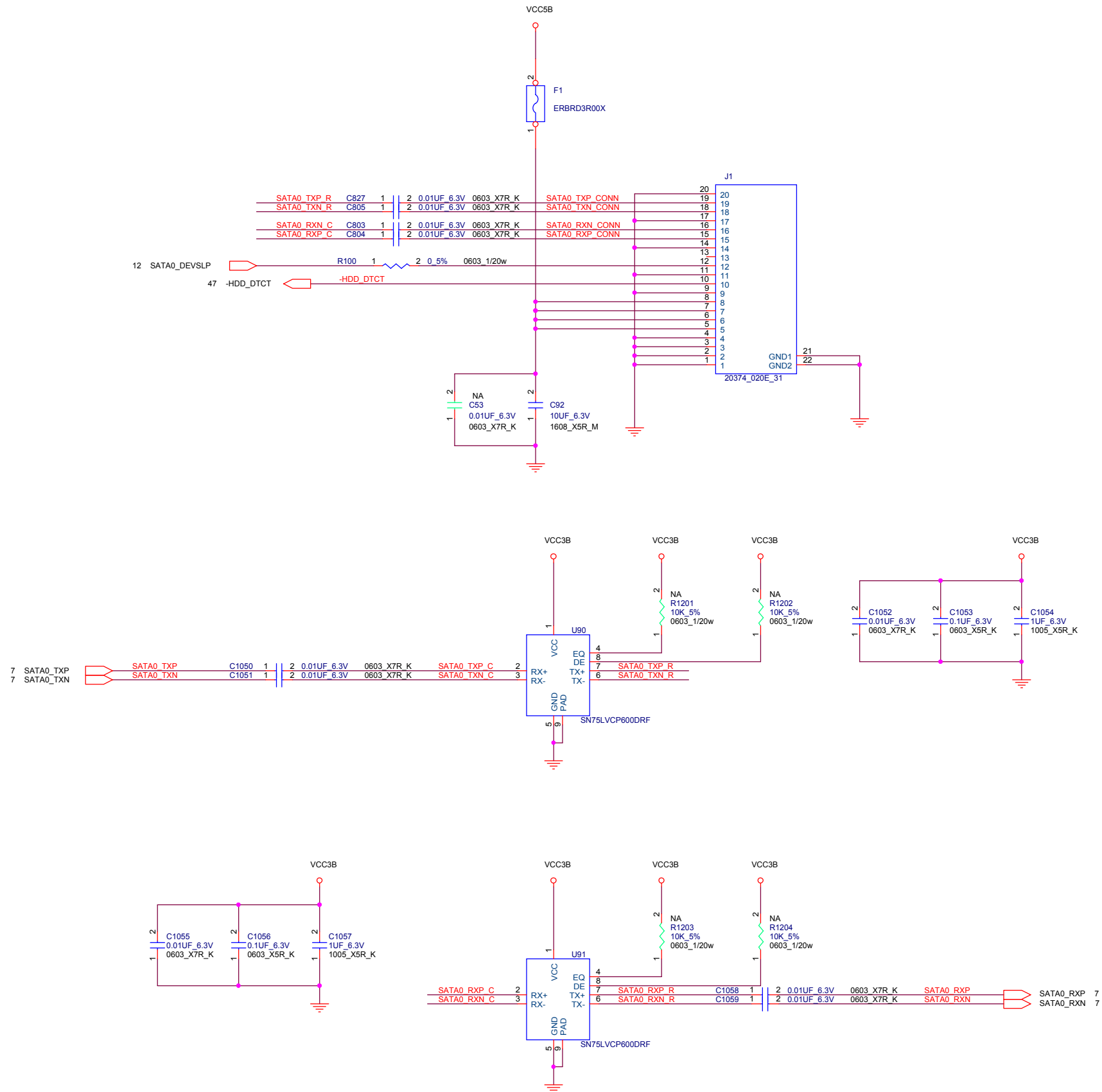


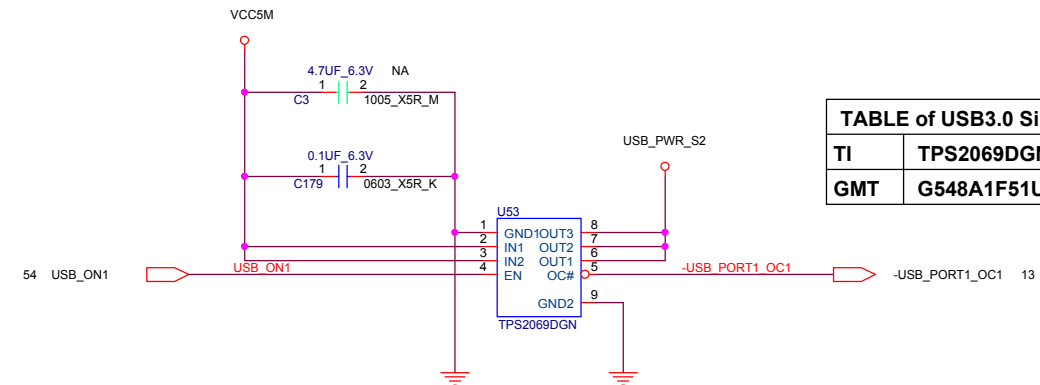
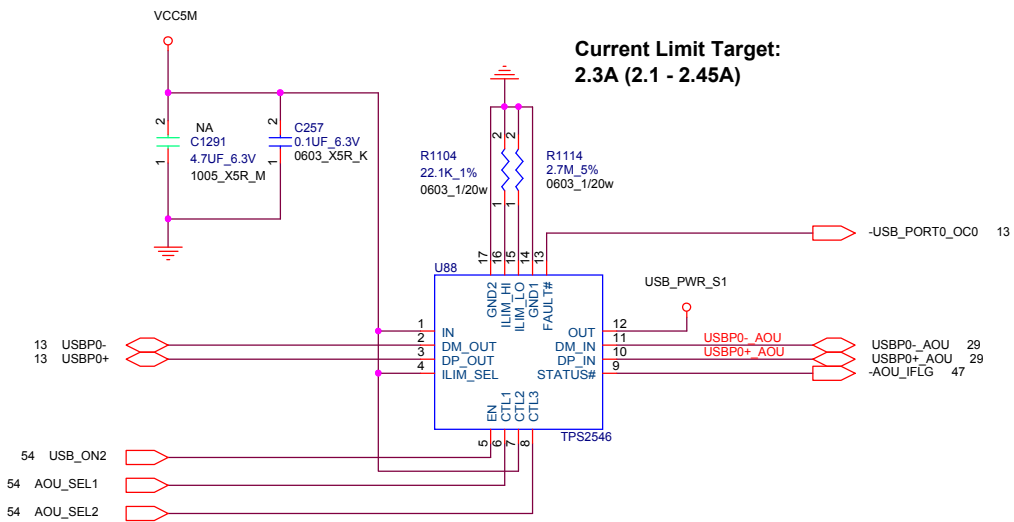
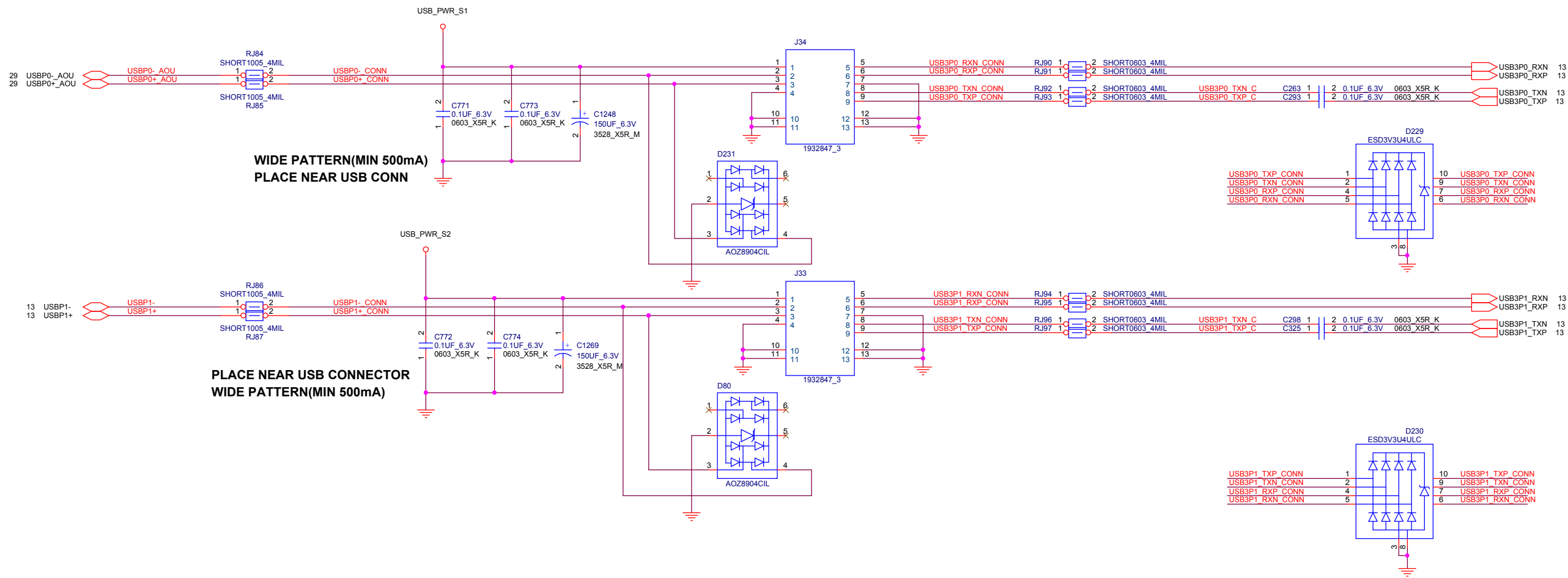
NEAR DP CONN

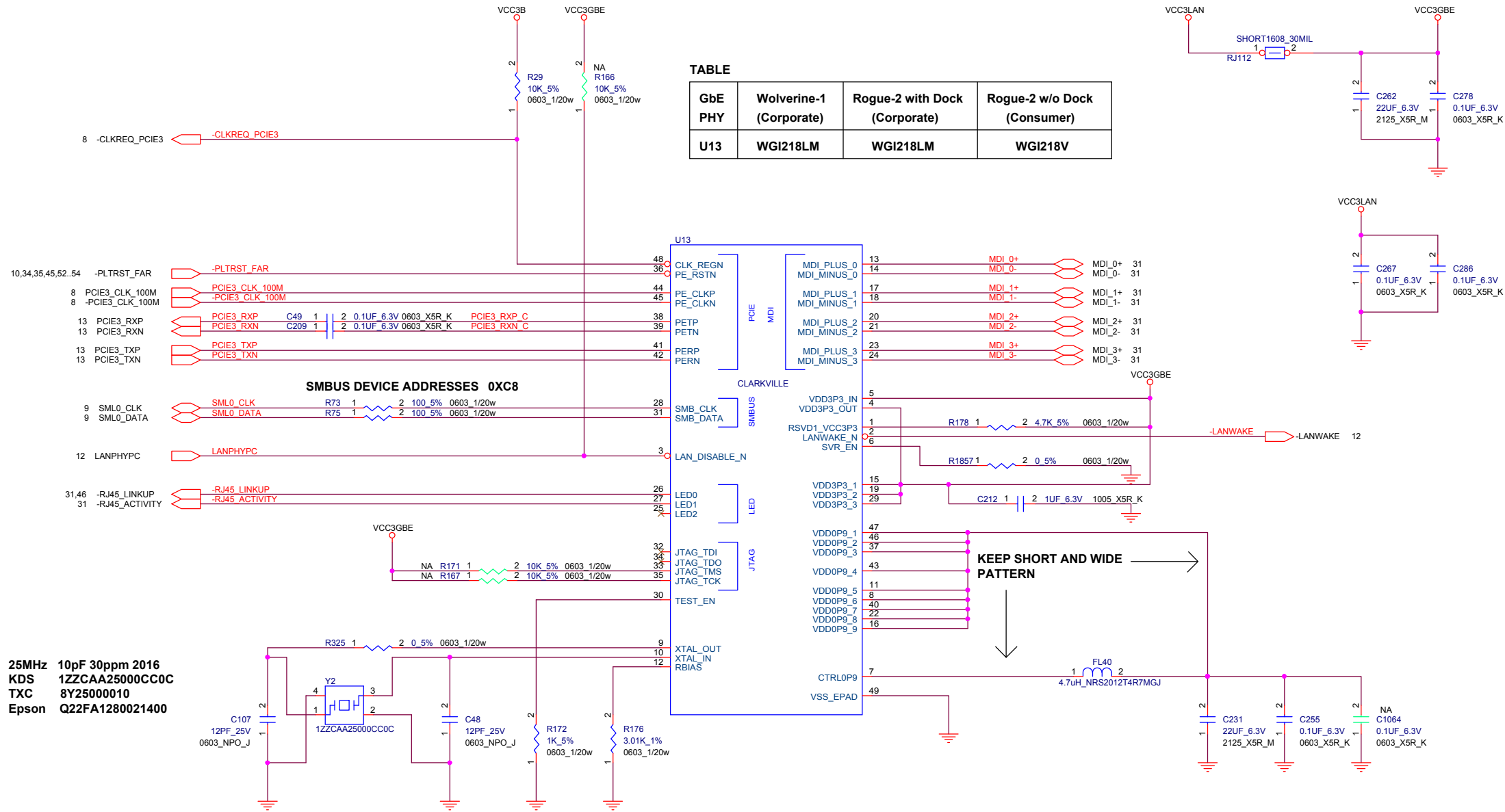


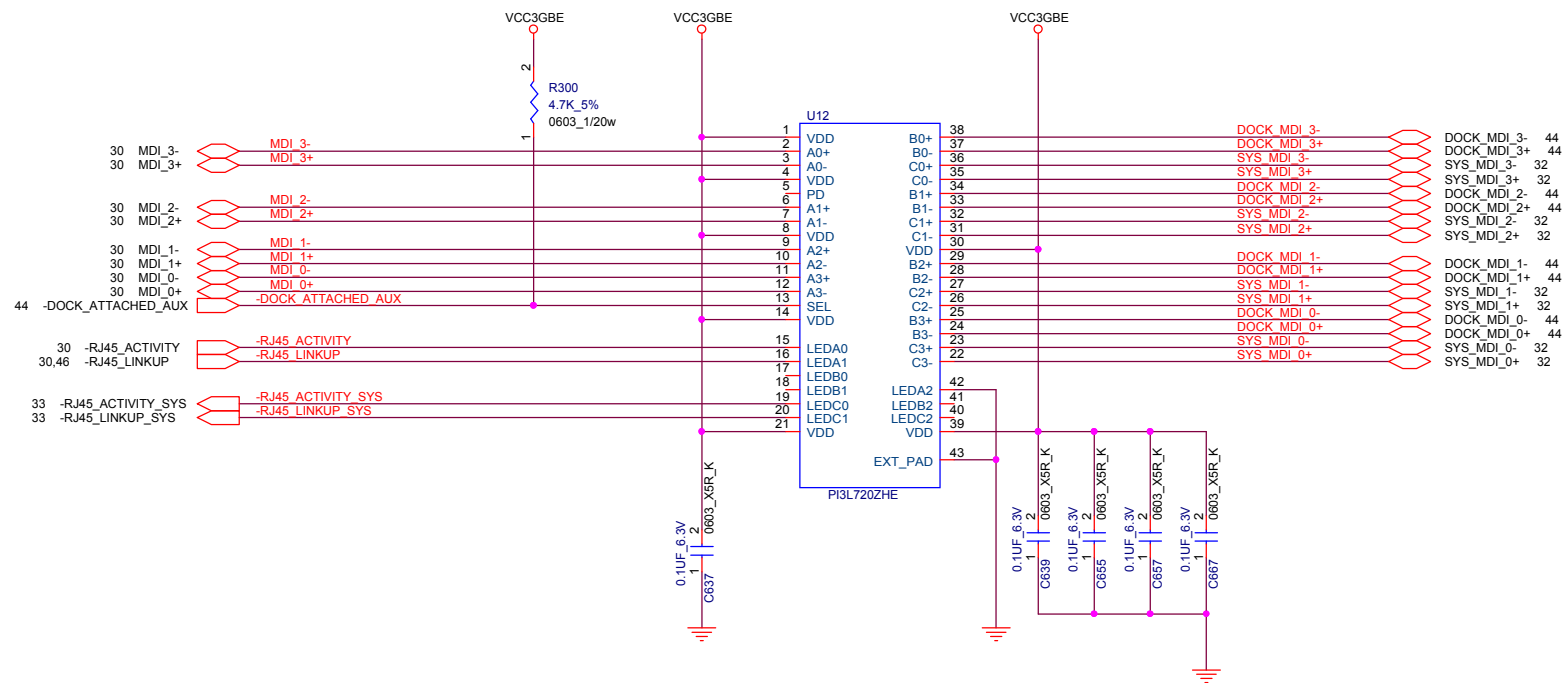
NEAR DP CONN

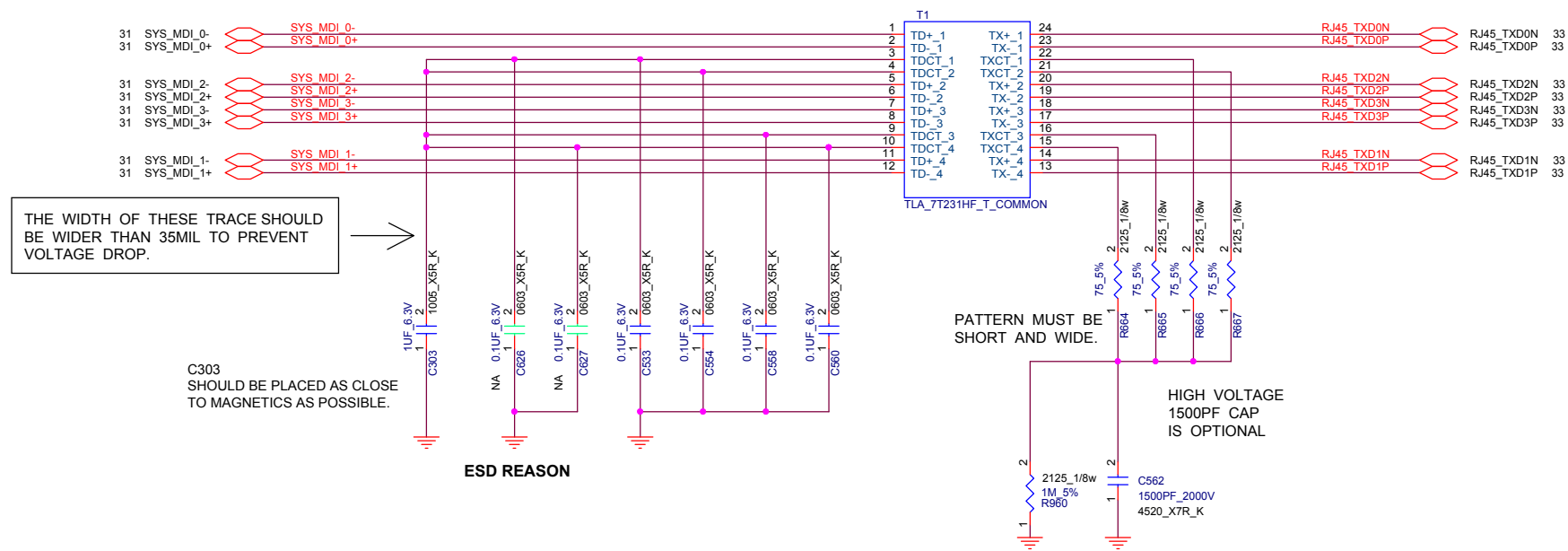


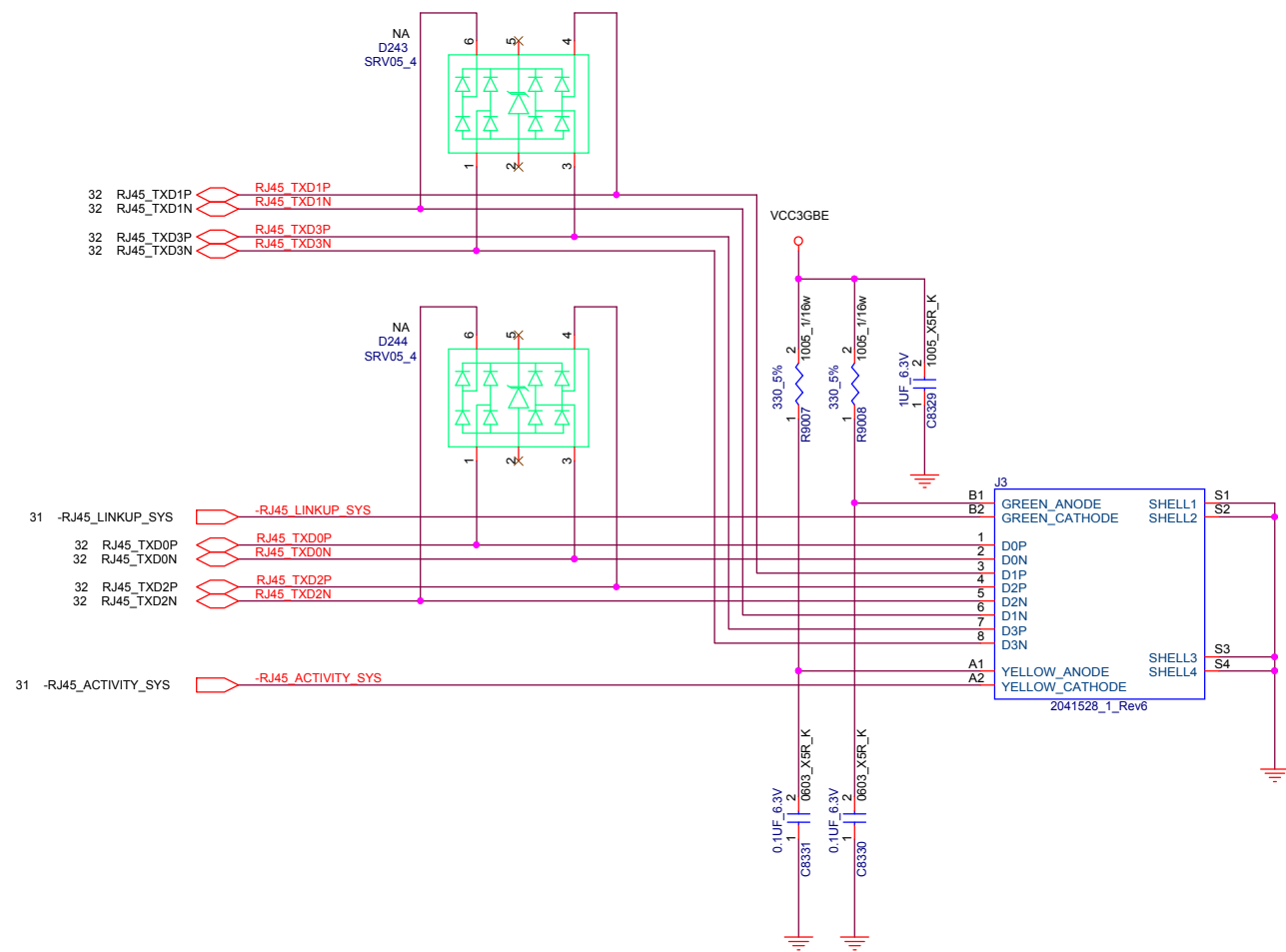




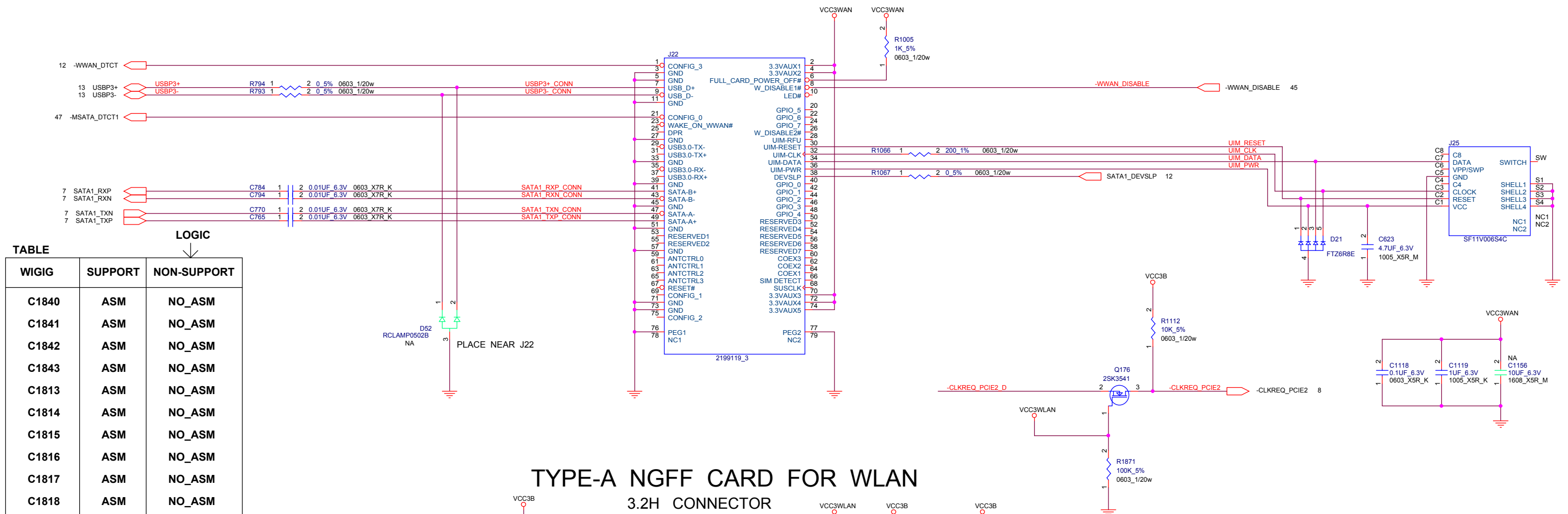




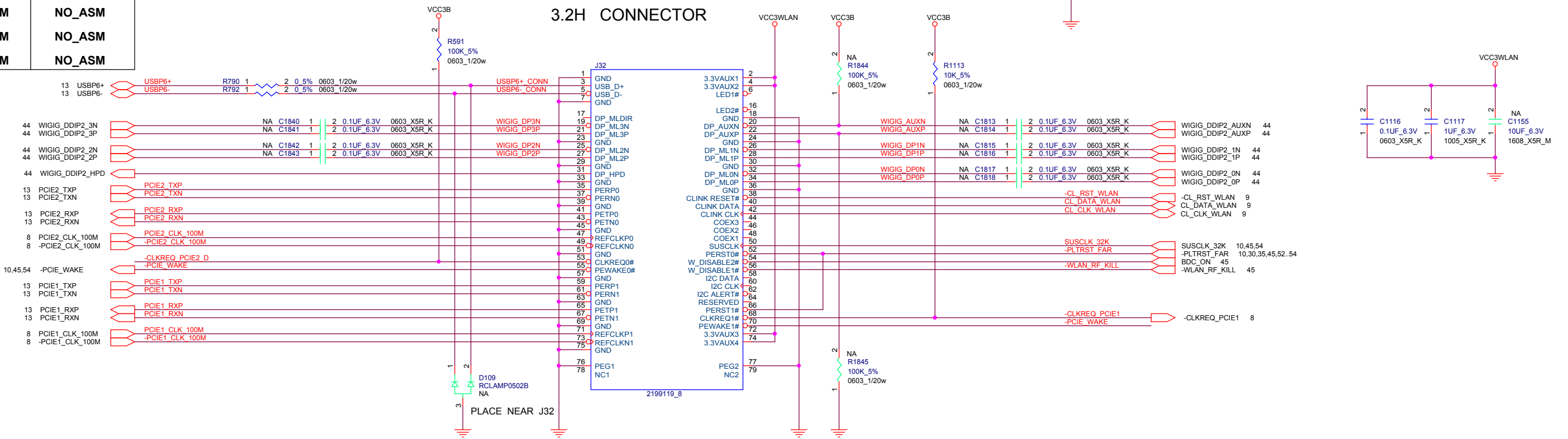




TYPE-B NGFF CARD FOR WWAN/SSD
3.2H CONNECTOR

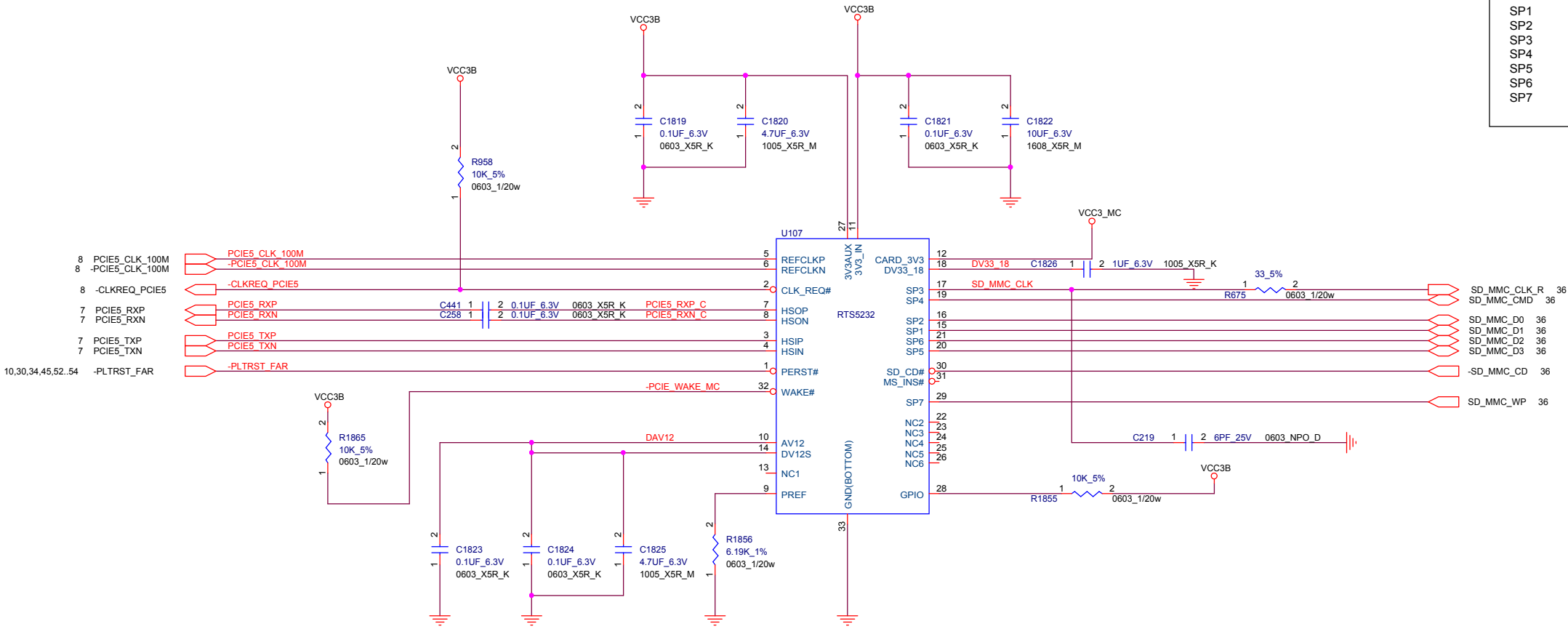


TYPE-A NGFF CARD FOR WLAN
3.2H CONNECTOR



TABLE

Pin name	SD/MMC	MEMORSTICK
SP1	SD_D1	MS_D1
SP2	SD_D0	MS_D0
SP3	SD_CLK	MS_D0
SP4	SD_CMD	MS_D2
SP5	SD_D3	MS_D3
SP6	SD_D2	MS_CLK
SP7		



Project Name :
RG2 SB SV1

Title :
MEDIA CARD CONTROLLER

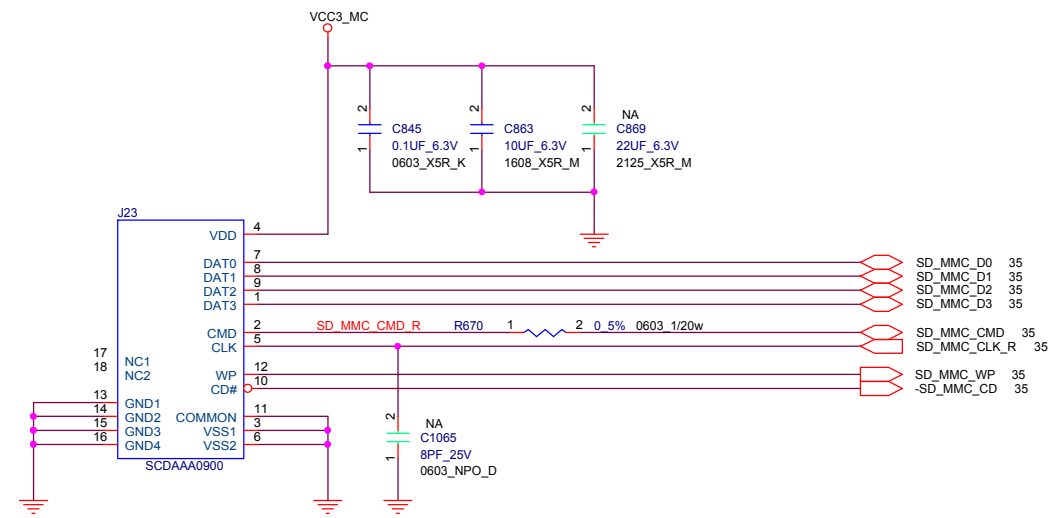
Size :
C

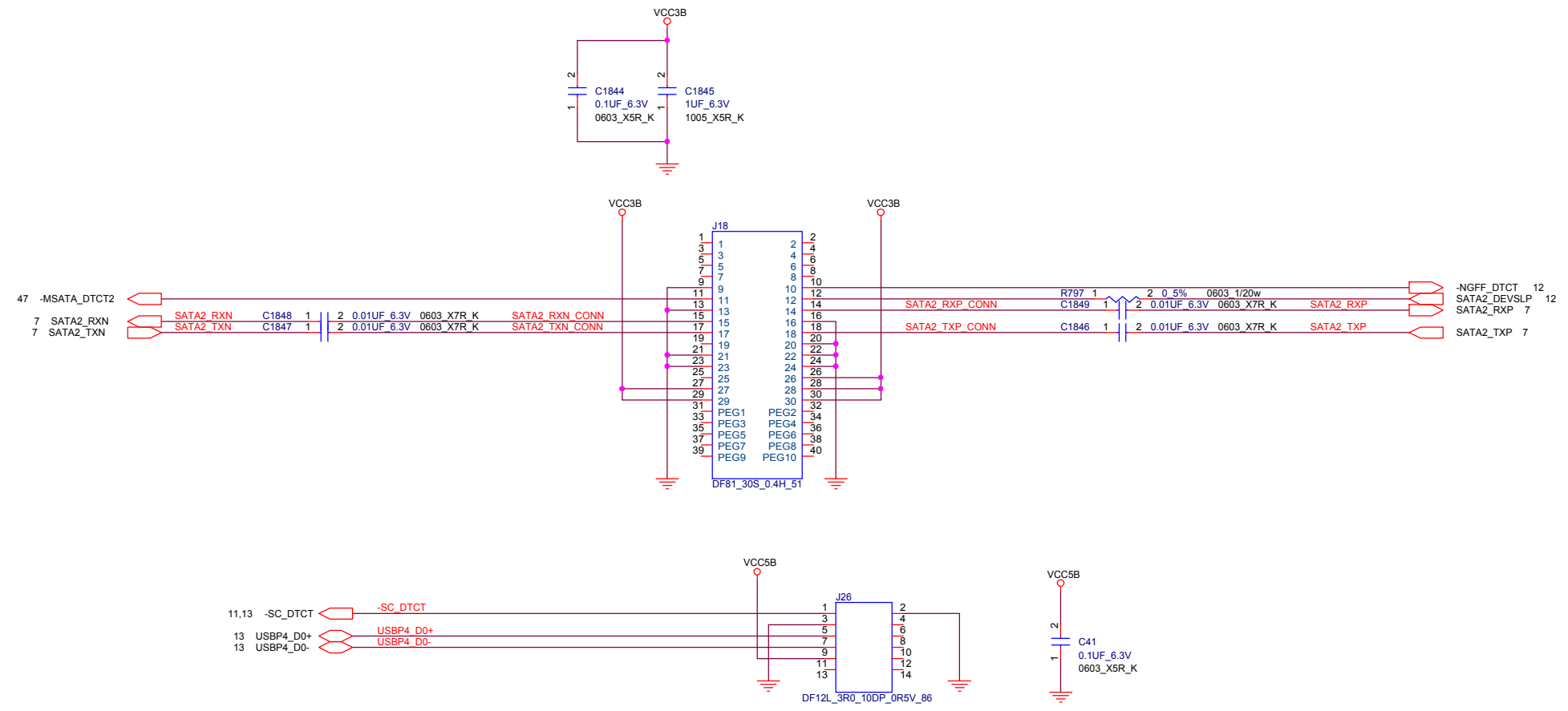
Document Number :

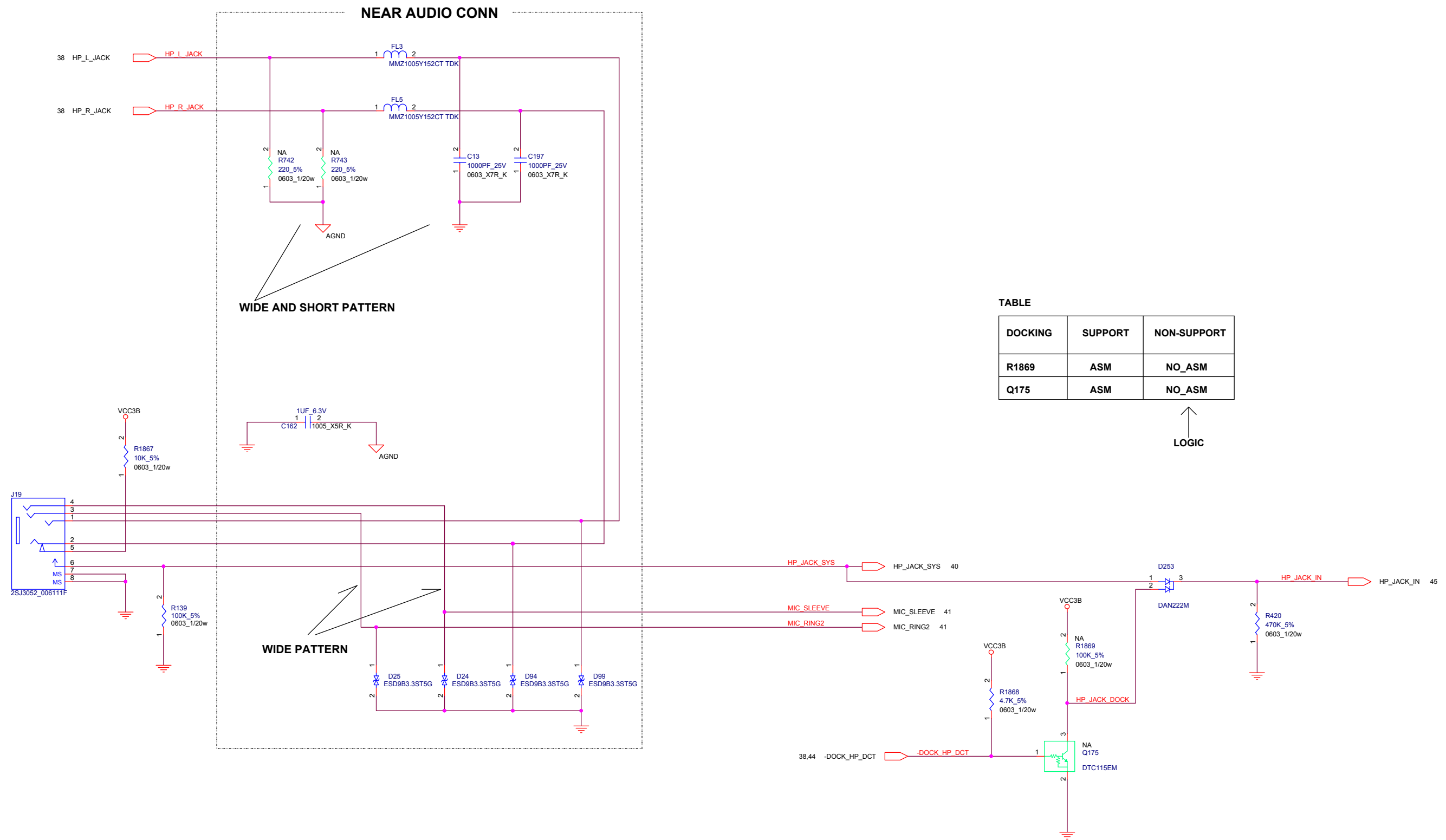
Rev :
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Date: Thursday, April 11, 2013

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TABLE

DOCKING	SUPPORT	NON-SUPPORT
R1869	ASM	NO_ASM
Q175	ASM	NO_ASM

↑
LOGIC



TABLE

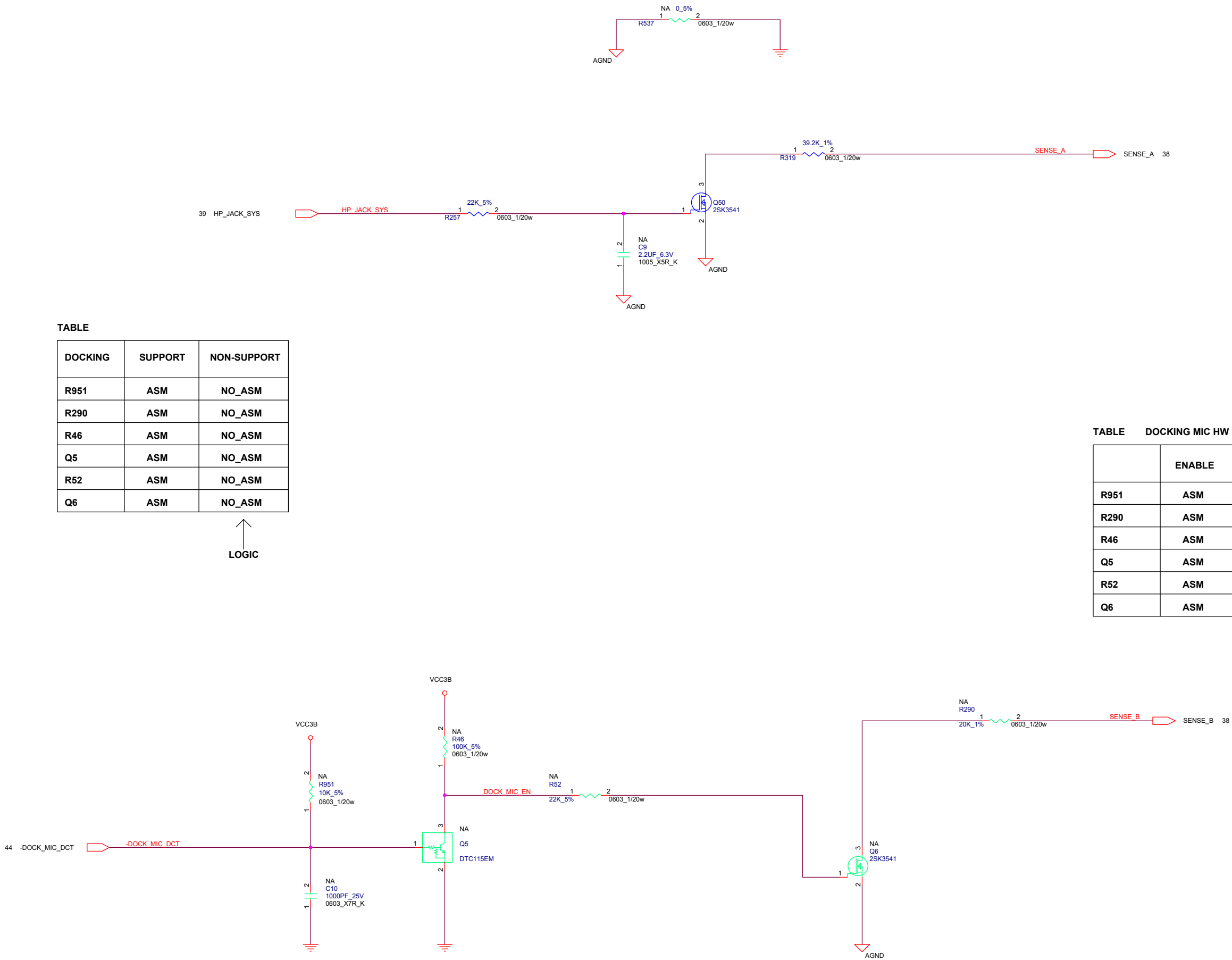
DOCKING	SUPPORT	NON-SUPPORT
R951	ASM	NO_ASM
R290	ASM	NO_ASM
R46	ASM	NO_ASM
Q5	ASM	NO_ASM
R52	ASM	NO_ASM
Q6	ASM	NO_ASM

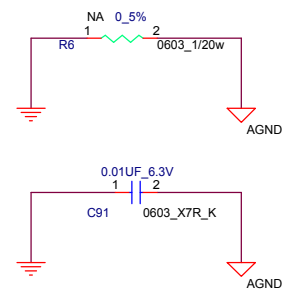
↑
LOGIC

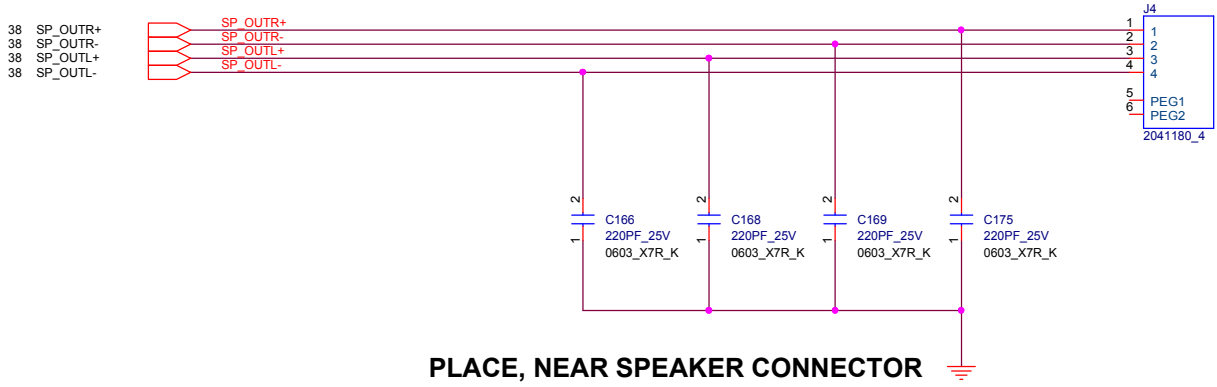
TABLE DOCKING MIC HW ENABLE/DISABLE

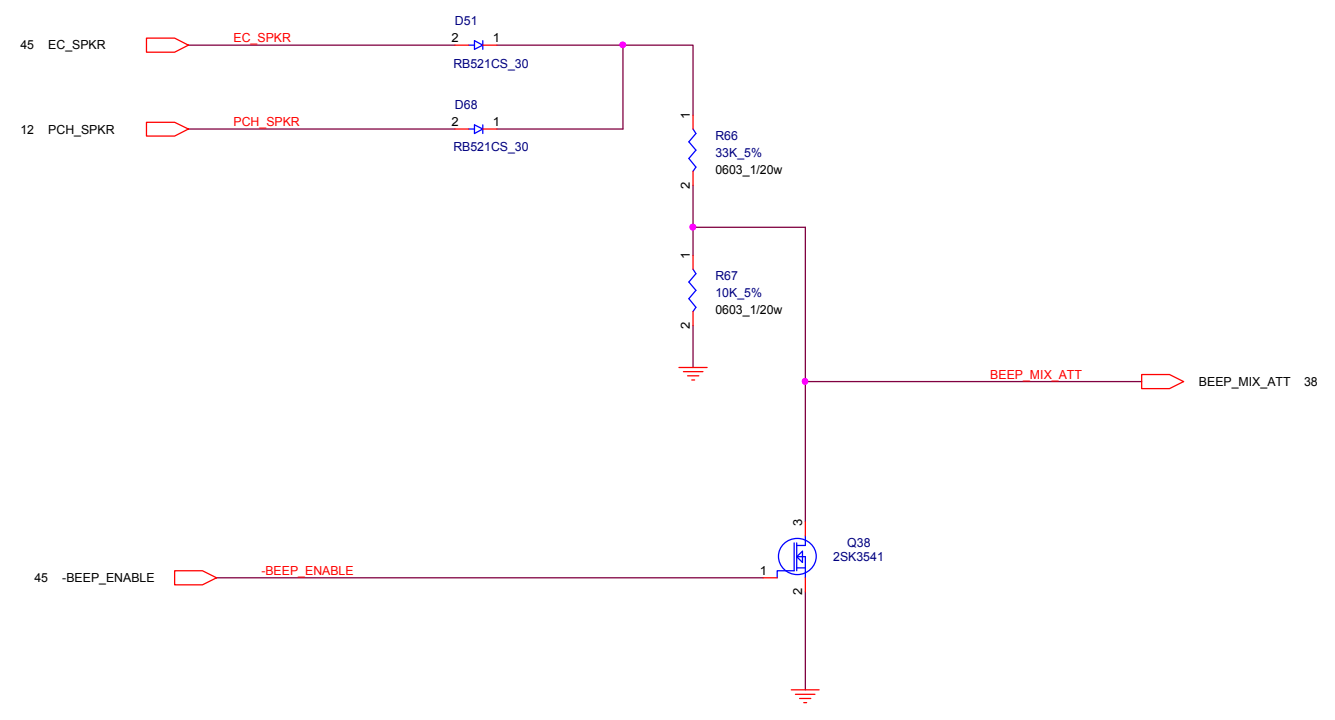
	ENABLE	DISABLE
R951	ASM	NO_ASM
R290	ASM	NO_ASM
R46	ASM	NO_ASM
Q5	ASM	NO_ASM
R52	ASM	NO_ASM
Q6	ASM	NO_ASM

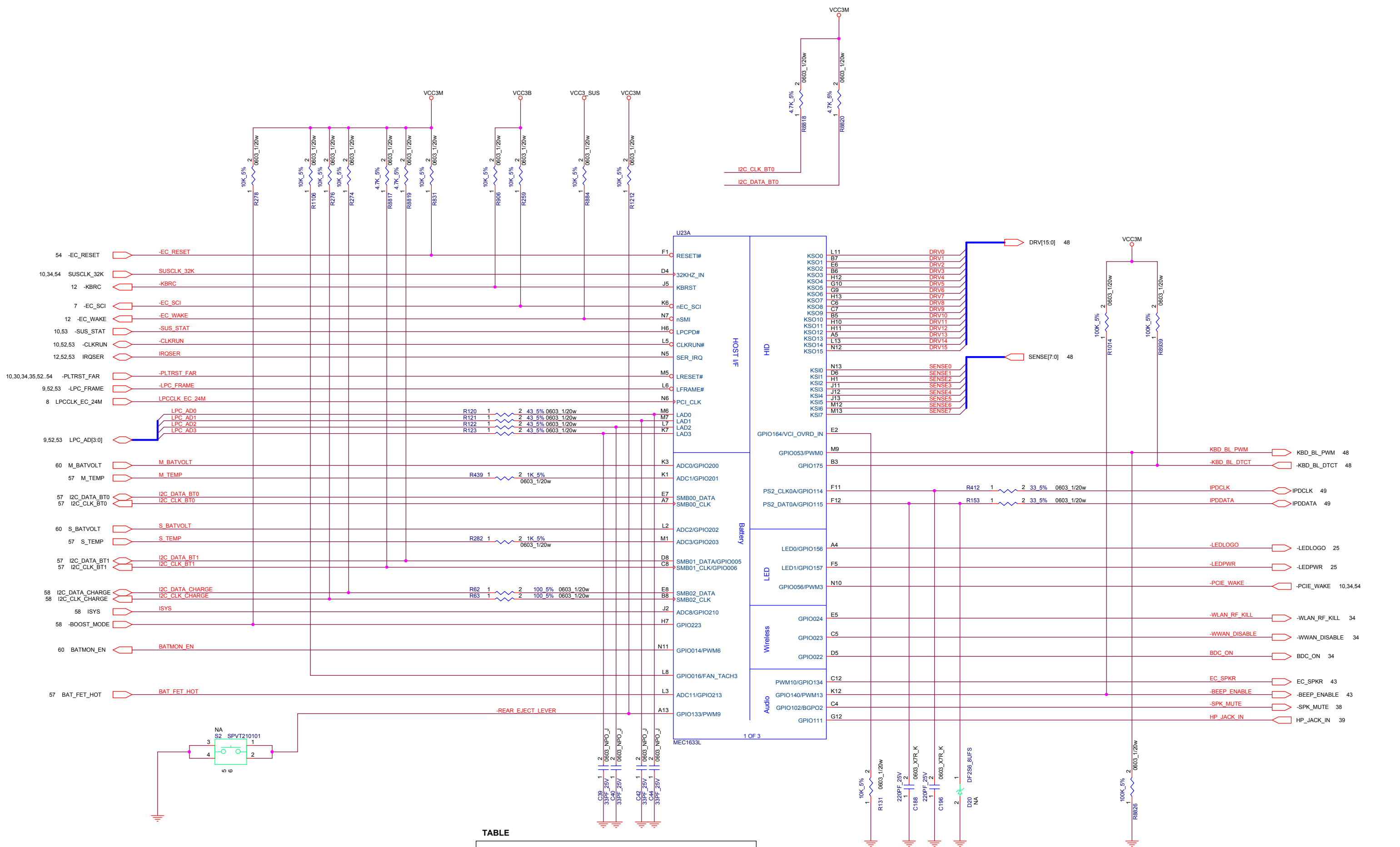
↑
LOGIC





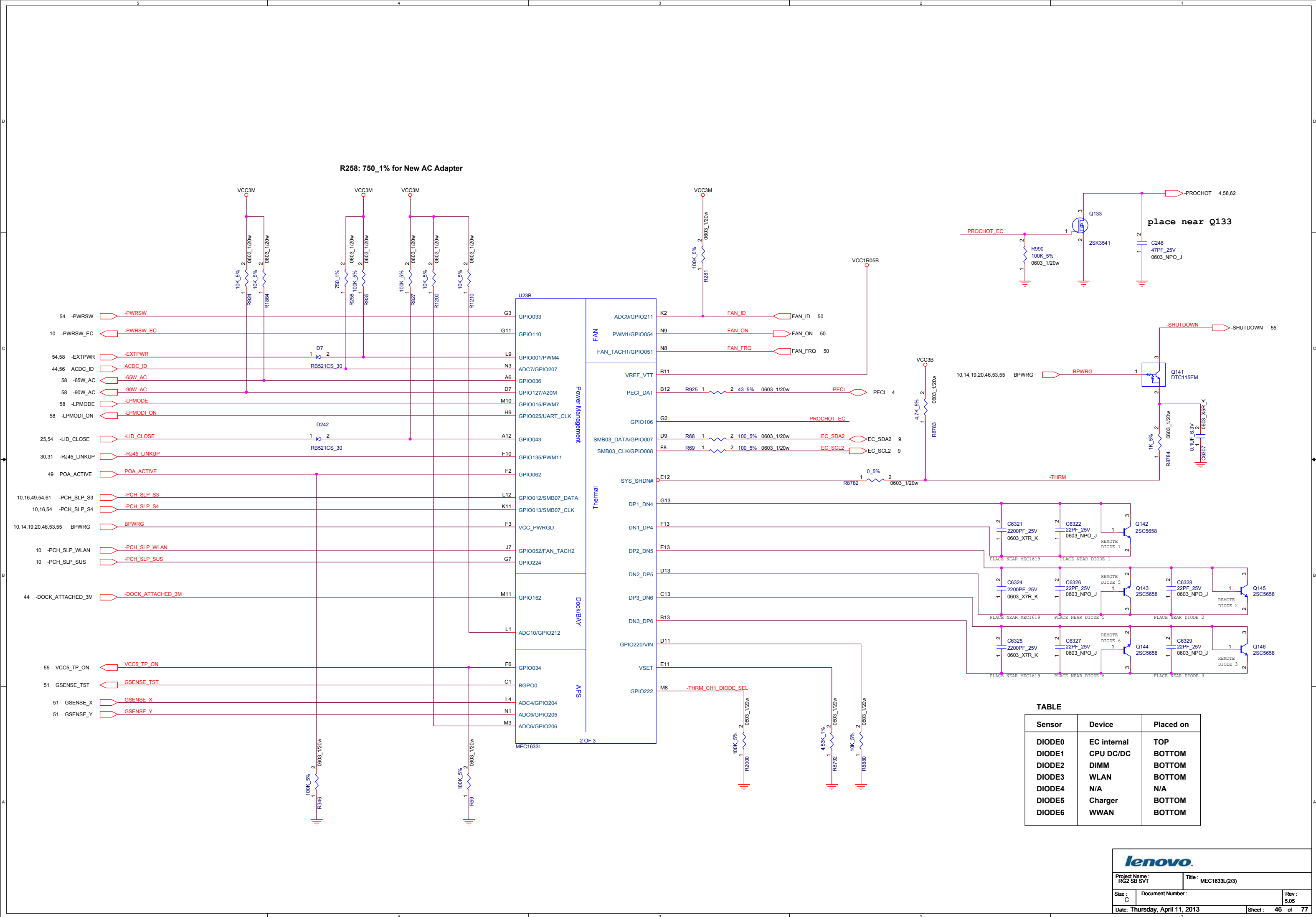


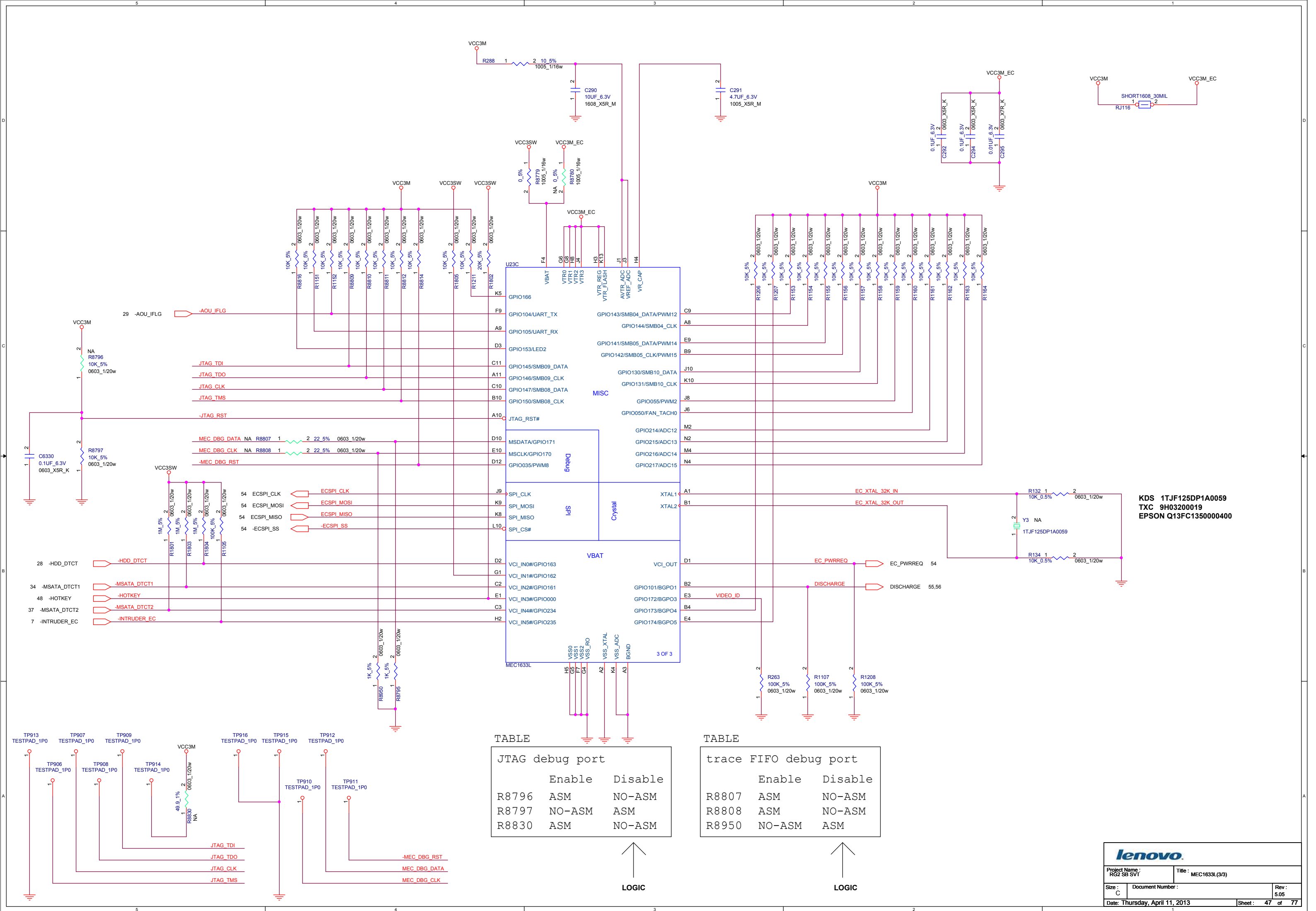


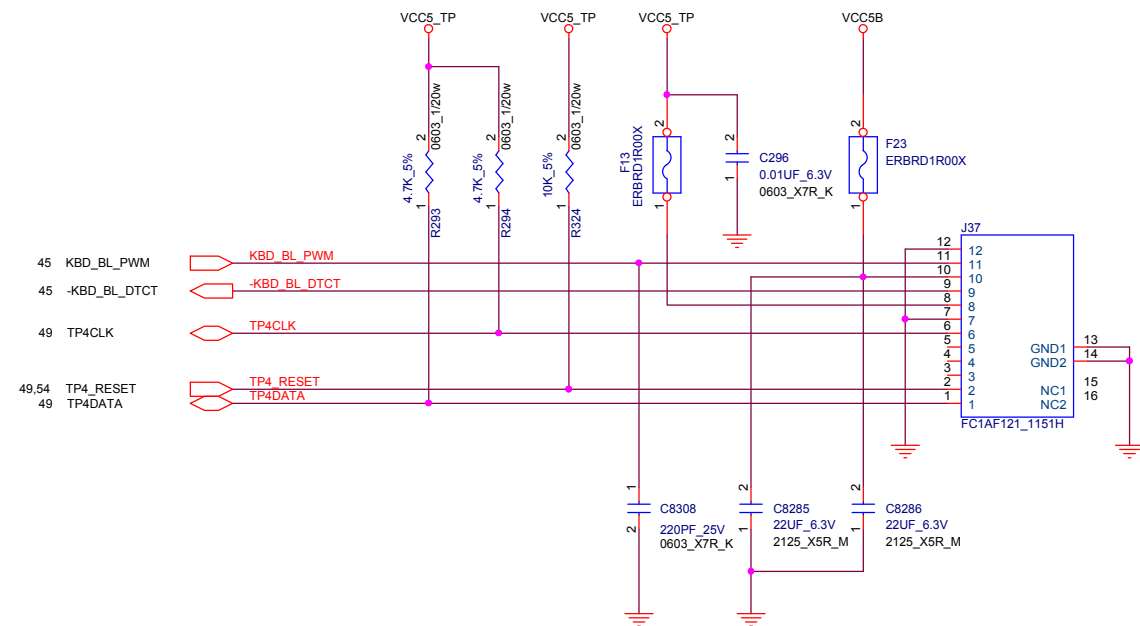
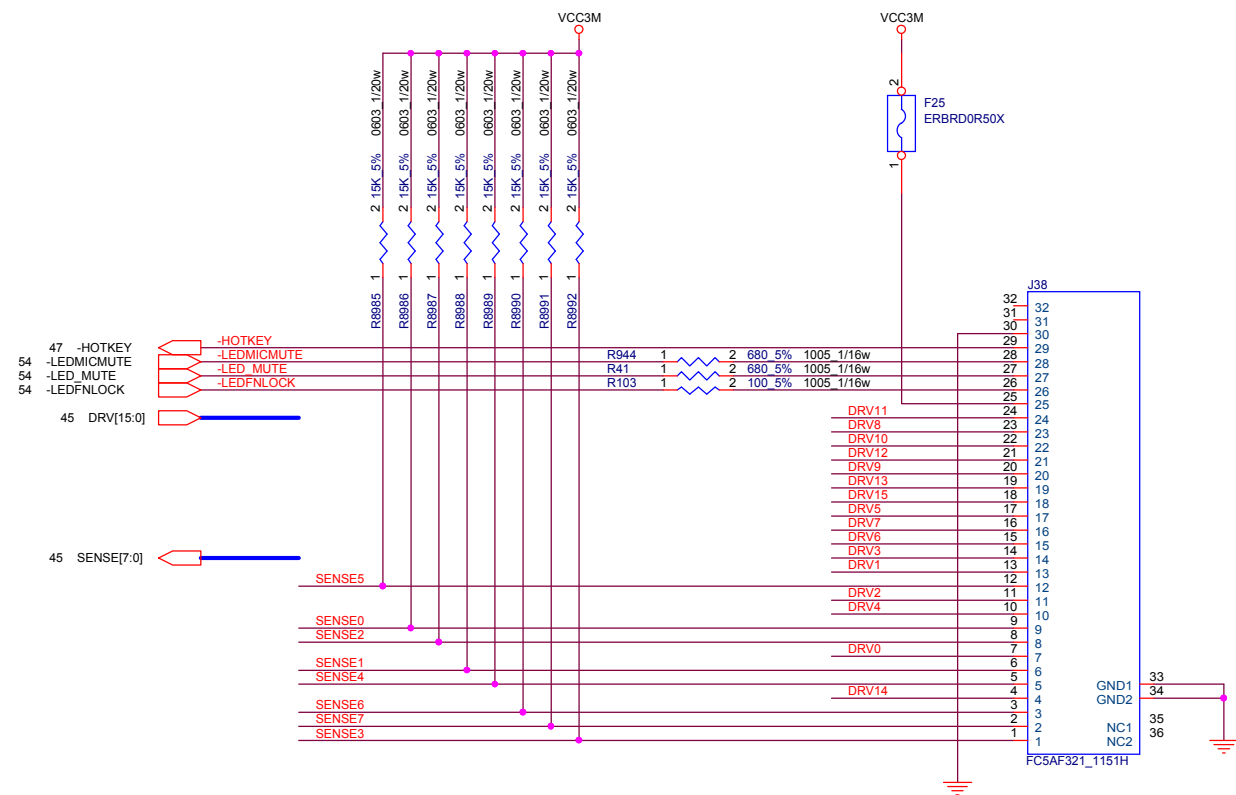


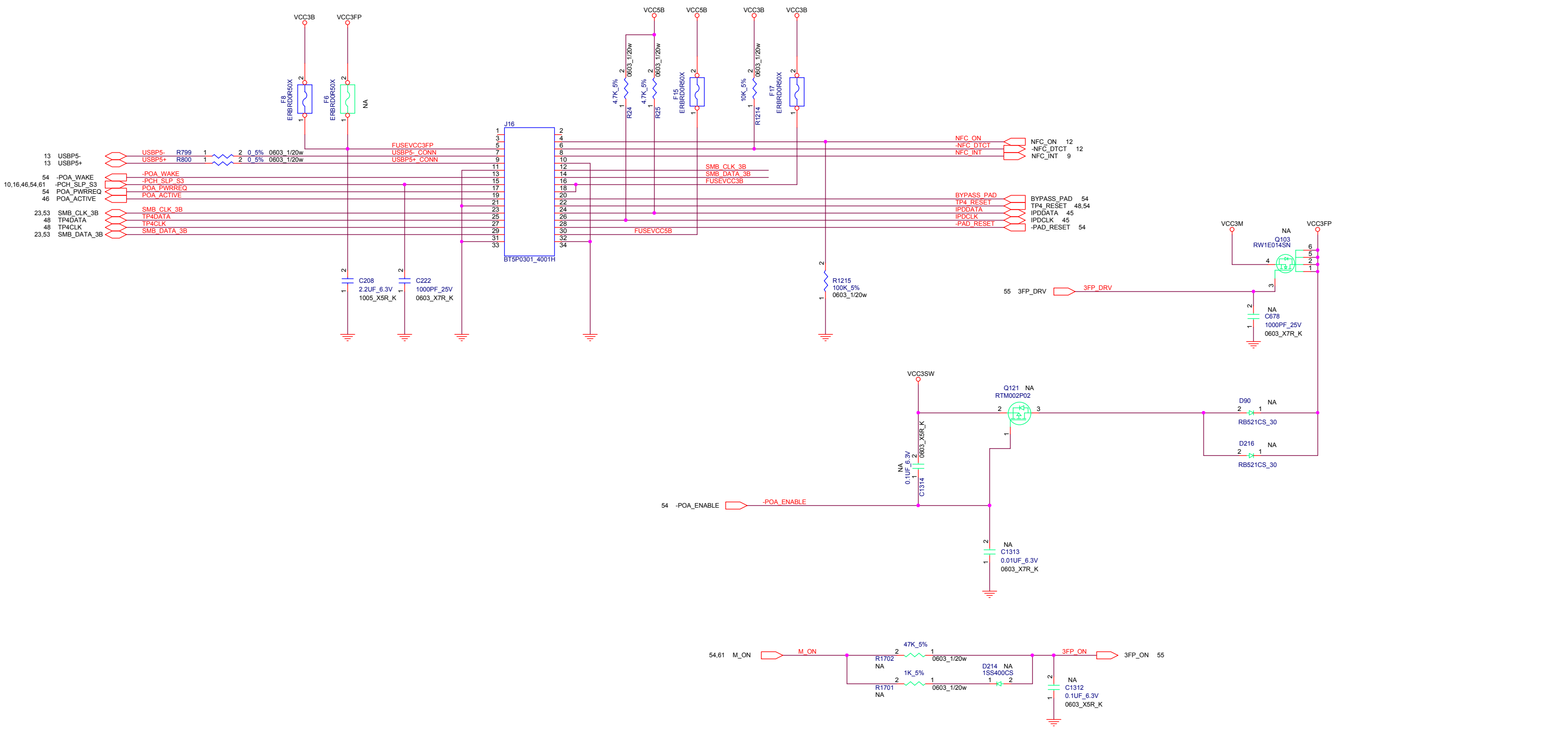
Swappable Rear Battery			
	Wolverine-1 (Support)	Rogue-2 with Dock (Non-Support)	Rogue-2 w/o Dock (Non-Support)
S2	ASM	ASM	NO-ASM

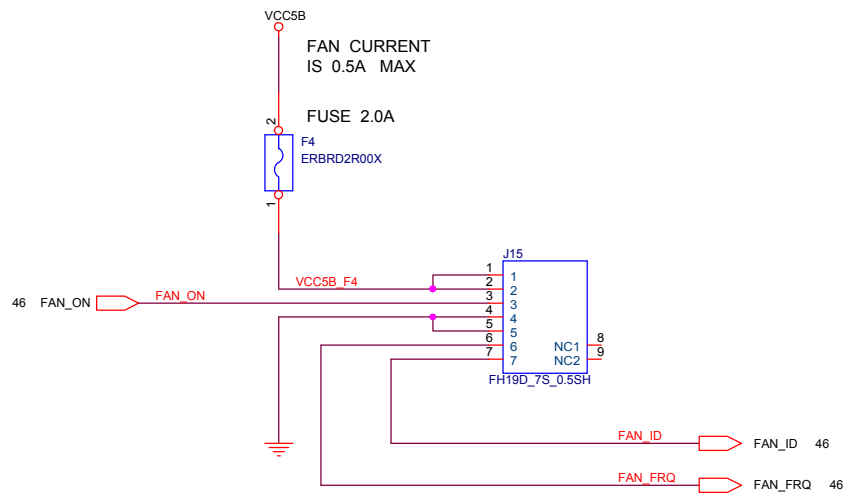
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LOGIC





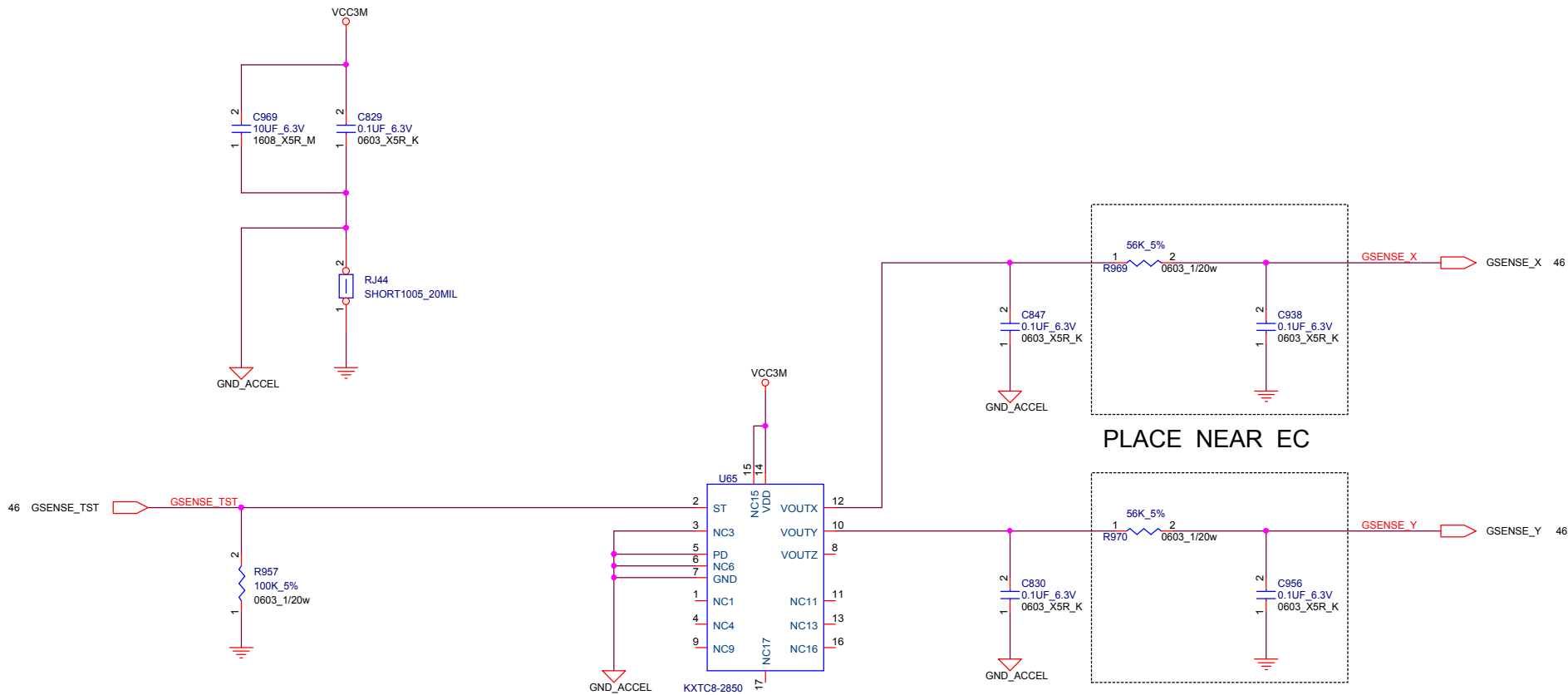






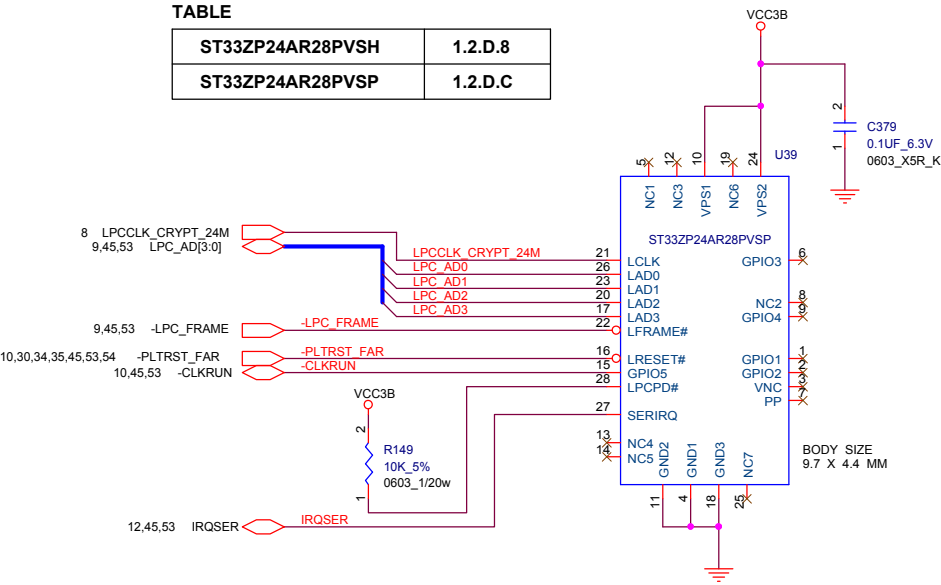
TABLE

	LIS34AL KXTC8-2850	NO ACC.
R957	ASM	ASM
U65	ASM	NO_ASM
C829 C969	ASM ASM	NO_ASM NO_ASM
C830 C847	ASM ASM	NO_ASM NO_ASM
R969 C938 R970 C956	56K ASM 56K ASM	NO_ASM NO_ASM NO_ASM NO_ASM



TABLE

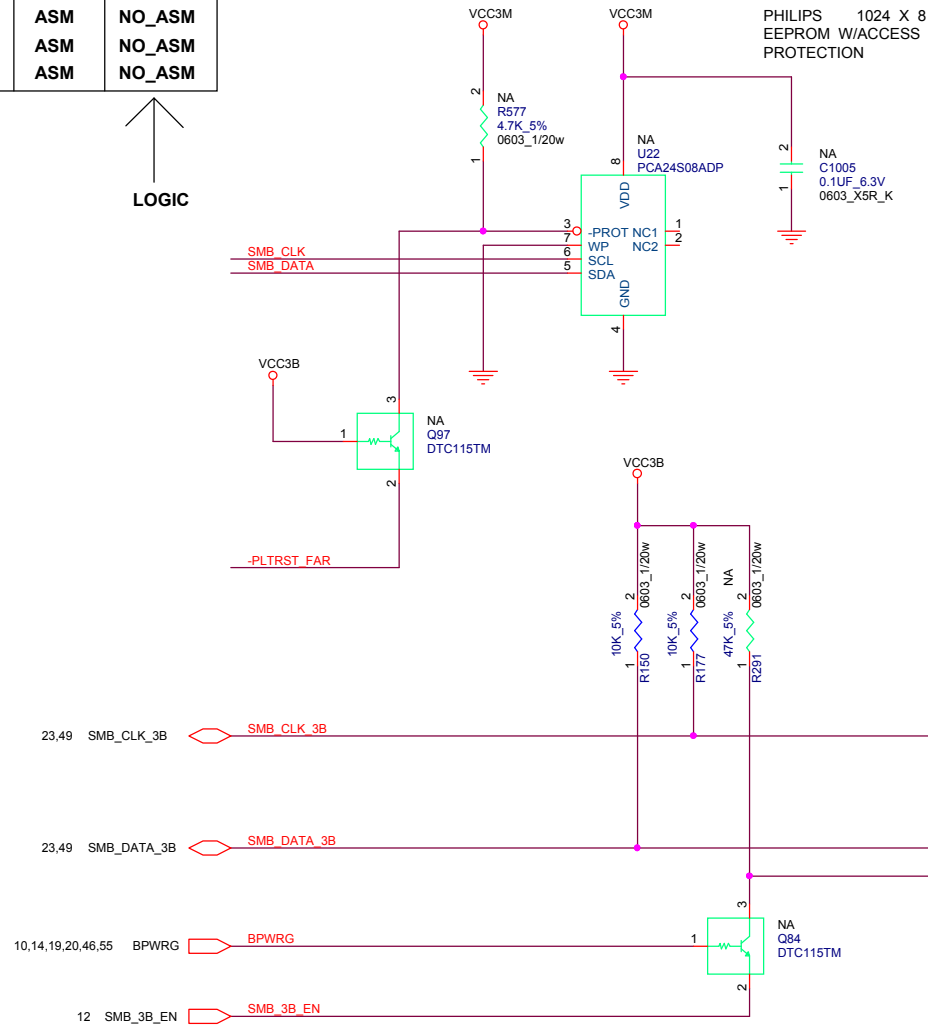
ST33ZP24AR28PVSH	1.2.D.8
ST33ZP24AR28PVSP	1.2.D.C



TABLE

EEPROM	U22	U23
U22	ASM	NO_ASM
C1005	ASM	NO_ASM
R577	ASM	NO_ASM
Q97	ASM	NO_ASM

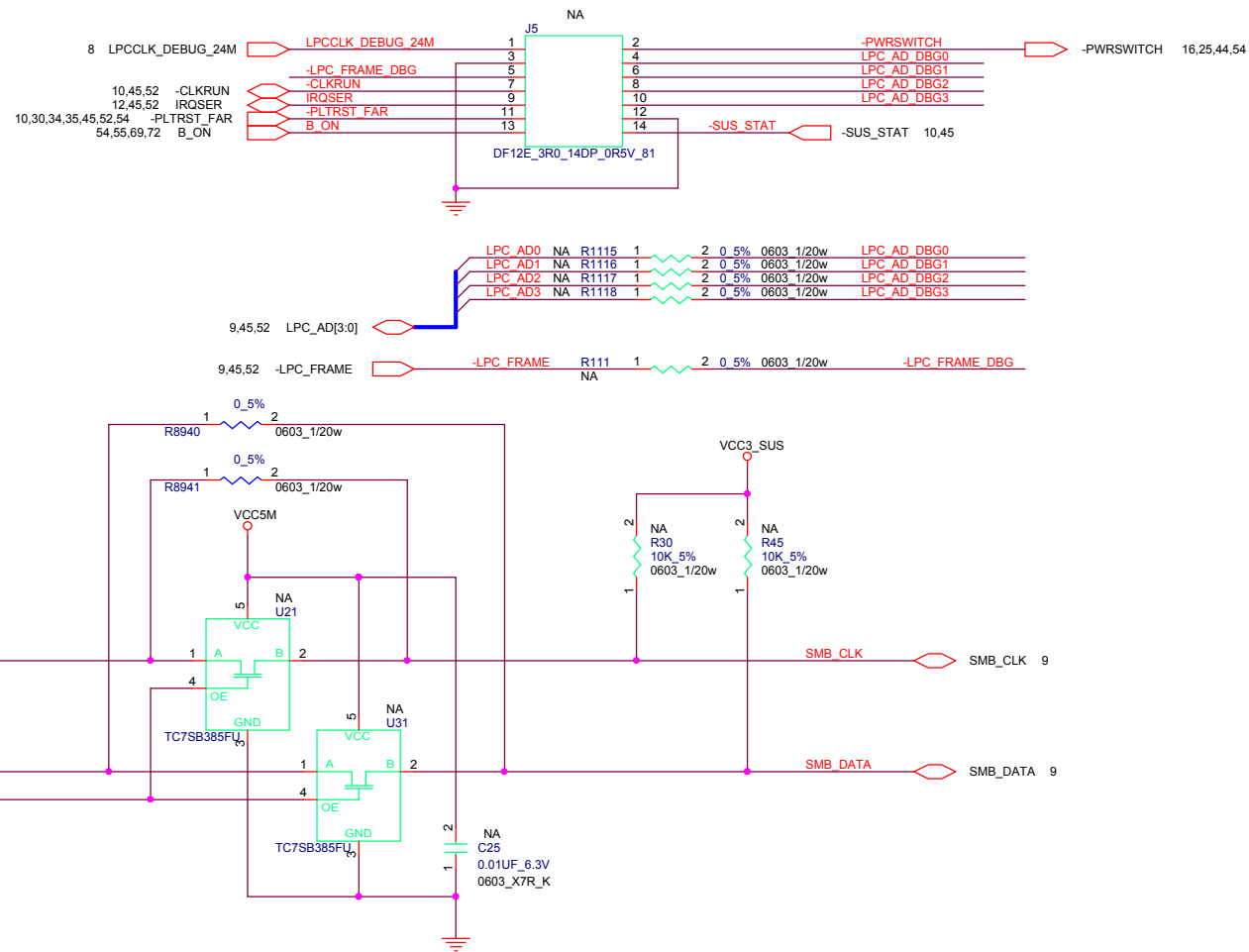
LOGIC



TABLE

REF DES	ENABLE	DISABLE
J5	ASM	NO_ASM
R1115	ASM	NO_ASM
R1116	ASM	NO_ASM
R1117	ASM	NO_ASM
R1118	ASM	NO_ASM
R111	ASM	NO_ASM
R220	ASM	NO_ASM

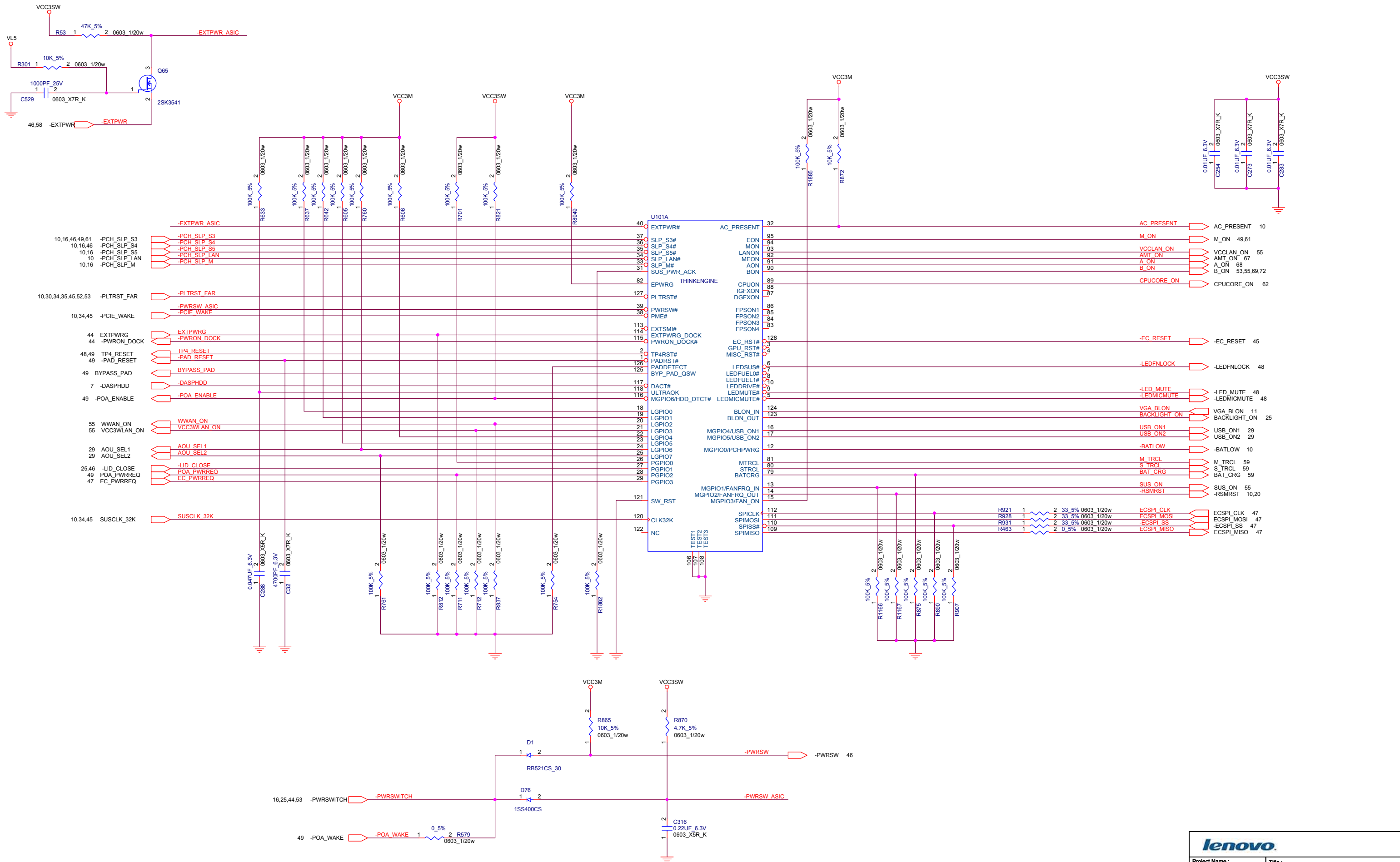
LOGIC



TABLE

EEPROM	U22	U23
U21	ASM	NO_ASM
U31	ASM	NO_ASM
C25	ASM	NO_ASM
R291	ASM	NO_ASM
Q84	ASM	NO_ASM
R8941	NO_ASM	ASM
R8940	NO_ASM	ASM
R30	ASM	NO_ASM
R45	ASM	NO_ASM

LOGIC



DCIN

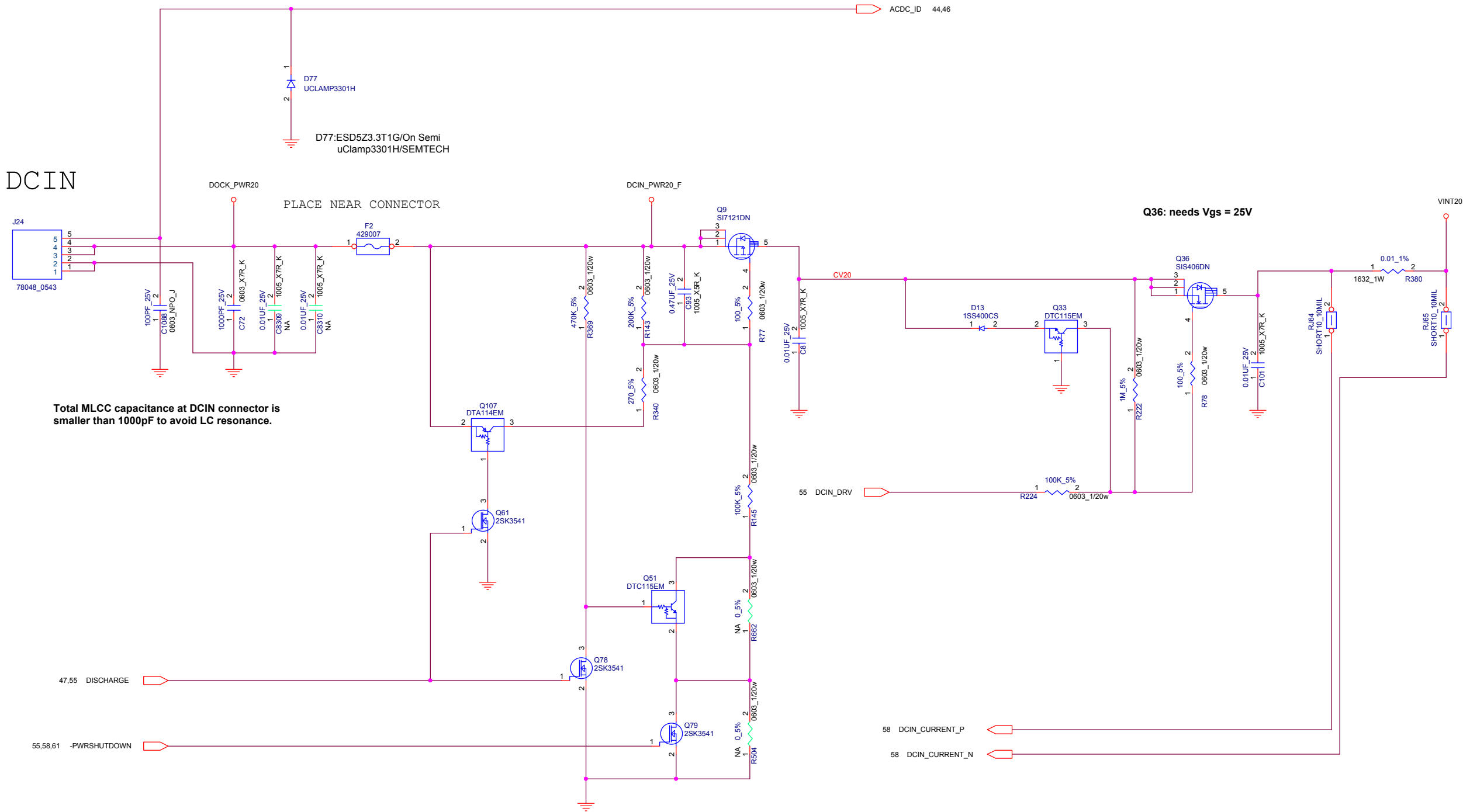
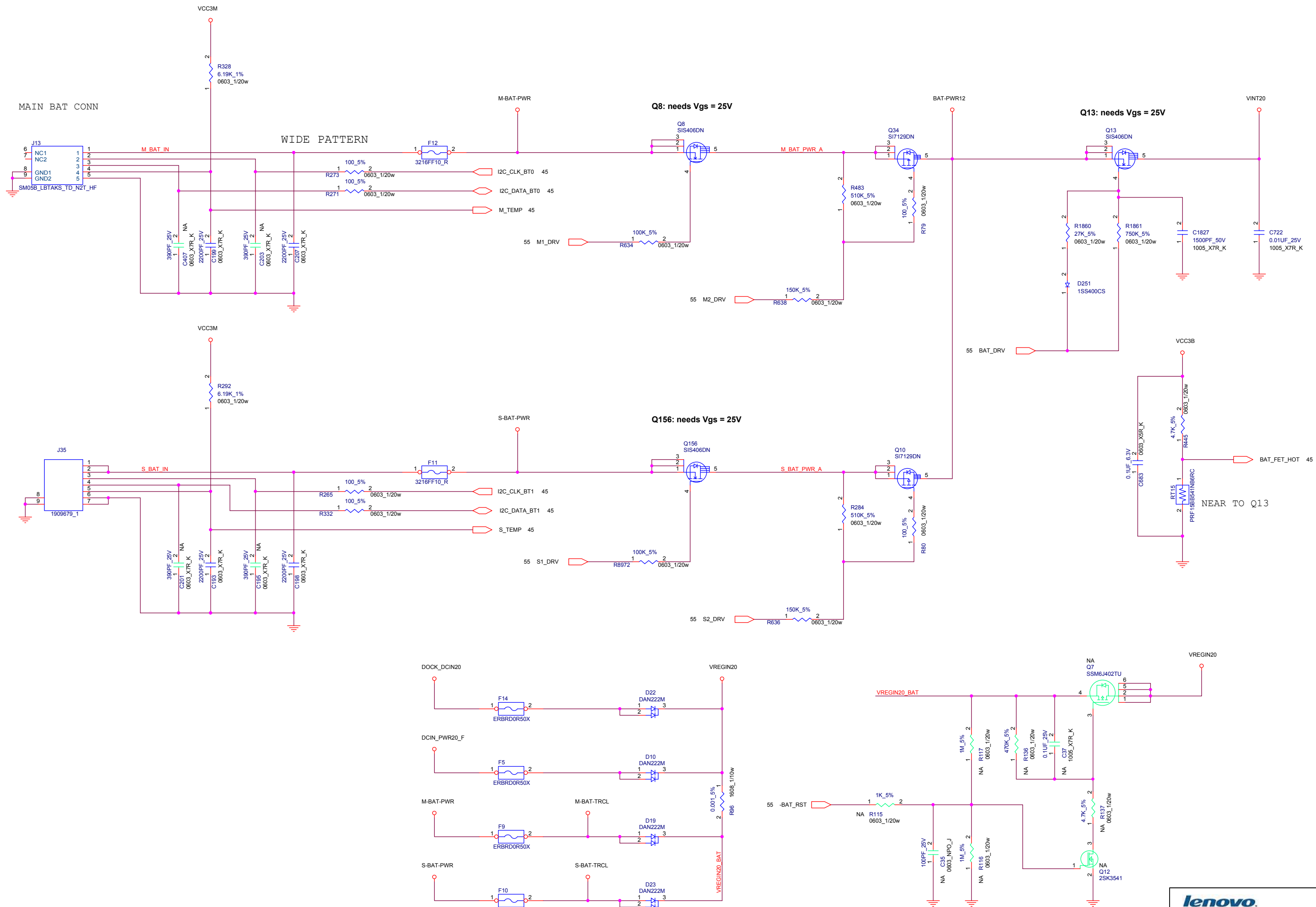
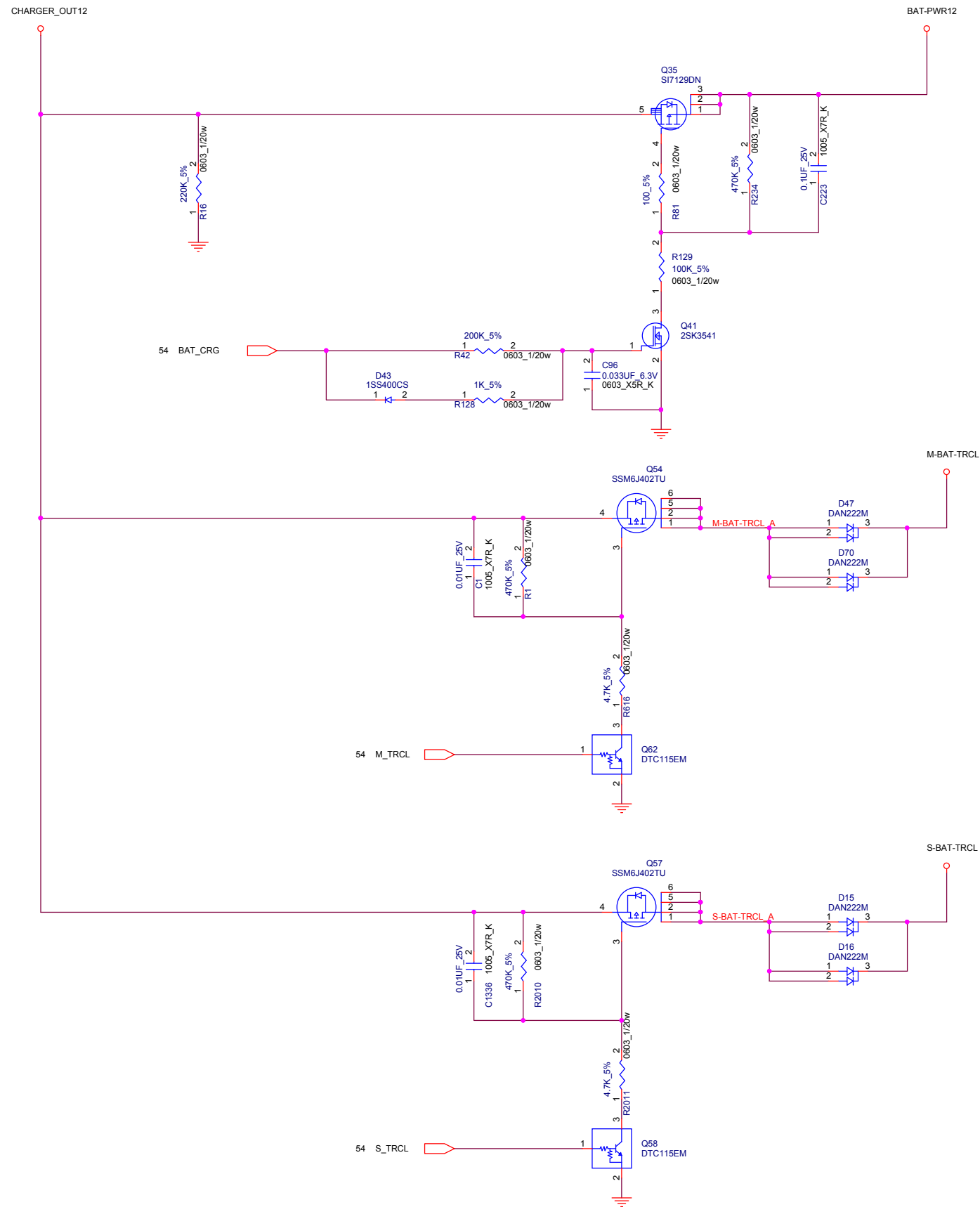
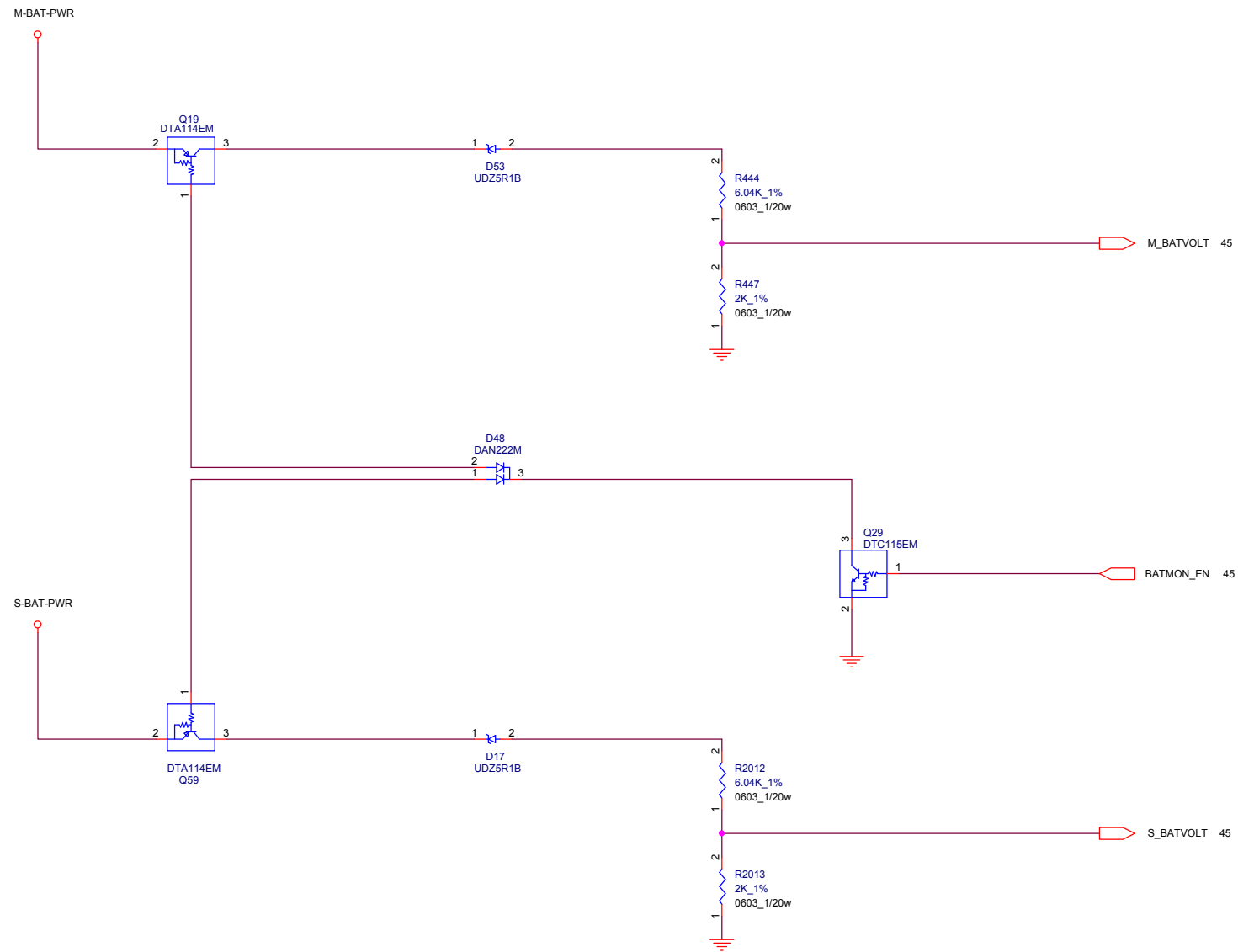


TABLE		
PEAK SHIFT	YES	NO
R662	NO-ASM	ASM
R369	ASM	NO-ASM
Q78	ASM	NO-ASM
Q51	ASM	NO-ASM

↑
LOGIC

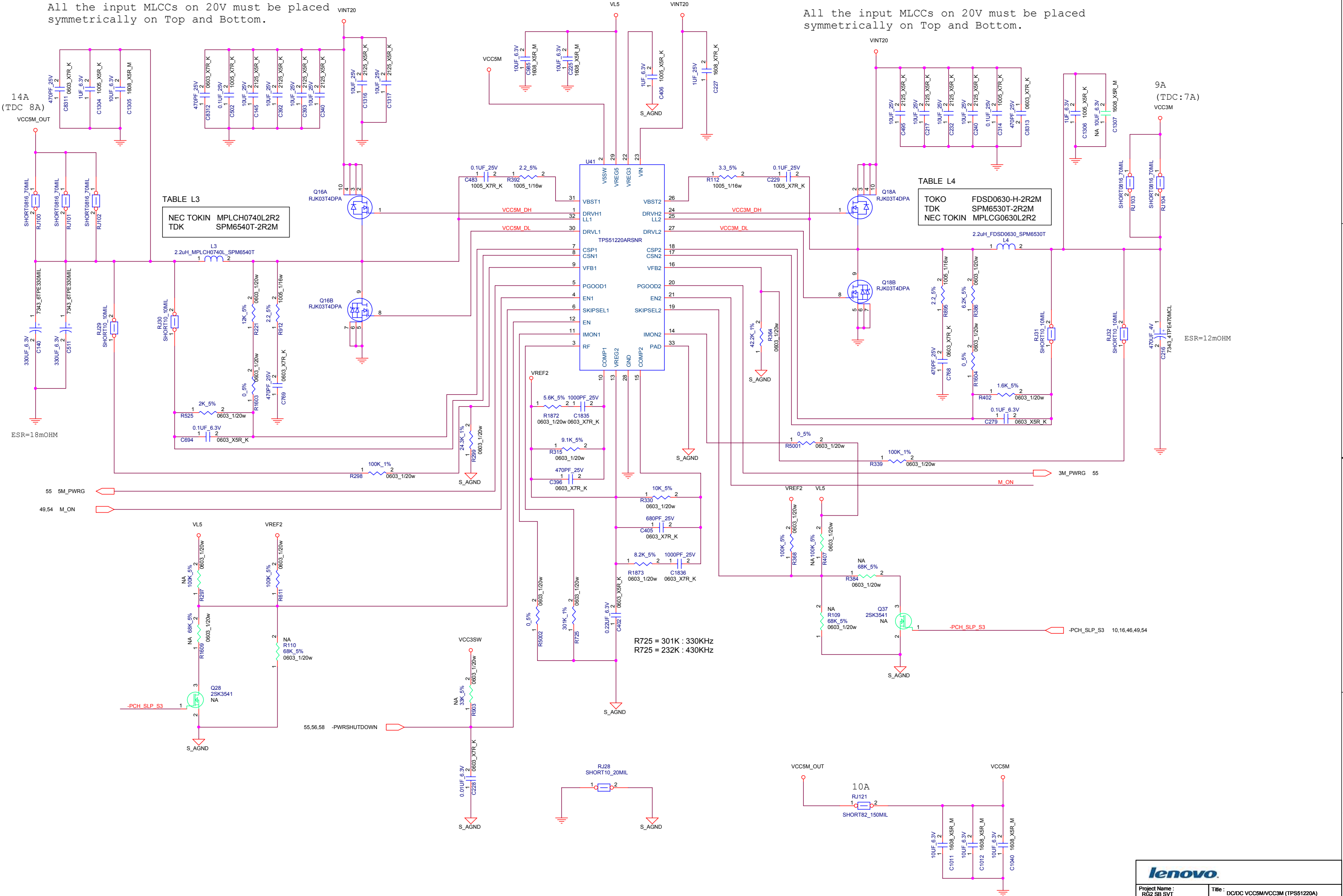






All the input MLCCs on 20V must be placed symmetrically on Top and Bottom.

All the input MLCCs on 20V must be placed symmetrically on Top and Bottom.



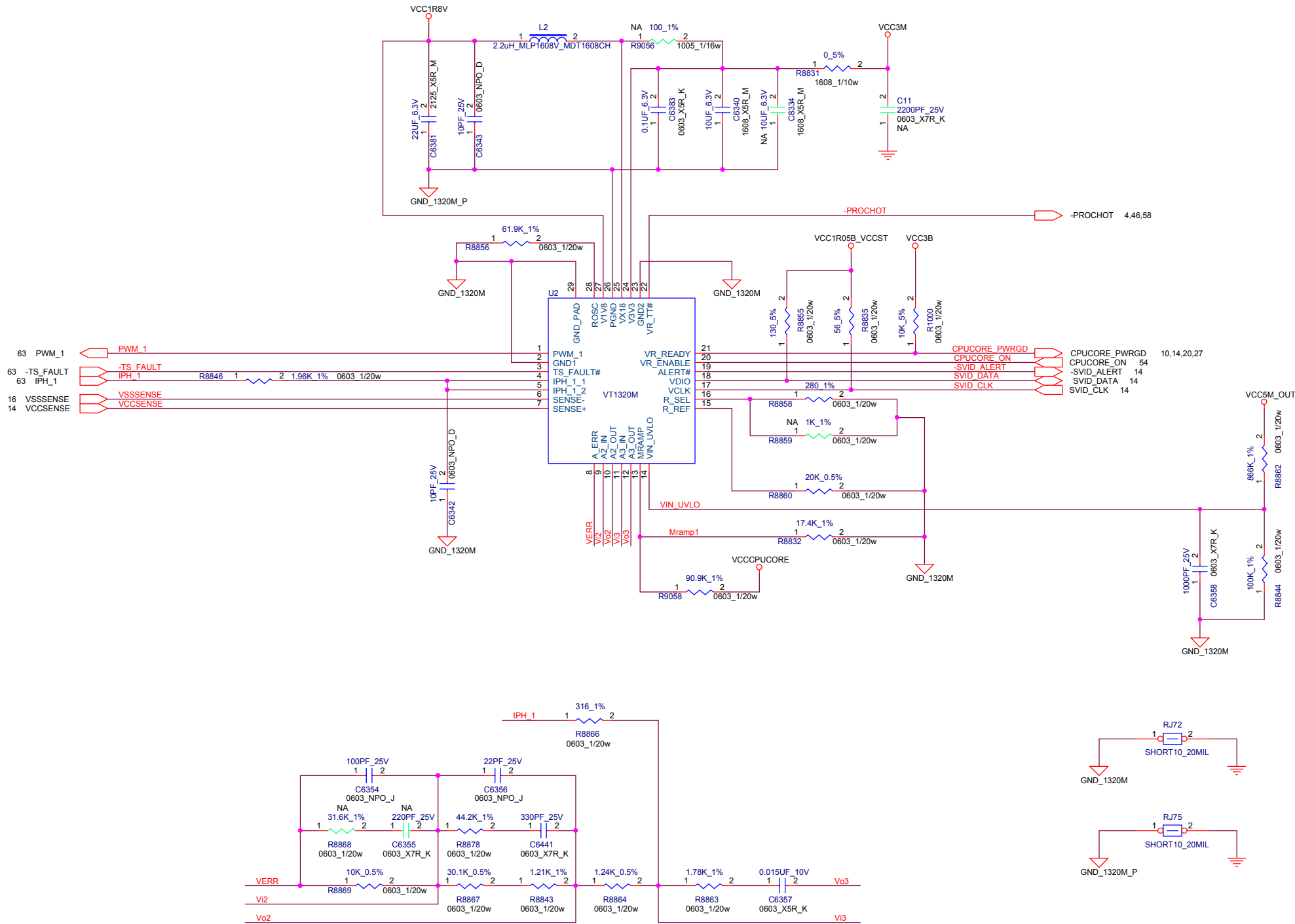
Project Name : RG2 SB SVT Title : DC/DC VCC5M/VCC3M (TPS51220A)

Size : C Document Number : Rev : 5.05

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TABLE L2

TDK	MLP1608V2R2B
TOKO	MDT1608-CH2R2N



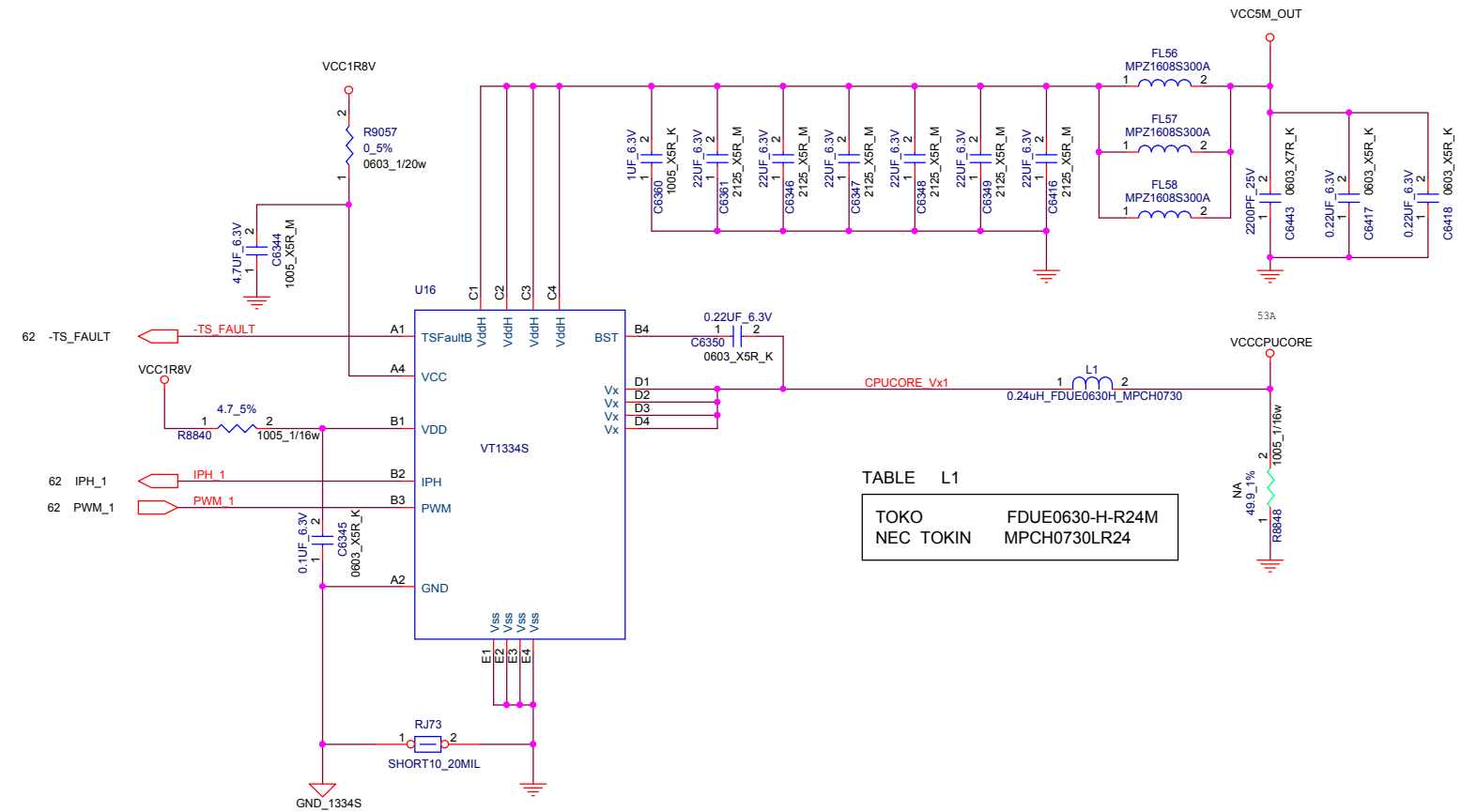
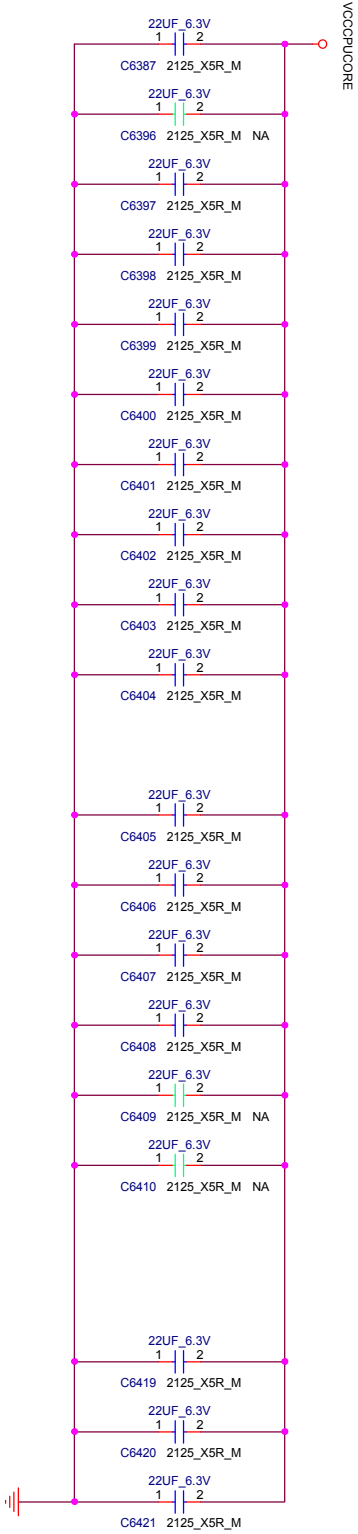


TABLE L1	
TOKO	FDUE0630-H-R24M
NEC TOKIN	MPCH0730LR24

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Project Name : RG2 SB SVT		Title : BLANK	
Size : C	Document Number :		Rev : 5.05
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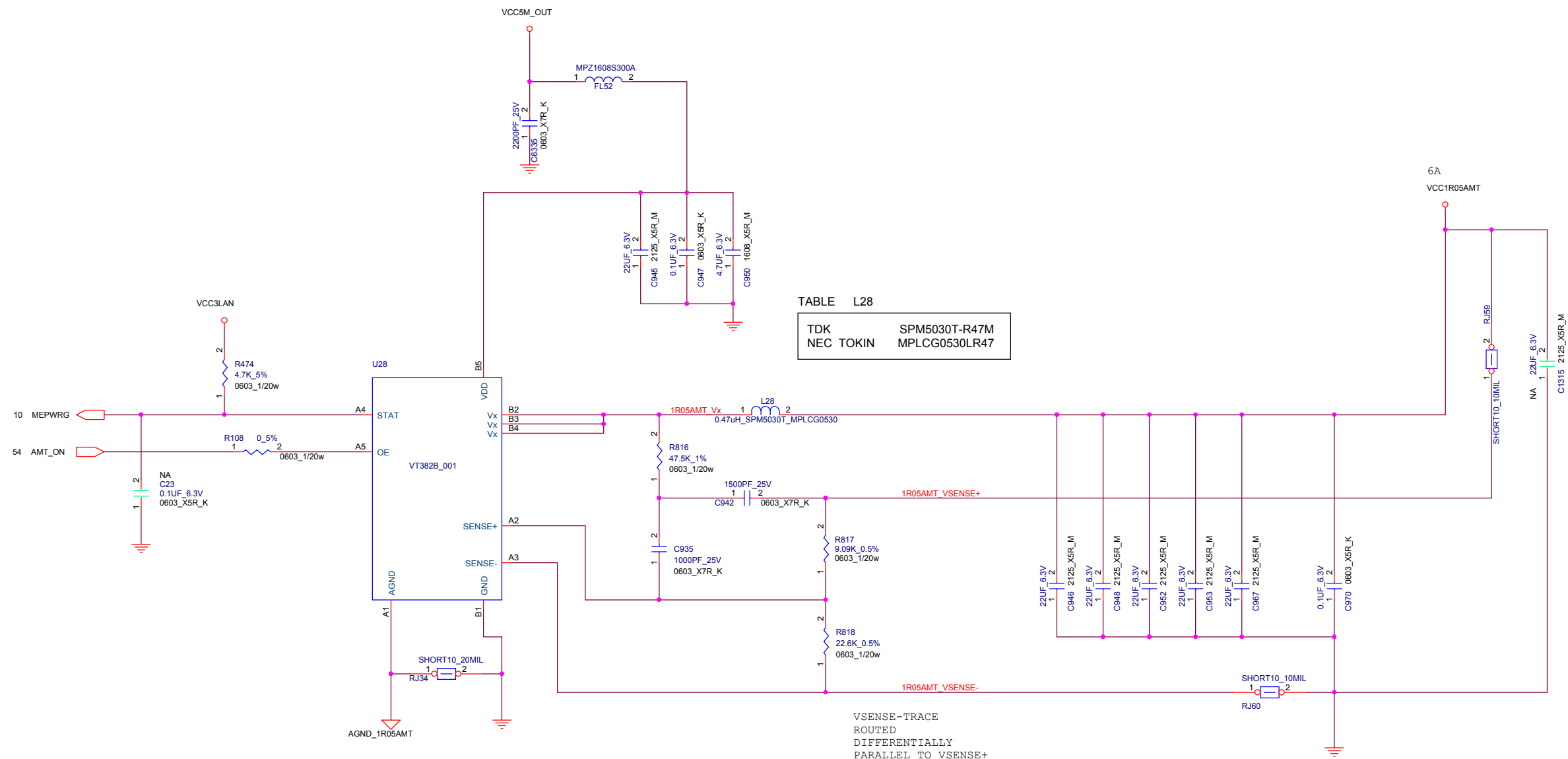


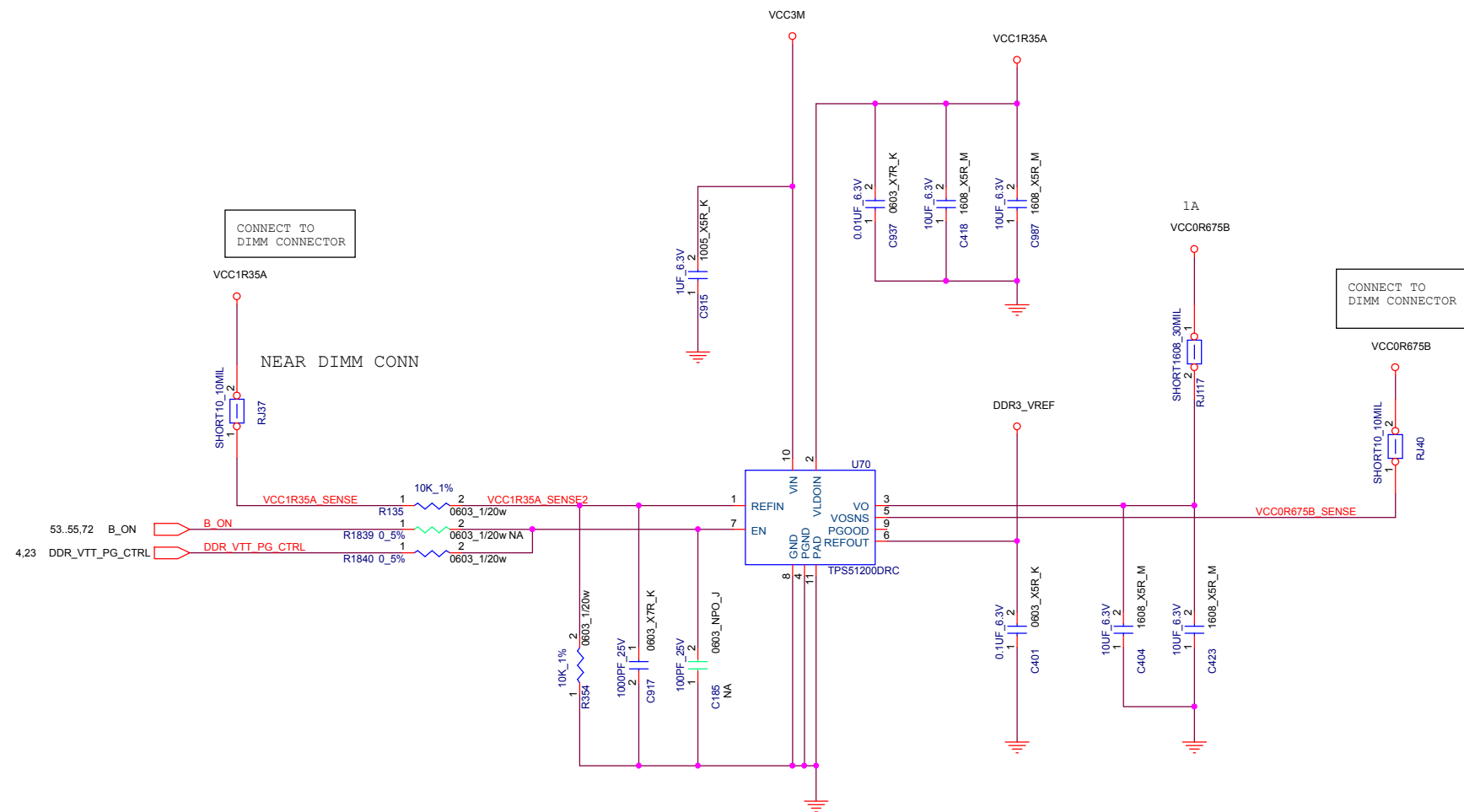
16pcs 22uF for VCCPUCORE

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Project Name : RG2 SB SVT		Title : BLANK	
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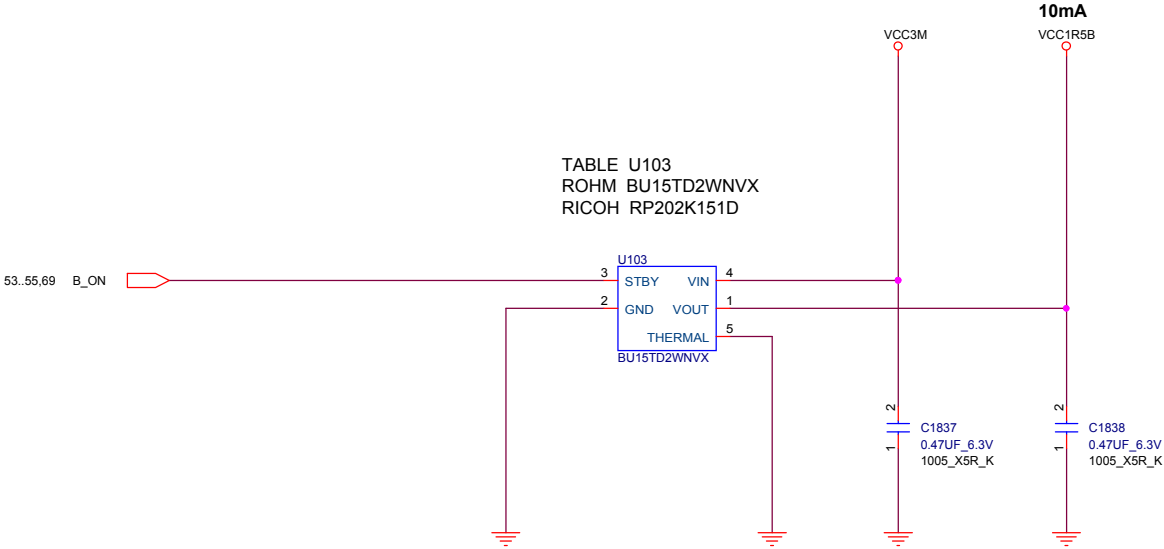
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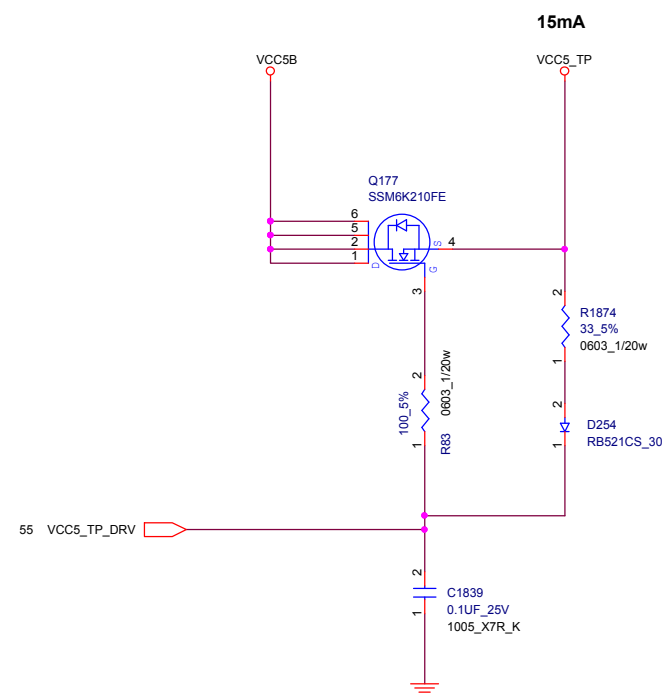
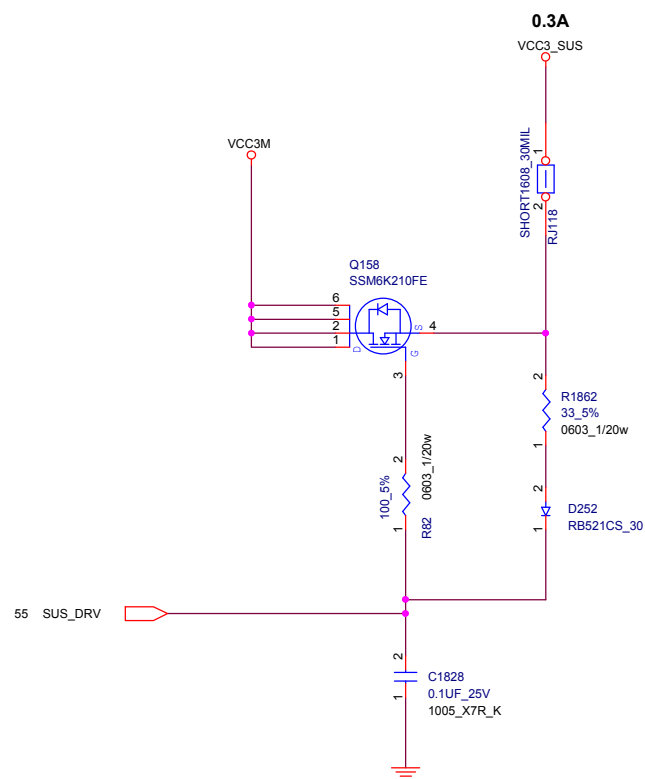
		
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Size : C	Document Number :	Rev : 5.05
Date: Thursday, April 11, 2013		Sheet : 70 of 77

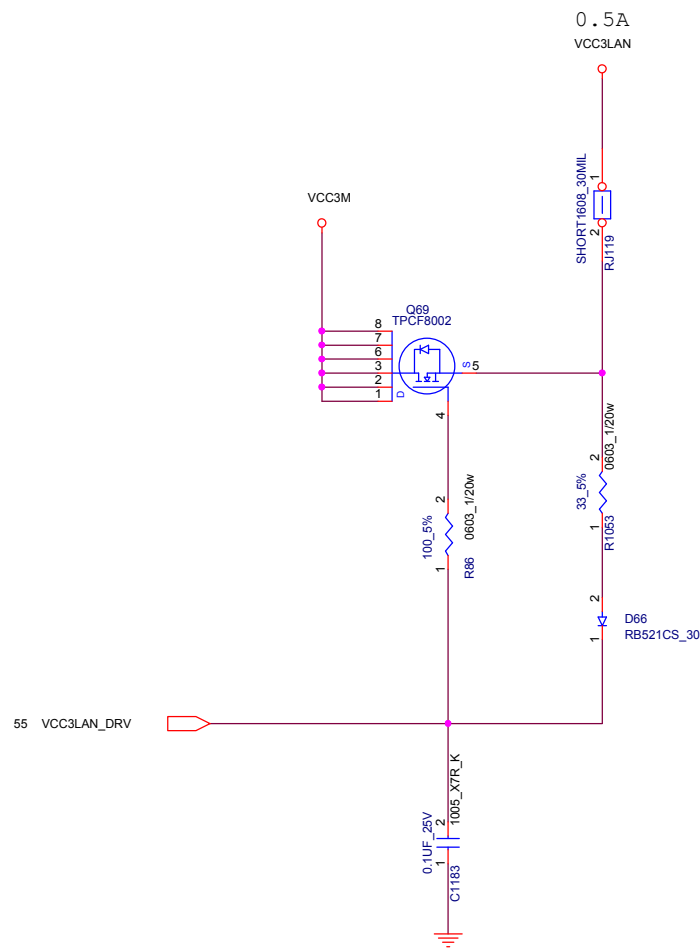
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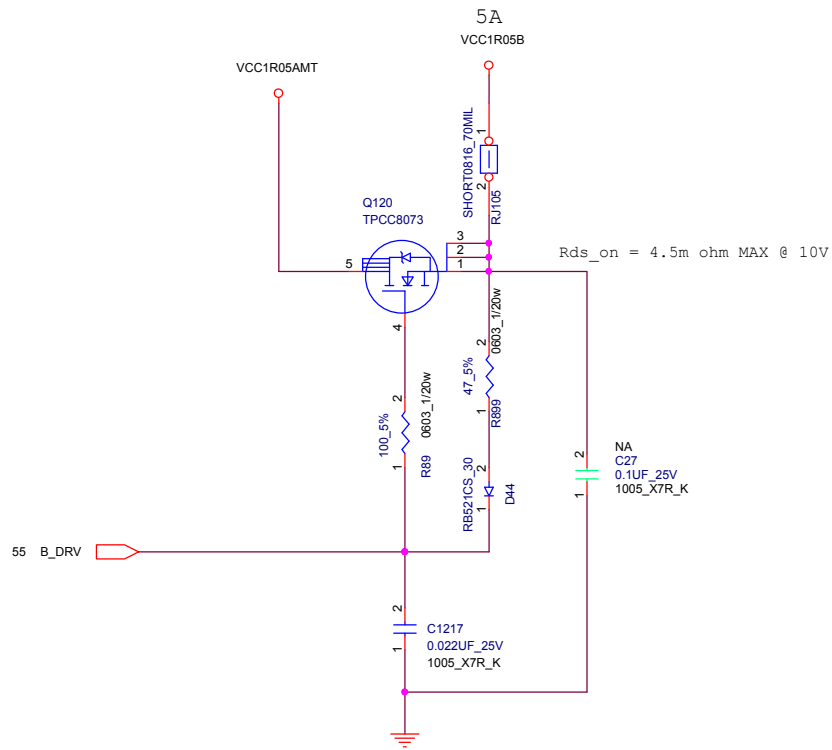
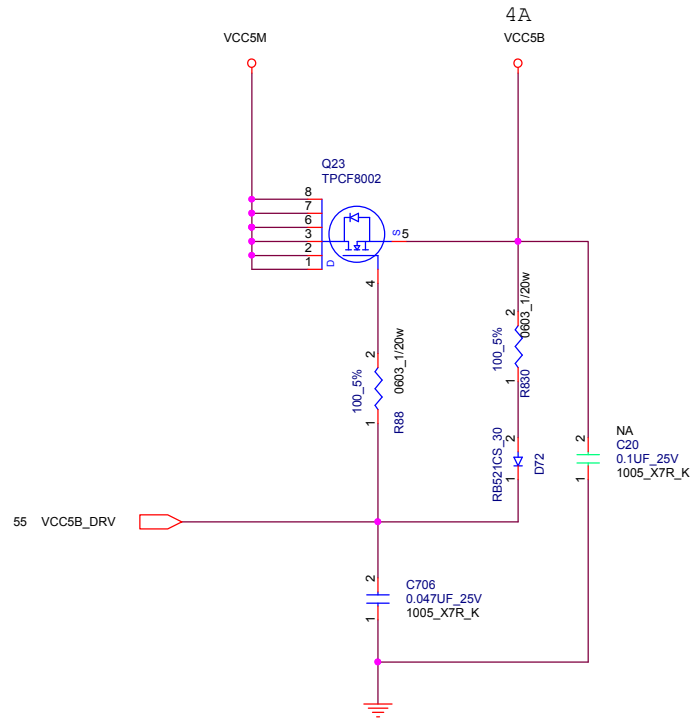
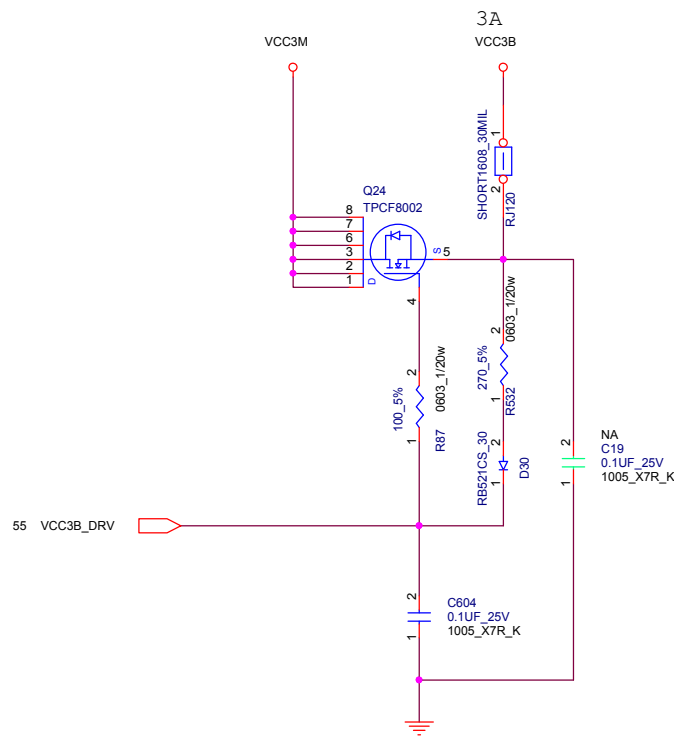


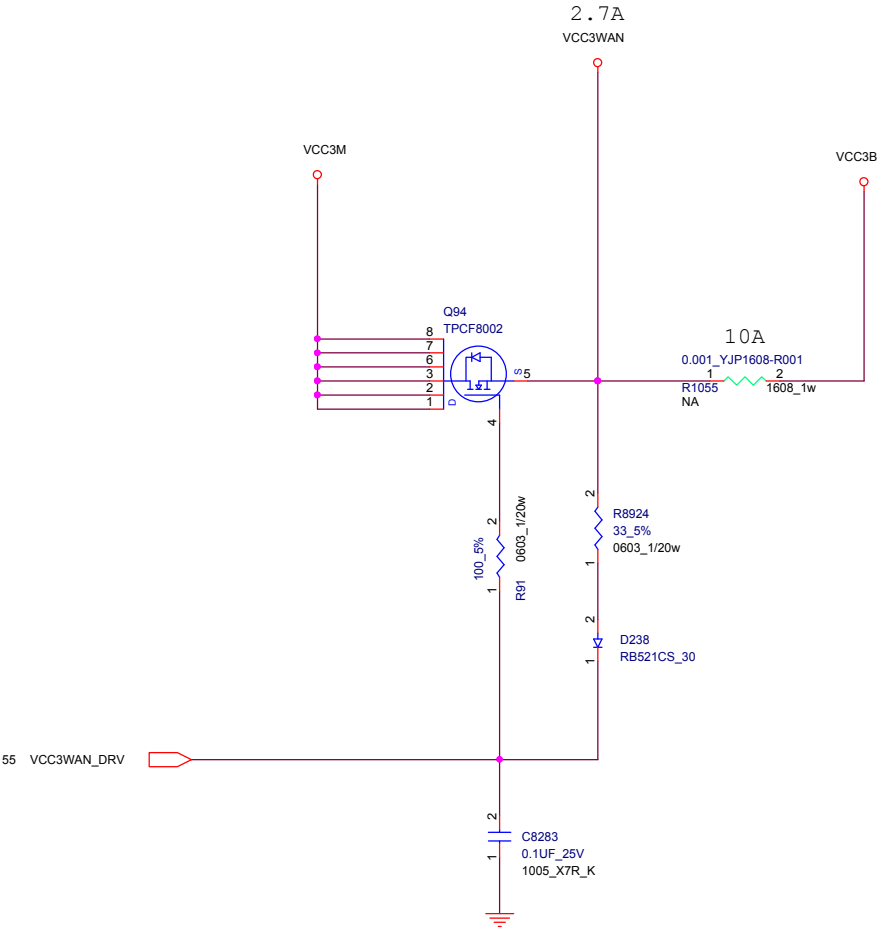
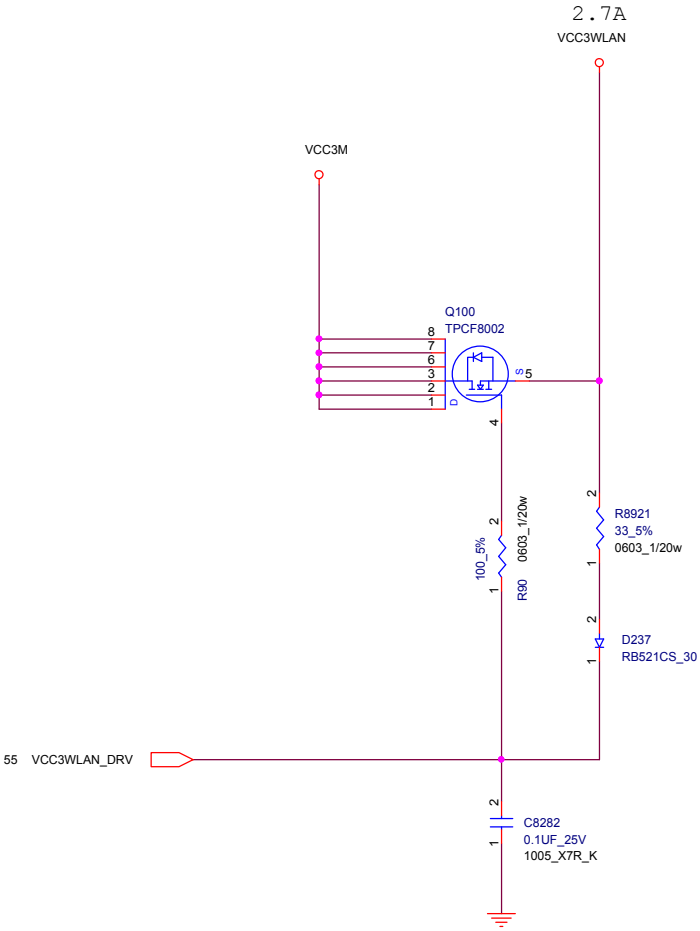
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Size : C	Document Number :		Rev : 5.05
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TABLE

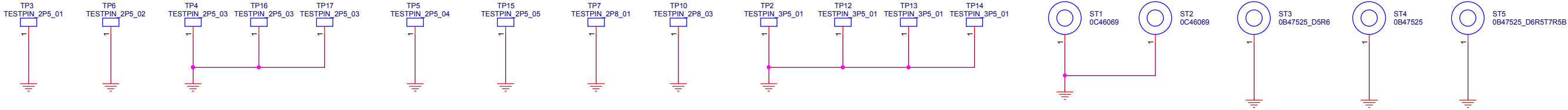
AOAC	YES	NO
R1055	NO-ASM	ASM
Q94	ASM	NO-ASM
R8924	ASM	NO-ASM
R8926	ASM	NO-ASM
C8283	ASM	NO-ASM
D238	ASM	NO-ASM

↑
LOGIC

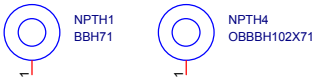


PTH FOR SCREW HOLE

Value	Hole Dia	Pad Dia		QTY
		TOP	BOTTOM	
TESTPIN_2P5_01	2.5	6.5	6.5	1
TESTPIN_2P5_02	2.5	6.5	8	1
TESTPIN_2P5_03	2.5	6.6	6.6	3
TESTPIN_2P5_04	2.5	6.5	5	1
TESTPIN_2P5_05	2.5	6.5	8x7.5	1
TESTPIN_2P8_01	2.8	6.5	6.5	1
TESTPIN_2P8_03	2.8	7	7.5	1
TESTPIN_3P5_01	3.5	7	6.5	4
0C46069	2.8	6.5	6.5	2
0B47525_D5R6	3.4	5.6		1
0B47525	3.4	6.6		1
0B47525_D6R5TB	3.4	6.5	6.5	1



NPTH



FID
Board Area



FID
Component Area

