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1

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.

2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.

3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

M60 - EVT

05/12/06

REV	ZONE	ECN	DESCRIPTION OF CHANGE	CK APPD DATE	ENG APPD DATE
06		400374	ENGINEERING RELEASED	09/16/05	06/22/04

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DIMENSIONS ARE IN MILLIMETERS

XX ± _____

X.XX ± _____

X.XXX ± _____

ANGLES ± _____

DO NOT SCALE DRAWING



THIRD ANGLE PROJECTION

METRIC

DRAFTER

ENG APPD

QA APPD

RELEASE

DESIGN CK

MFG APPD

DESIGNER

SCALE

SIZE

D

MATERIAL/FINISH NOTED AS APPLICABLE

 Apple Computer Inc.

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TITLE

SCH, MLB, M60

DRAWING NUMBER

051-7124

REV.

13

SHT

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OF

110

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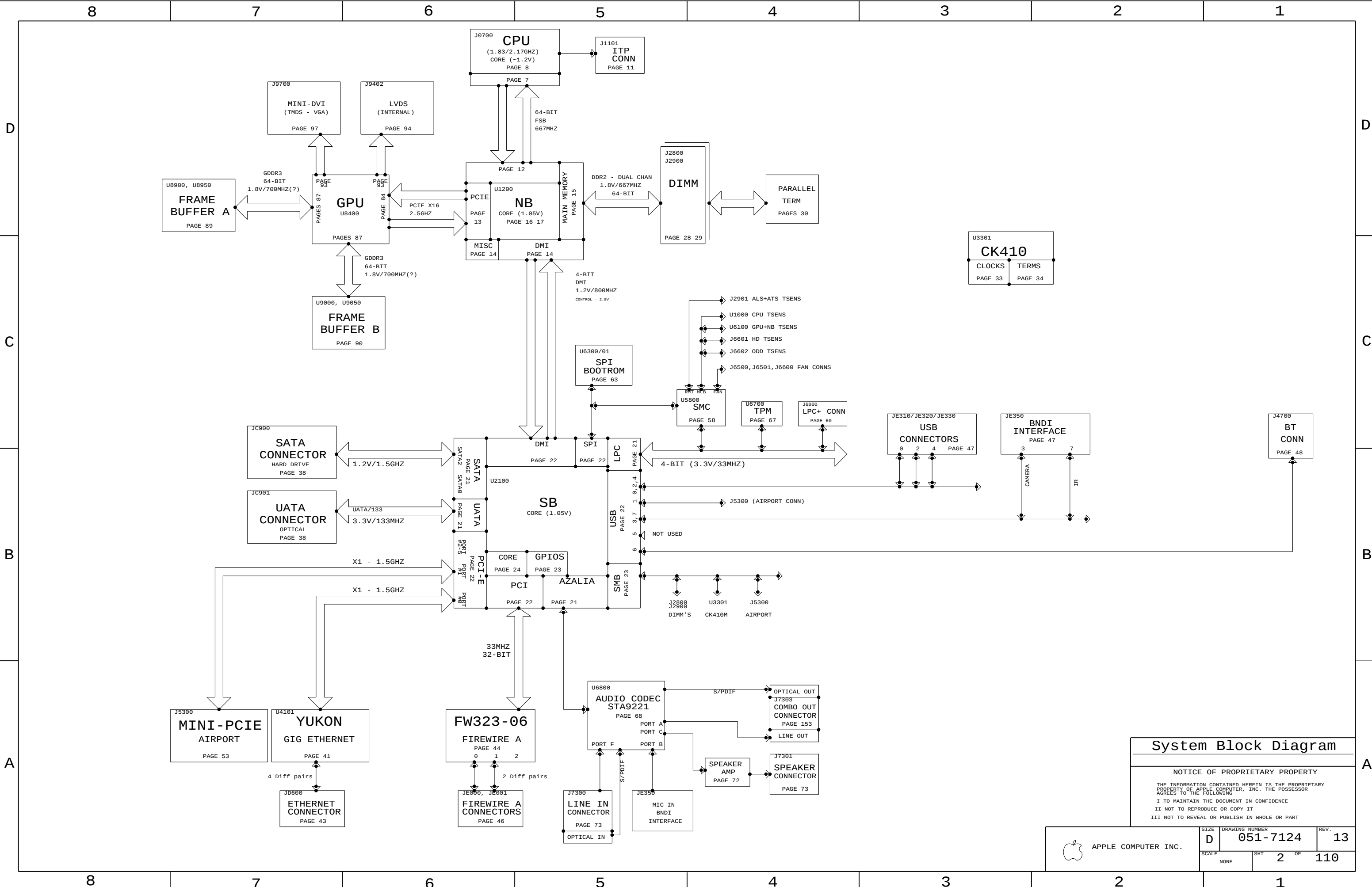
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System Block Diagram

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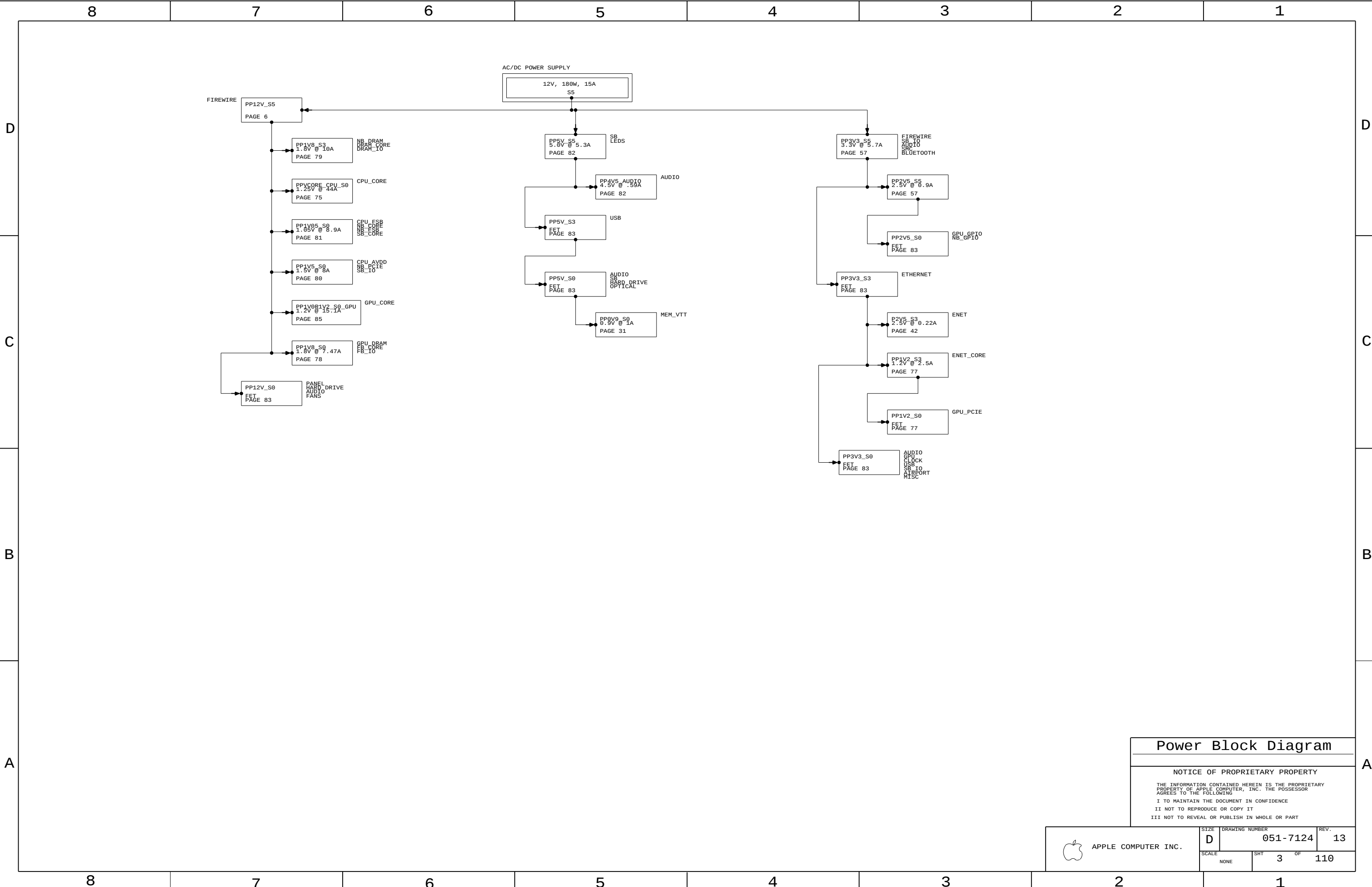
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	D	051-7124	13
SCALE		SHT	2 OF 110
NONE			



Power Block Diagram

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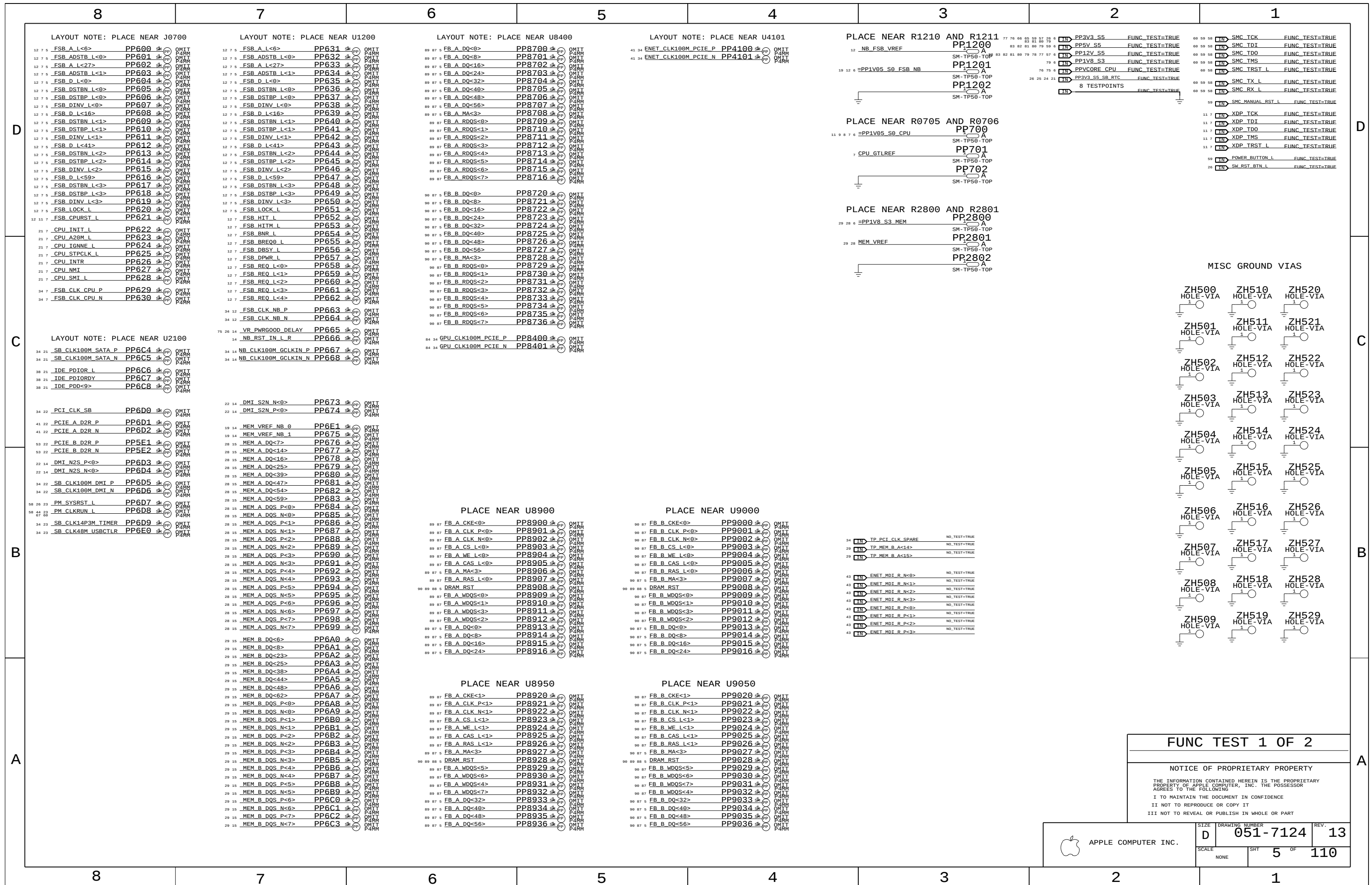
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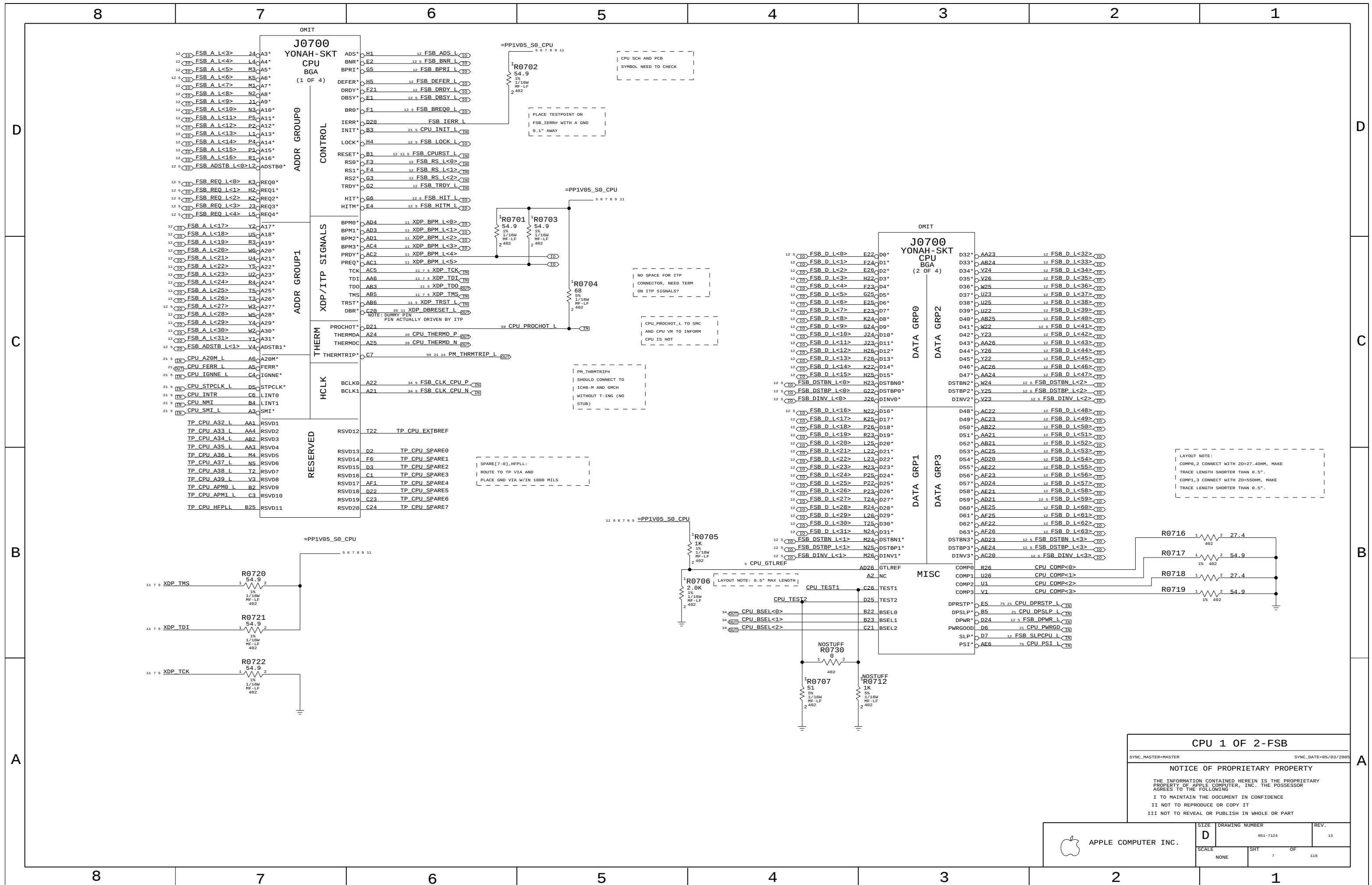


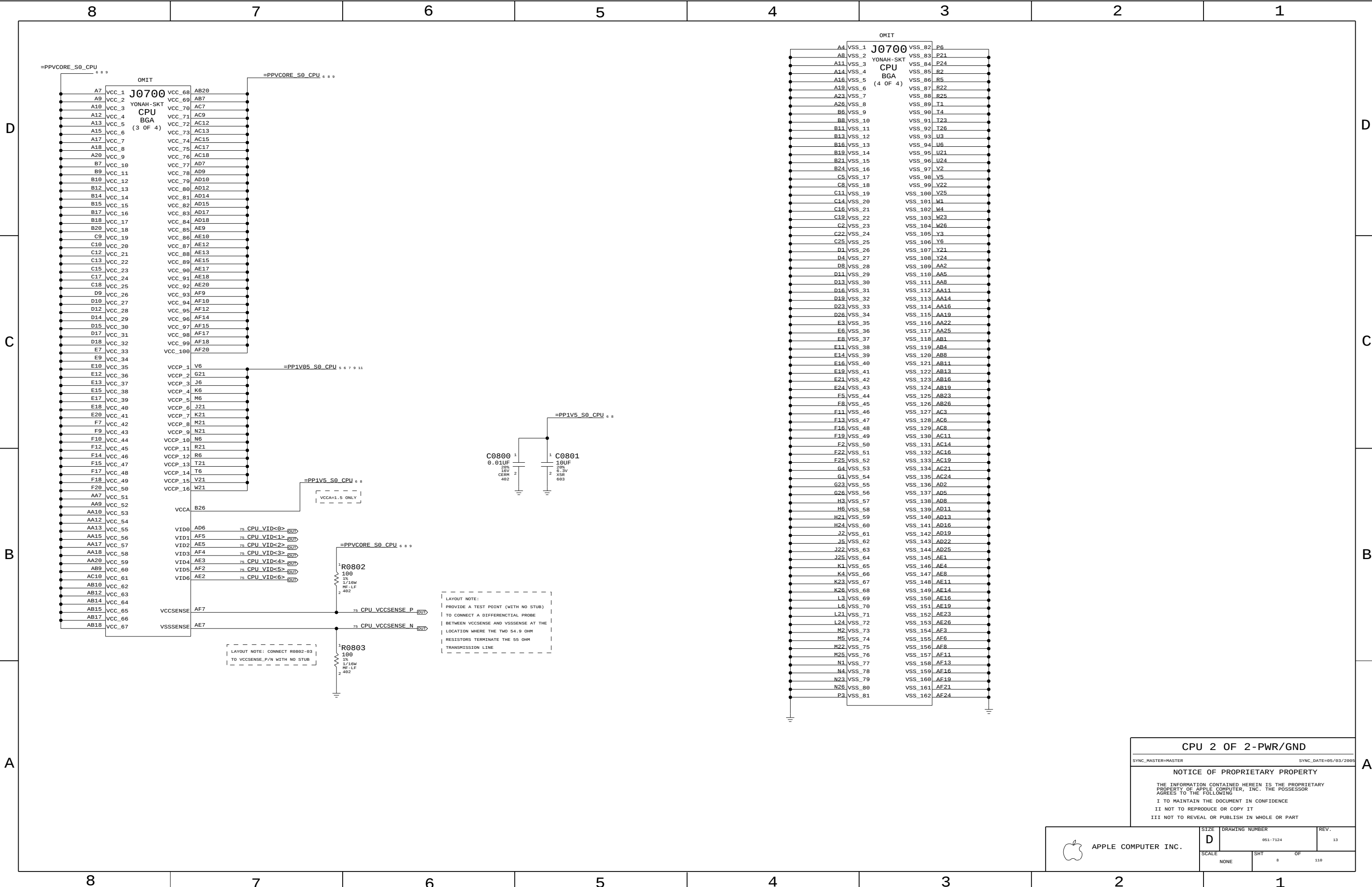
APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
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CPU 2 OF 2 - PWR/GND

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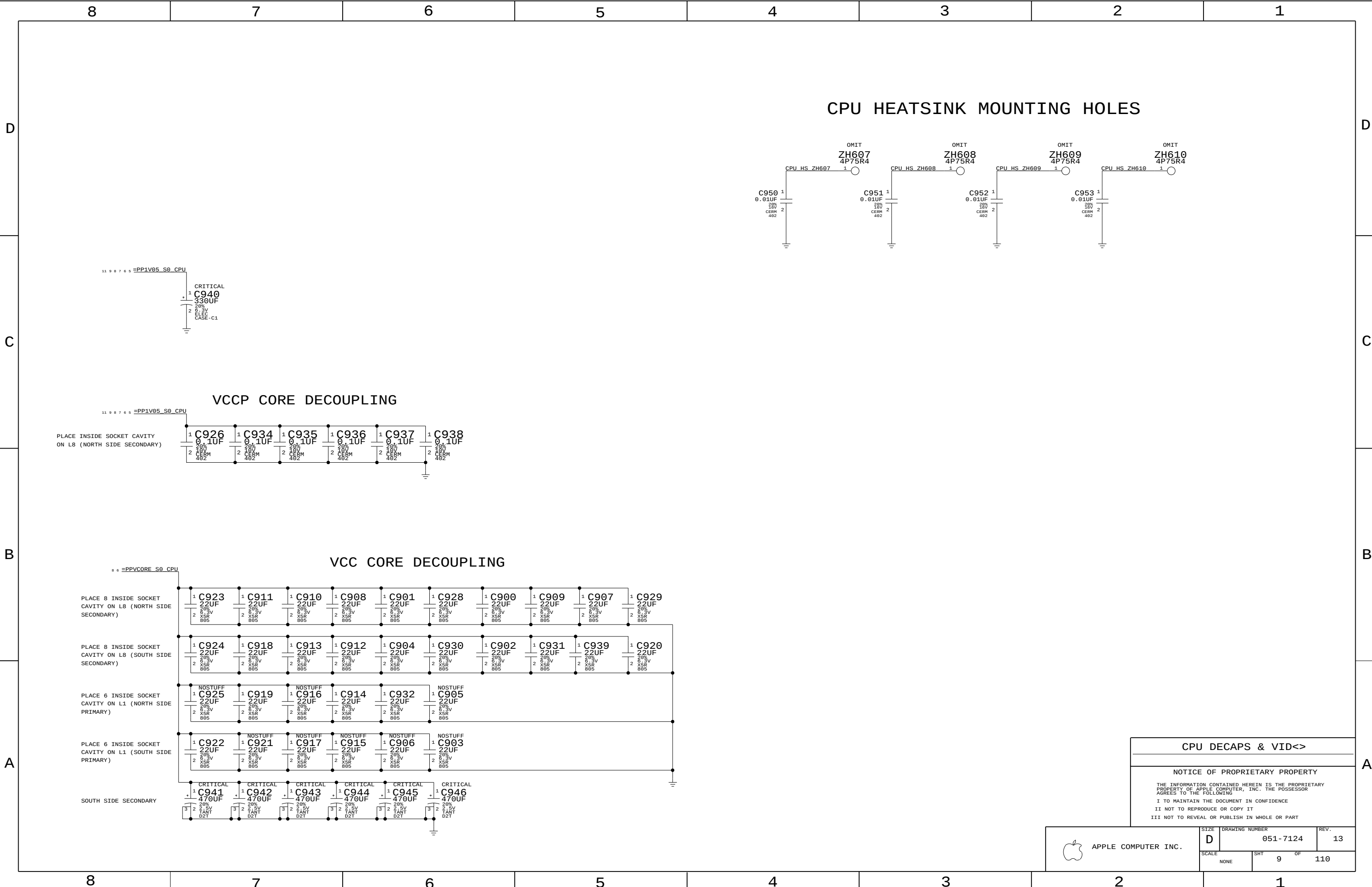
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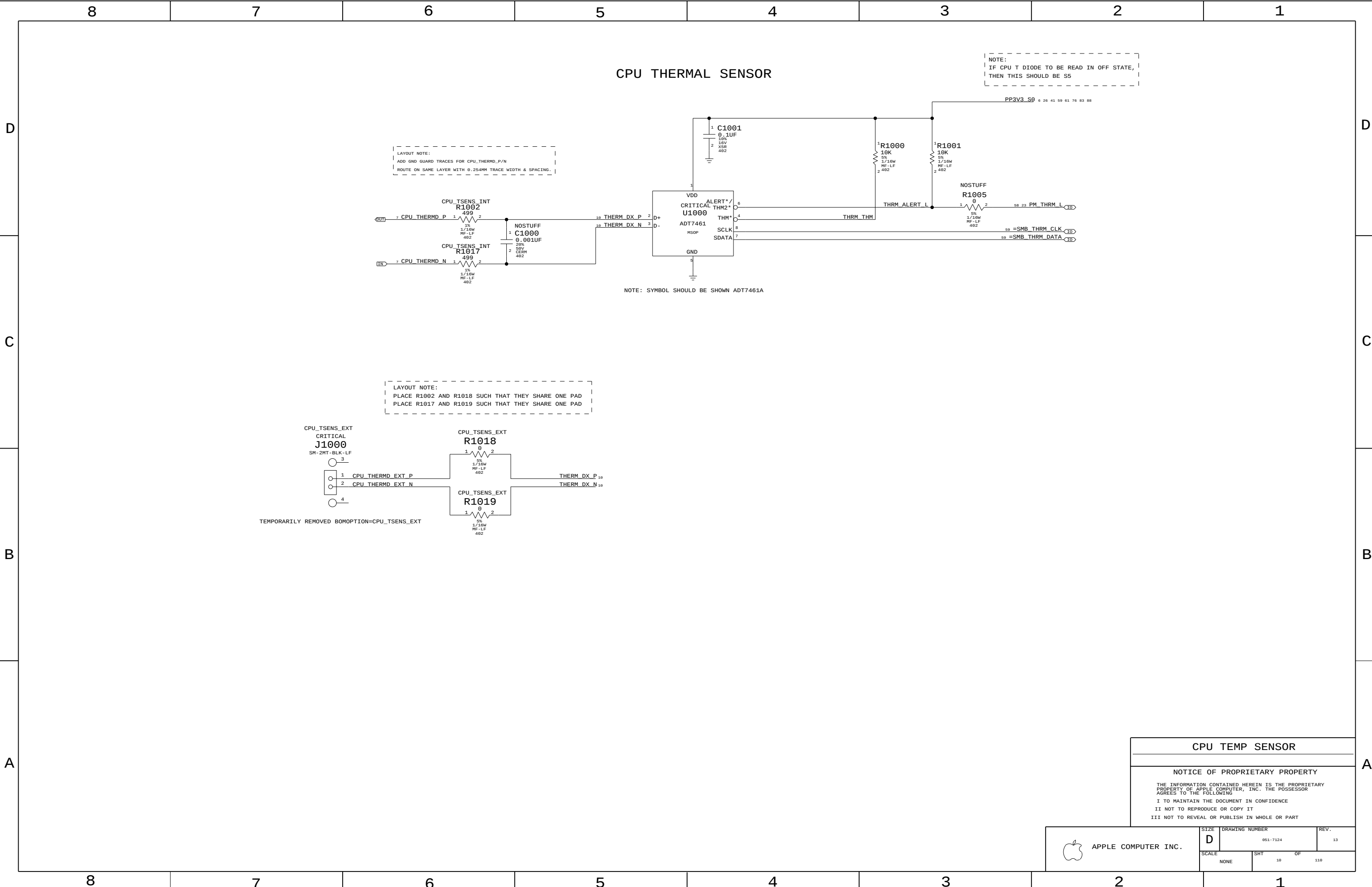
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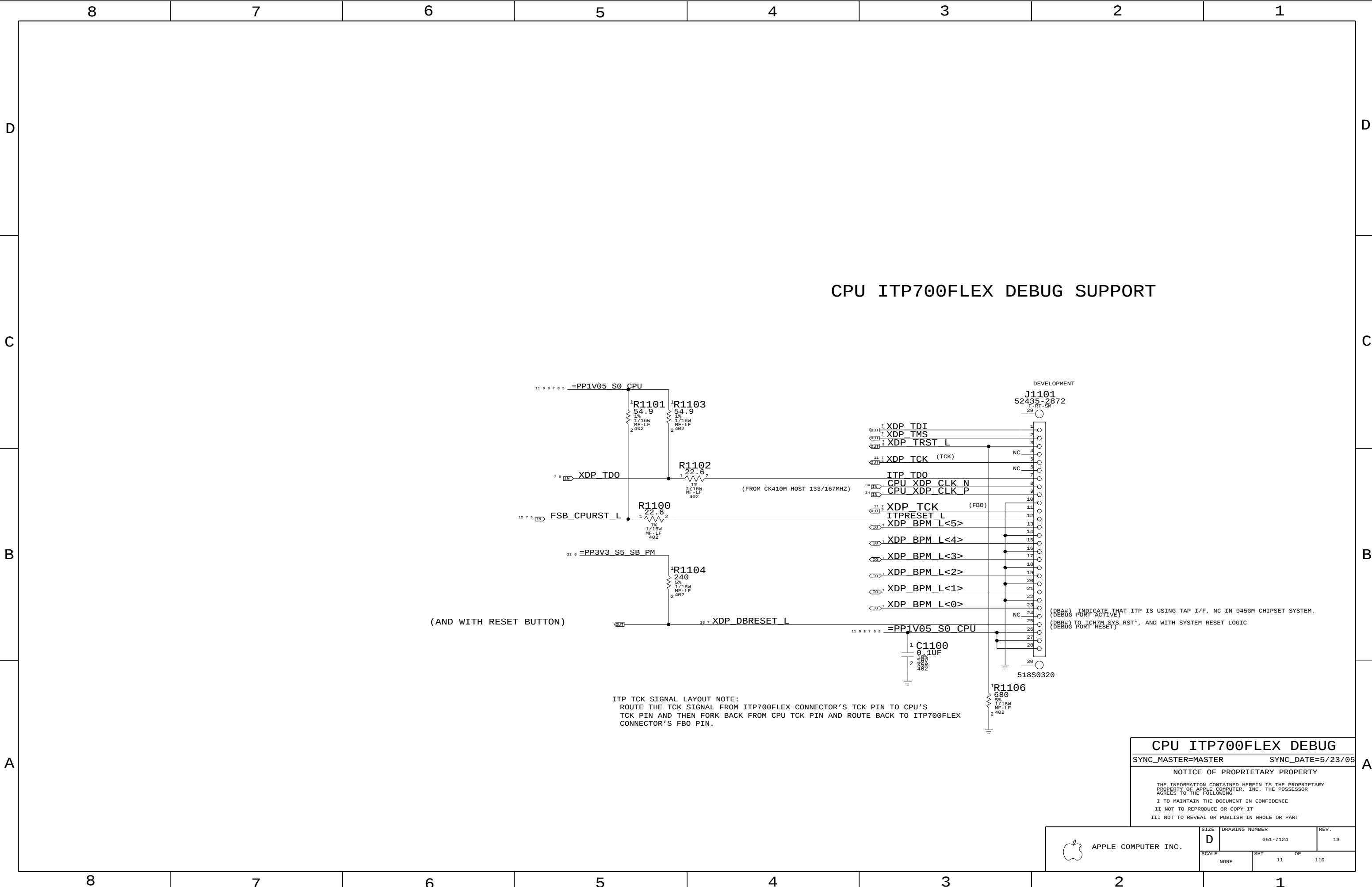
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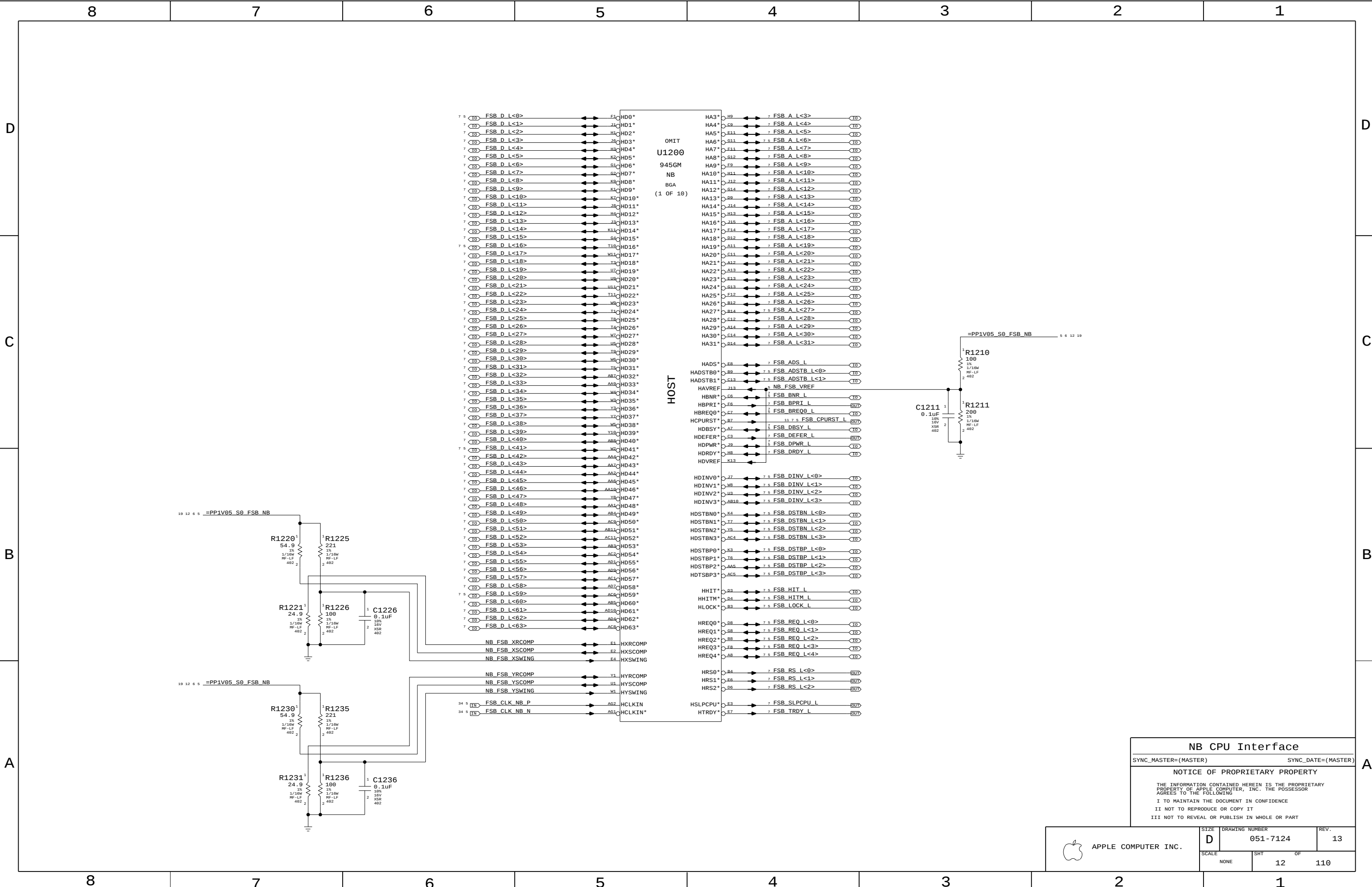
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NB CPU Interface

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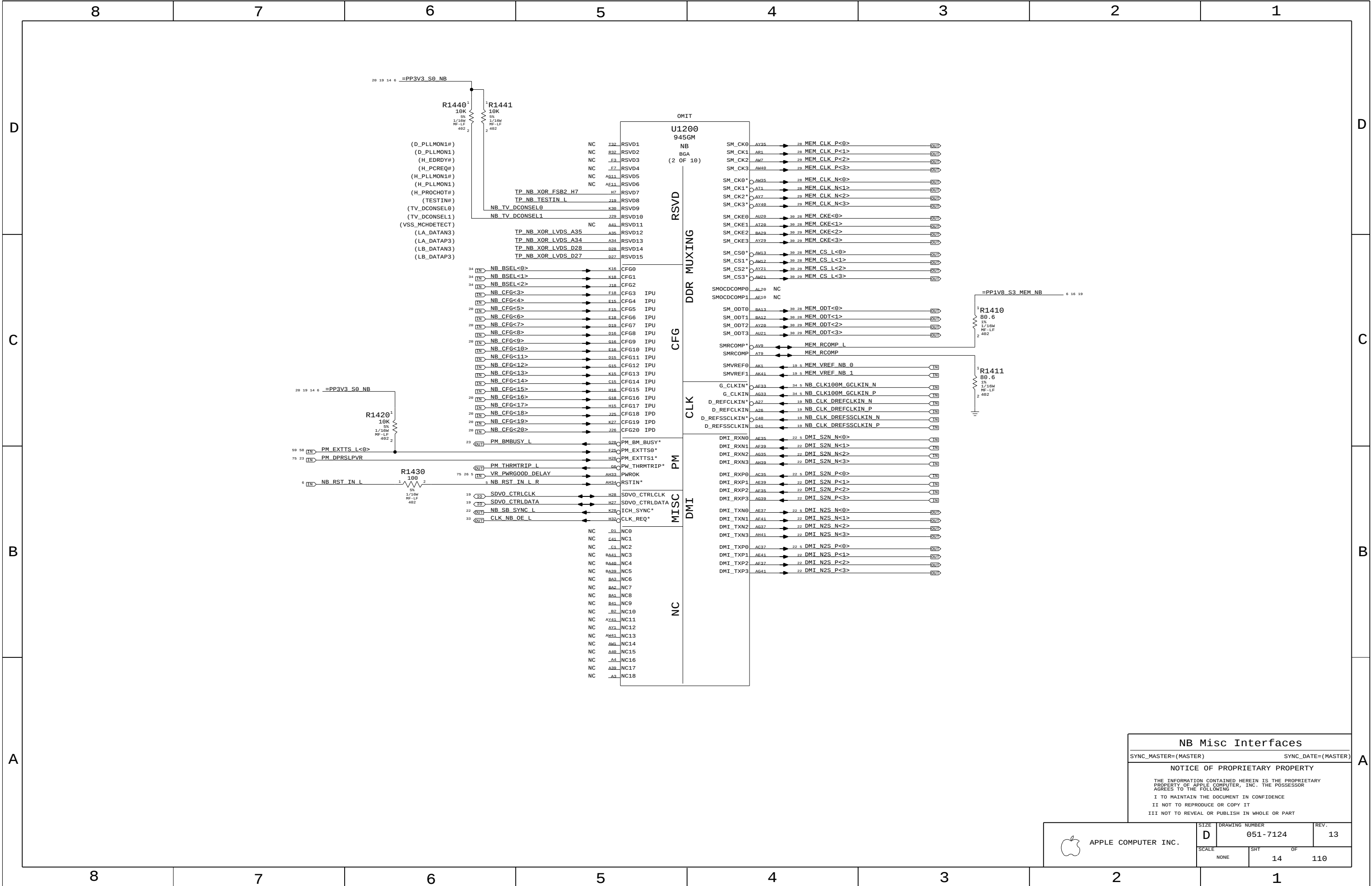
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NB Misc Interfaces

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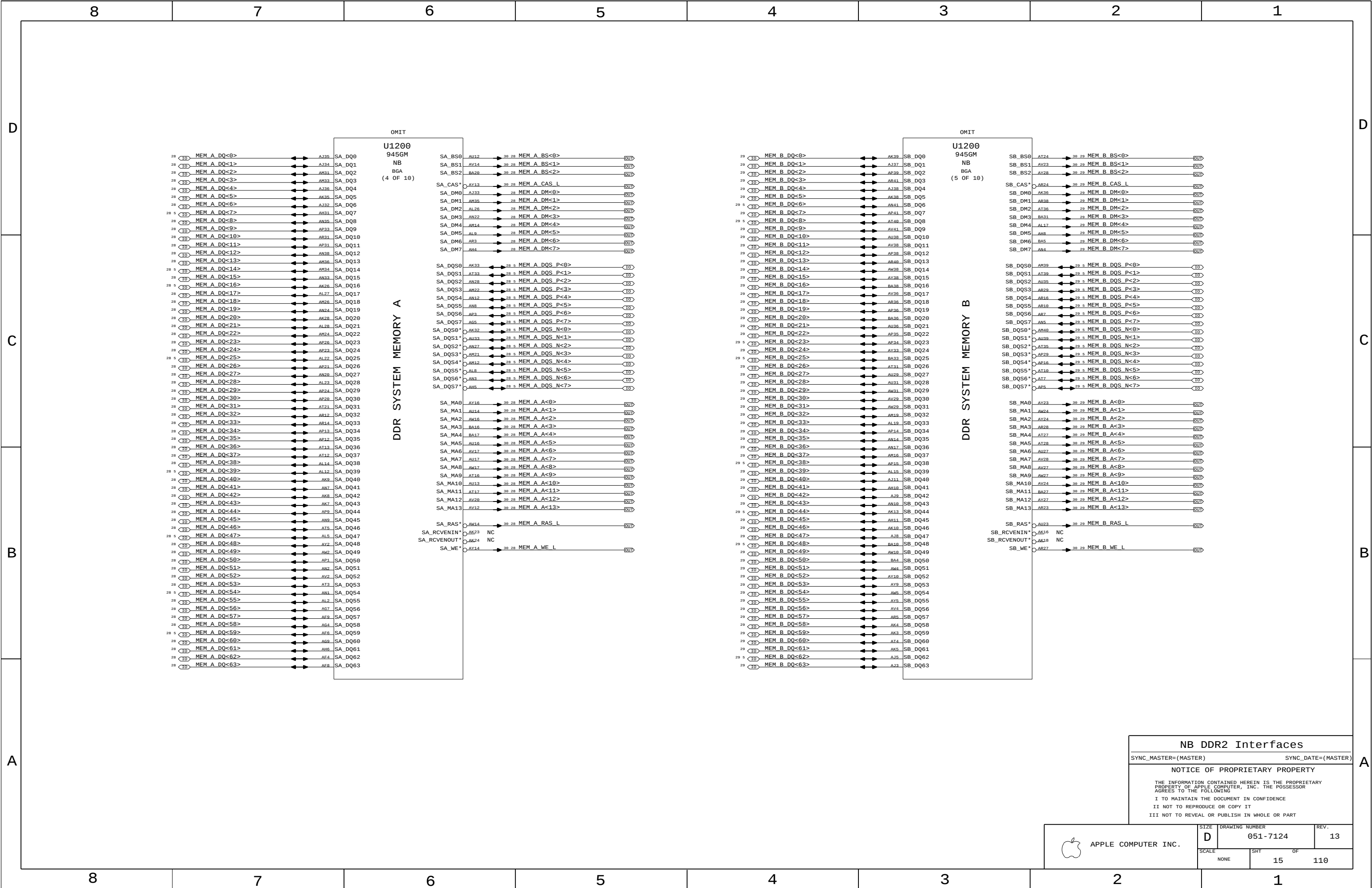
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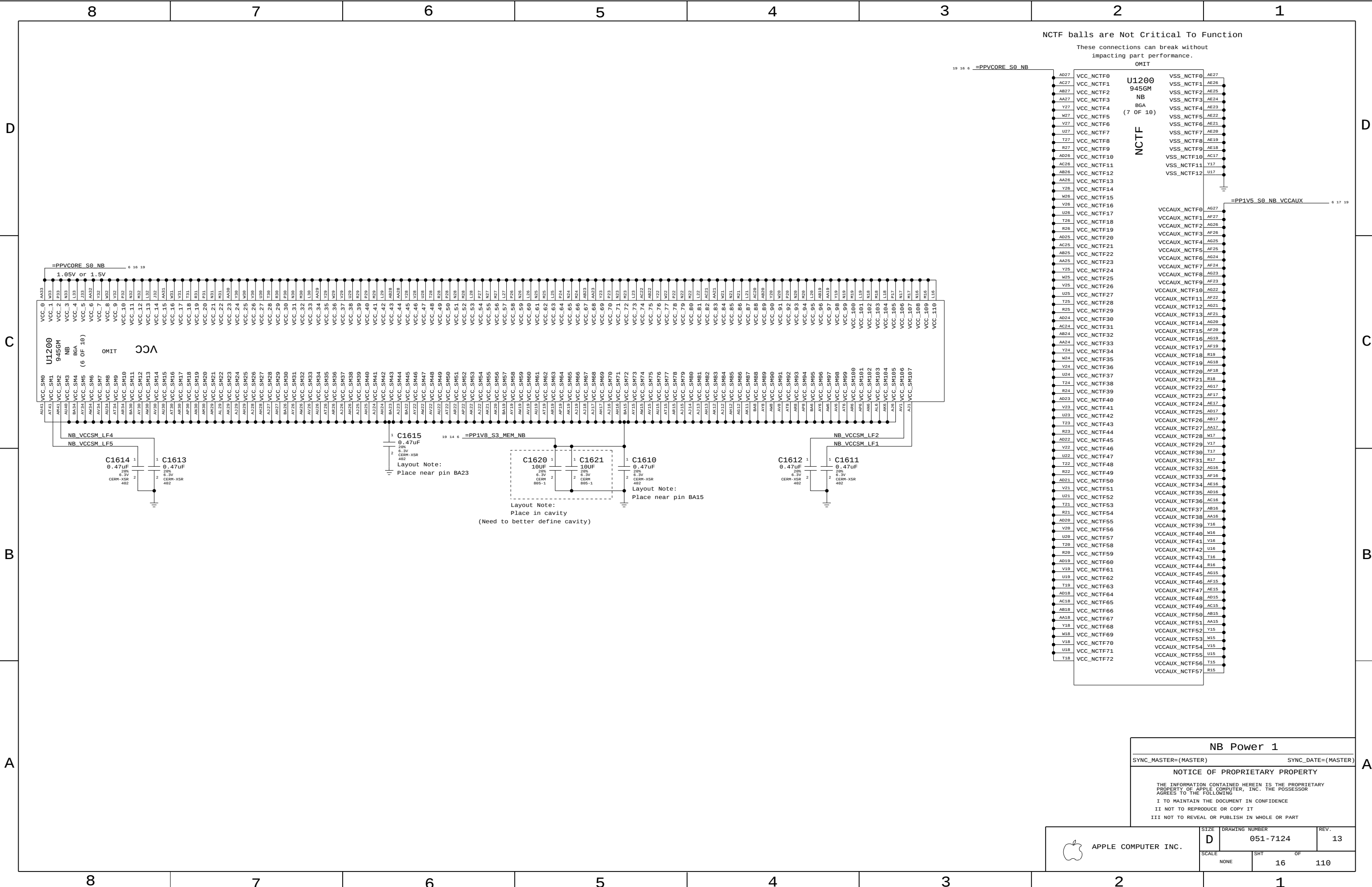
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NB Power 1

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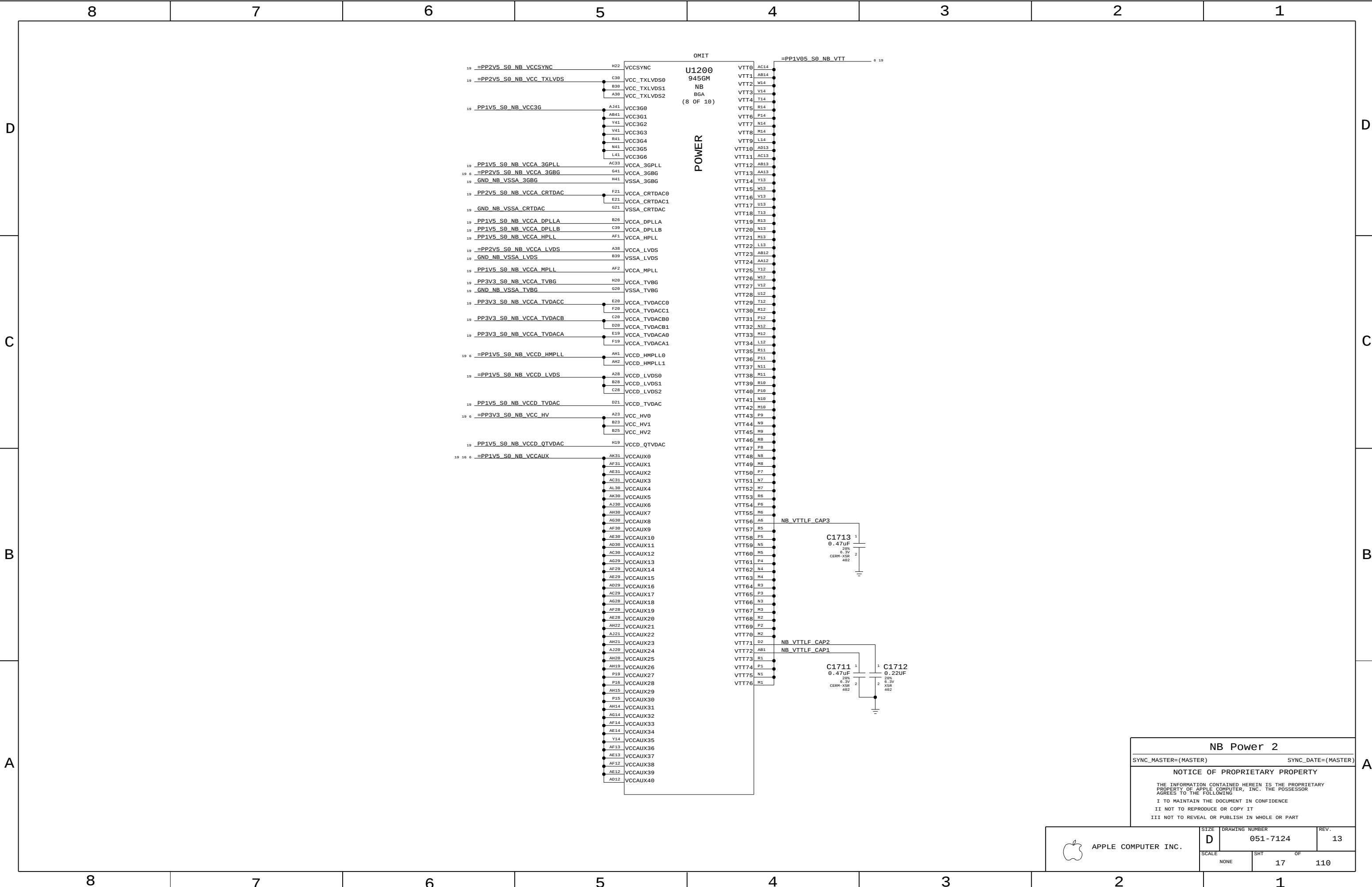
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	13
SCALE	NONE		16
	SHT		OF 110



NB Power 2

SYNC_MASTER=(MASTER)SYNC_DATE=(MASTER)

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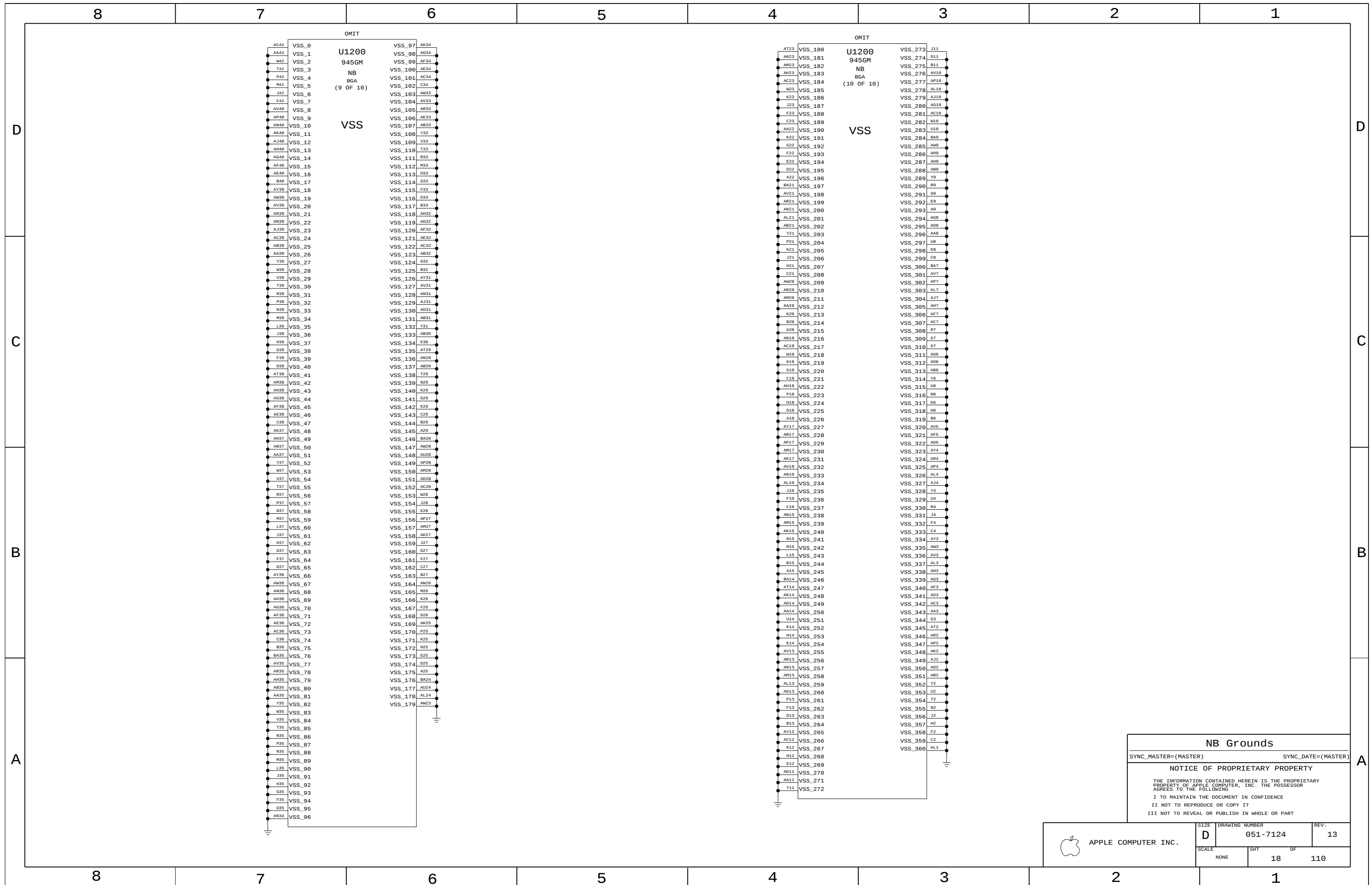
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

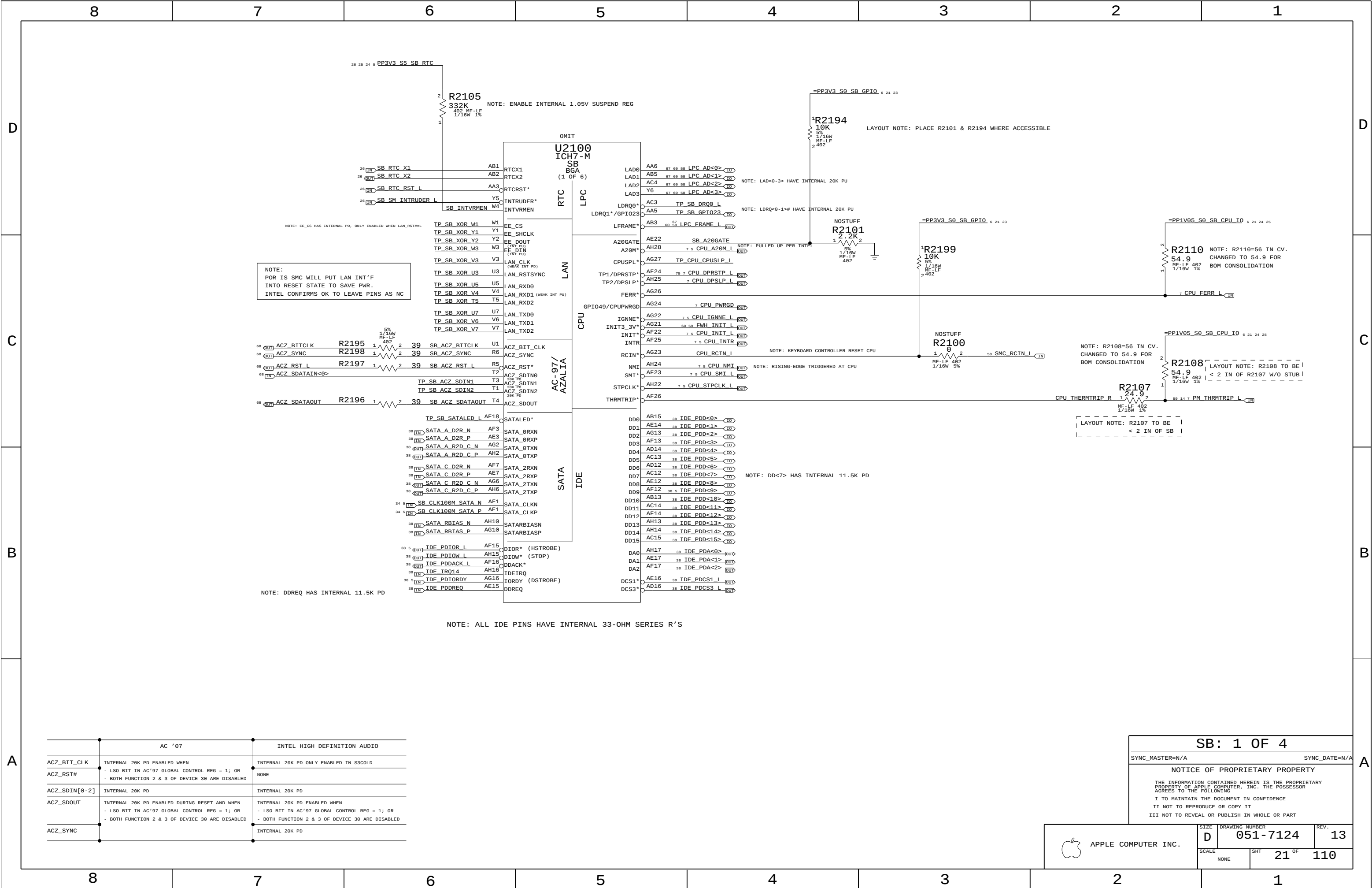
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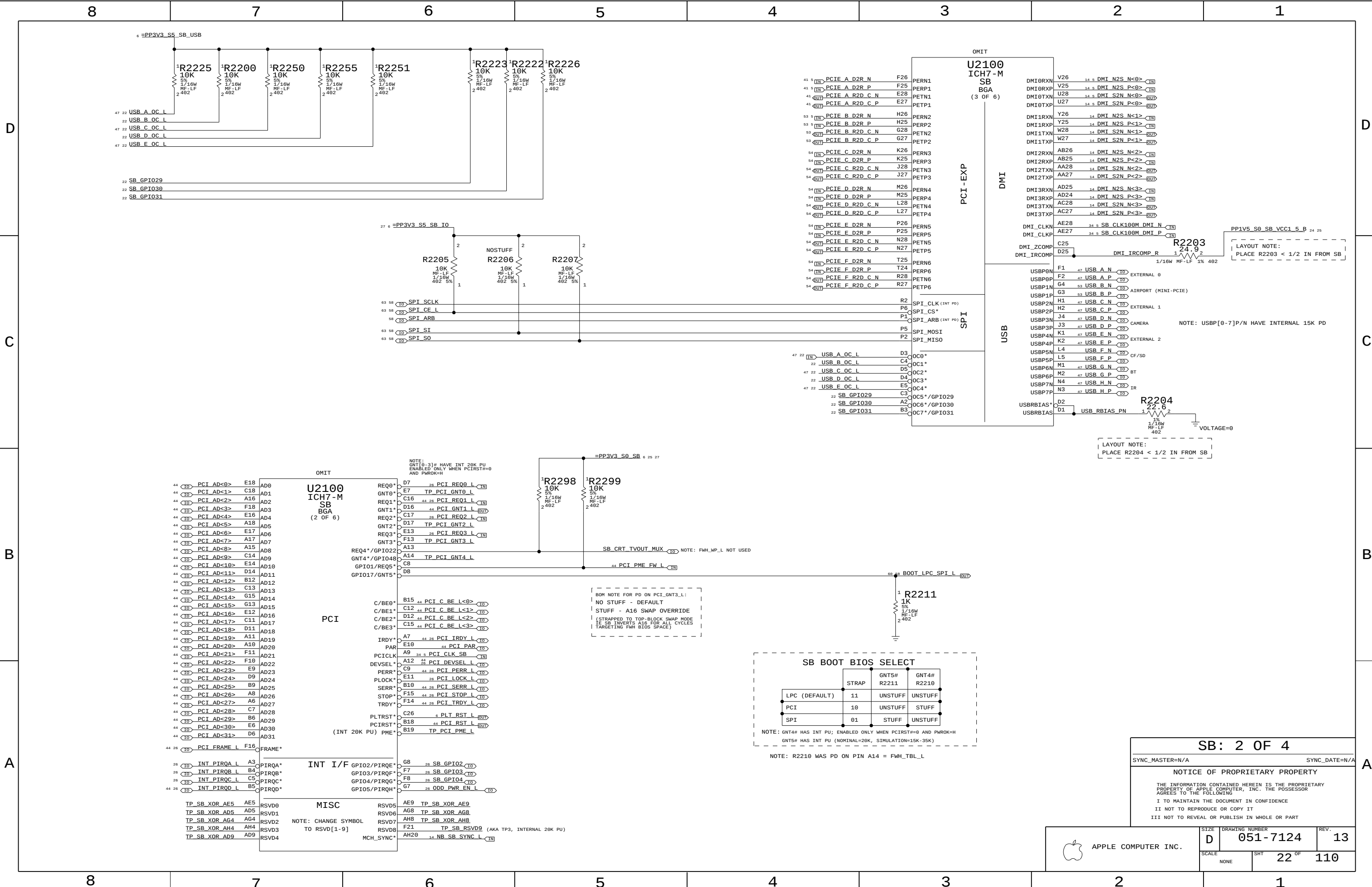
II NOT TO REPRODUCE OR COPY IT

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	13
SCALE		SHT	OF
NONE		17	110







SB BOOT BIOS SELECT			
	STRAP	GNT5# R2211	GNT4# R2210
LPC (DEFAULT)	11	UNSTUFF	UNSTUFF
PCI	10	UNSTUFF	STUFF
SPI	01	STUFF	UNSTUFF

NOTE: GNT4# HAS INT PU; ENABLED ONLY WHEN PCIRST#0 AND PWROK=H
GNT5# HAS INT PU (NOMINAL=29K, SIMULATION=15K-35K)
NOTE: R2210 WAS PD ON PIN A14 = FWH_TBL_L

SB: 2 OF 4

SYNC_MASTER=N/A

SYNC_DATE=N/A

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SCALE
NONE

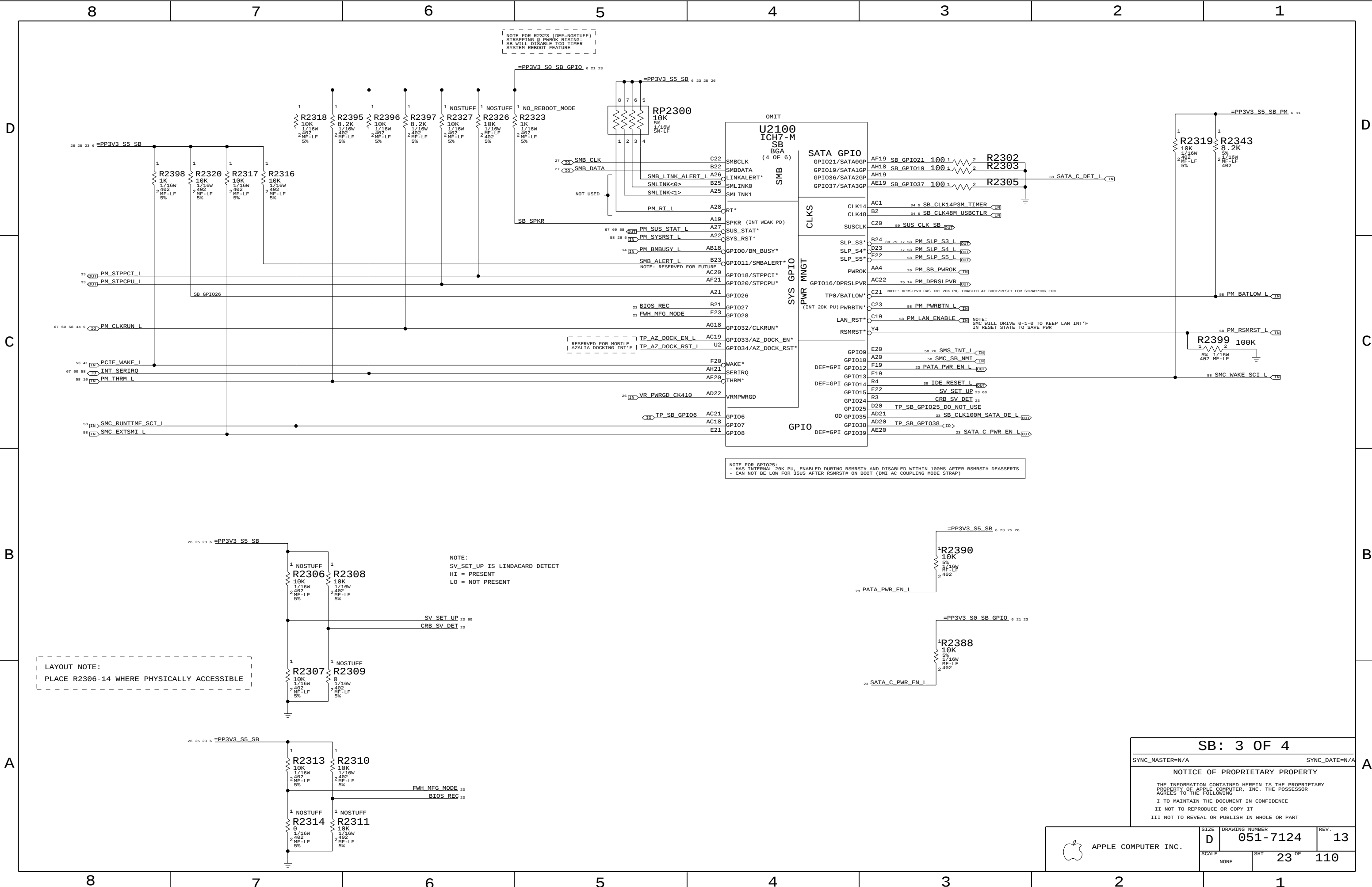
D

DRAWING NUMBER
051-7124

SHT
22 OF 110

13

REV.



NOTE FOR GPIO25:
- HAS INTERNAL 20K PU, ENABLED DURING RSMRST# AND DISABLED WITHIN 100MS AFTER RSMRST# DEASSERTS
- CAN NOT BE LOW FOR 35US AFTER RSMRST# ON BOOT (OWI AC COUPLING MODE STRAP)

NOTE:
SV_SET_UP IS LINDACARD DETECT
HI = PRESENT
LO = NOT PRESENT

LAYOUT NOTE:
PLACE R2306-14 WHERE PHYSICALLY ACCESSIBLE

SB: 3 OF 4

SYNC_MASTER=N/A

SYNC_DATE=N/A

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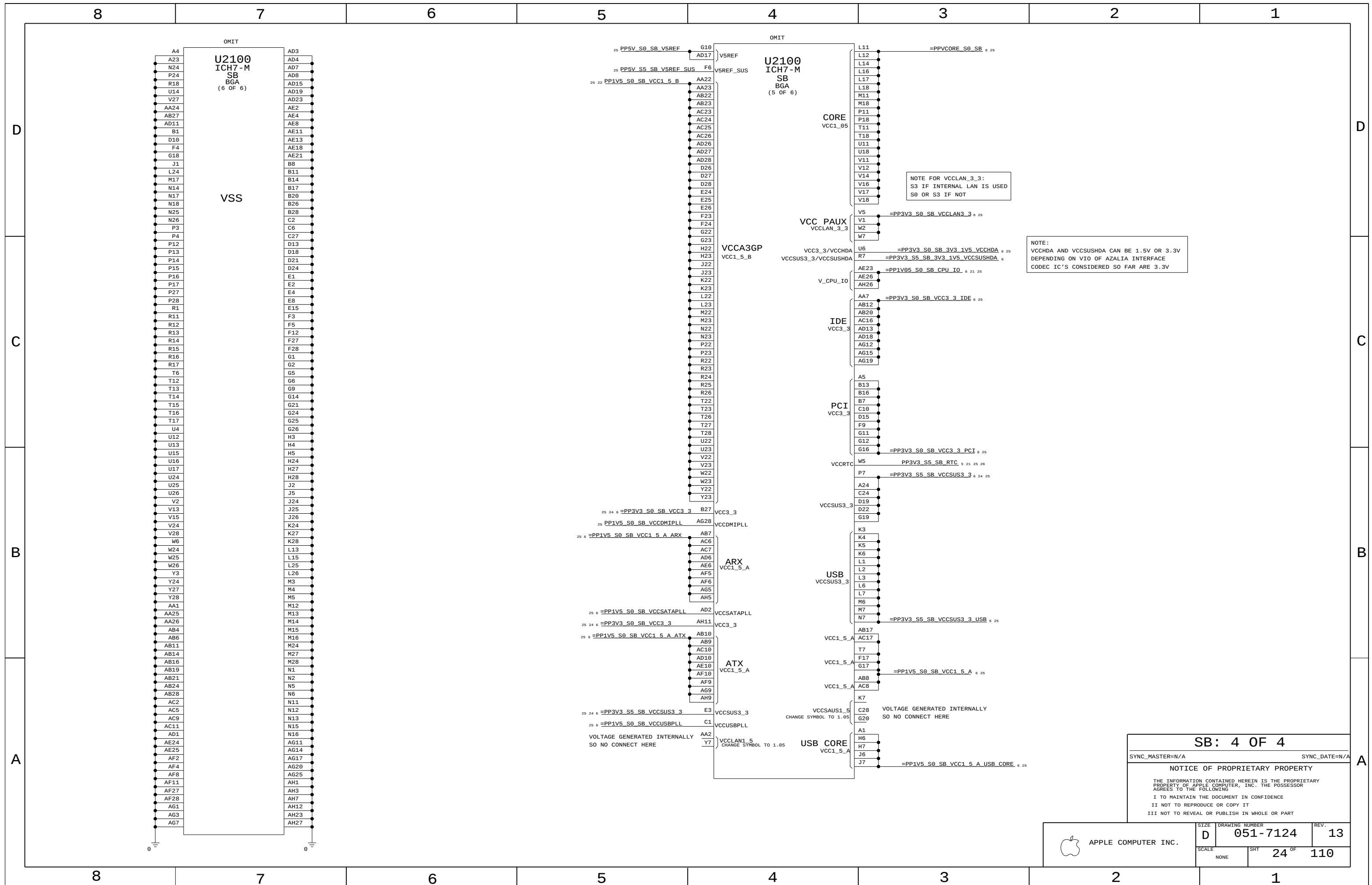
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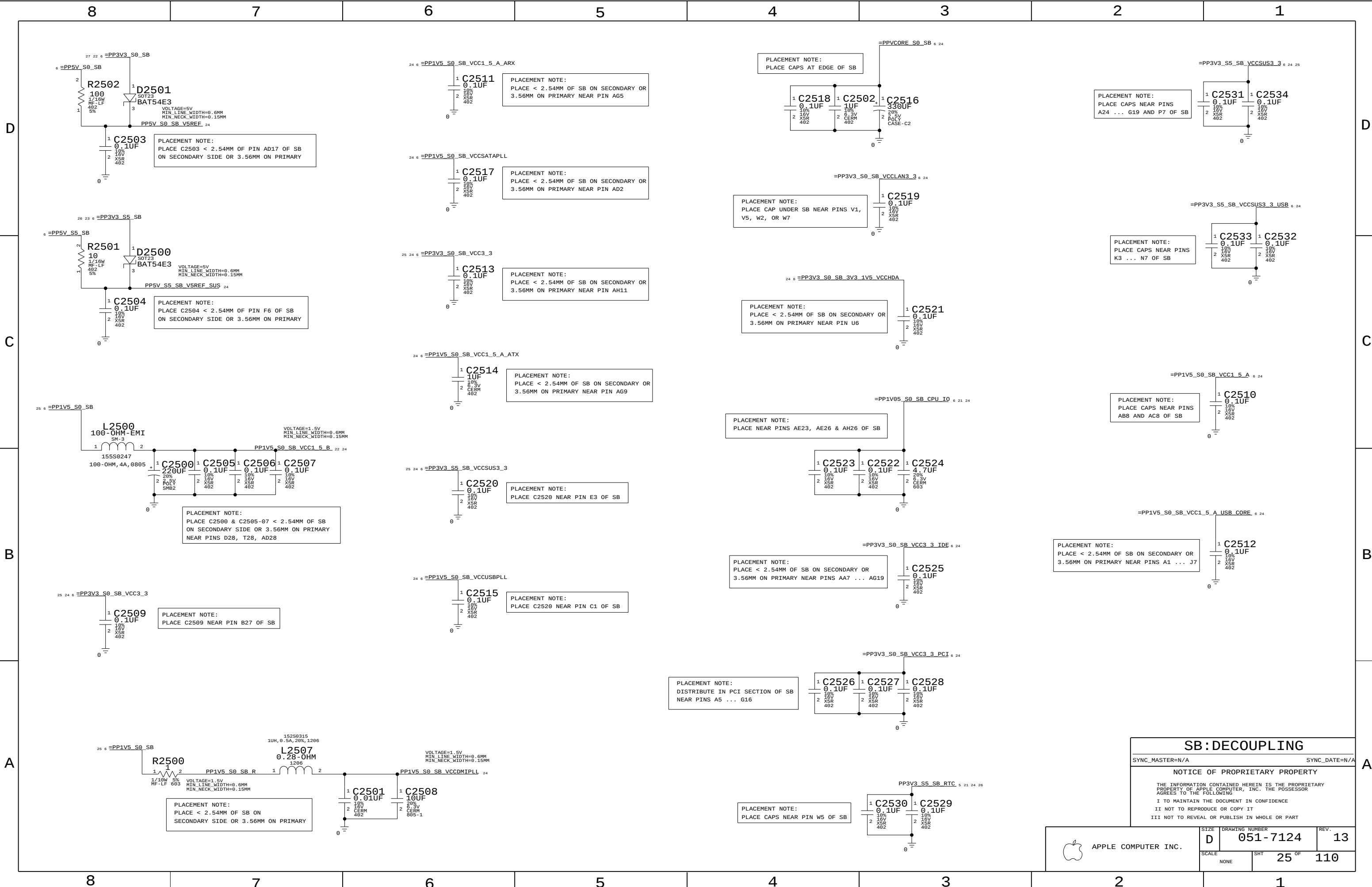
DRAWING NUMBER 051-7124

REV. 13

SCALE NONE

SHT 23 OF 110





SB: DECOUPLING

SYNC_MASTER=N/A

SYNC_DATE=N/A

NOTICE OF PROPRIETARY PROPERTY

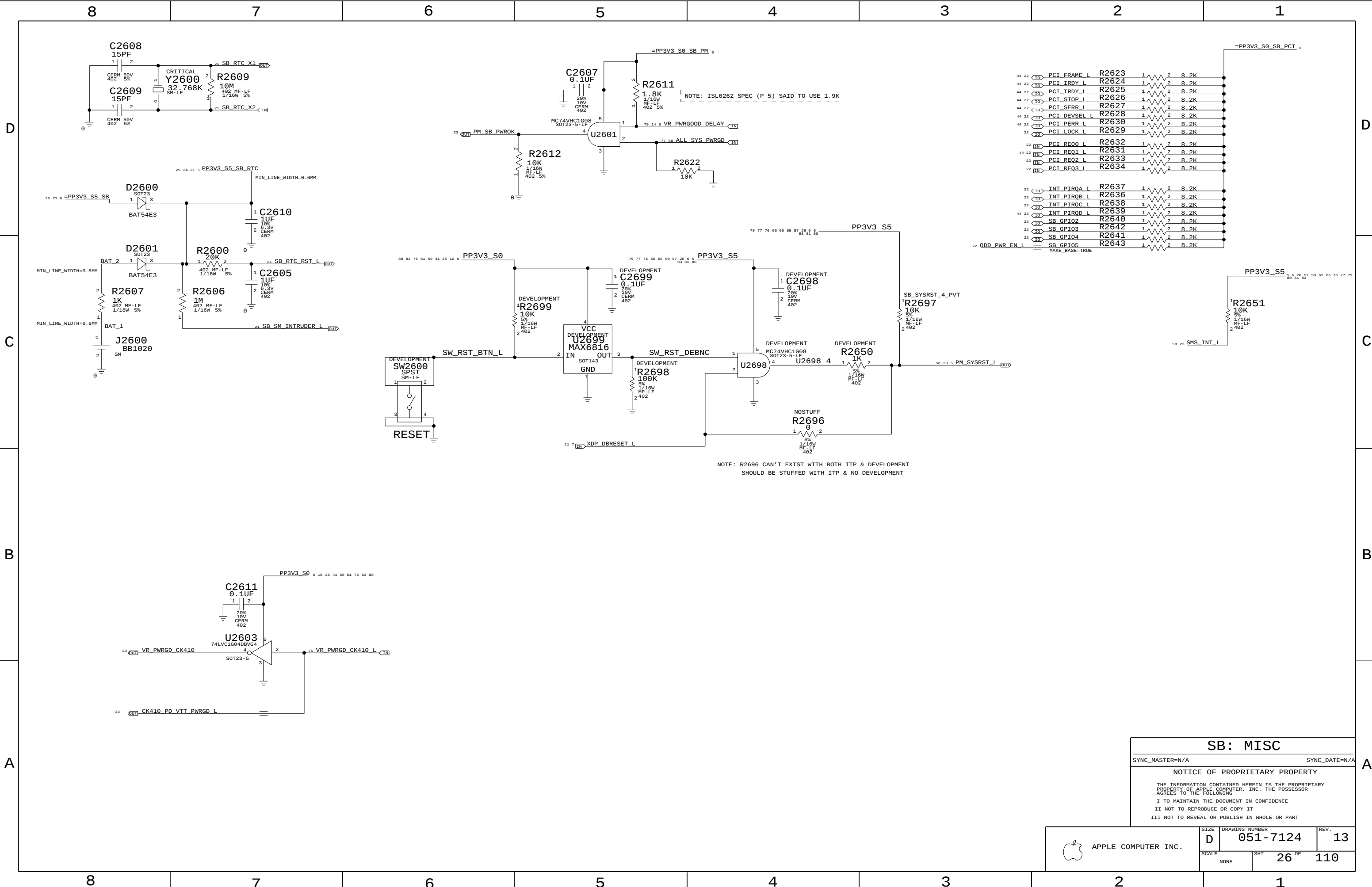
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	D	051-7124	13
SCALE		SHT	25 OF 110
NONE			



SB: MISC

SYNC_MASTER=N/A SYNC_DATE=N/A

NOTICE OF PROPRIETARY PROPERTY

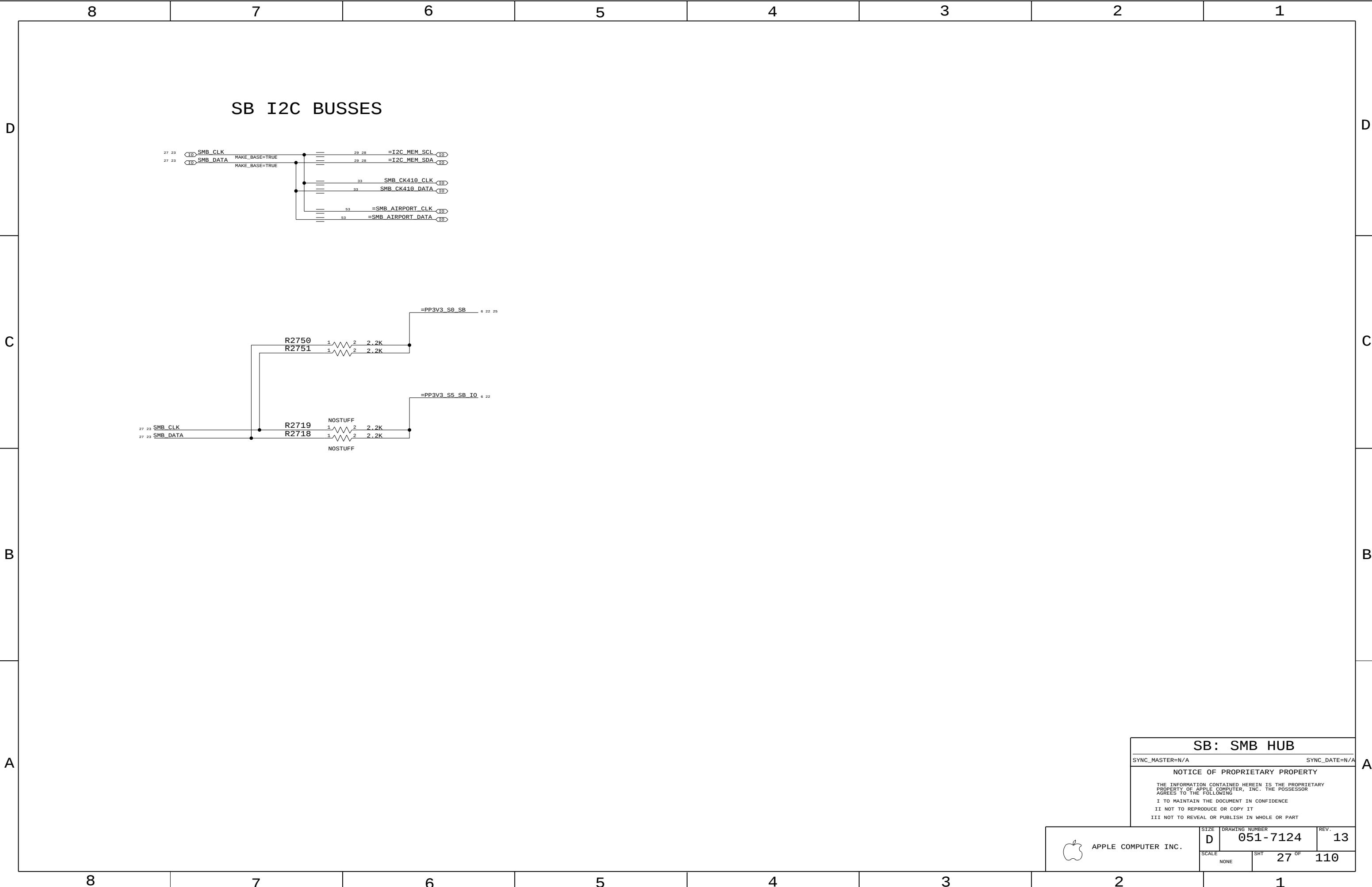
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

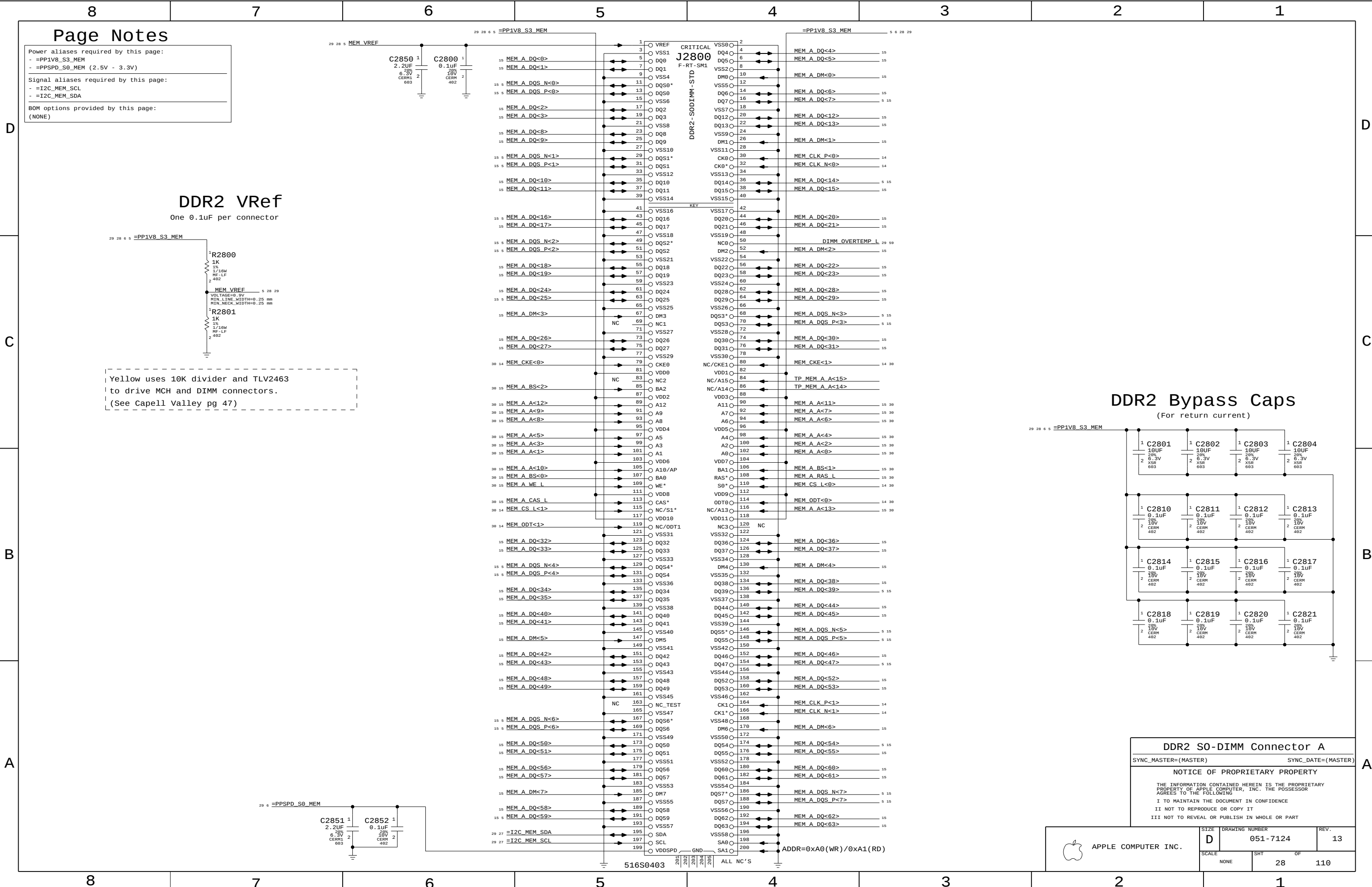
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	13
SCALE		SHT	26 OF 110
NONE			





Page Notes

Power aliases required by this page:

- =PP1V8_S3_MEM
- =PPSPD_S0_MEM (2.5V - 3.3V)

Signal aliases required by this page:

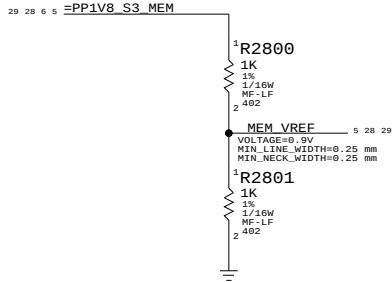
- =I2C_MEM_SCL
- =I2C_MEM_SDA

BOM options provided by this page:

(NONE)

DDR2 Vref

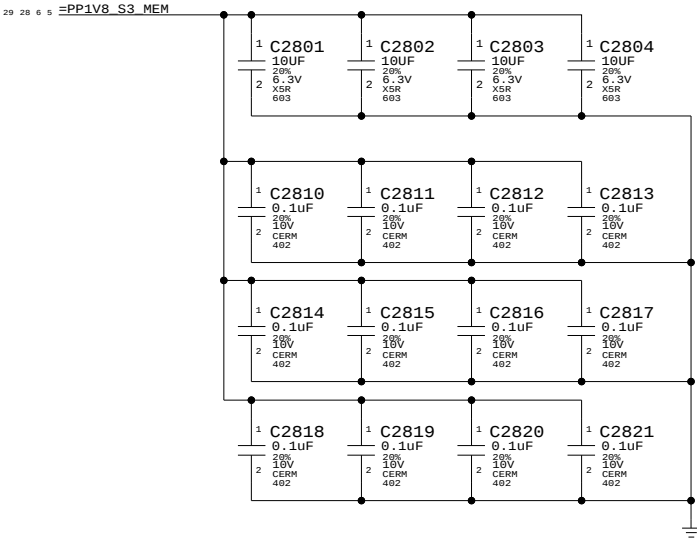
One 0.1uF per connector



Yellow uses 10K divider and TLV2463 to drive MCH and DIMM connectors.
(See Capell Valley pg 47)

DDR2 Bypass Caps

(For return current)



DDR2 S0-DIMM Connector A

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7124	REV. 13
	SCALE NONE	SHT 28	OF 110

Power aliases required by this page:

- =PP1V8_S3_MEM
- =PPSPD_S0_MEM (2.5V ~ 3.3V)

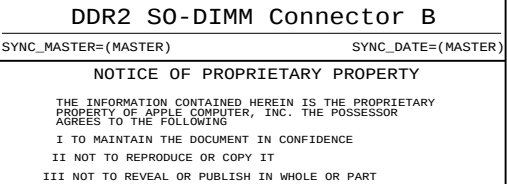
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- =I2C_MEM_SCL
- =I2C_MEM_SDA

BOM options provided by this page:

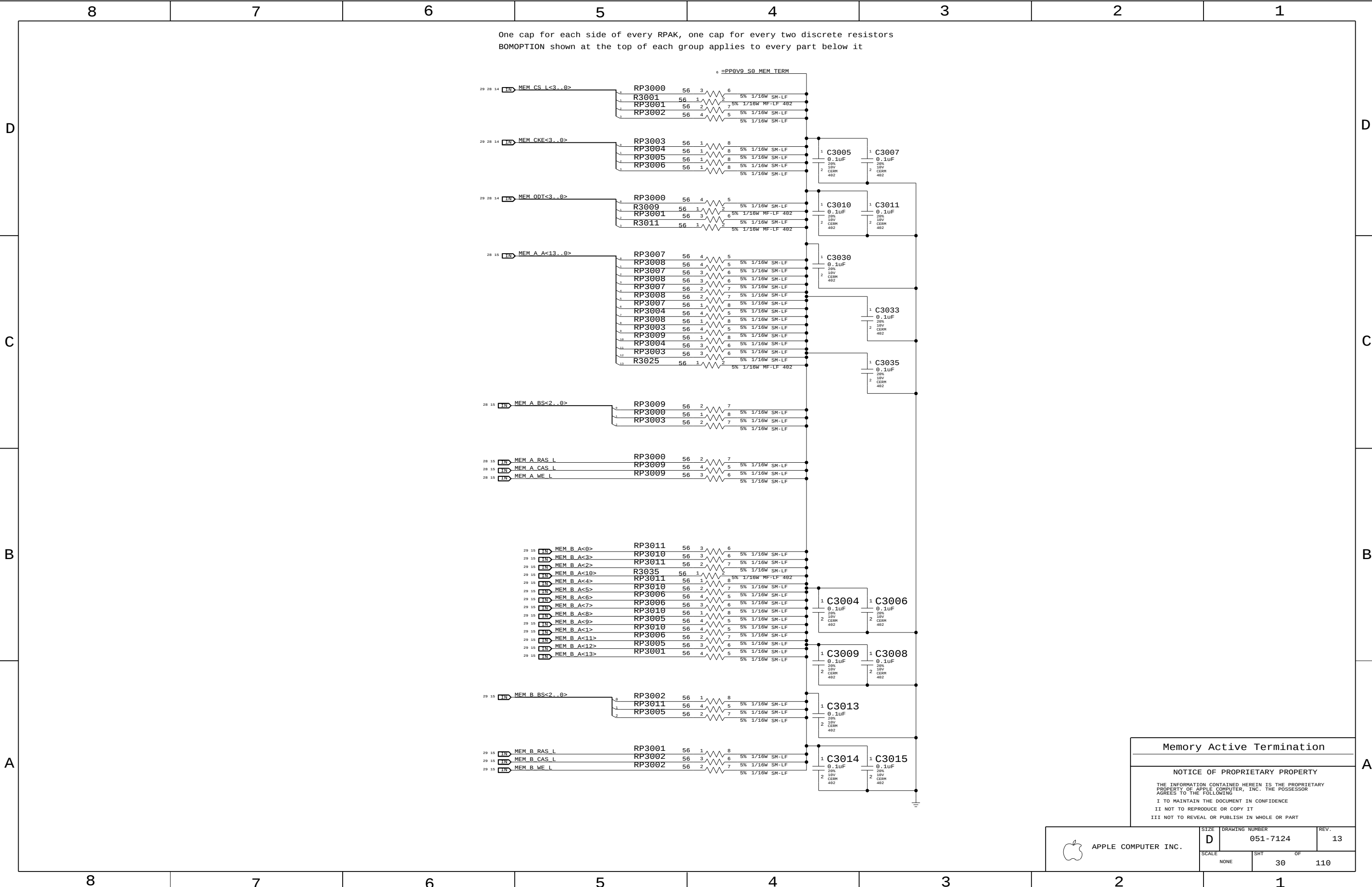
(NONE)

NOTE: This page does not supply VREF.
The reference voltage must be provided
by another page.



APPLE COMPUTER INC.

SIZE D	DRAWING NUMBER 051-7124	REV. 13
SCALE NONE	SHT 29	OF 110



Memory Active Termination

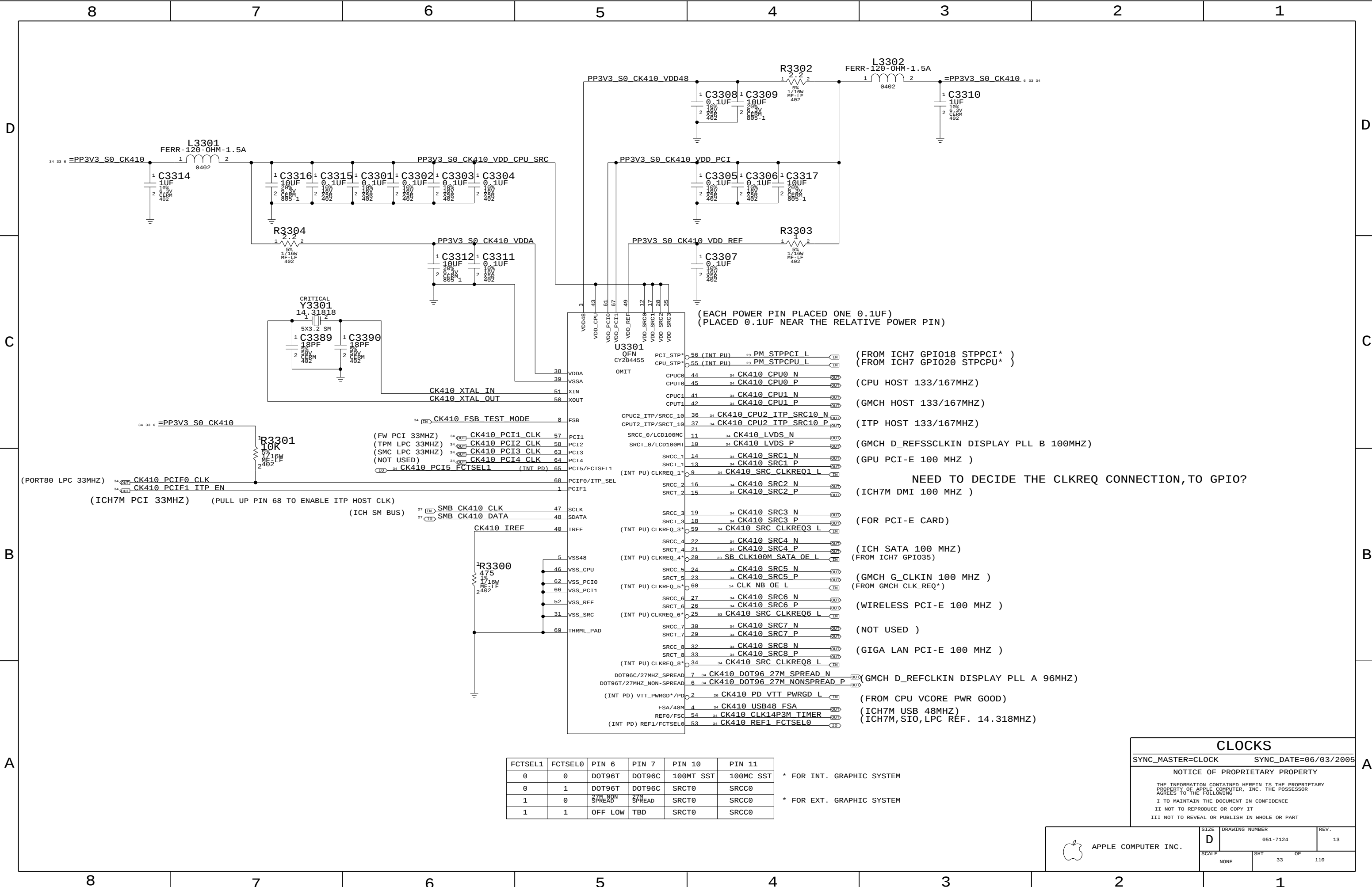
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(EACH POWER PIN PLACED ONE 0.1UF)
(PLACED 0.1UF NEAR THE RELATIVE POWER PIN)

(FROM ICH7 GPIO18 STPPCI*)
(FROM ICH7 GPIO20 STPCPU*)

(CPU HOST 133/167MHZ)

(GMCH HOST 133/167MHZ)

(ITP HOST 133/167MHZ)

(GMCH D_REFSSCLKIN DISPLAY PLL B 100MHZ)

(GPU PCI-E 100 MHZ)

NEED TO DECIDE THE CLKREQ CONNECTION, TO GPIO?

(ICH7M DMI 100 MHZ)

(FOR PCI-E CARD)

(ICH SATA 100 MHZ)
(FROM ICH7 GPIO35)

(GMCH G_CLKIN 100 MHZ)
(FROM GMCH CLK_REQ*)

(WIRELESS PCI-E 100 MHZ)

(NOT USED)

(GIGA LAN PCI-E 100 MHZ)

(GMCH D_REFCLKIN DISPLAY PLL A 96MHZ)

(FROM CPU VCORE PWR GOOD)

(ICH7M USB 48MHZ)
(ICH7M, SIO, LPC REF. 14.318MHZ)

FCTSEL1	FCTSEL0	PIN 6	PIN 7	PIN 10	PIN 11
0	0	DOT96T	DOT96C	100MT_SST	100MC_SST
0	1	DOT96T	DOT96C	SRCT0	SRCC0
1	0	27M NON SPREAD	27M SPREAD	SRCT0	SRCC0
1	1	OFF LOW	TBD	SRCT0	SRCC0

* FOR INT. GRAPHIC SYSTEM

* FOR EXT. GRAPHIC SYSTEM

CLOCKS

SYNC_MASTER=CLOCK

SYNC_DATE=06/03/2005

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APPLE COMPUTER INC.

SIZE

D

DRAWING NUMBER

051-7124

REV.

13

SCALE

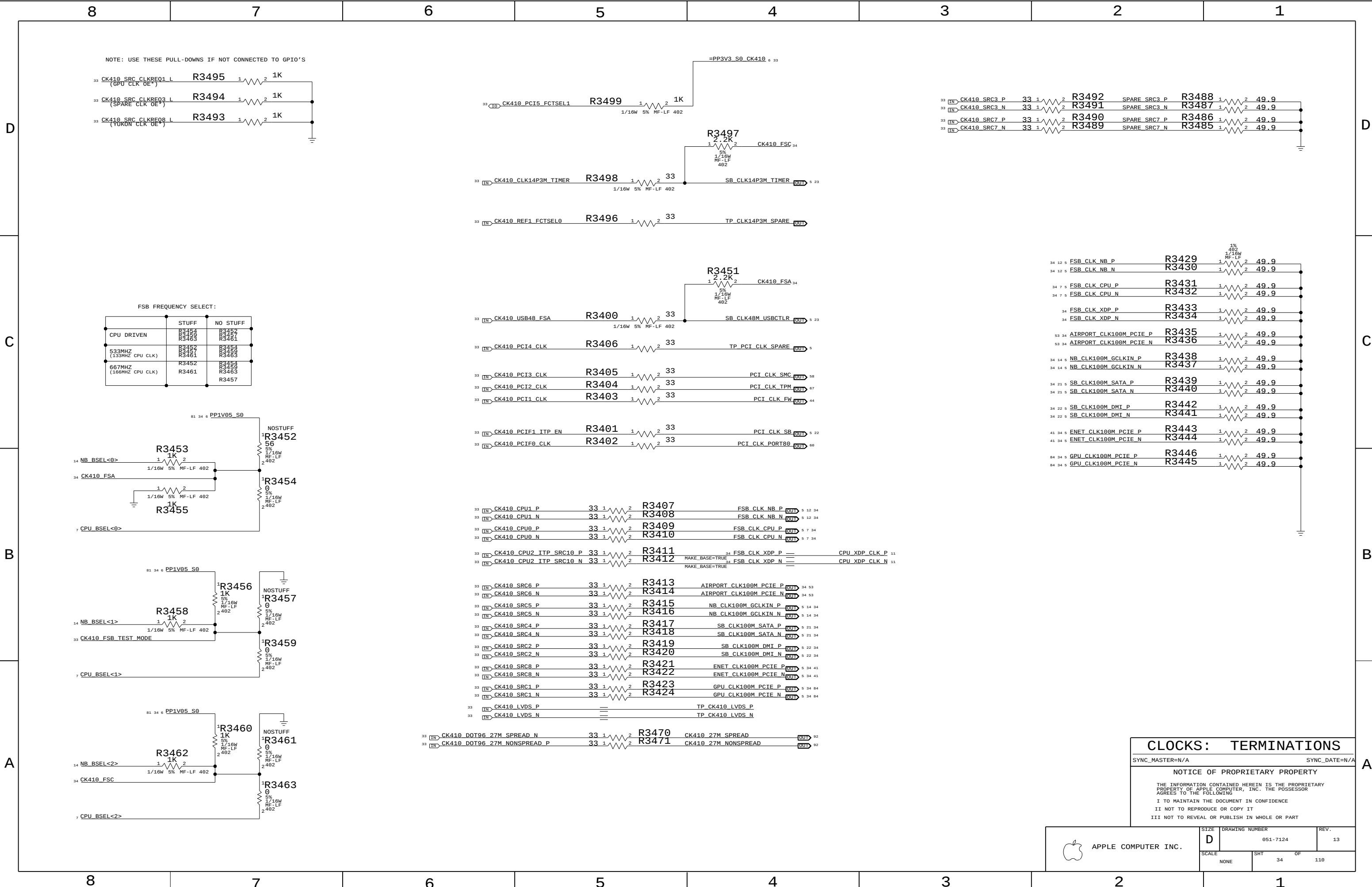
NONE

SHT

33

OF

110



CLOCKS: TERMINATIONS

SYNC_MASTER=N/A SYNC_DATE=N/A

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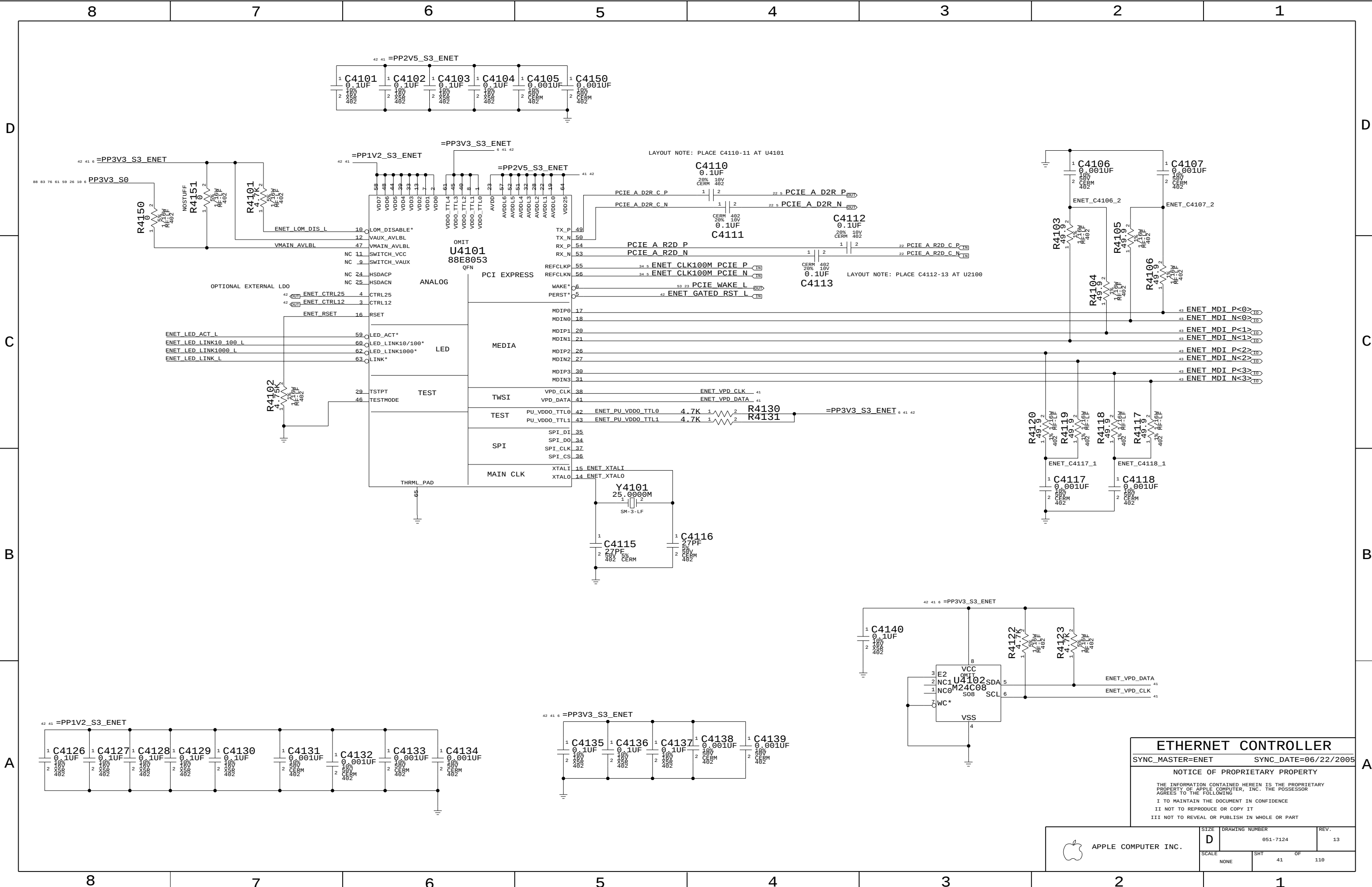
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APPLE COMPUTER INC.

SIZE D DRAWING NUMBER 051-7124 REV. 13

SCALE NONE SHT 34 OF 110



ETHERNET CONTROLLER
SYNC_MASTER=ENET SYNC_DATE=06/22/2005

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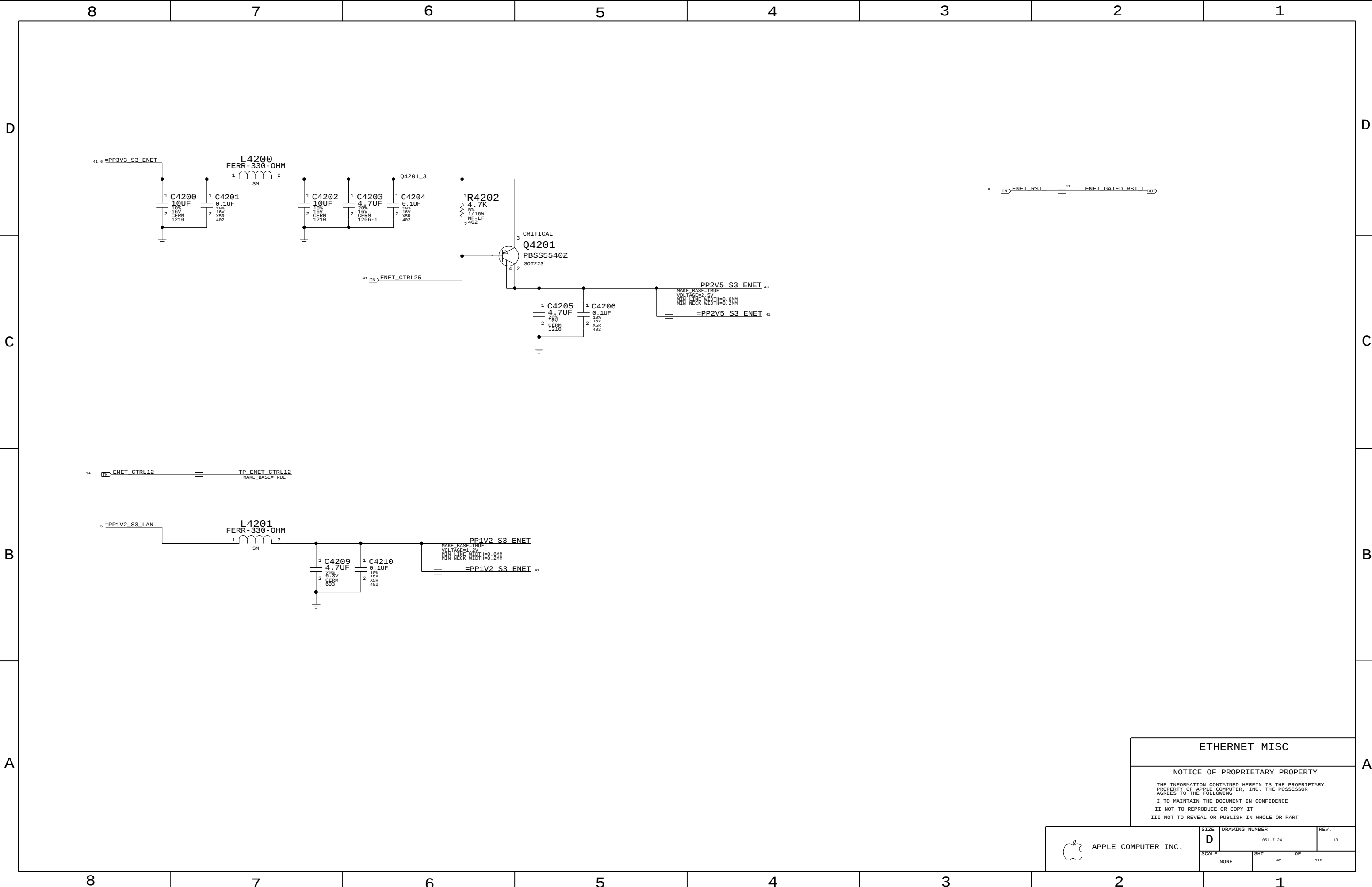
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SIZE D	DRAWING NUMBER 051-7124	REV. 13
	SCALE NONE	SHT 41

APPLE COMPUTER INC.



ETHERNET MISC

NOTICE OF PROPRIETARY PROPERTY

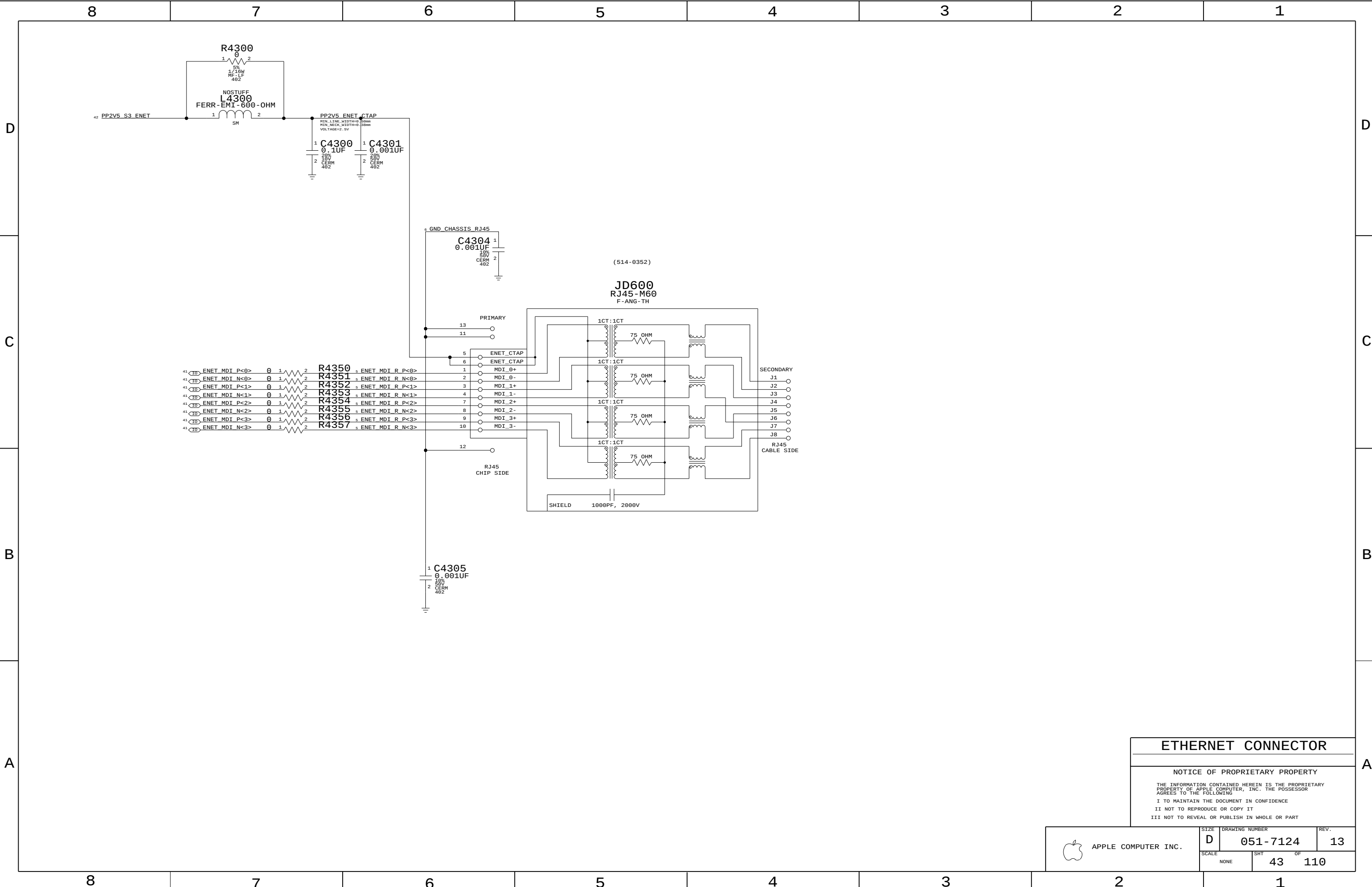
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	D	001-7124	13
SCALE		SHT	OF
NONE		42	110



ETHERNET CONNECTOR

NOTICE OF PROPRIETARY PROPERTY

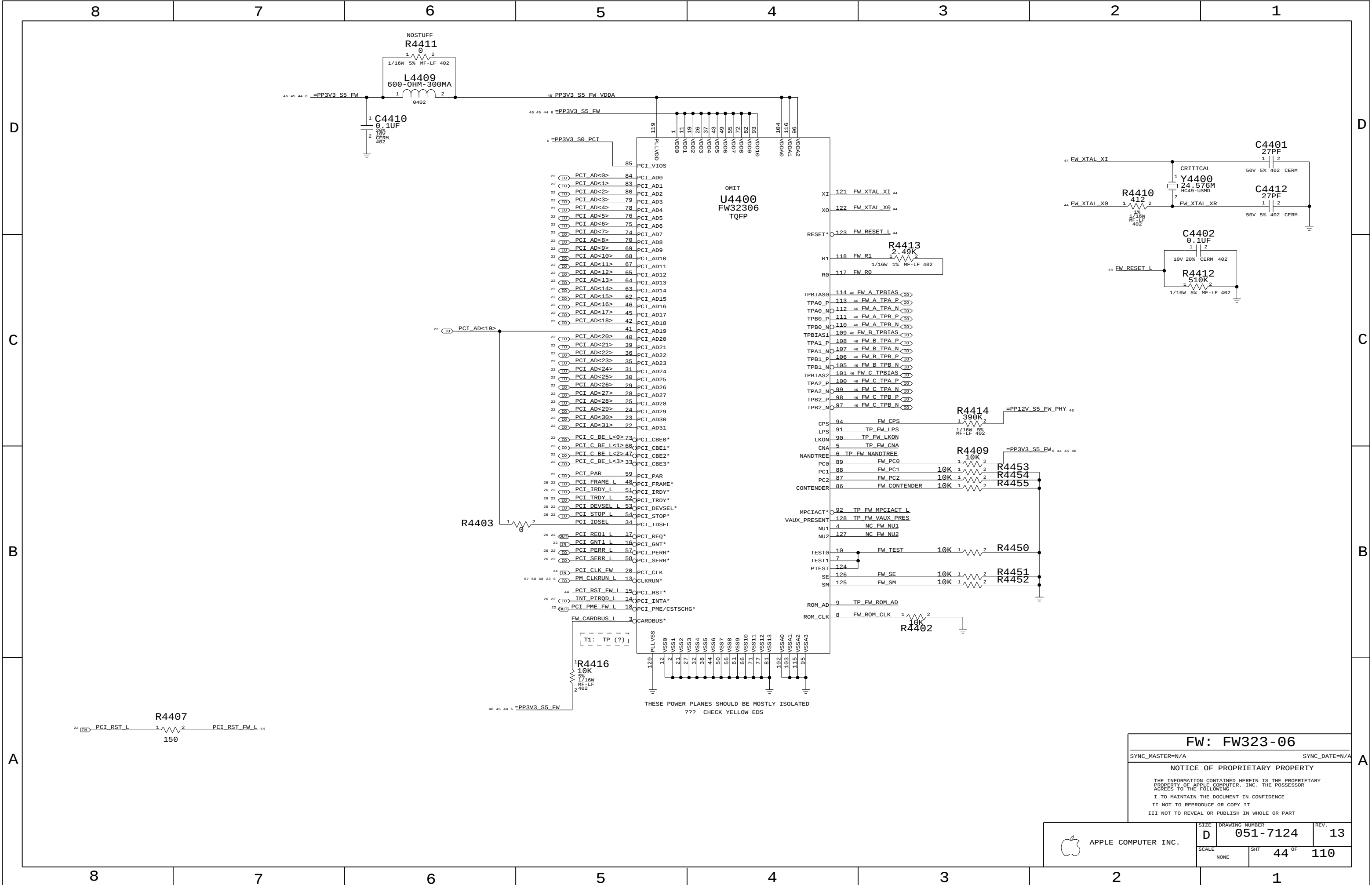
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER		REV.
	D	051-7124		13
SCALE		SHT	OF	
NONE		43	110	



FW: FW323-06

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SYNC_DATE=N/A

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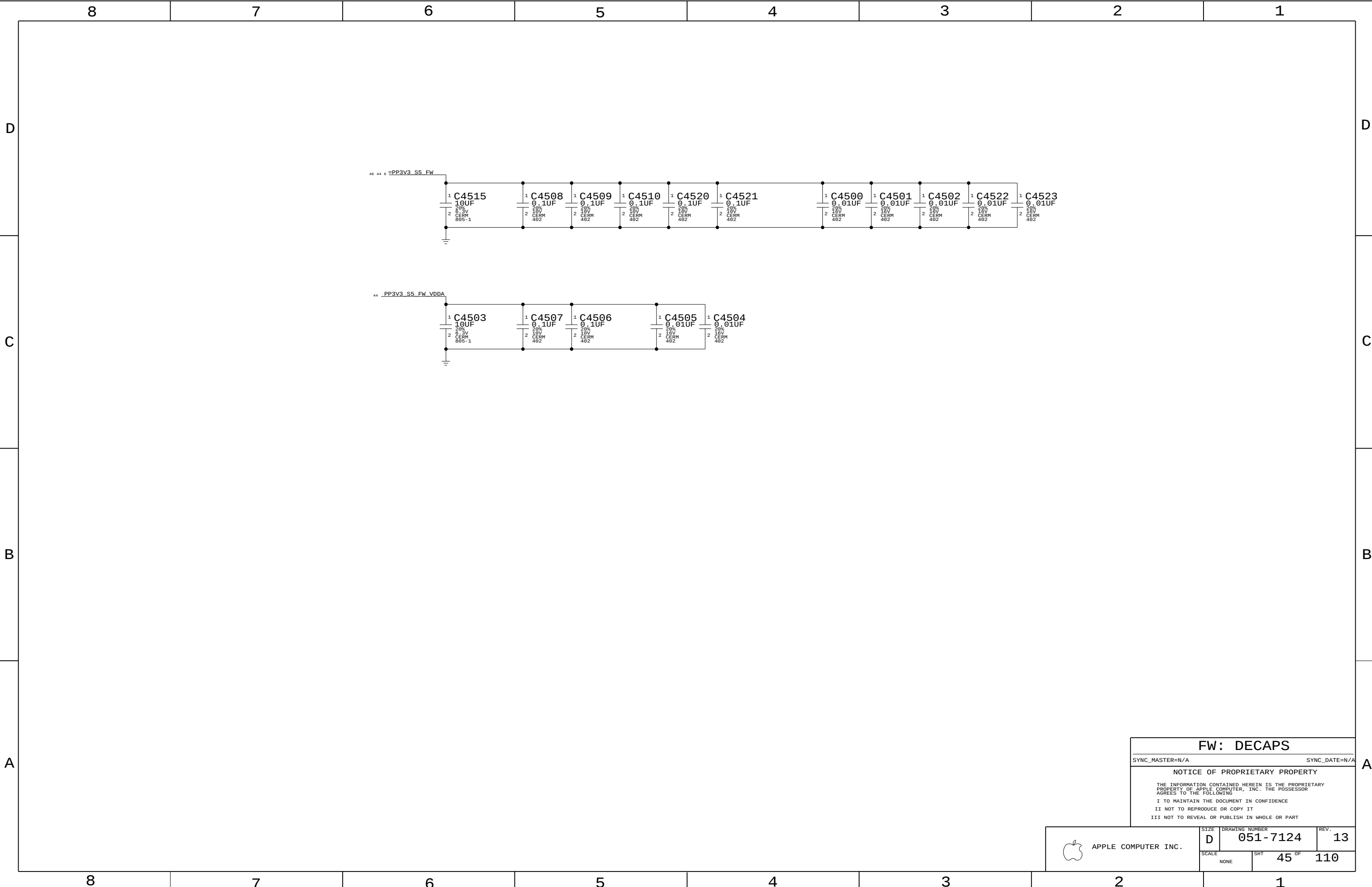
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	D	051-7124	13
SCALE		SHT	44 OF 110
NONE			



FW: DECAPS

SYNC_MASTER=N/A

SYNC_DATE=N/A

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APPLE COMPUTER INC.

SIZE

D

DRAWING NUMBER

051-7124

REV.

13

SCALE

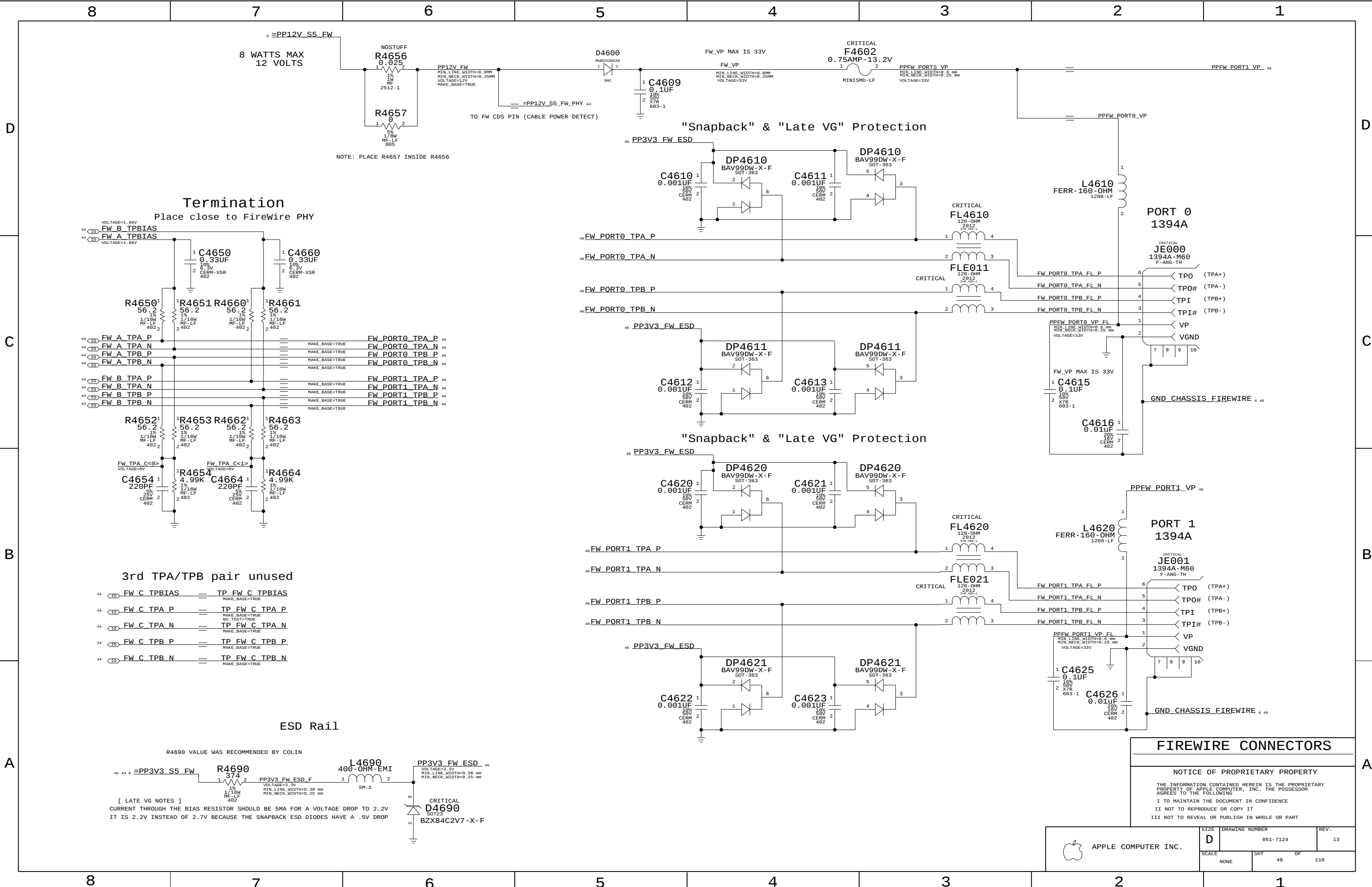
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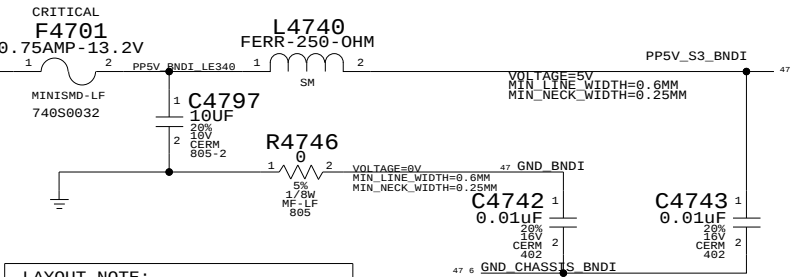
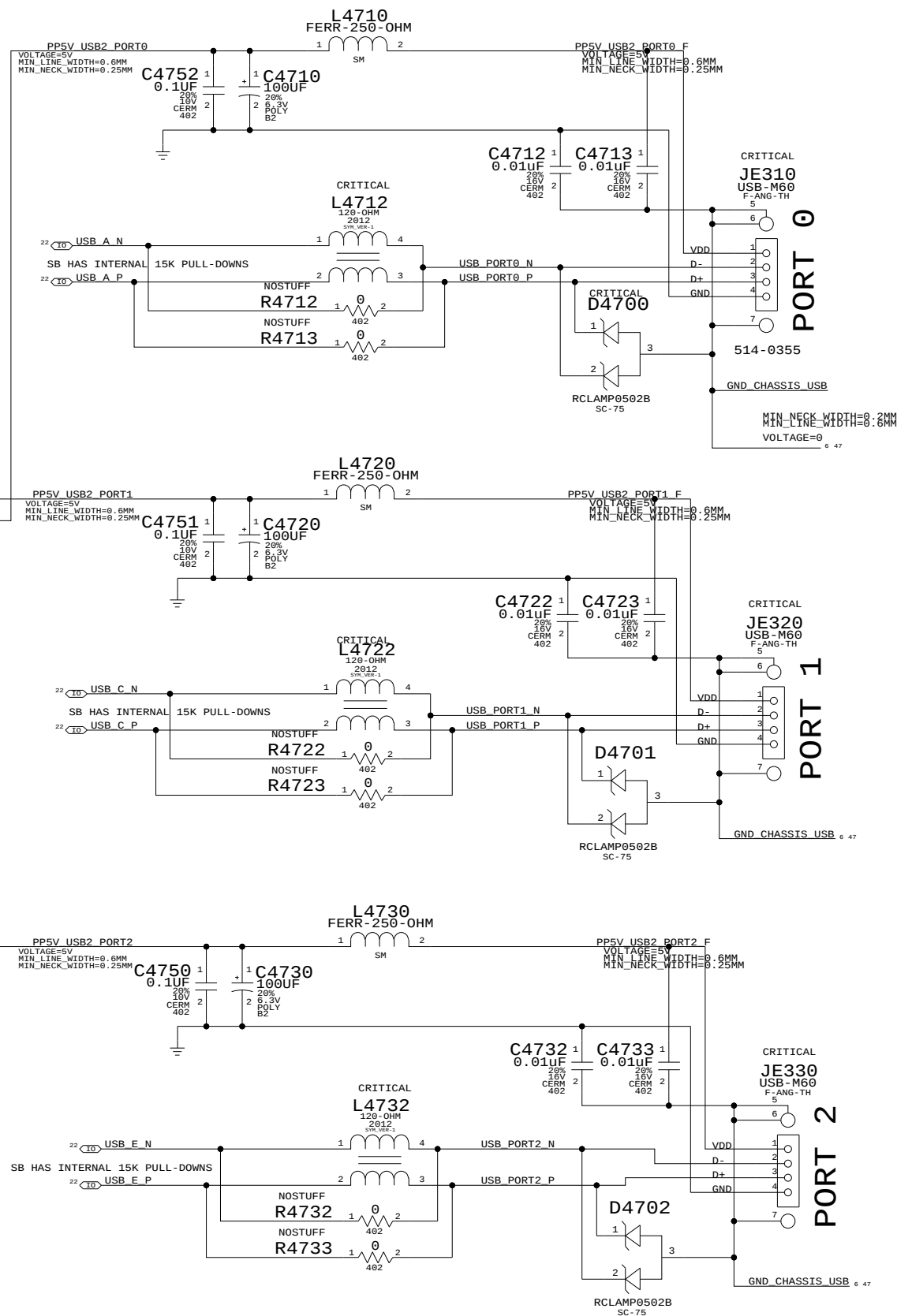
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OF

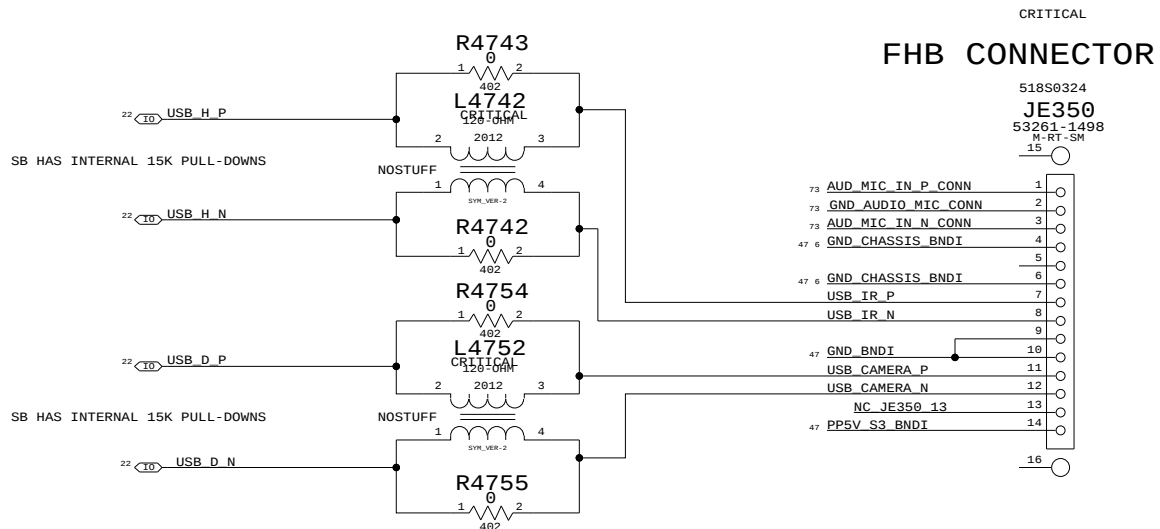
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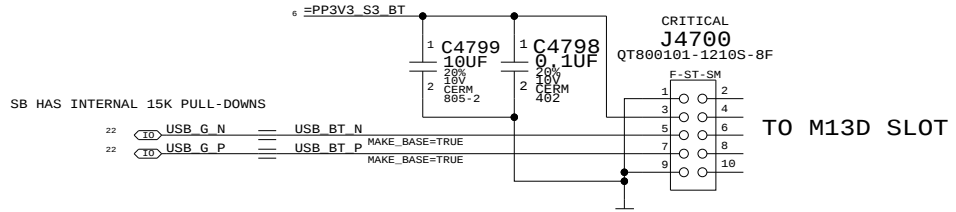
External USB Ports



LAYOUT NOTE:
PLACE C4743, C4797 & L4740
NEAR JE350 PIN 14 IN THE
ORDER LISTED, AND NOT ON
BOTH SIDES OF THE PIN.
PLACE C4742 CLOSED TO JE350.



BLUETOOTH



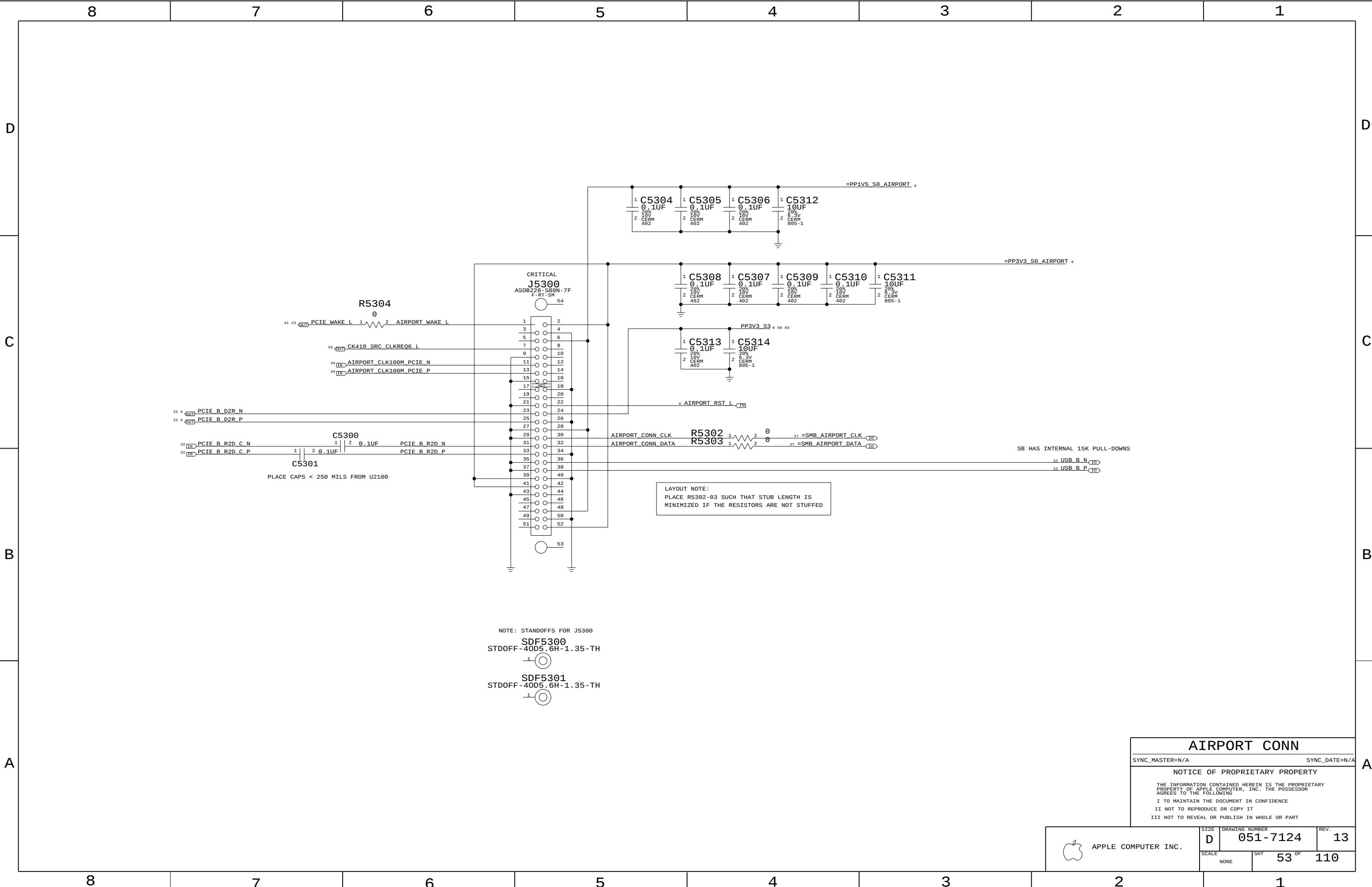
NOTE: STANDOFFS FOR J4700
SDF4700
STDOFF-40D4.5H-1.35-TH
SDF4701
STDOFF-40D4.5H-1.35-TH

USB Device Interfaces

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PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0355	3	USB RECEPTACLE, 4P, RIBLESS	JE310, JE320, JE330	CRITICAL	20_INCH_LCD

APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7124	REV. 13
	SCALE NONE	SHT 47	OF 110



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SYNC_MASTER=N/A

SYNC_DATE=N/A

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051-7124

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13

SCALE

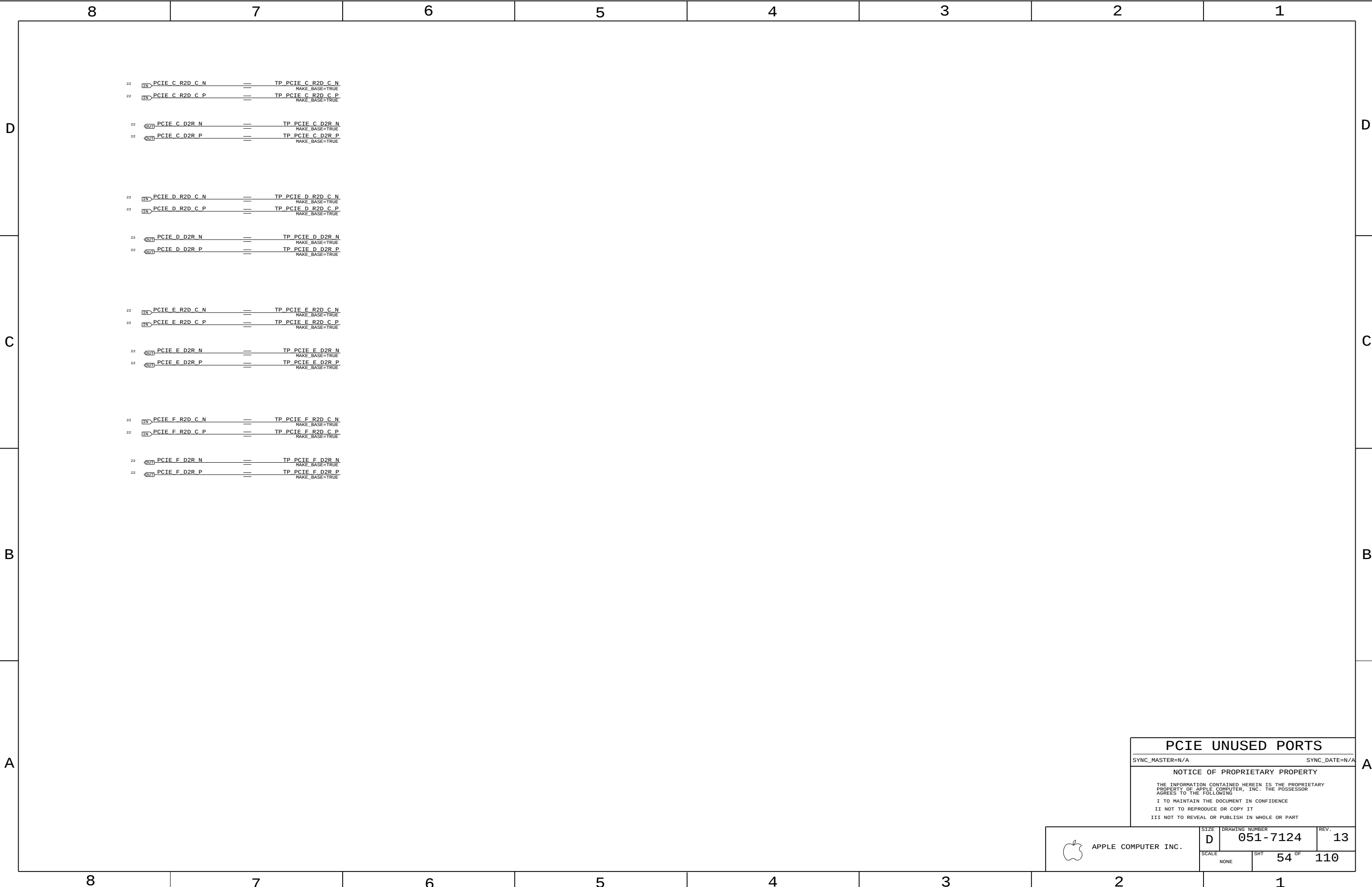
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SHT

53

OF

110



PCIE UNUSED PORTS

SYNC_MASTER=N/A

SYNC_DATE=N/A

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SIZE

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DRAWING NUMBER

051-7124

REV.

13

SCALE

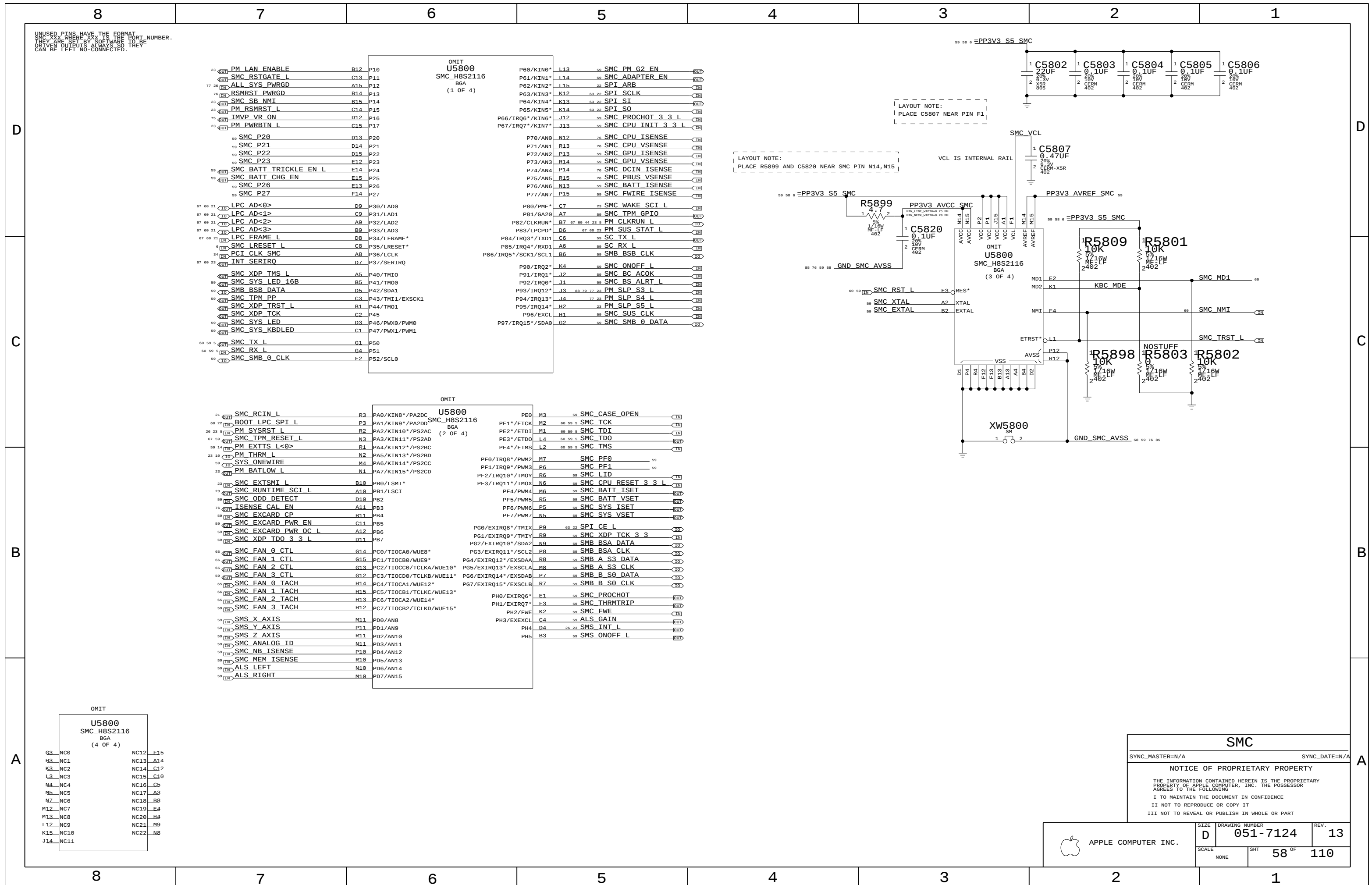
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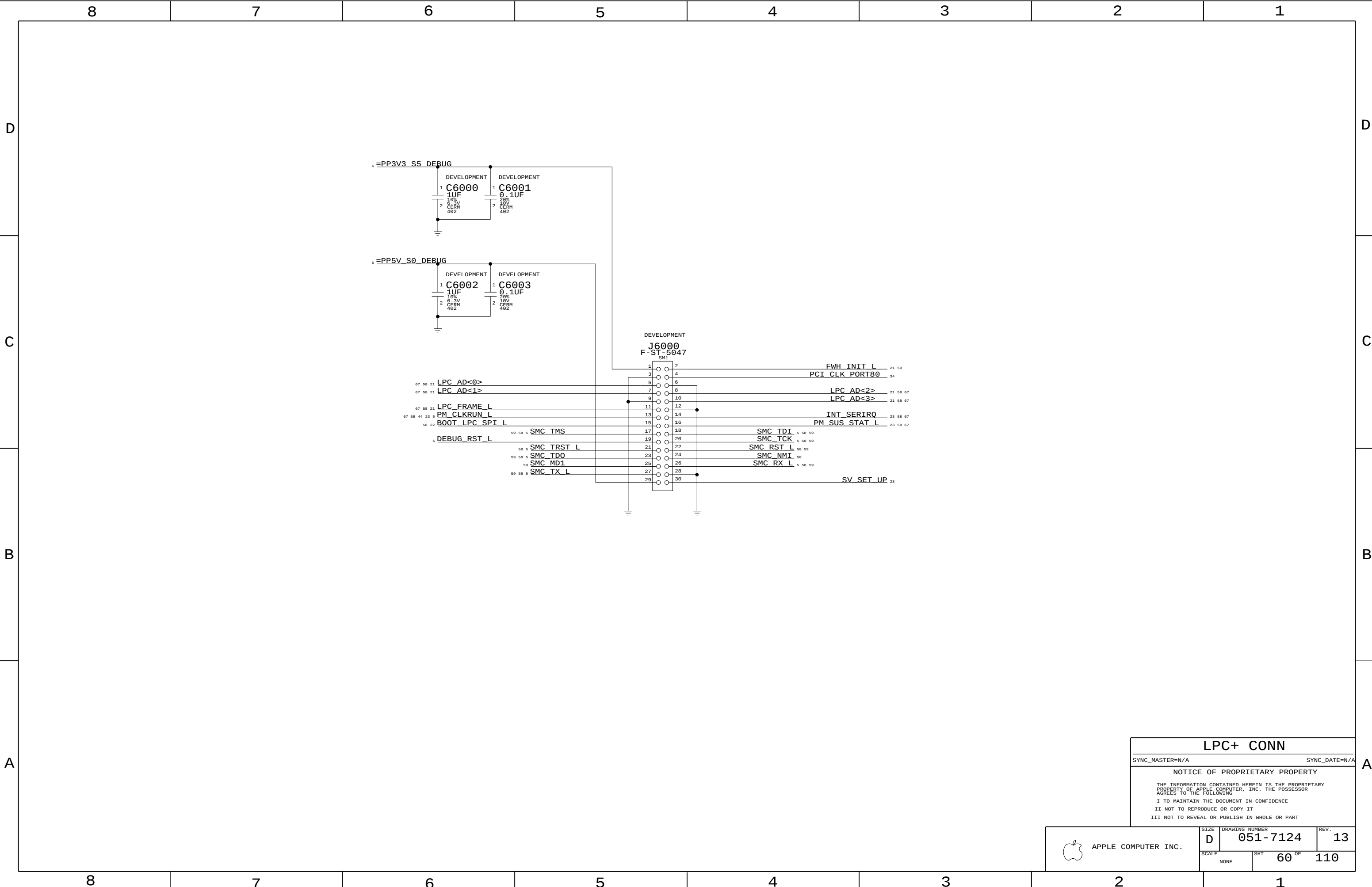
SHT

54

OF

110





LPC+ CONN

SYNC_MASTER=N/A

SYNC_DATE=N/A

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DRAWING NUMBER

051-7124

REV.

13

SCALE

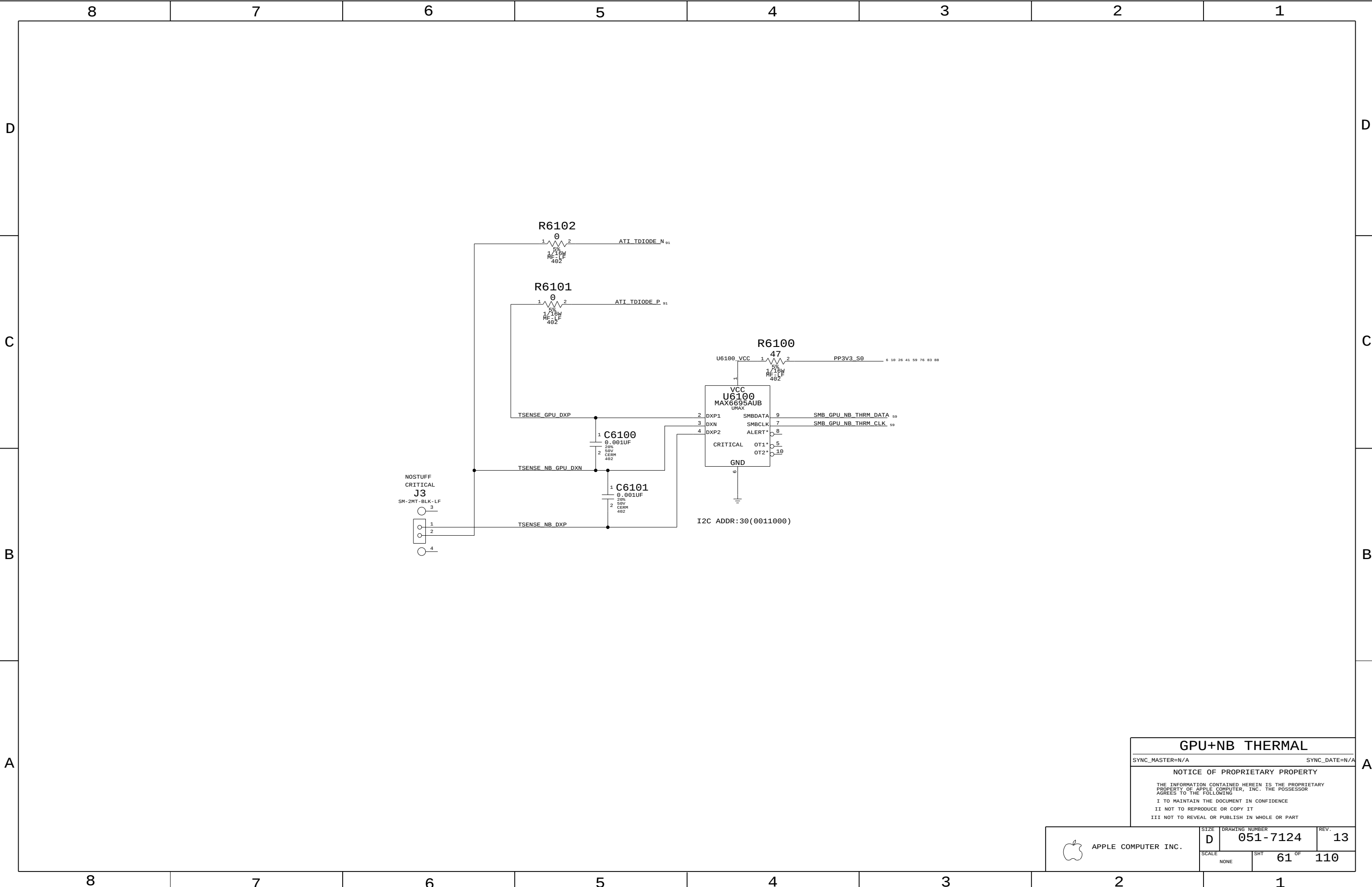
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SHT

60

OF

110



GPU+NB THERMAL

SYNC_MASTER=N/A

SYNC_DATE=N/A

NOTICE OF PROPRIETARY PROPERTY

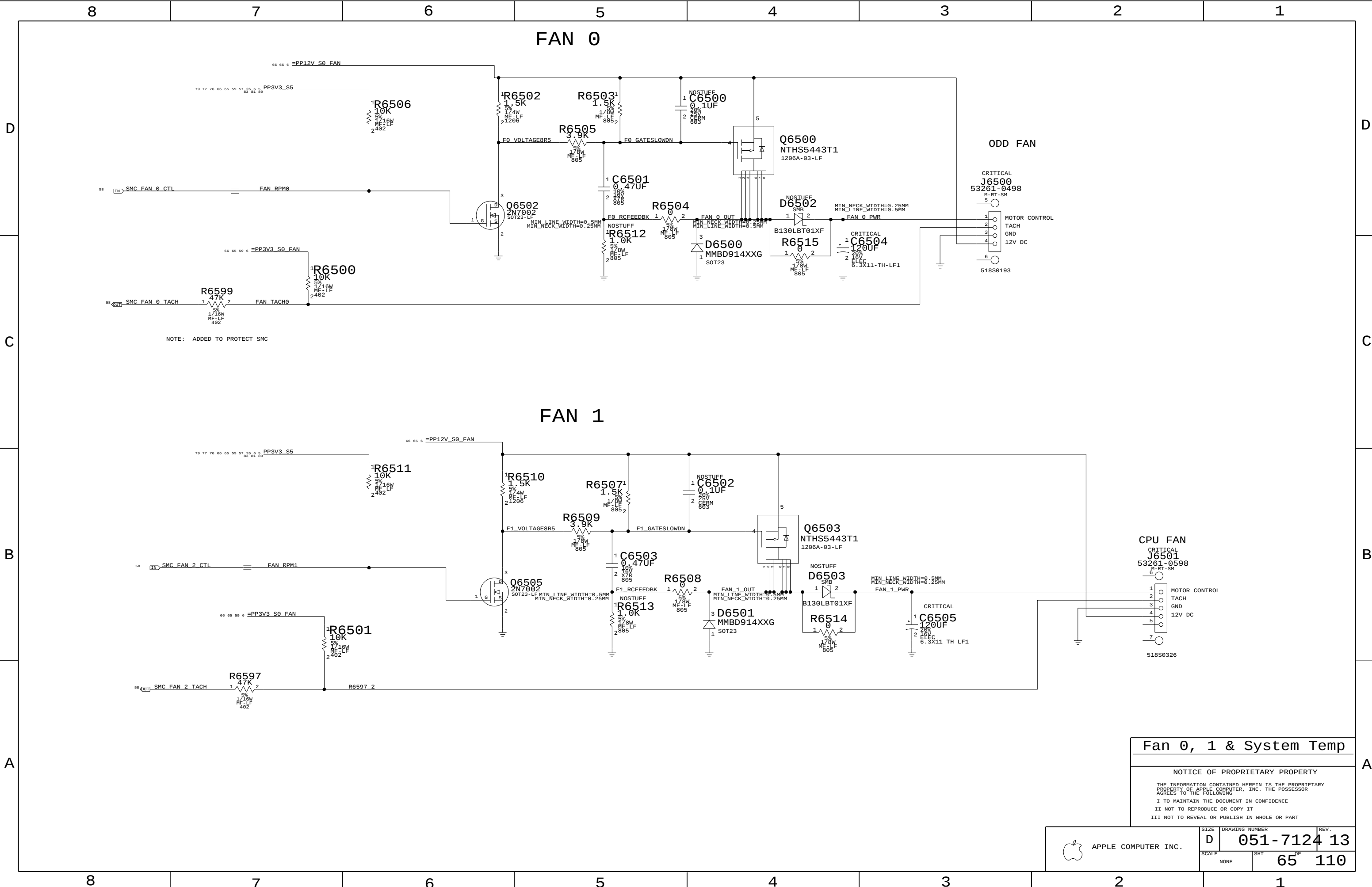
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 APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	13
SCALE	SHT		OF
NONE	61		110



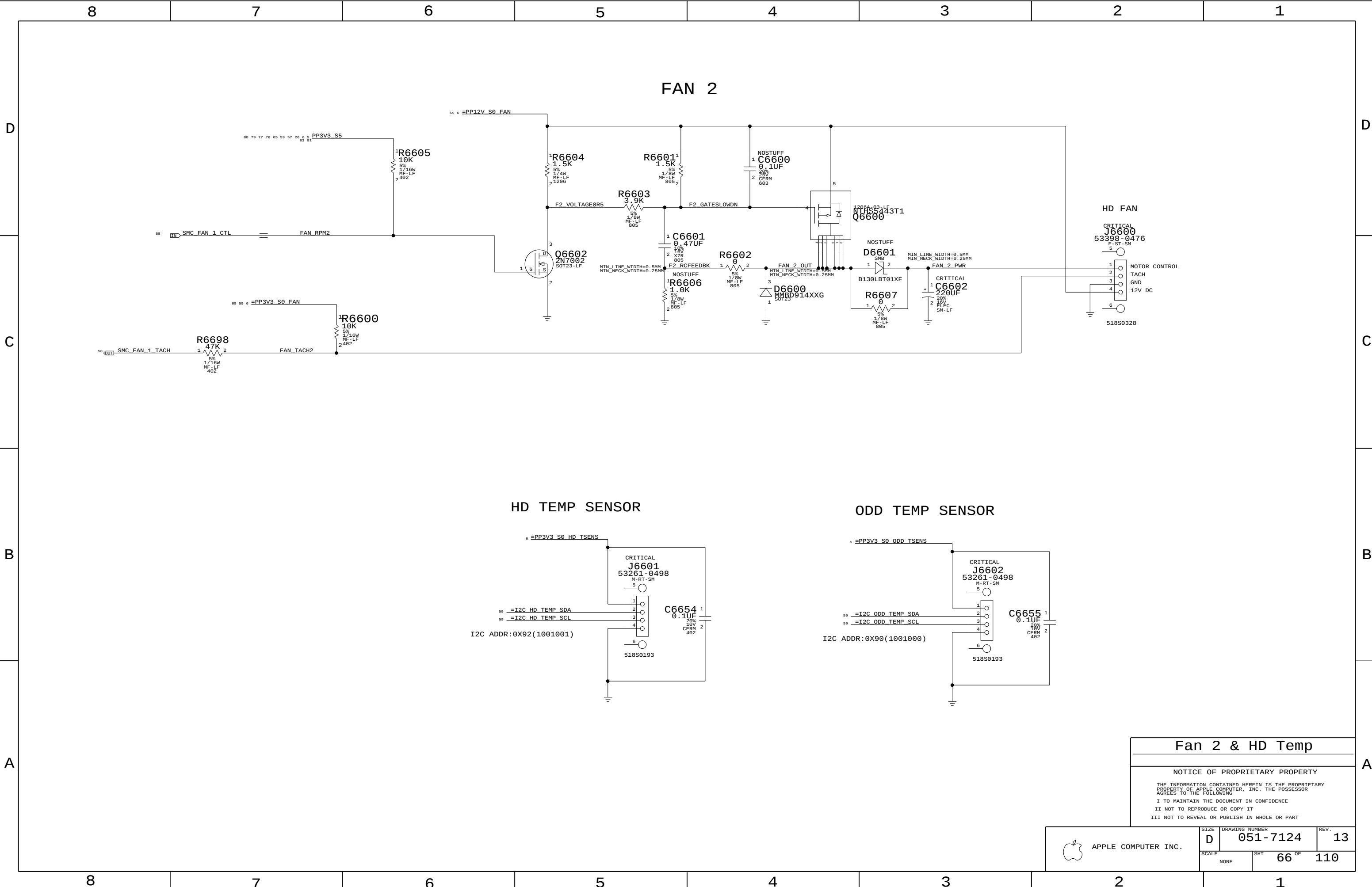
NOTE: ADDED TO PROTECT SMC

Fan 0, 1 & System Temp

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	13
	SCALE	SHT	65 OF 110
	NONE		



Fan 2 & HD Temp

NOTICE OF PROPRIETARY PROPERTY

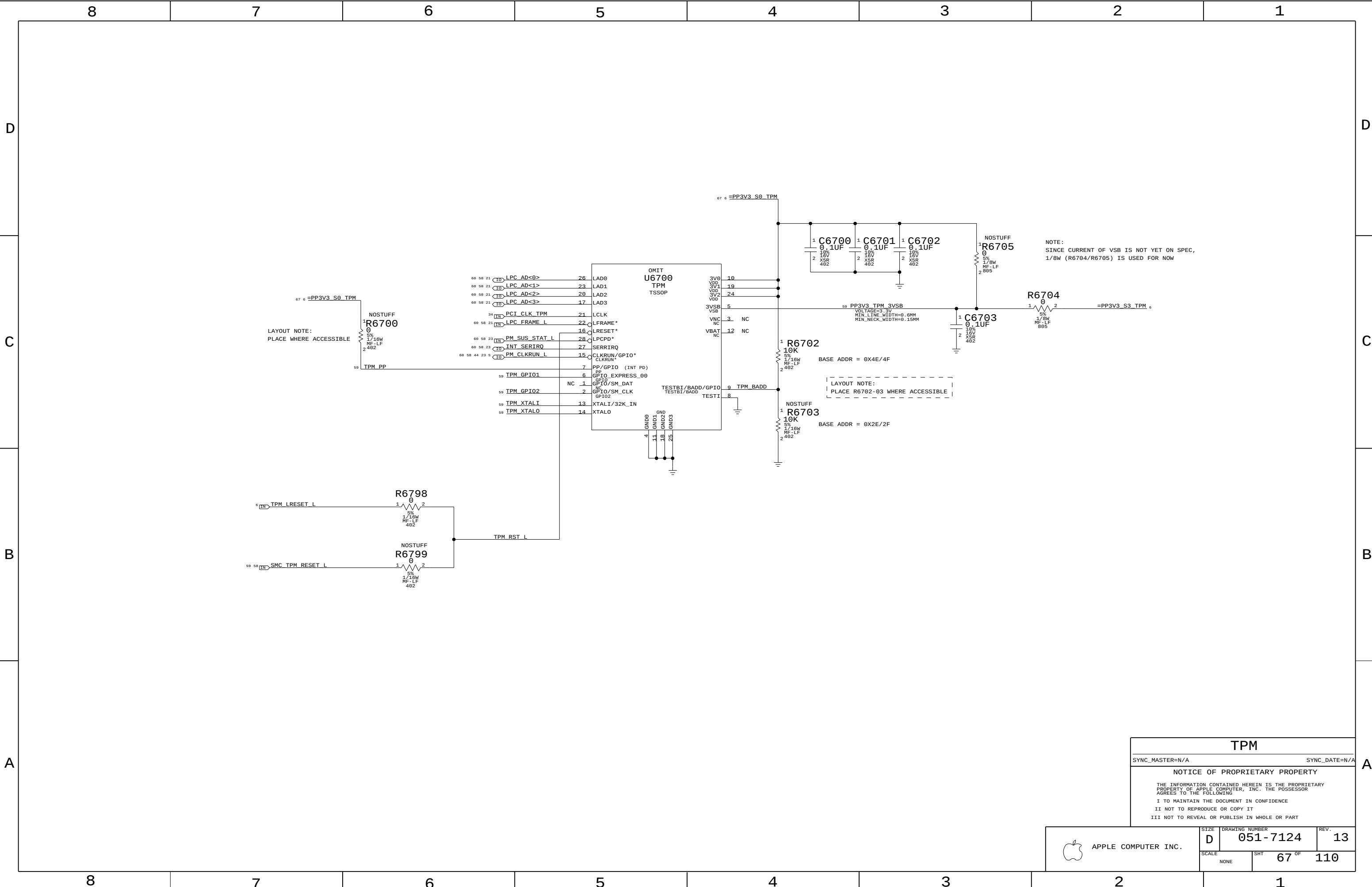
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	D	051-7124	13
SCALE		SHT	66 OF 110
NONE			



TPM

SYNC_MASTER=N/A

SYNC_DATE=N/A

NOTICE OF PROPRIETARY PROPERTY

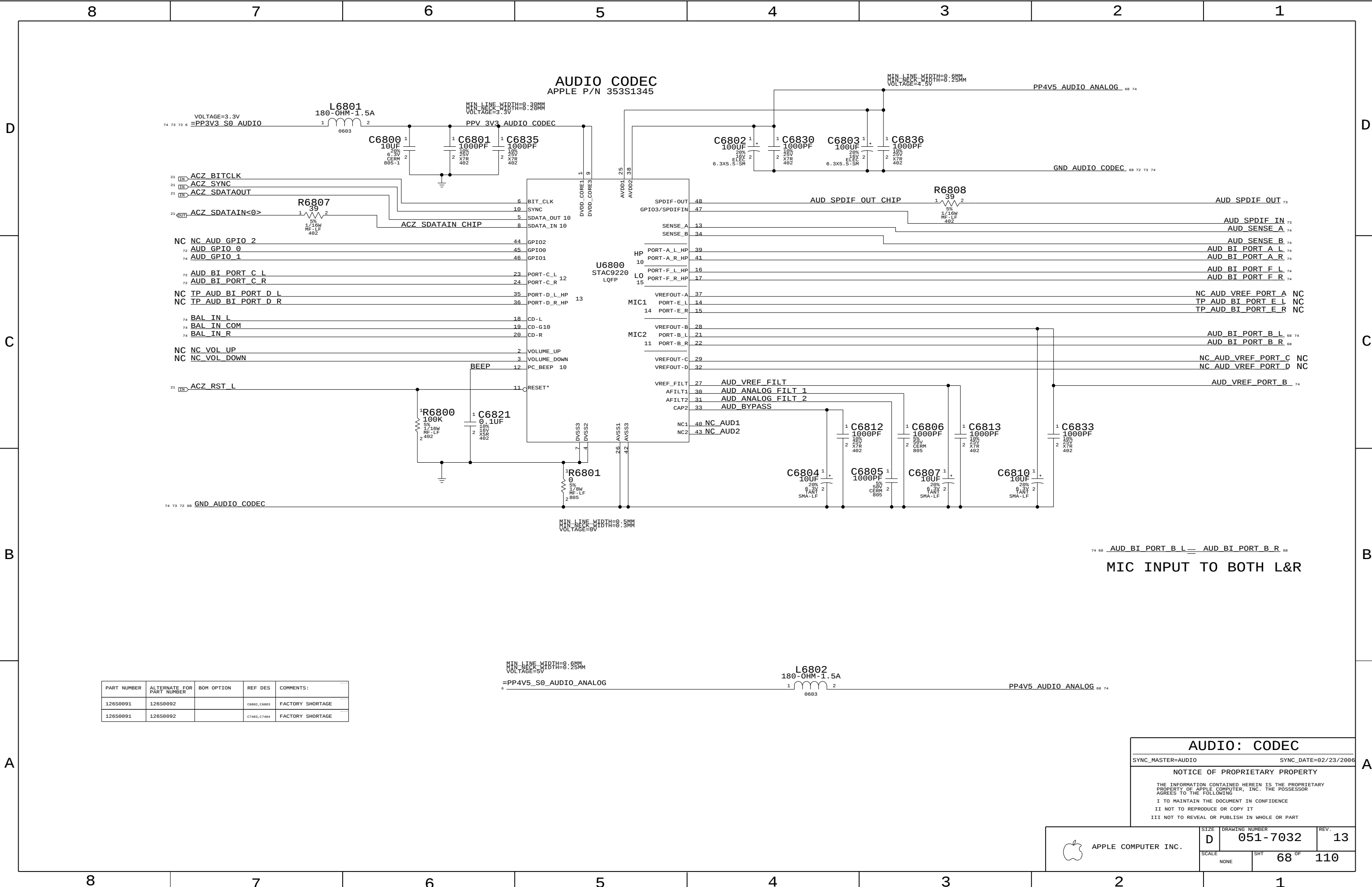
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 APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	13
SCALE		SHT	67 OF 110
NONE			



PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
126S0091	126S0092		C6802, C6803	FACTORY SHORTAGE
126S0091	126S0092		C7403, C7404	FACTORY SHORTAGE

AUDIO: CODEC

SYNC_MASTER=AUDIO

SYNC_DATE=02/23/2006

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APPLE COMPUTER INC.

SIZE

D

DRAWING NUMBER

051-7032

REV.

13

SCALE

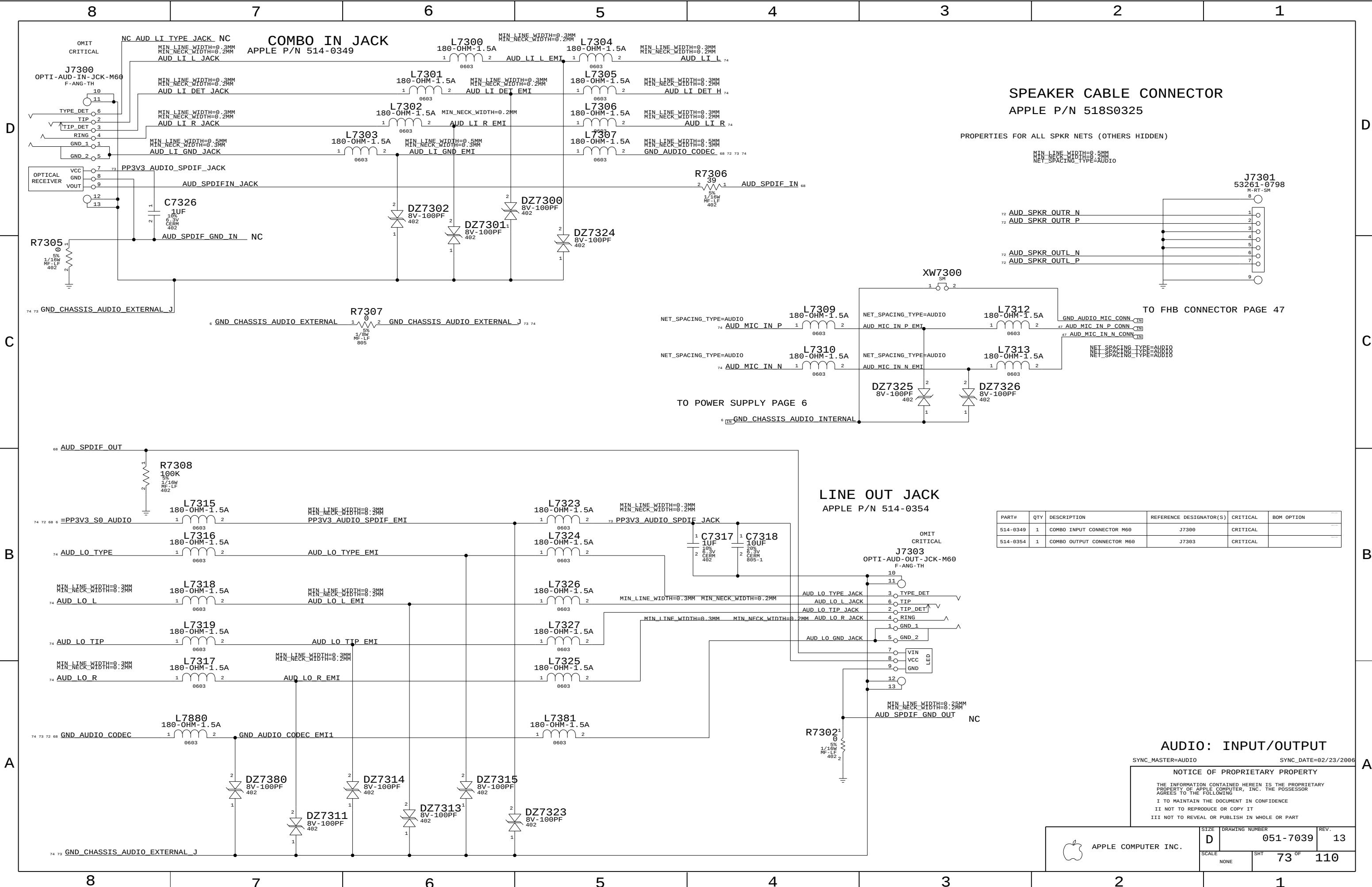
NONE

SHT

68

OF

110



SPEAKER CABLE CONNECTOR
APPLE P/N 518S0325

PROPERTIES FOR ALL SPKR NETS (OTHERS HIDDEN)

MIN LINE WIDTH=0.5MM
MIN NECK WIDTH=0.3MM
NET_SPACING_TYPE=AUDIO

J7301
53261-0798
N-RT-SM

AUD_SPKR_OUTR_N
AUD_SPKR_OUTR_P

AUD_SPKR_OUTL_N
AUD_SPKR_OUTL_P

TO FHB CONNECTOR PAGE 47

AUD_MIC_IN_P_CONN
AUD_MIC_IN_N_CONN

NET_SPACING_TYPE=AUDIO
NET_SPACING_TYPE=AUDIO
NET_SPACING_TYPE=AUDIO

XW7300
SM

AUD_MIC_IN_P
AUD_MIC_IN_N

DZ7325
8V-100PF
402

DZ7326
8V-100PF
402

TO POWER SUPPLY PAGE 6

GND_CHASSIS_AUDIO_INTERNAL

LINE OUT JACK
APPLE P/N 514-0354

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0349	1	COMBO INPUT CONNECTOR M60	J7300	CRITICAL	
514-0354	1	COMBO OUTPUT CONNECTOR M60	J7303	CRITICAL	

AUDIO: INPUT/OUTPUT

SYNC_MASTER=AUDIO SYNC_DATE=02/23/2006

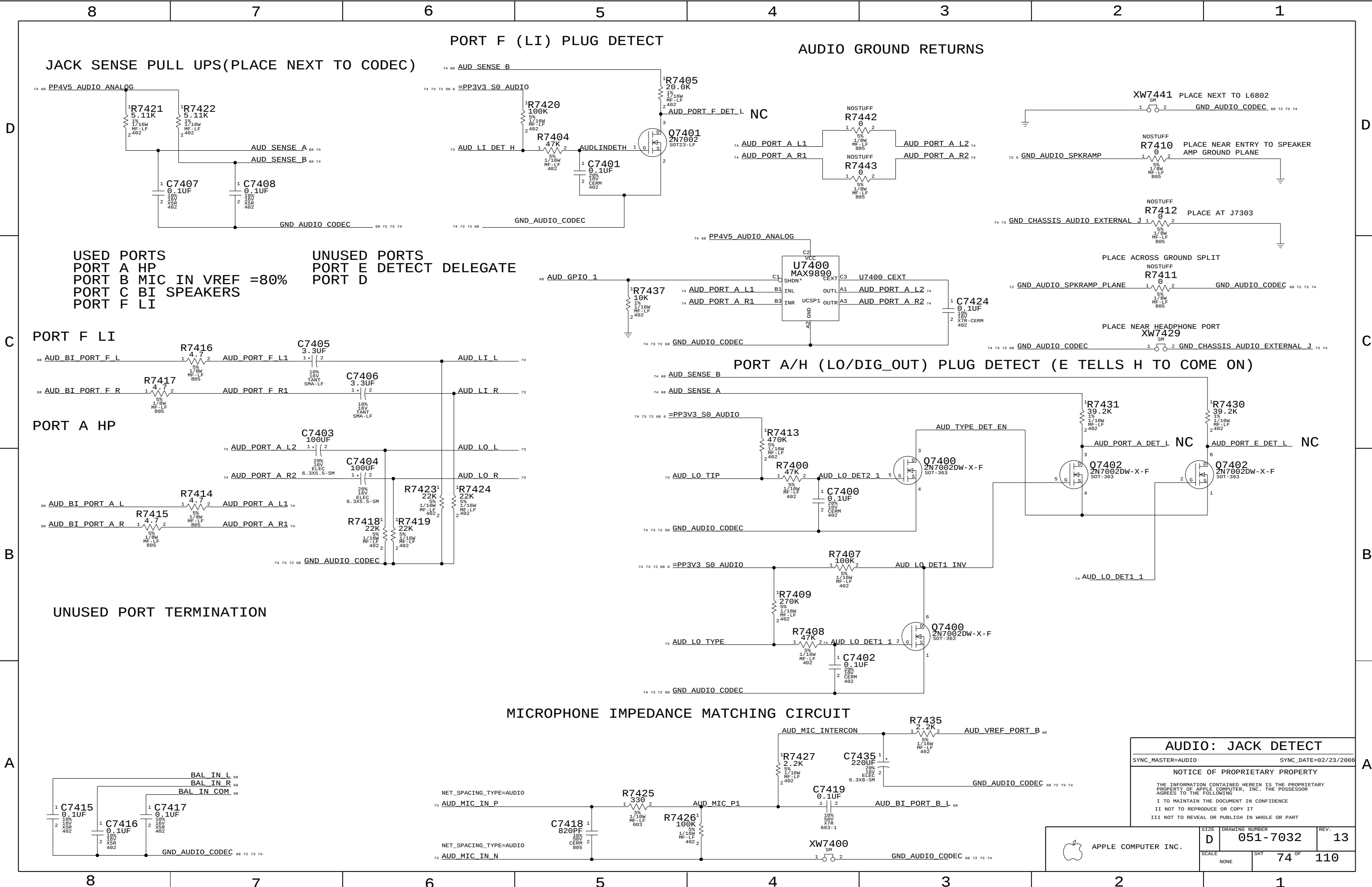
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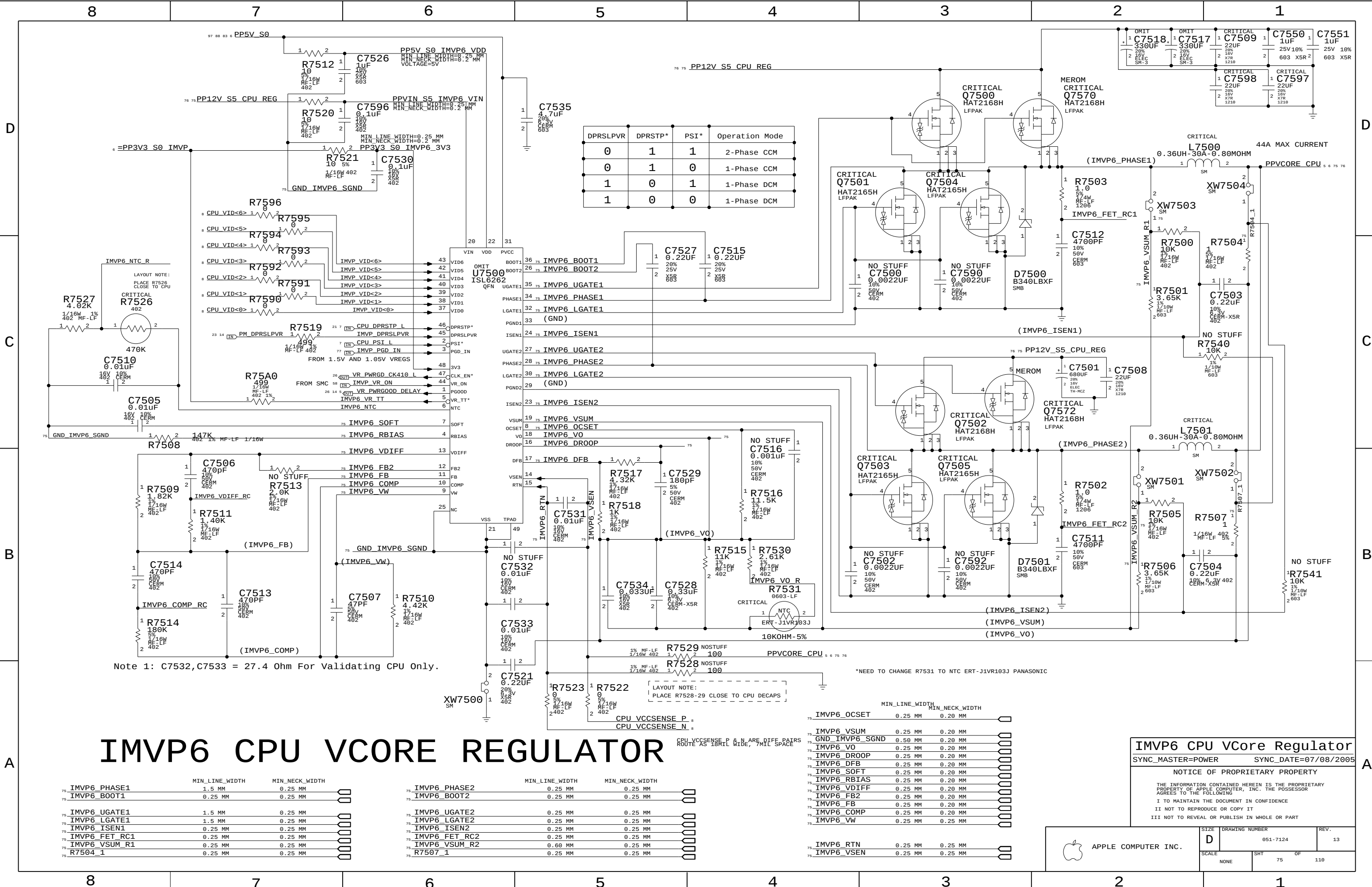
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SIZE	DRAWING NUMBER	REV.
D	051-7039	13
SCALE	SHT	73 OF 110
NONE		





IMVP6 CPU VCore Regulator

	MIN_LINE_WIDTH	MIN_NECK_WIDTH
IMVP6_PHASE1	1.5 MM	0.25 MM
IMVP6_BOOT1	0.25 MM	0.25 MM
IMVP6_UGATE1	1.5 MM	0.25 MM
IMVP6_LGATE1	1.5 MM	0.25 MM
IMVP6_ISEN1	0.25 MM	0.25 MM
IMVP6_FET_RC1	0.25 MM	0.25 MM
IMVP6_VSUM_R1	0.25 MM	0.25 MM
R7504_1	0.25 MM	0.25 MM

	MIN_LINE_WIDTH	MIN_NECK_WIDTH
IMVP6_PHASE2	0.25 MM	0.25 MM
IMVP6_BOOT2	0.25 MM	0.25 MM
IMVP6_UGATE2	0.25 MM	0.25 MM
IMVP6_LGATE2	0.25 MM	0.25 MM
IMVP6_ISEN2	0.25 MM	0.25 MM
IMVP6_FET_RC2	0.25 MM	0.25 MM
IMVP6_VSUM_R2	0.60 MM	0.25 MM
R7507_1	0.25 MM	0.25 MM

	MIN_LINE_WIDTH	MIN_NECK_WIDTH
IMVP6_OCSET	0.25 MM	0.20 MM
IMVP6_VSUM	0.25 MM	0.20 MM
GND_IMVP6_SGND	0.50 MM	0.20 MM
IMVP6_V0	0.25 MM	0.20 MM
IMVP6_DR00P	0.25 MM	0.20 MM
IMVP6_DFB	0.25 MM	0.20 MM
IMVP6_SOFT	0.25 MM	0.20 MM
IMVP6_RBIAS	0.25 MM	0.20 MM
IMVP6_VDIFF	0.25 MM	0.20 MM
IMVP6_FB2	0.25 MM	0.20 MM
IMVP6_FB	0.25 MM	0.20 MM
IMVP6_COMP	0.25 MM	0.20 MM
IMVP6_VW	0.25 MM	0.25 MM
IMVP6_RTIN	0.25 MM	0.25 MM
IMVP6_VSEN	0.25 MM	0.25 MM

IMVP6 CPU VCore Regulator

SYNC_MASTER=POWER SYNC_DATE=07/08/2005

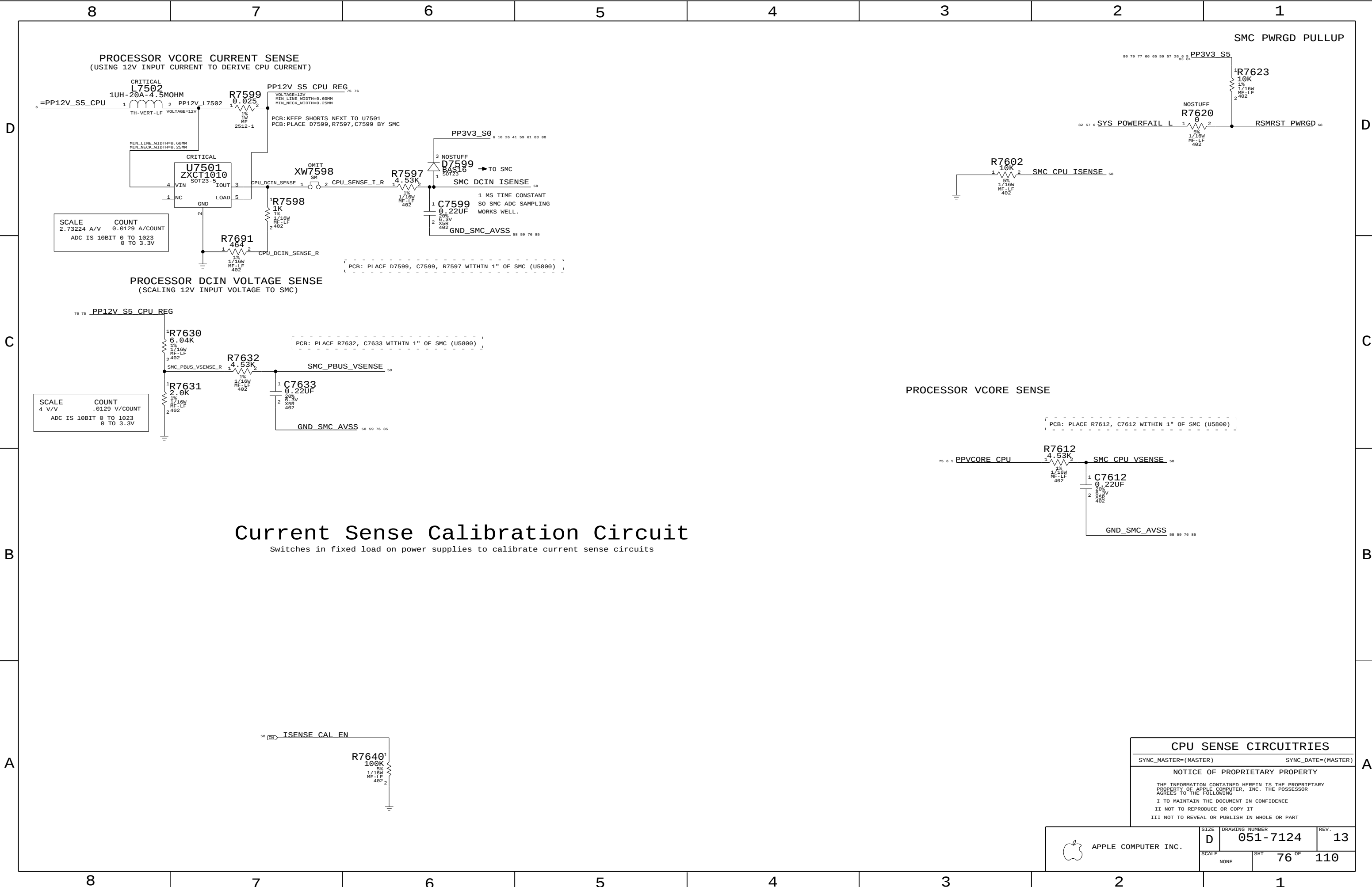
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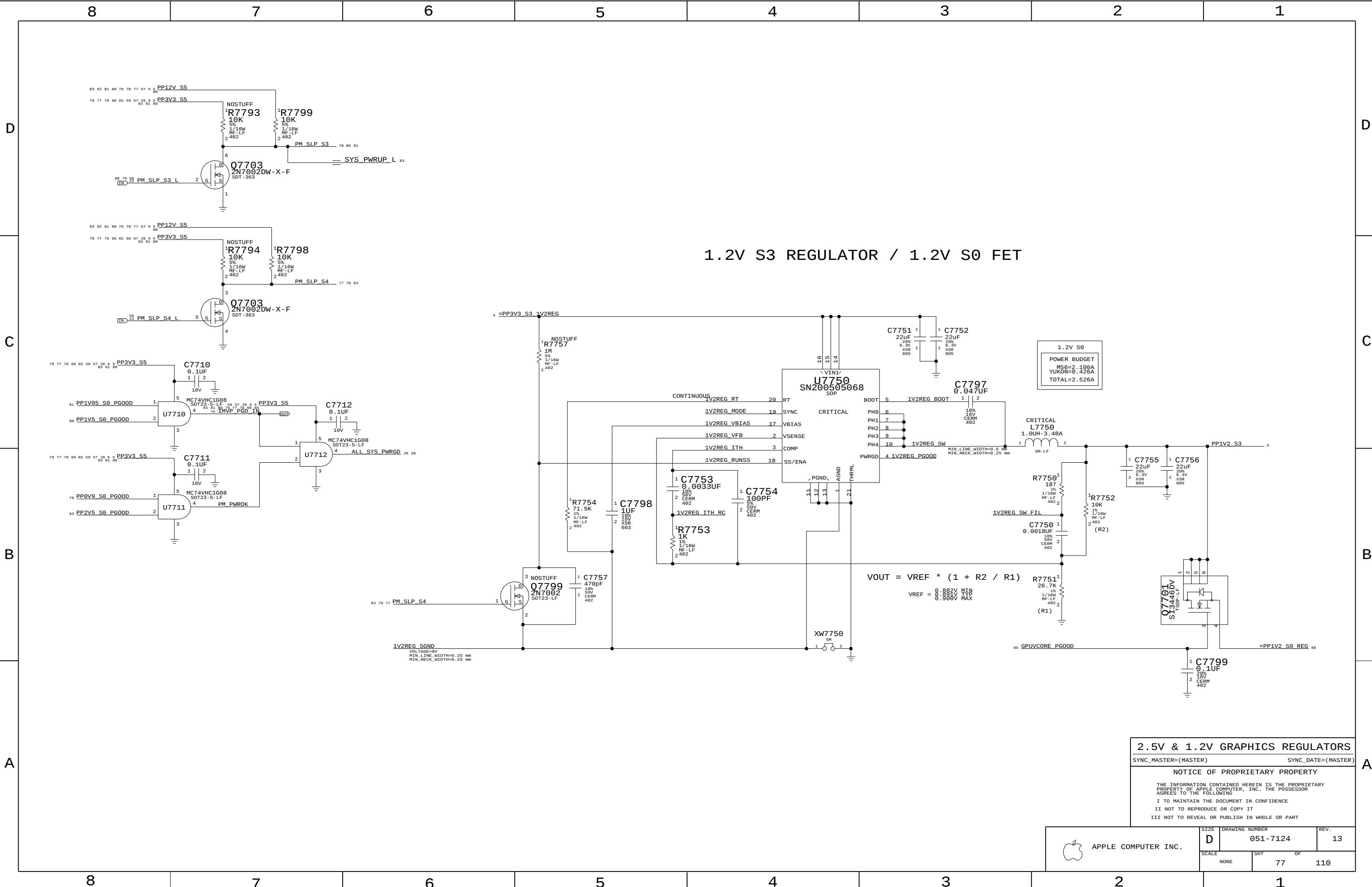
SIZE	DRAWING NUMBER	REV.
D	051-7124	13
SCALE	SHT	OF
NONE	75	110



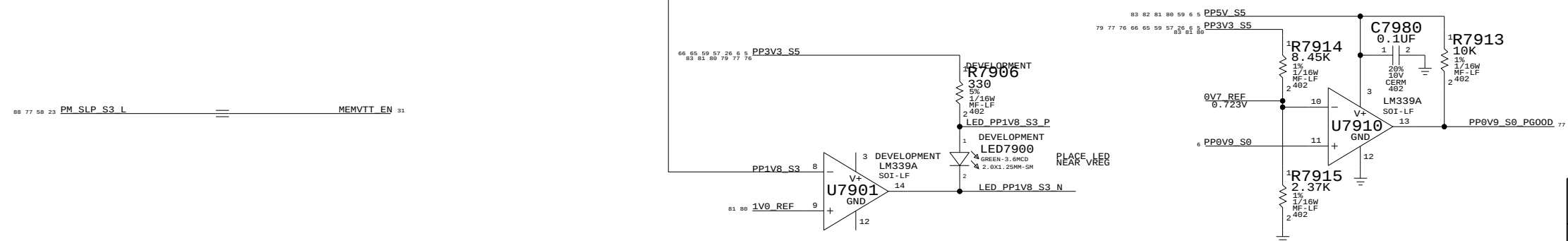
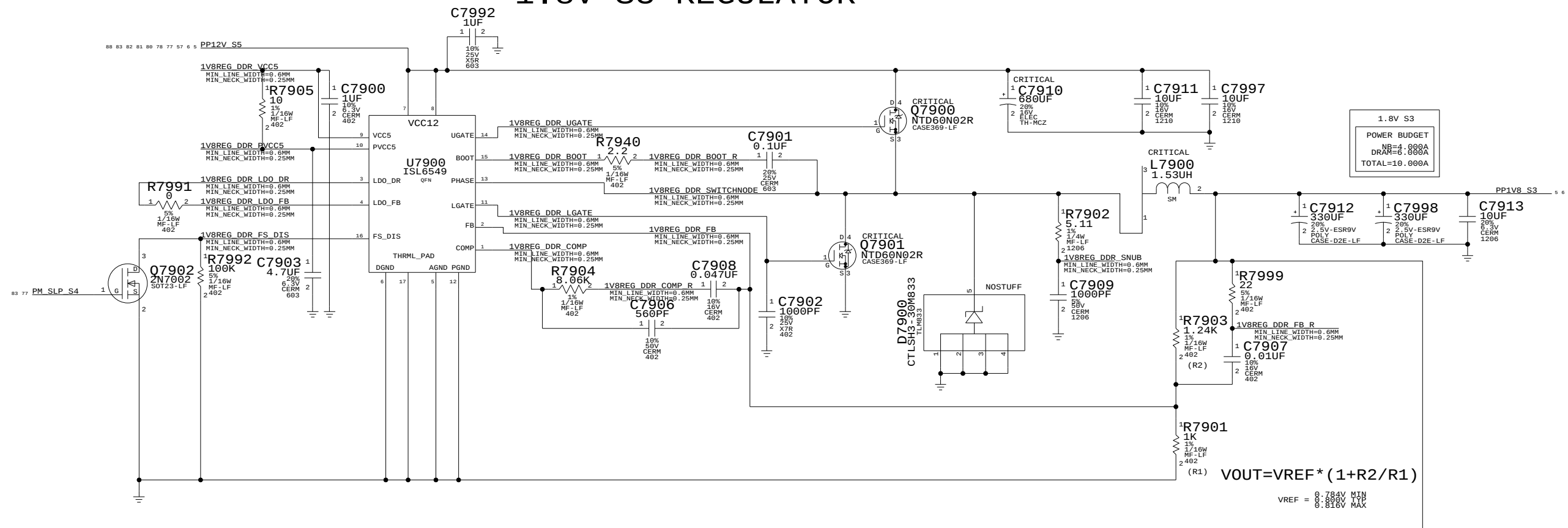
Current Sense Calibration Circuit

Switches in fixed load on power supplies to calibrate current sense circuits

CPU SENSE CIRCUITRIES		
SYNC_MASTER=(MASTER)		SYNC_DATE=(MASTER)
NOTICE OF PROPRIETARY PROPERTY		
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1.8V S3 REGULATOR



1.8V Vreg

SYNC_MASTER=M23-PC SYNC_DATE=04/12/2005

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SIZE	DRAWING NUMBER	REV.
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D	051-7124	13
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5		
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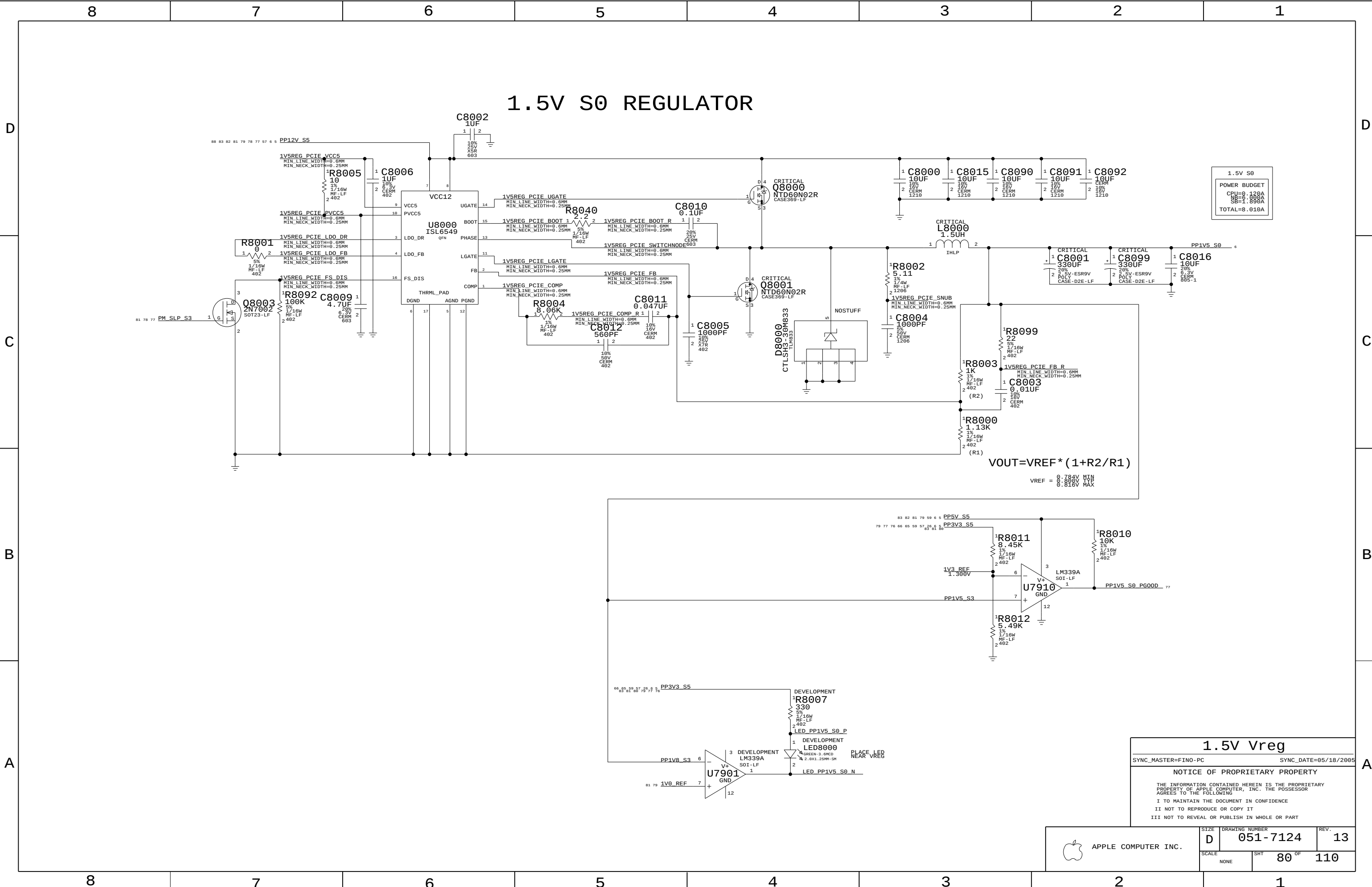
APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
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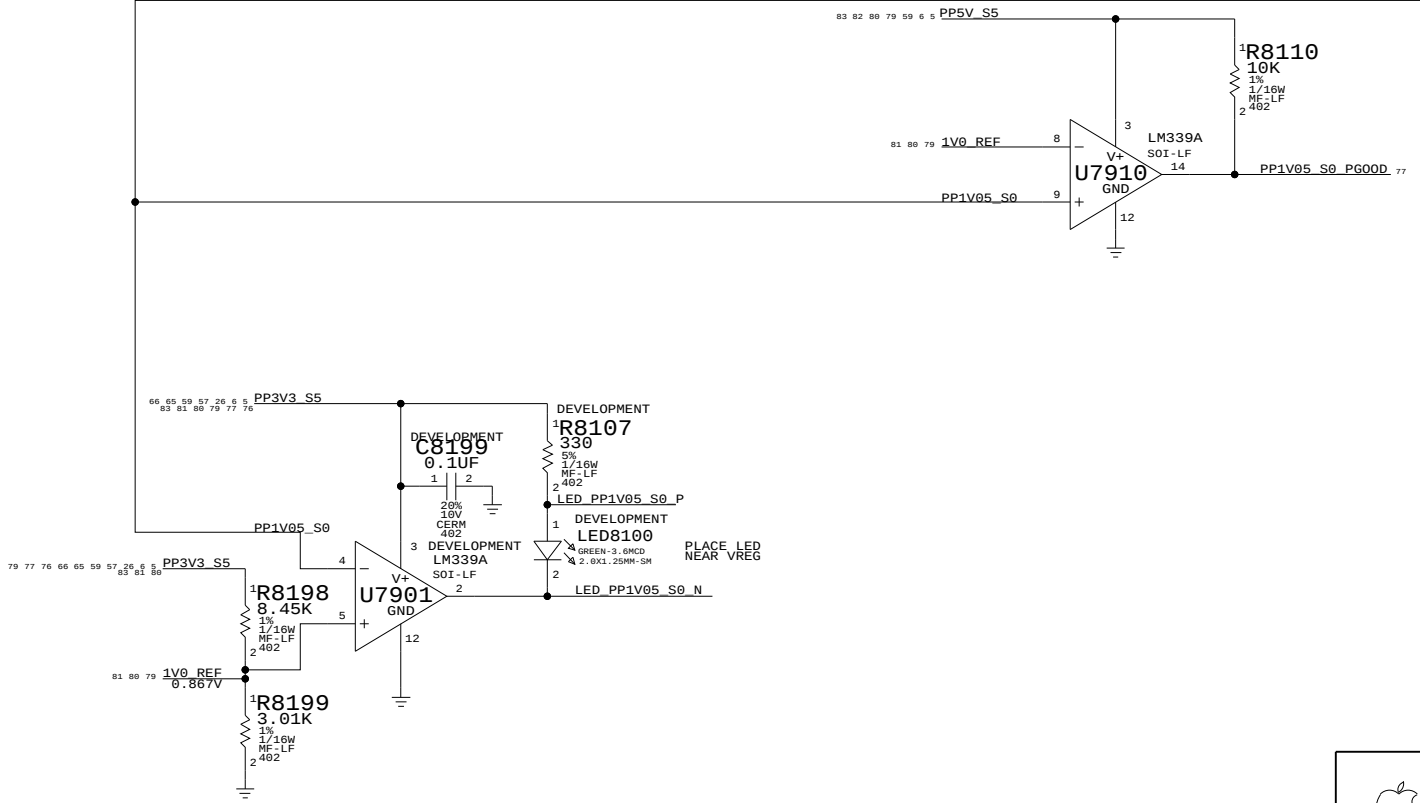
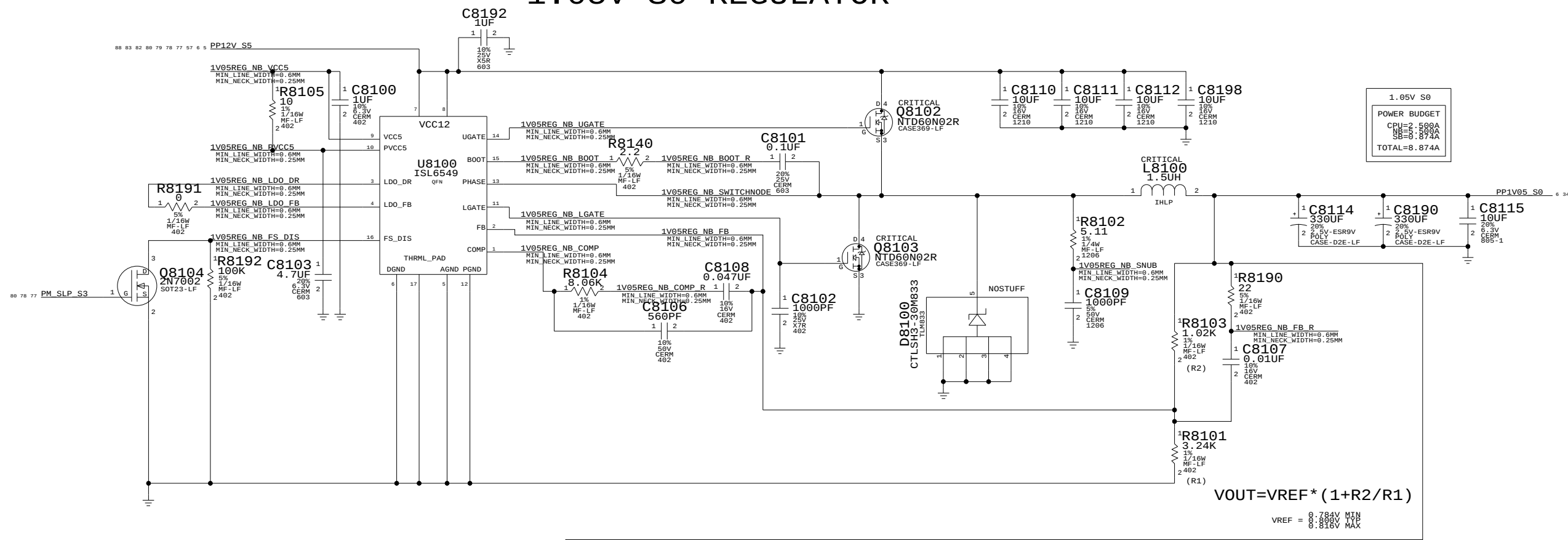
D	051-7124	13
---	----------	----

5		
---	--	--

SCALE	SHT	79	OF	110
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1.05V S0 REGULATOR



1.05V VREG

SYNC_MASTER=M38-RT SYNC_DATE=05/18/2005

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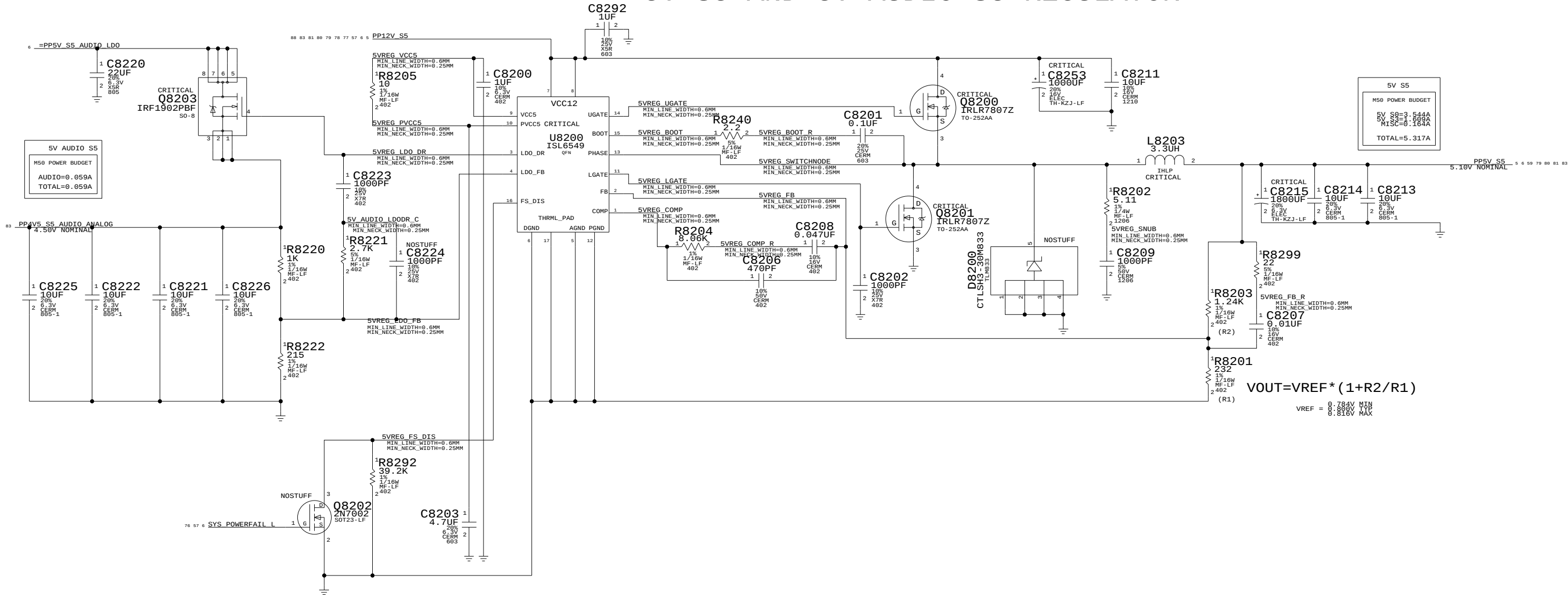
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	13
SCALE		SHT	81 OF 110
NONE			

5V S5 AND 5V AUDIO S5 REGULATOR



5V DC/DC 4.5V

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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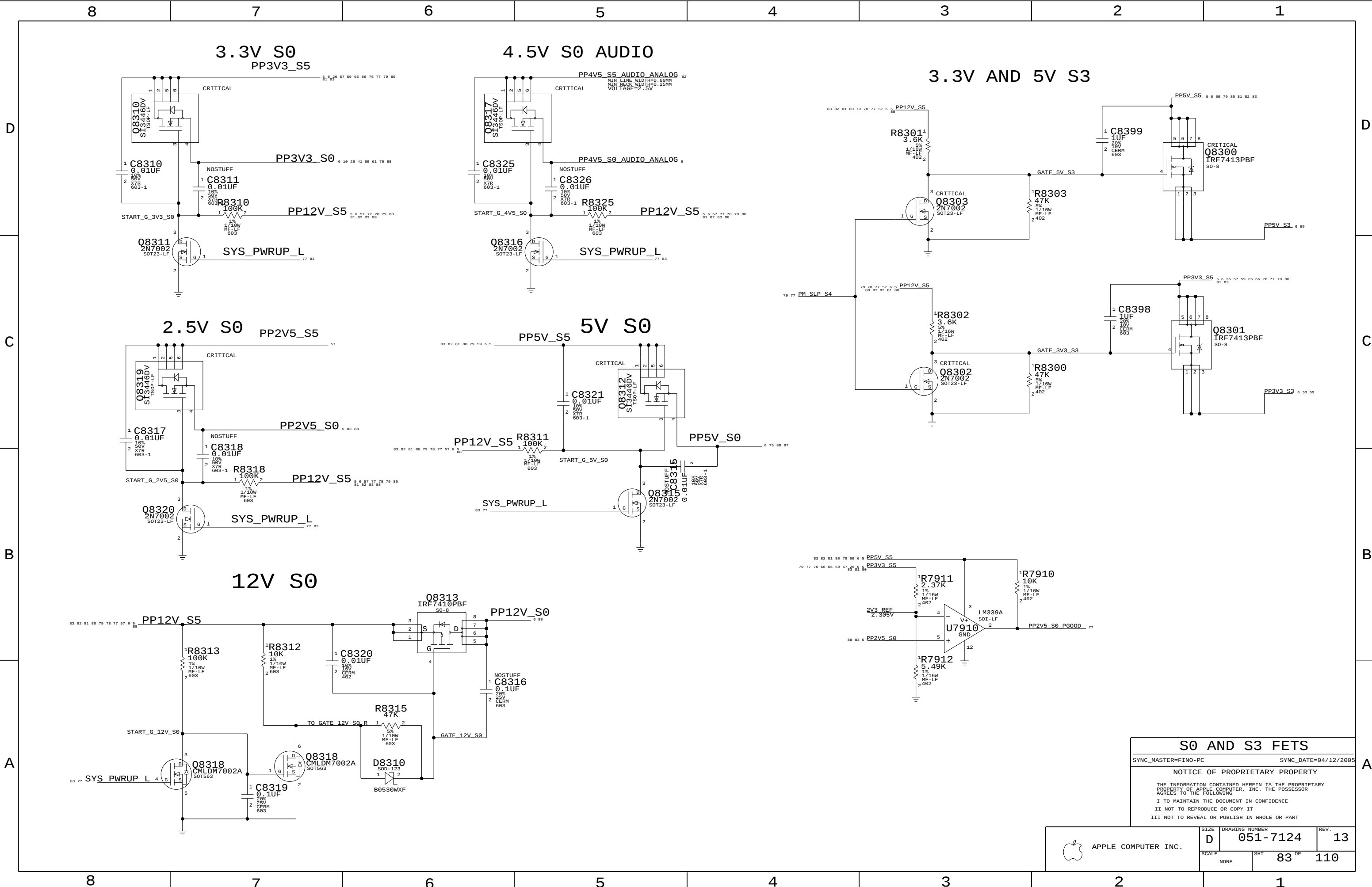
II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

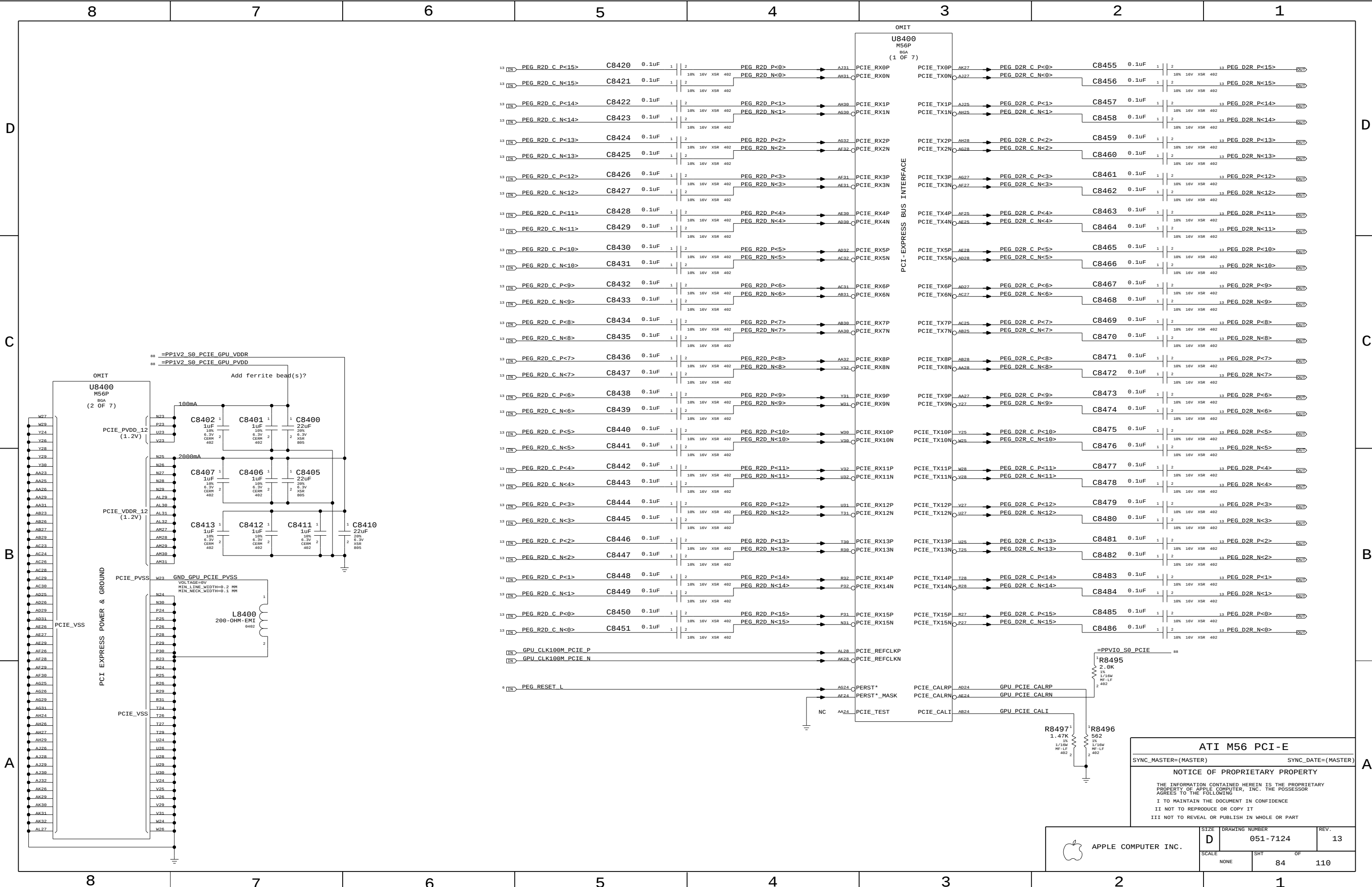


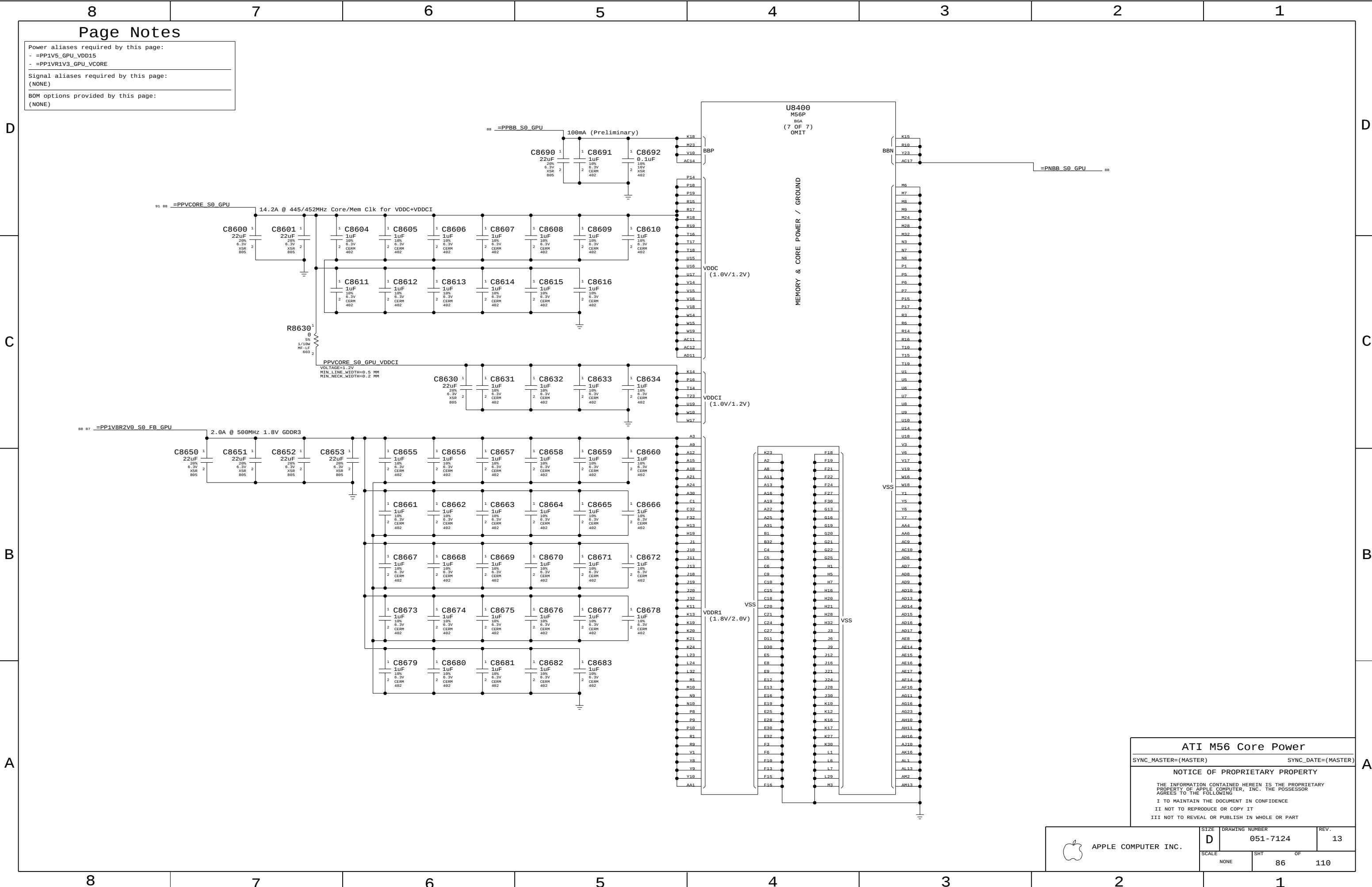
APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7124	13
SCALE	SHT	OF
NONE	82	110



S0 AND S3 FETS		
SYNC_MASTER=FINO-PC		SYNC_DATE=04/12/2005
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Power aliases required by this page:
- =PP1V5_GPU_VDD15
- =PP1VR1V3_GPU_VCORE

Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)

ATI M56 Core Power

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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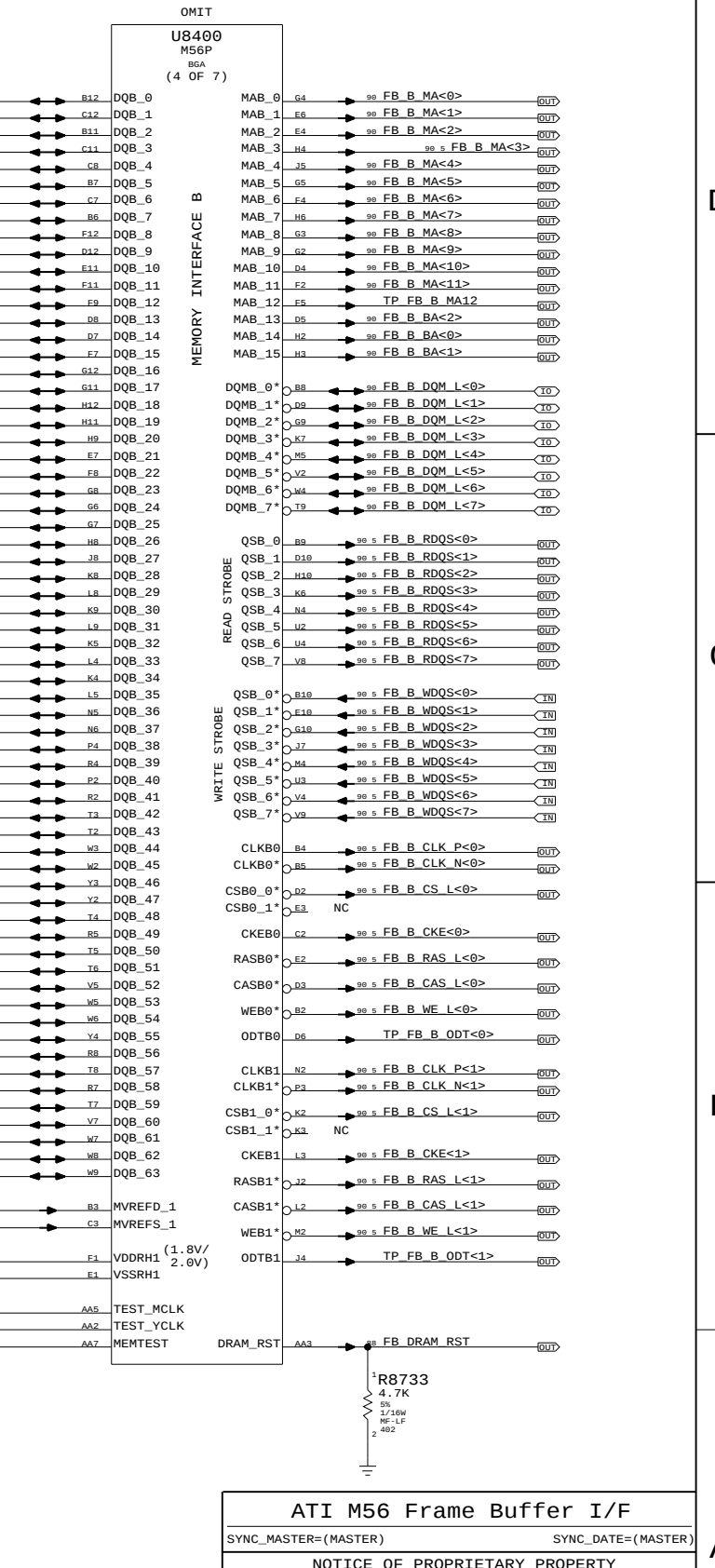
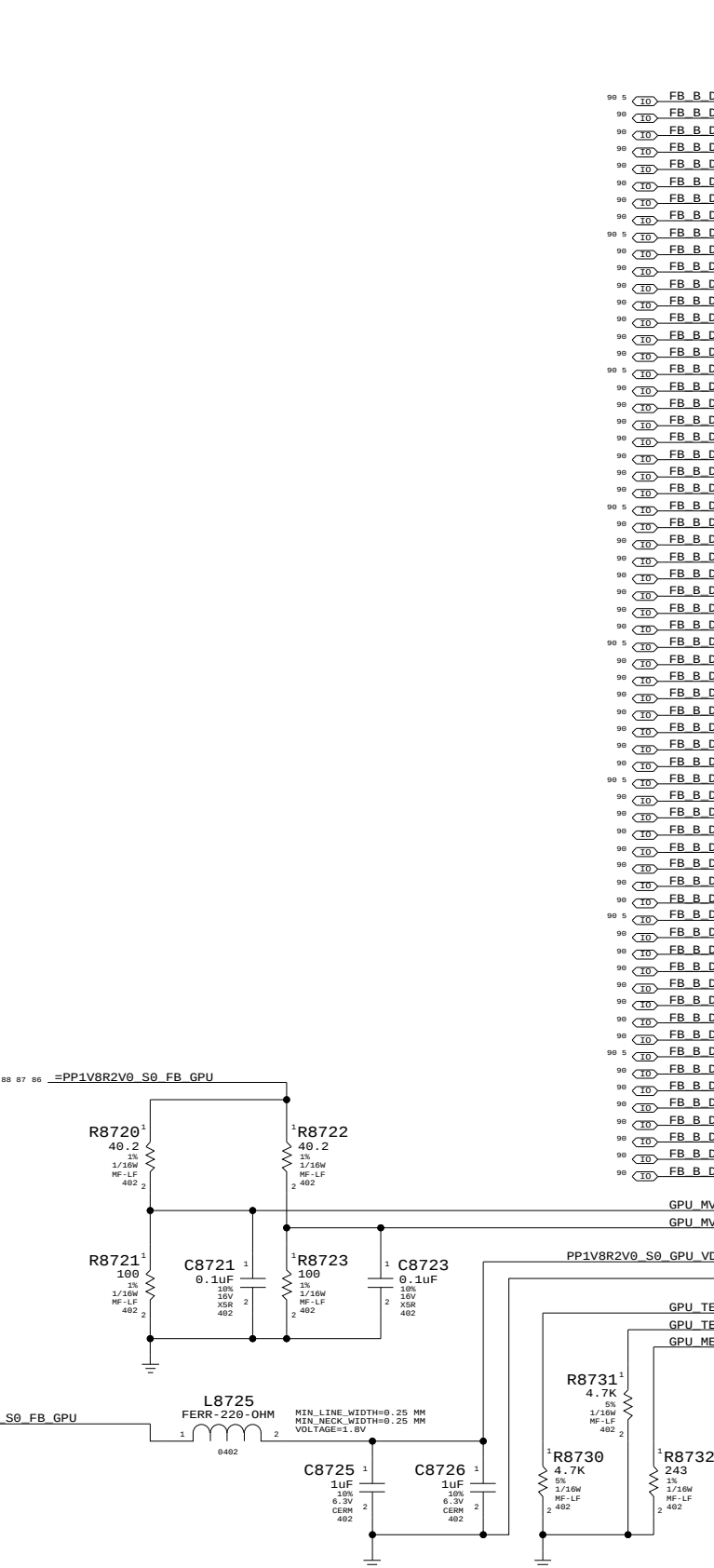
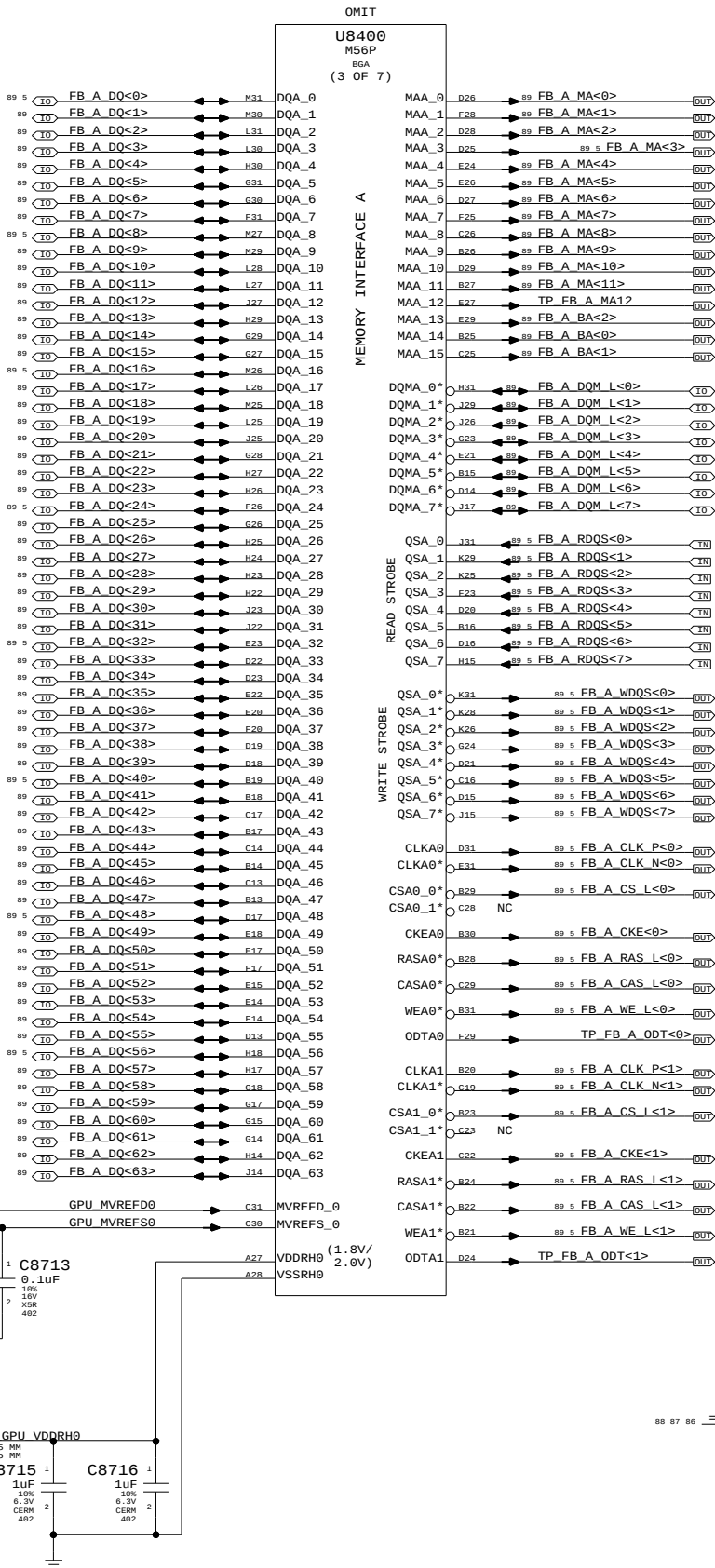
APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7124	13
SCALE	SHT		OF
	NONE		86 110

Page Notes

Power aliases required by this page:
- =PP1V8R2V0_S0_FB_GPU

Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)



ATI M56 Frame Buffer I/F

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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Page Notes

Power aliases required by this page:

- =PP1V8_S0_FB_VDD
- =PP1V8_S0_FB_VDDQ

Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)

GDDR3 Frame Buffer A

SYNC_MASTER=(MASTER)

SYNC_DATE=(MASTER)

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APPLE COMPUTER INC.

SIZE

D

DRAWING NUMBER

051-7124

REV.

13

SCALE

NONE

SHT

89

OF

110



Page Notes

Power aliases required by this page:
- =PP1V8_S0_FB_VDD
- =PP1V8_S0_FB_VDDQ

Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)

GDDR3 Frame Buffer B

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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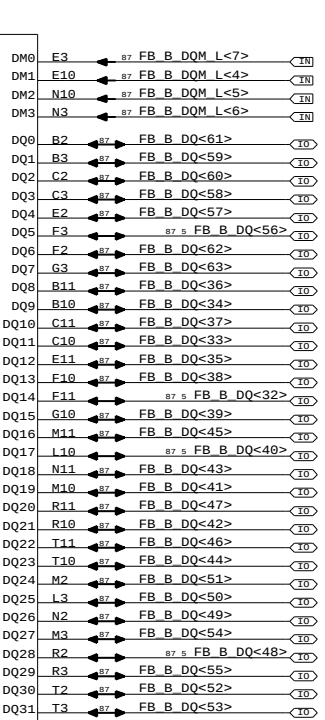
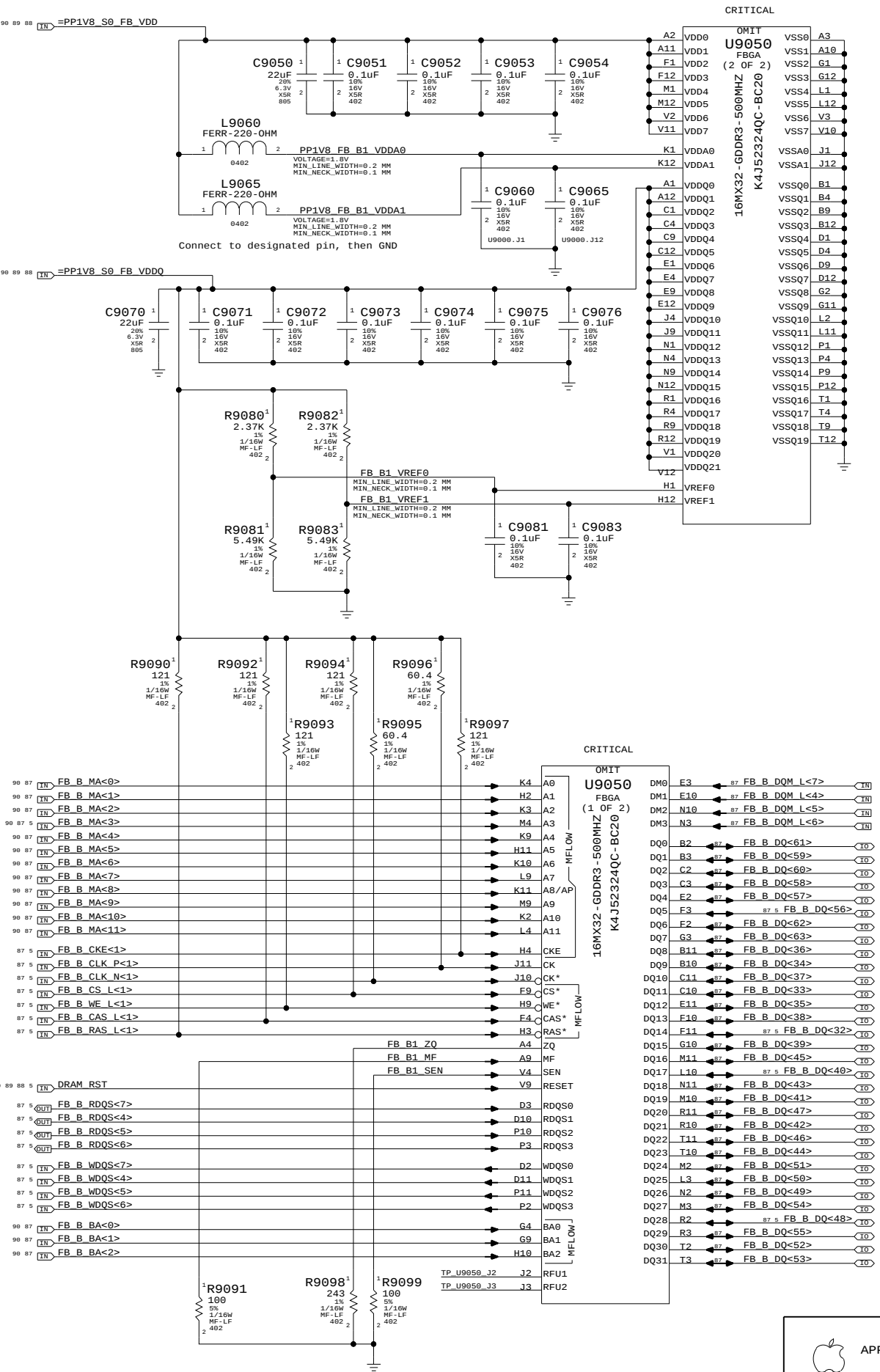
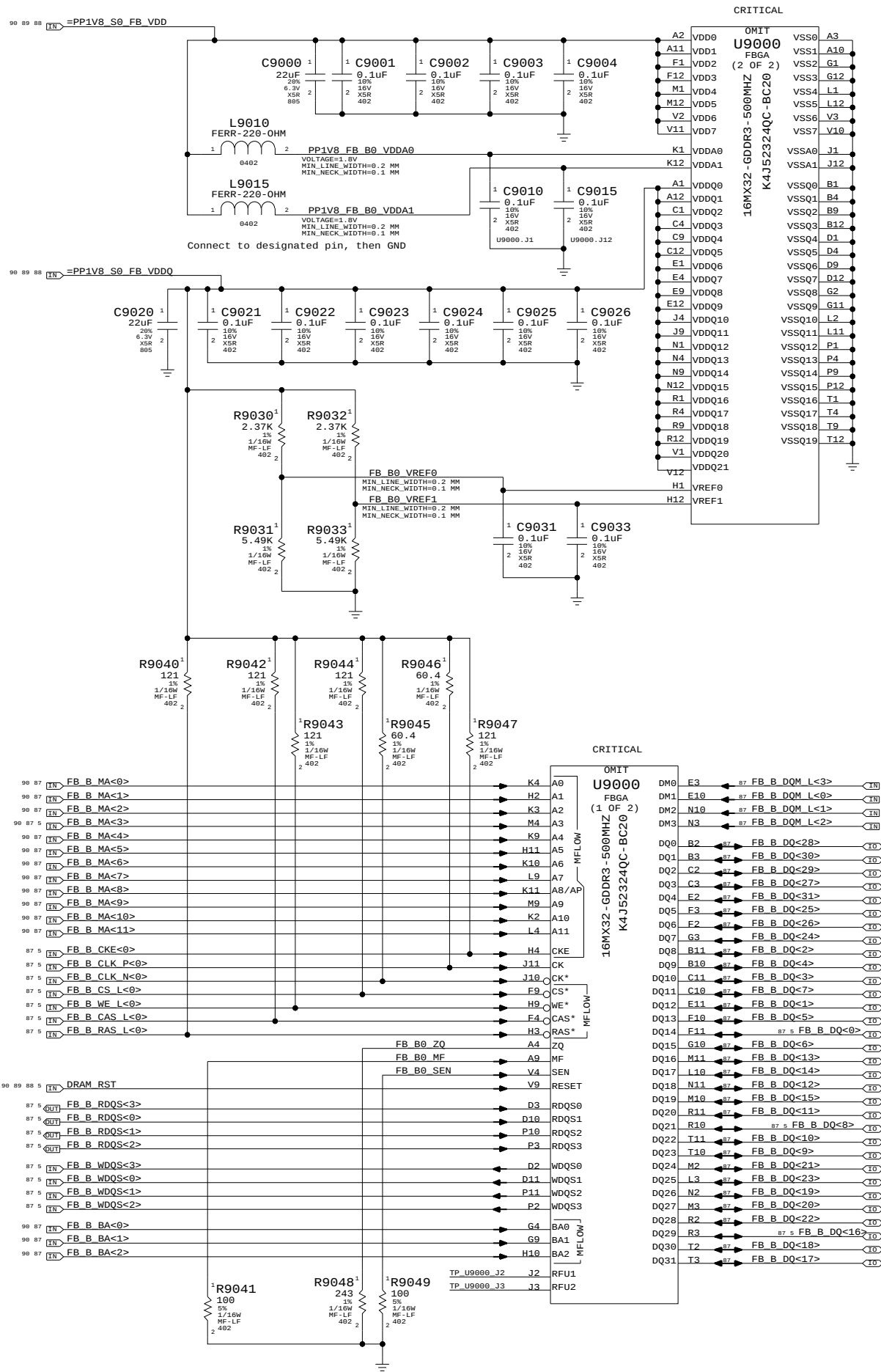
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APPLE COMPUTER INC.

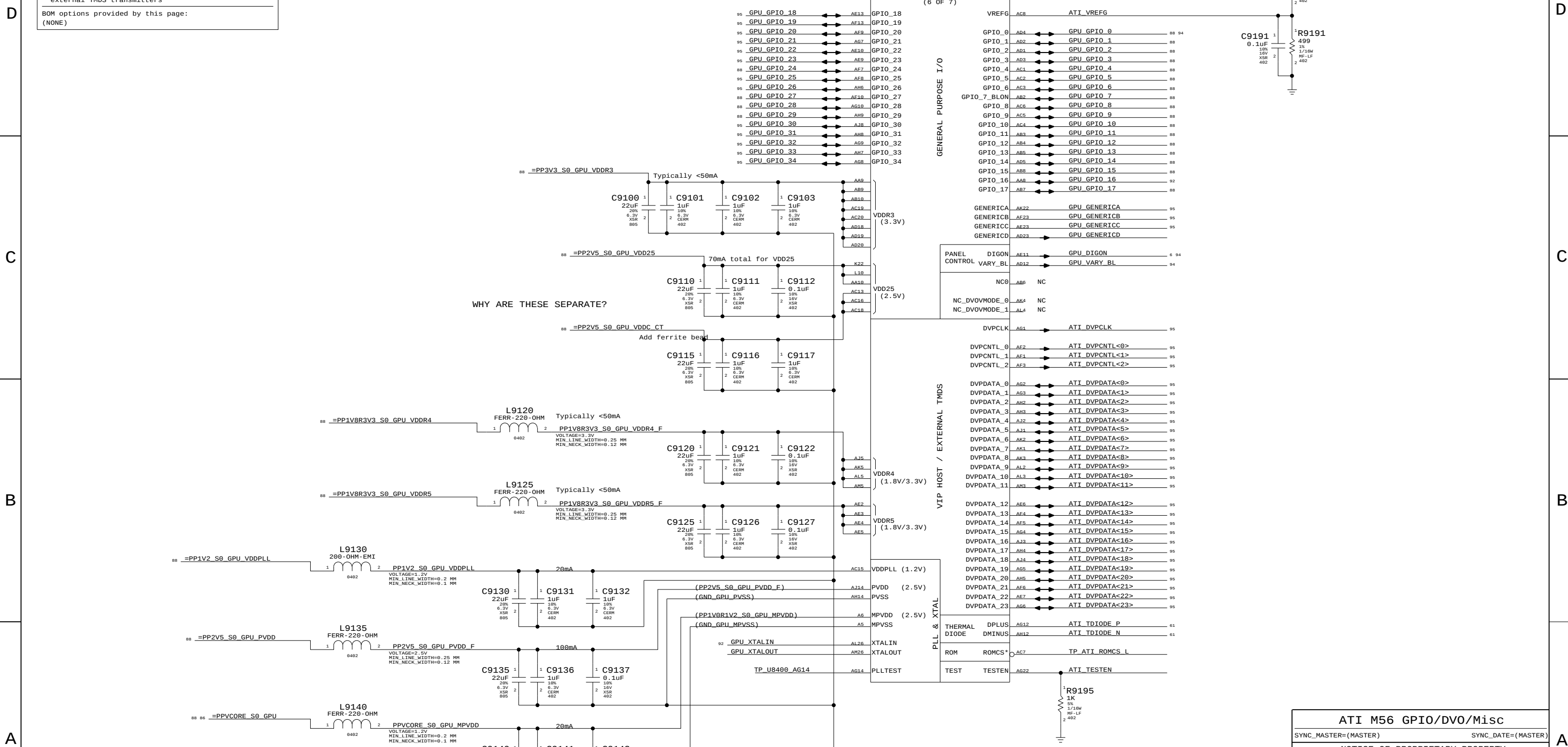
SIZE	DRAWING NUMBER	REV.
D	051-7124	13
SCALE	SHT	OF
NONE	90	110



[illegible]

- =PP3V3_GPU_GPIOIS
- =PP2V5_PVDD
- =PP1V8_GPU_LVDS_PLL

```
- =I2C_GPU_TMDS_SDA - I2C data line for
  external TMDS transmitters
- =I2C_GPU_TMDS_SCL - I2C clock line for
  external TMDS transmitters
```

BOM op
(NONE)

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

NONE	91	110
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[illegible]

Page Notes

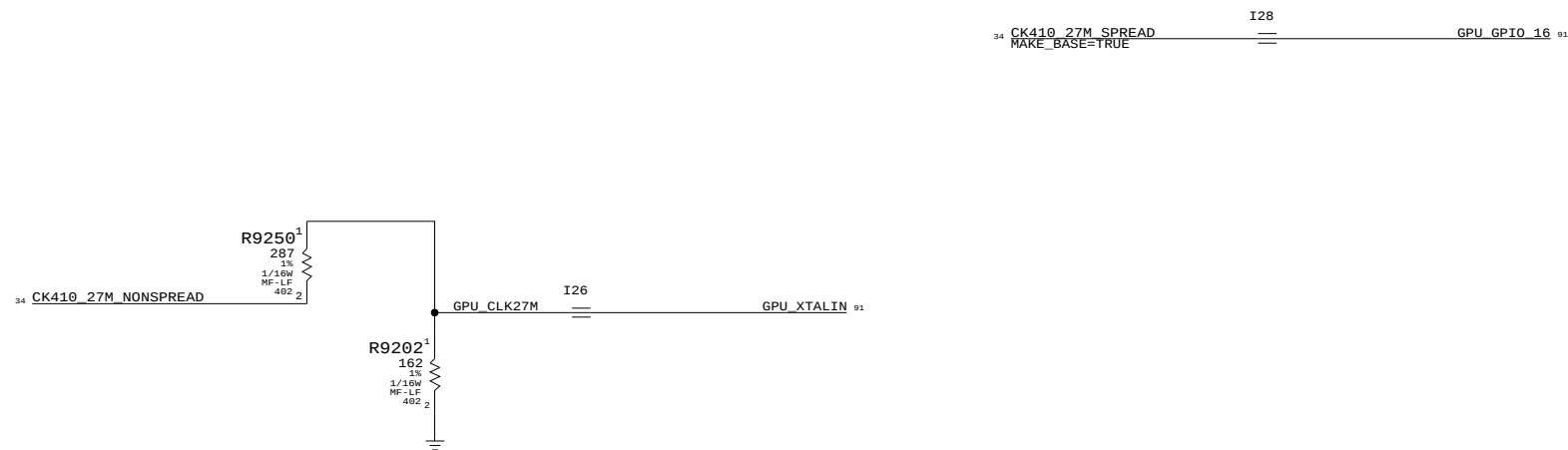
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- =PPVIN_GPU_LVDDR_LDO    - =PP2V5_GPU_PWRSEQ
- =PP2V5_GPU_LVDDR_LDO    - =PP1V8_GPU_PWRSEQ
                           - =PP1V5_GPU_PWRSEQ
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Signal aliases required by this page:
(NONE)

BOM options provided by this page:

- GPU_SS
- GPU_LVDDR_2V8



GPU CLOCKS

```
SYNC_MASTER=BOZEMAN
```

SYNC_DATE=05/21/2005

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APPLE COMPUTER INC.

SIZE
D

SIZE	DRAWING NUMBER
D	051-7124

REV.	13
------	----

SCALE	
NONE	

SHT	OF
92	110

Power aliases required by this page:

- =PP2V5_S0_GPU
- =PP1V8R2V5_S0_GPU_LVDDR

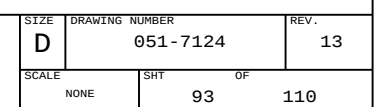
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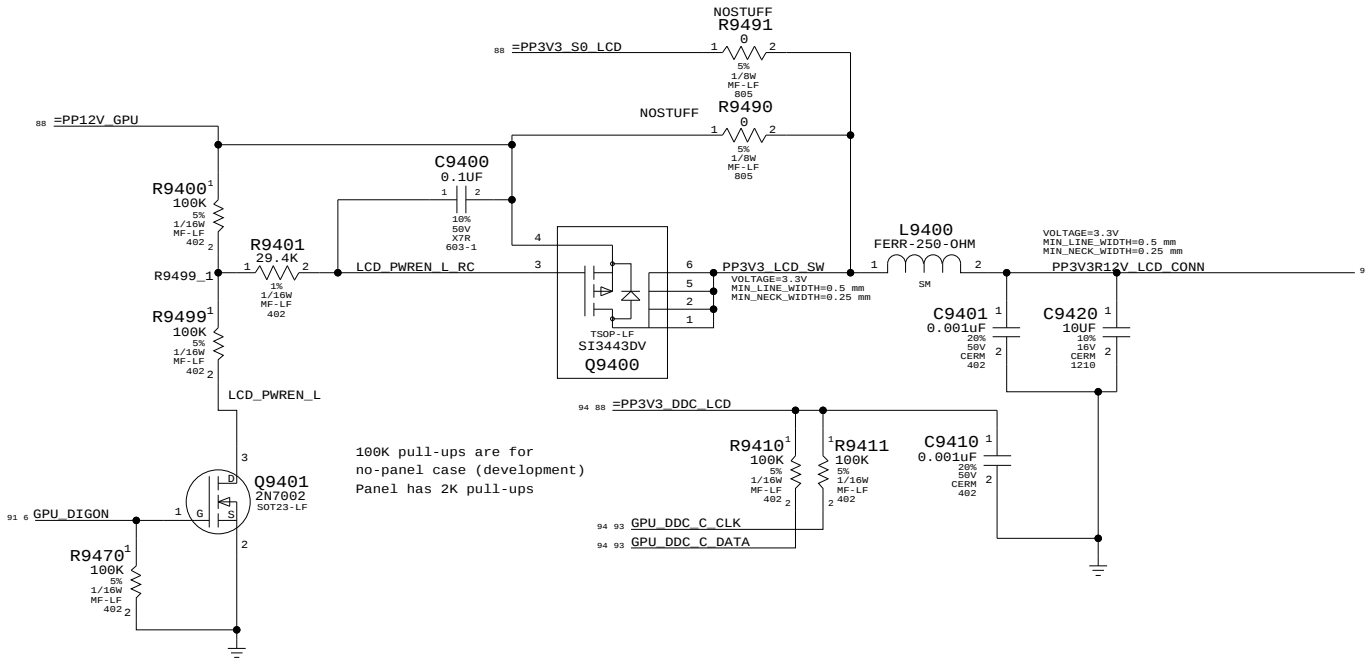
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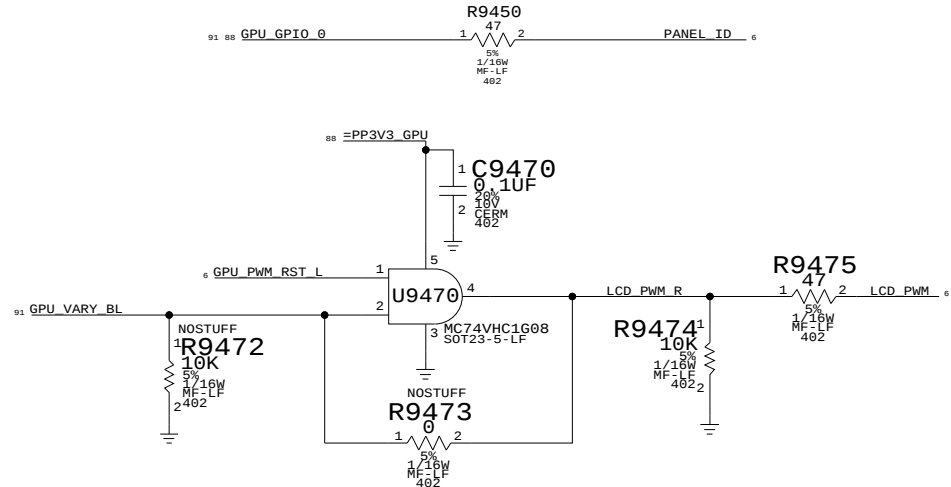
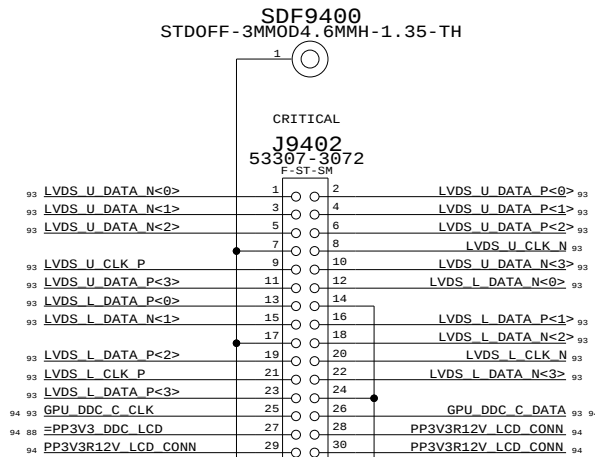
BOM options provided by this page:
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LCD (LVDS) INTERFACE



100K pull-ups are for
no-panel case (development)
Panel has 2K pull-ups



GATE TO PREVENT LEAKAGE ONTO PWM
MIGHT BE ABLE TO BYPASS IF SMC DRIVES SIGNAL

Internal Display Conns

SYNC_MASTER=BOZEMAN SYNC_DATE=04/27/2005

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	D	051-7124	13
	SCALE	SHT	OF
	NONE	94	110

[illegible]

	8	7	6	5	4	3	2	1	
D	FB_B_DQ<29>	FB_B_DQ<29> - @m60_11b.M60	87C3 9086	FSB_DIVN_L<3> - @m60_11b.M60	507 508 782 1284	FW_PC1 - @m60_11b.M60	4483	GPU_GP10_20 - @m60_11b.M60	91D5 9584
	FB_B_DQ<30>	FB_B_DQ<30> - @m60_11b.M60	87C3 9086	FSB_DPNR_L - @m60_11b.M60	507 783 1284	FW_PC2 - @m60_11b.M60	4483	GPU_GP10_21 - @m60_11b.M60	91D5 9584
	FB_B_DQ<31>	FB_B_DQ<31> - @m60_11b.M60	87C3 9086	FSB_DRDY_L - @m60_11b.M60	706 1284	FW_PORT0_TPA_FL_N - @m60_11b.M60	46C2	GPU_GP10_22 - @m60_11b.M60	91D5 9584
	FB_B_DQ<32>	FB_B_DQ<32> - @m60_11b.M60	5A5 5C6 87C3 90A2	FSB_DSTBN_L<0> - @m60_11b.M60	507 508 7C4 1284	FW_PORT0_TPA_FL_P - @m60_11b.M60	46C2	GPU_GP10_23 - @m60_11b.M60	91D5 9584
	FB_B_DQ<33>	FB_B_DQ<33> - @m60_11b.M60	87C3 9083	FSB_DSTBN_L<1> - @m60_11b.M60	507 508 784 1284	FW_PORT0_TPB_FL_N - @m60_11b.M60	46C2	GPU_GP10_24 - @m60_11b.M60	88B5 91D5
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	FB_B_DQ<36>	FB_B_DQ<36> - @m60_11b.M60	87C3 9083	FSB_DSTBP_L<0> - @m60_11b.M60	507 508 7C4 1284	FW_PORT1_TPA_FL_P - @m60_11b.M60	46B2	GPU_GP10_27 - @m60_11b.M60	88B5 91D5
	FB_B_DQ<37>	FB_B_DQ<37> - @m60_11b.M60	87C3 9083	FSB_DSTBP_L<1> - @m60_11b.M60	507 508 784 1284	FW_PORT1_TPB_FL_N - @m60_11b.M60	46B2	GPU_GP10_28 - @m60_11b.M60	88B5 91D5
	FB_B_DQ<38>	FB_B_DQ<38> - @m60_11b.M60	87C3 90A3	FSB_DSTBP_L<2> - @m60_11b.M60	507 508 7C2 1284	FW_PORT1_TPB_FL_P - @m60_11b.M60	46B2	GPU_GP10_29 - @m60_11b.M60	88B5 91C5
C	FB_B_DQ<39>	FB_B_DQ<39> - @m60_11b.M60	87C3 90A3	FSB_DSTBP_L<3> - @m60_11b.M60	507 508 782 1284	FW_R0 - @m60_11b.M60	44C4	GPU_GP10_30 - @m60_11b.M60	91C5 95C4
	FB_B_DQ<40>	FB_B_DQ<40> - @m60_11b.M60	5A5 5C6 87C3 90A2	FSB_D_L<0> - @m60_11b.M60	507 508 7C4 12D6	FW_R1 - @m60_11b.M60	44C4	GPU_GP10_31 - @m60_11b.M60	91C5 95C4
	FB_B_DQ<41>	FB_B_DQ<41> - @m60_11b.M60	87C3 90A3	FSB_D_L<1> - @m60_11b.M60	7C4 12D6	FW_RESET_L - @m60_11b.M60	44C2 44C3	GPU_GP10_32 - @m60_11b.M60	91C5 95C4
	FB_B_DQ<42>	FB_B_DQ<42> - @m60_11b.M60	87C3 90A3	FSB_D_L<2> - @m60_11b.M60	7C4 12D6	FW_ROM_CLK - @m60_11b.M60	44B4	GPU_GP10_33 - @m60_11b.M60	91C5 95C4
	FB_B_DQ<43>	FB_B_DQ<43> - @m60_11b.M60	87C3 90A3	FSB_D_L<3> - @m60_11b.M60	7C4 12D6	FW_SE - @m60_11b.M60	44B3	GPU_GP10_34 - @m60_11b.M60	91C5 95C4
	FB_B_DQ<44>	FB_B_DQ<44> - @m60_11b.M60	87B3 90A3	FSB_D_L<4> - @m60_11b.M60	7C4 12D6	FW_SM - @m60_11b.M60	44B3	GPU_H2SYNC - @m60_11b.M60	93B3 97A4
	FB_B_DQ<45>	FB_B_DQ<45> - @m60_11b.M60	87B3 90A3	FSB_D_L<5> - @m60_11b.M60	7C4 12D6	FW_TEST - @m60_11b.M60	44B3	GPU_HPD - @m60_11b.M60	93A5 97C1
	FB_B_DQ<46>	FB_B_DQ<46> - @m60_11b.M60	87B3 90A3	FSB_D_L<6> - @m60_11b.M60	7C4 12D6	FW_TPA_C<0> - @m60_11b.M60	46B8	GPU_HSYNC_BUF - @m60_11b.M60	97A4
	FB_B_DQ<47>	FB_B_DQ<47> - @m60_11b.M60	87B3 90A3	FSB_D_L<7> - @m60_11b.M60	7C4 12D6	FW_TPA_C<1> - @m60_11b.M60	46B7	GPU_MEMTEST - @m60_11b.M60	87A3
	FB_B_DQ<48>	FB_B_DQ<48> - @m60_11b.M60	5A5 5C6 87B3 90A2	FSB_D_L<8> - @m60_11b.M60	7C4 12D6	FW_VP - @m60_11b.M60	46D4	GPU_MVREFD0 - @m60_11b.M60	87B7
B	FB_B_DQ<49>	FB_B_DQ<49> - @m60_11b.M60	87B3 90A3	FSB_D_L<9> - @m60_11b.M60	7C4 12D6	FW_XTAL_X0 - @m60_11b.M60	44D2 44D3	GPU_MVREFD1 - @m60_11b.M60	87B3
	FB_B_DQ<50>	FB_B_DQ<50> - @m60_11b.M60	87B3 90A3	FSB_D_L<10> - @m60_11b.M60	7C4 12D6	FW_XTAL_X1 - @m60_11b.M60	44D2 44D3	GPU_MVREFS0 - @m60_11b.M60	87B7

8			7			6			5			4			3			2			1		
ZH608MTGHOLEm60[9D3] ZH609MTGHOLEm60[9D2] ZH610MTGHOLEm60[9D2]																							
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