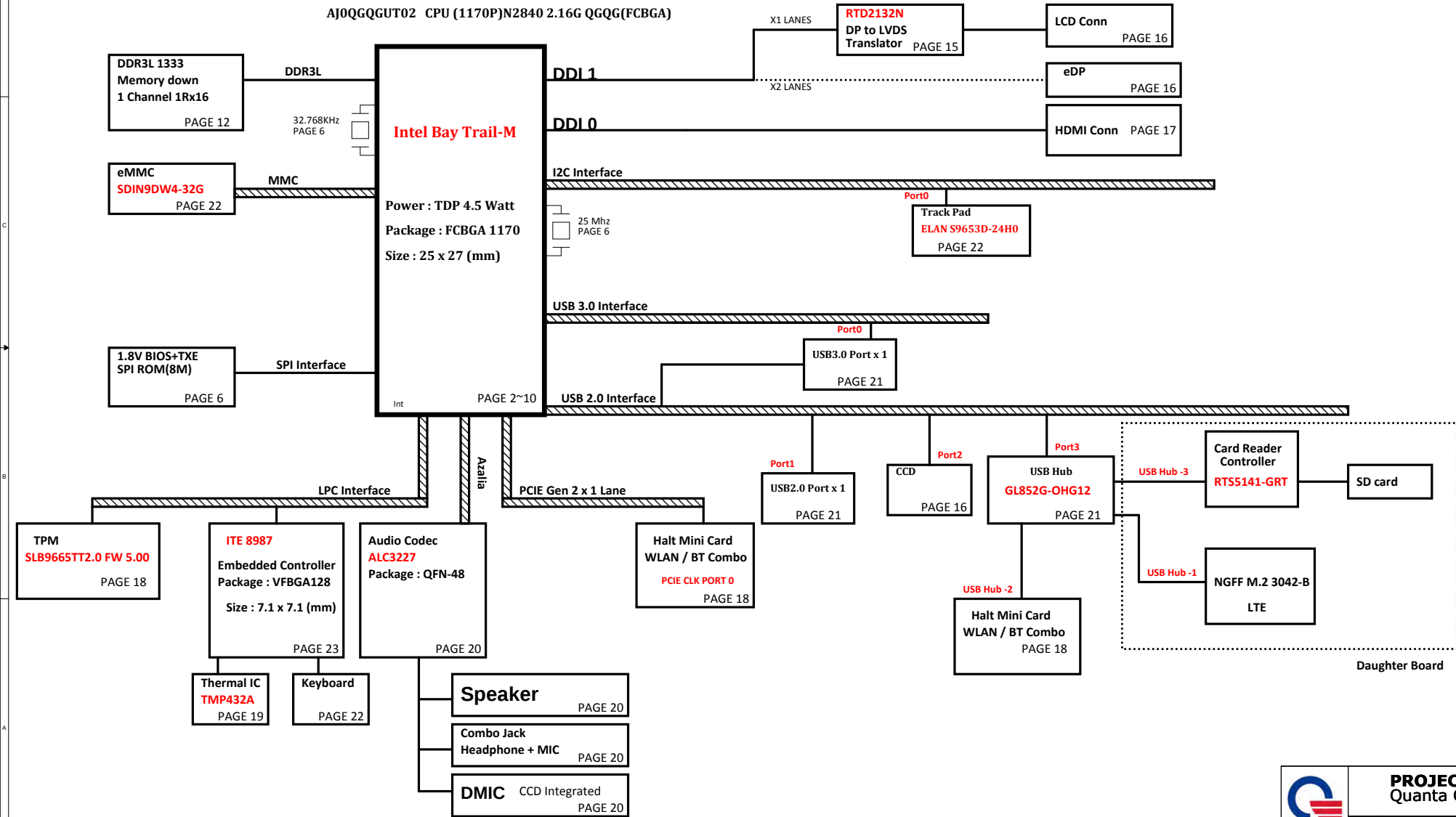
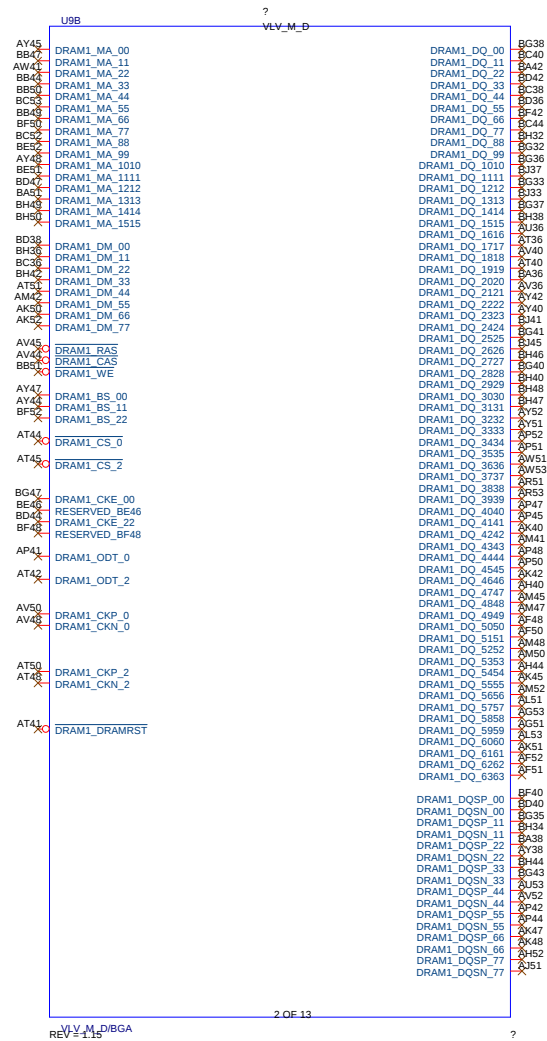


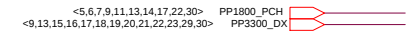
Y0A Intel Bay Trail-M Platform Block Diagram

AJ0QGQLUT02 CPU(1170P)N2940 1.83G QGQL(FCBGA)

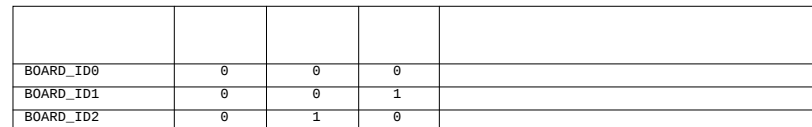
AJ0QGQGUT02 CPU (1170P)N2840 2.16G QGQG(FCBGA)



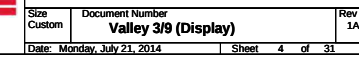


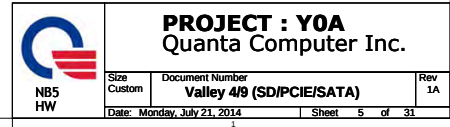


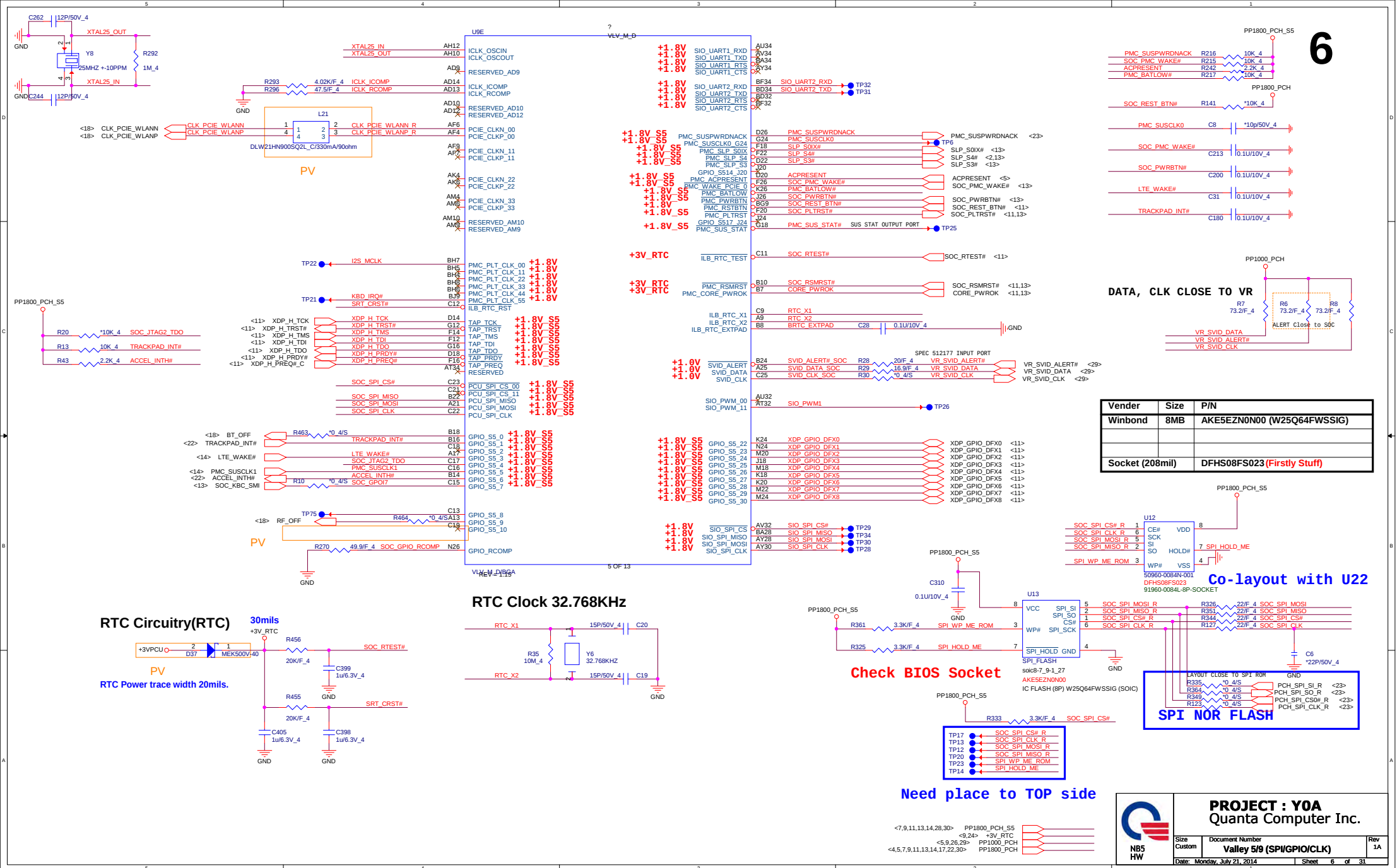
PP1800 PCH



BOARD_ID0	0	0	0	
BOARD_ID1	0	0	1	
BOARD_ID2	0	1	0	

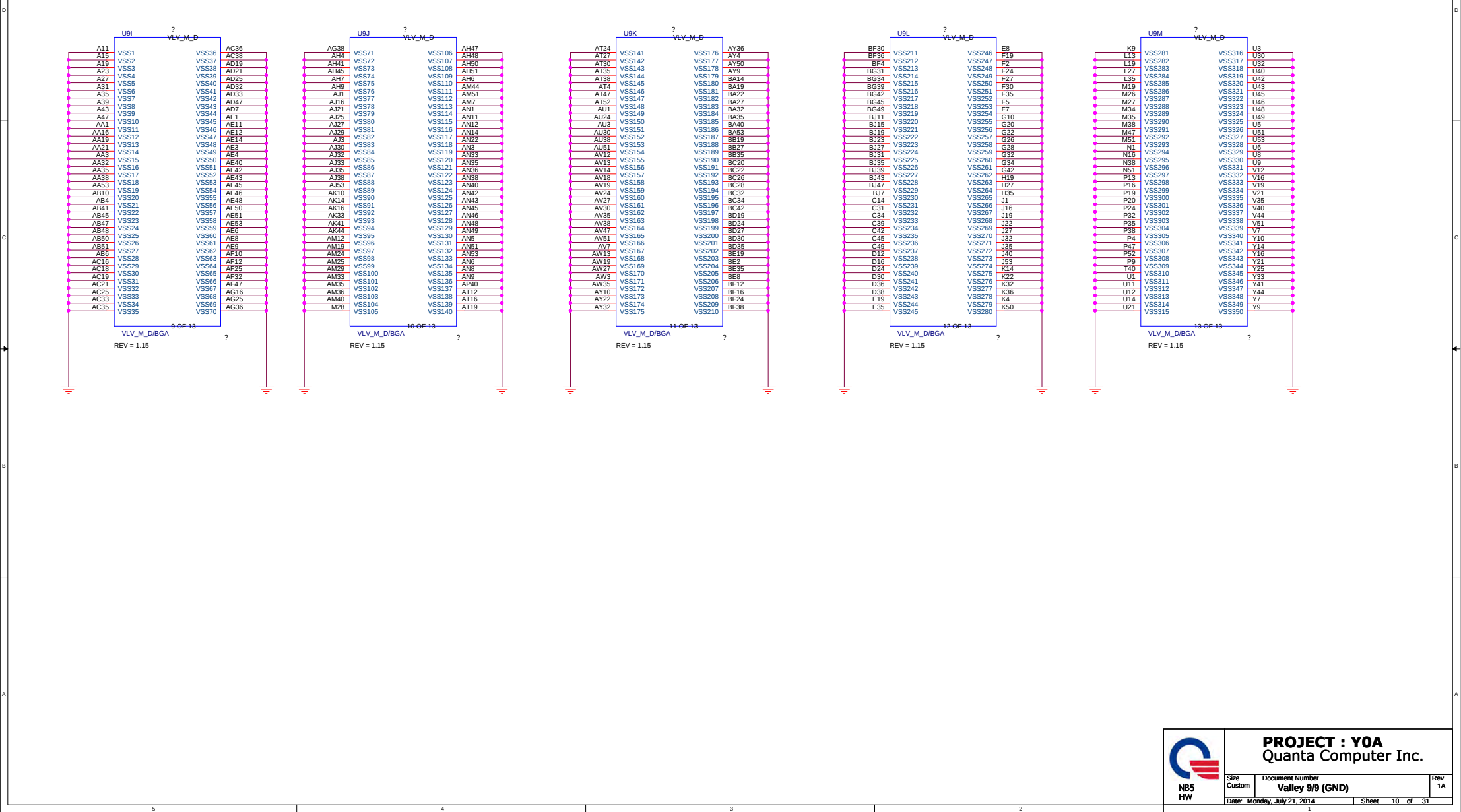


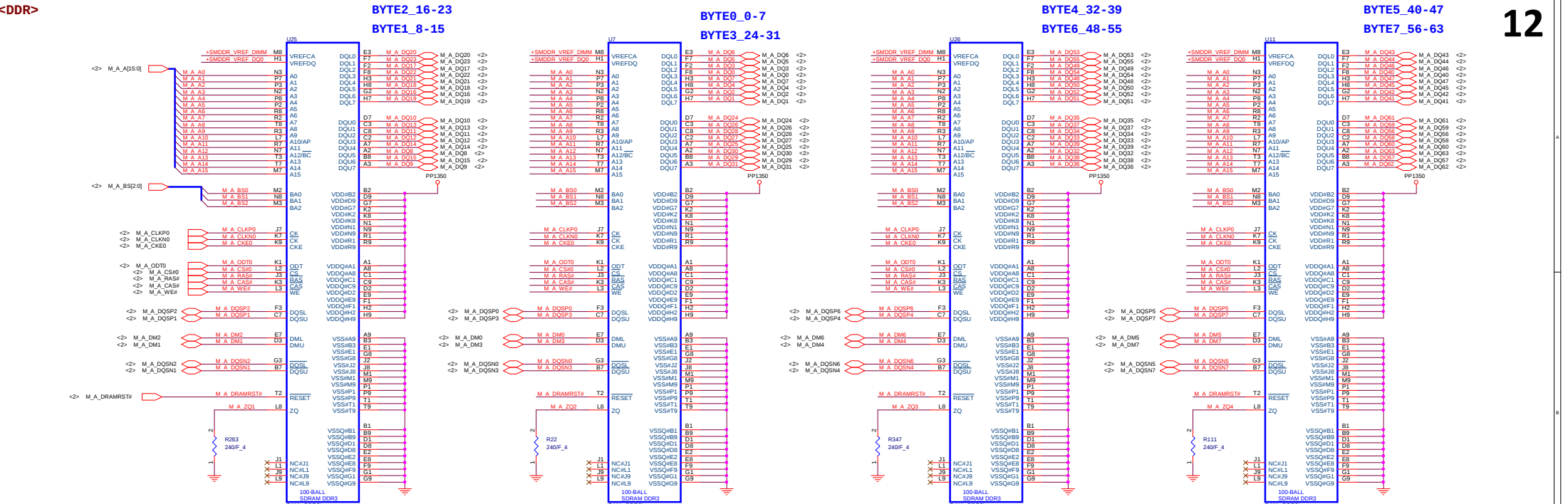




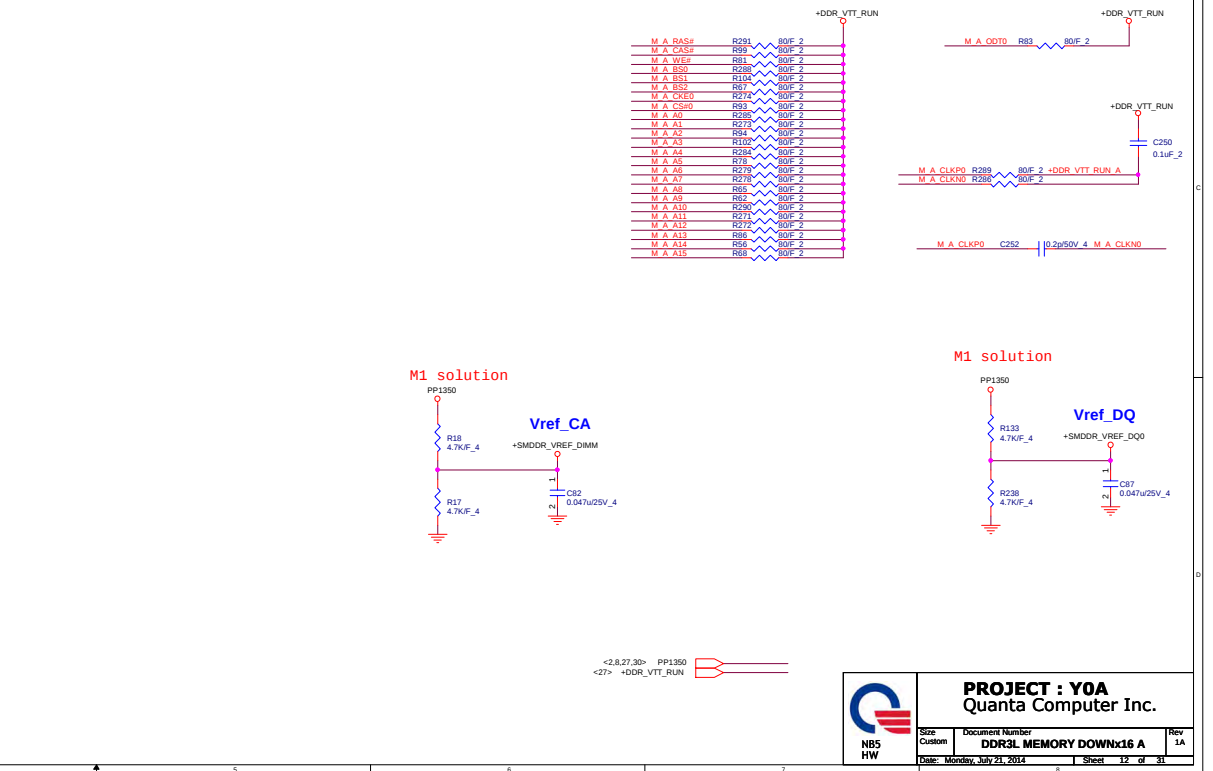
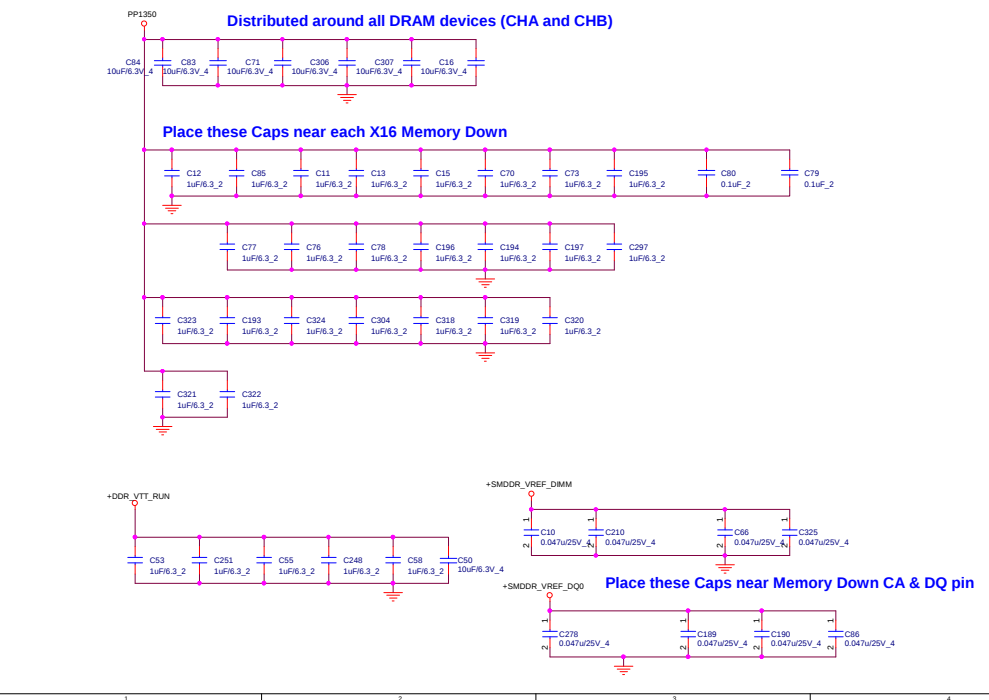


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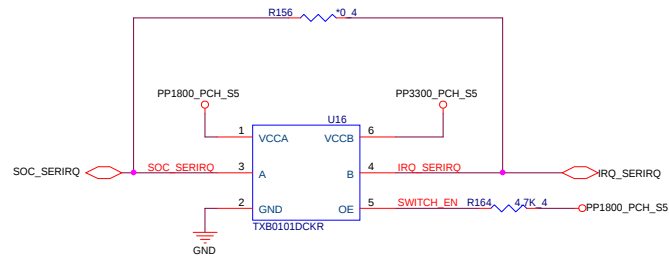




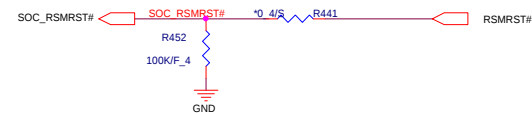
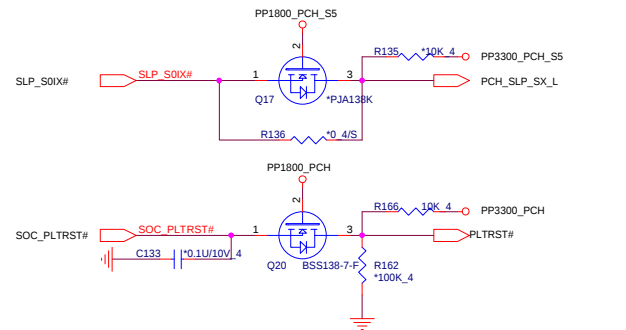
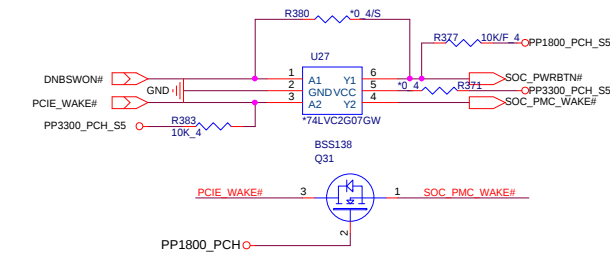
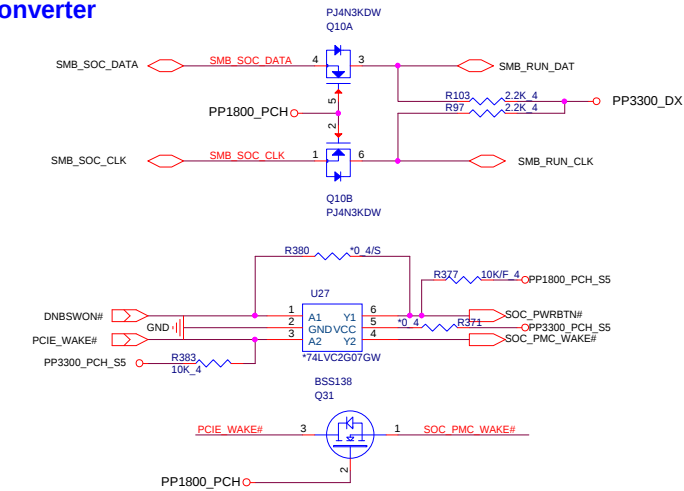
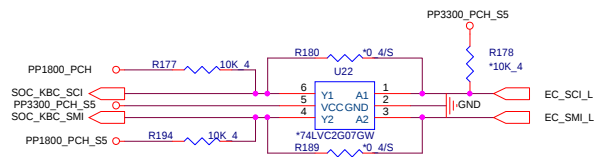
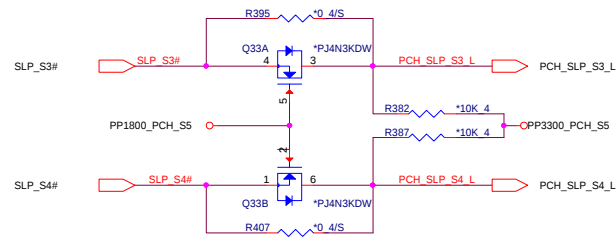
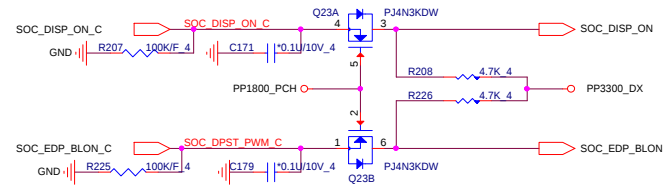
Vendor	P/N
Hynix	AKD5JGET400
Elpida	AKD5JGET404



eDP control pin (For eDP)

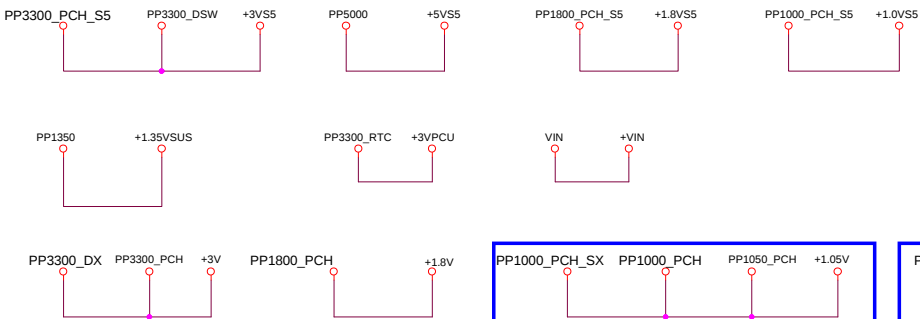
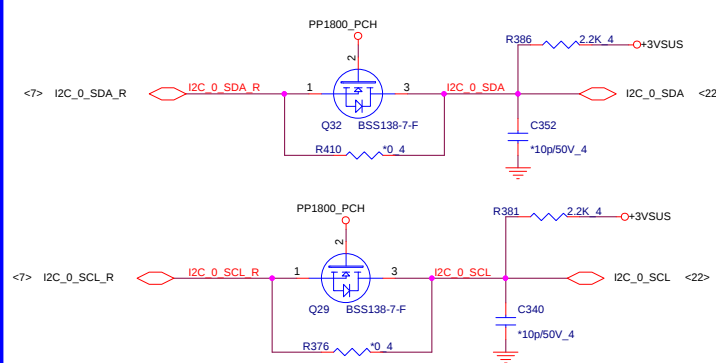
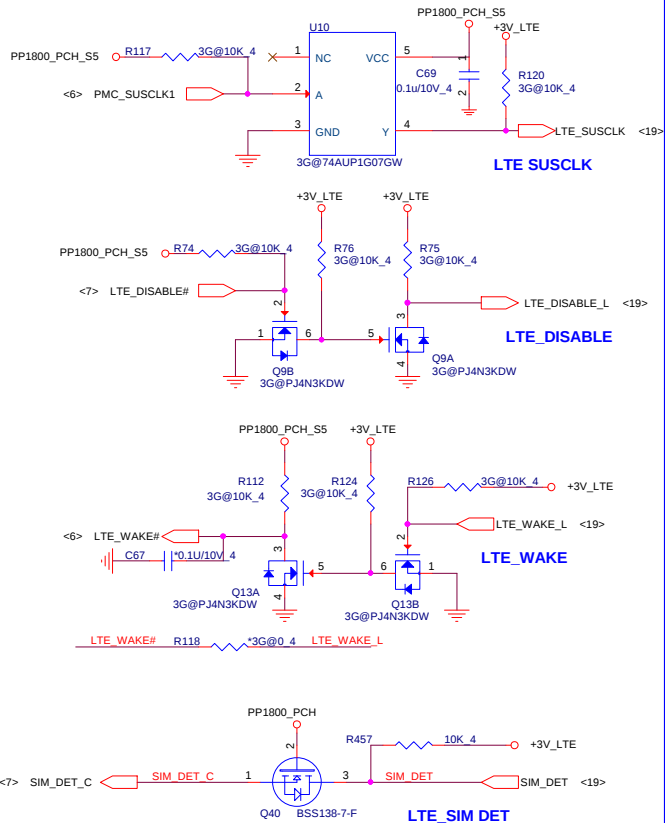


LVDS converter



PP1800_PCH_S5
PP3300_PCH_S5
PP1800_PCH
PP3300_PCH
PP3300_DX

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<6,7,9,11,13,28,30> PP1800_PCH_S5
 <19> +3V_LTE
 <4,5,6,7,9,11,13,17,22,30> PP1800_PCH
 <22,27,30> +3VSUS
 <2,8,12,27,30> PP1350

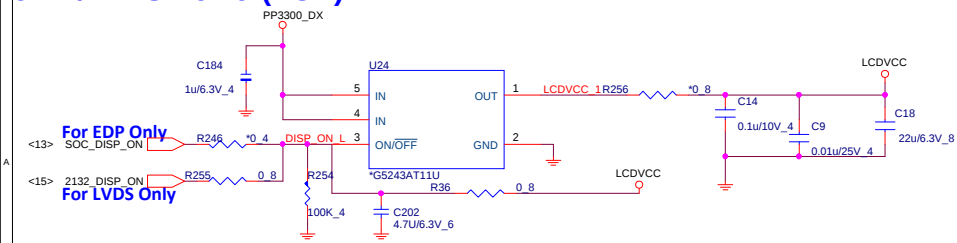
Combine +1.0VSX,+1.0V,+1.05V
 power rail for cost saving

Combine +1.35VSFR,+1.35V
 power rail for cost saving

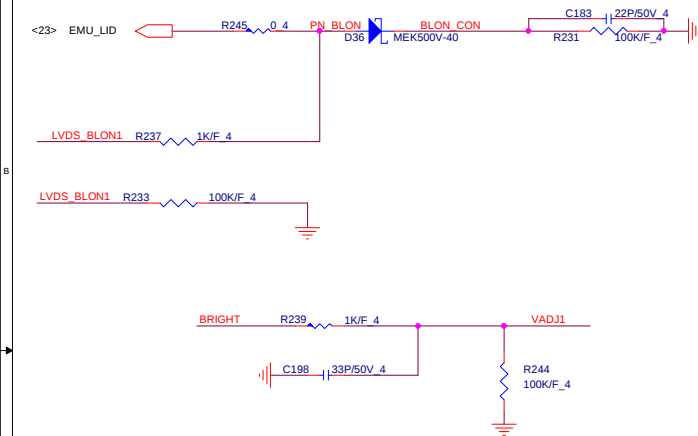
PROJECT : Y0A
 Quanta Computer Inc.

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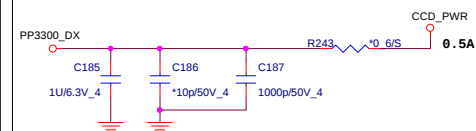
eDP /LVDS Power(VGA)



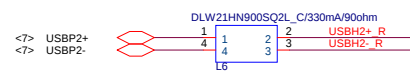
LID Switch



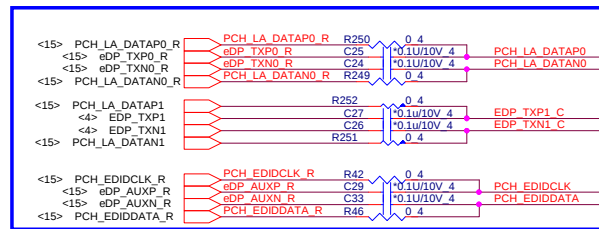
CCD power(CCD)



CCD USB(CCD)

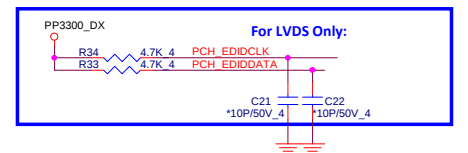
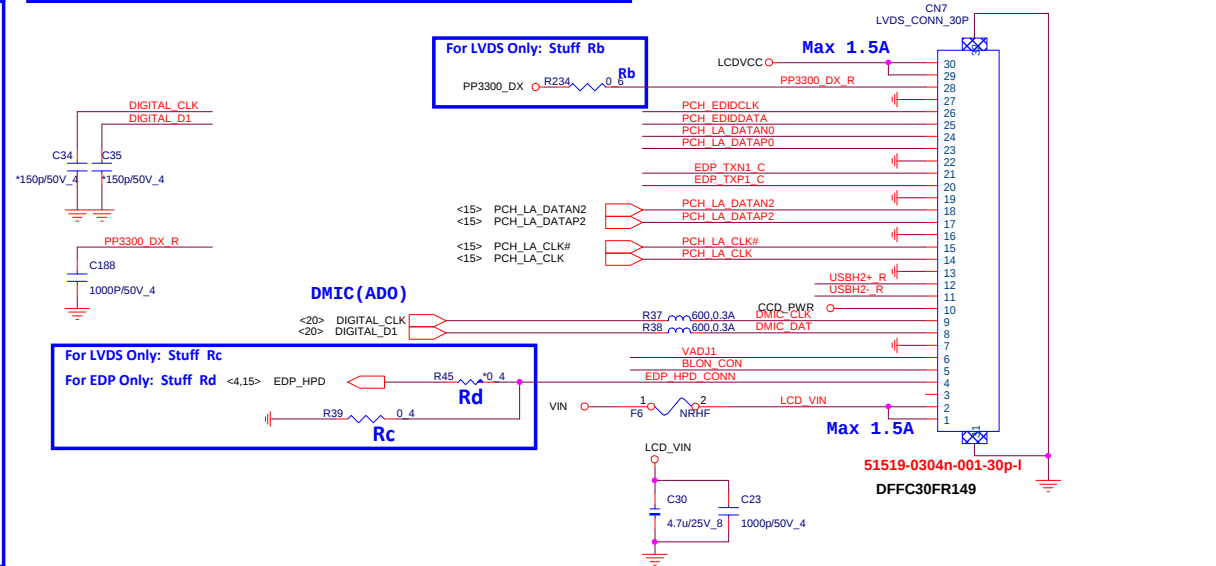


For LVDS Only stuff R, For EDP only stuff C



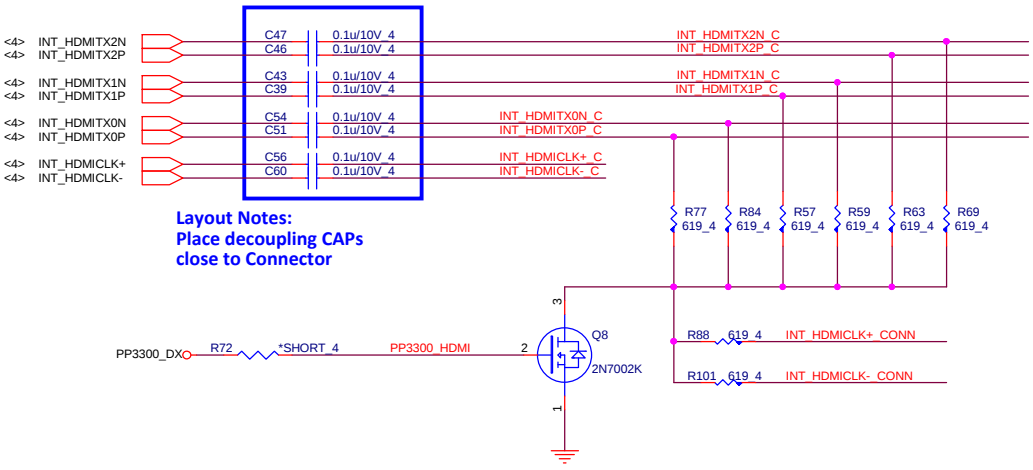
eDP/LVDS

Need New LCD Cable

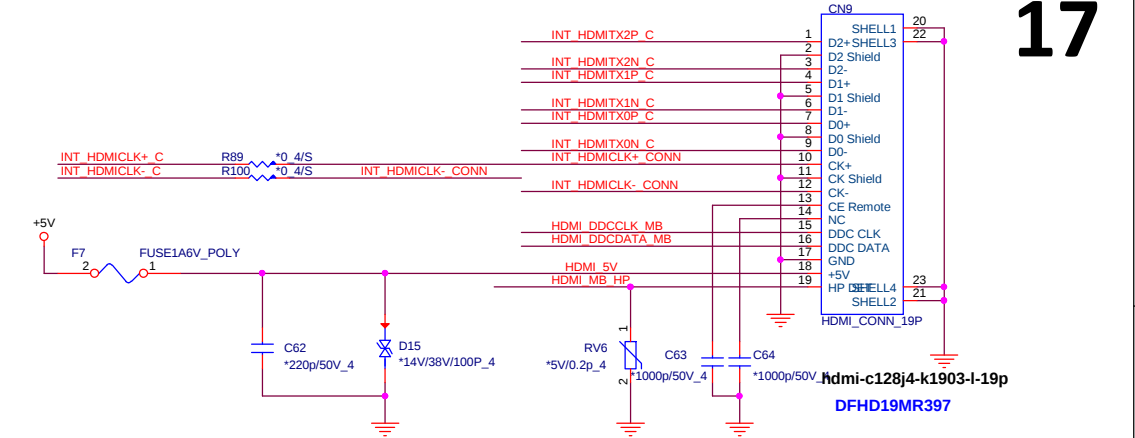


<4,9,13,15,17,18,19,20,21,22,23,29,30> PP3300_DX
 <19,24,25,26,27,29,30,31> VIN

HDMI Cost Reduced level shift (HDM)

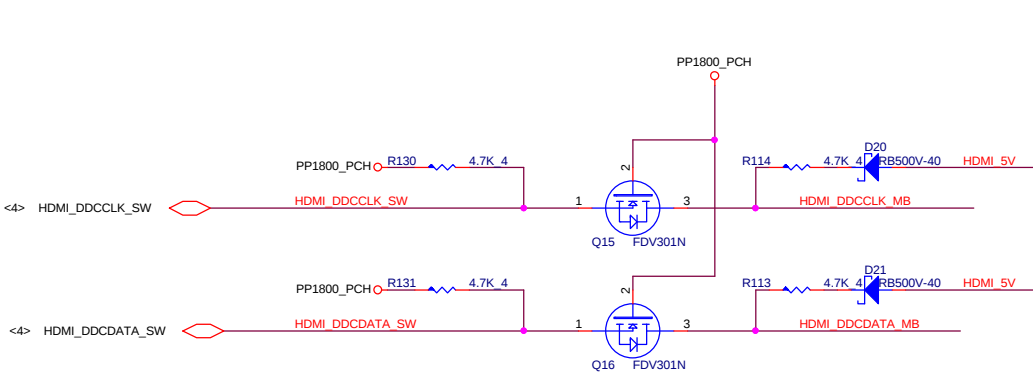


HDMI connector (HDM)

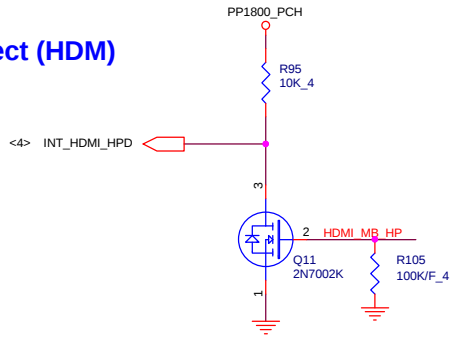


17

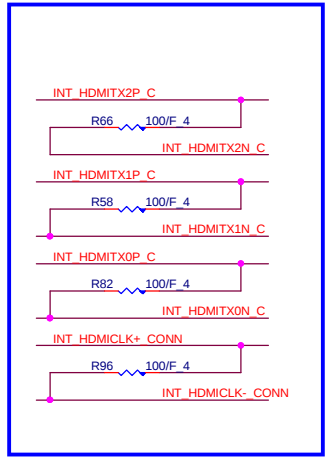
HDMI DDC (HDM)



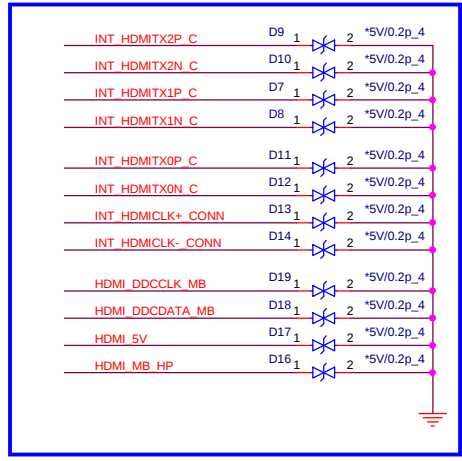
HDMI-detect (HDM)



EMI



ESD 靠近HDMI CONNECTOR(CN16)



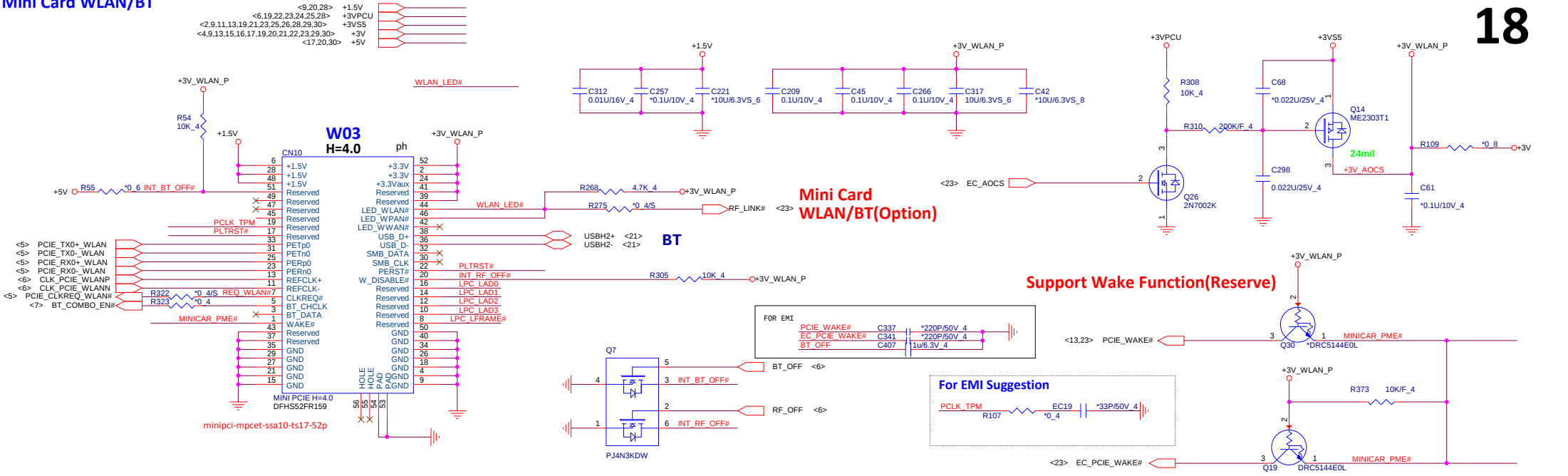
<4,9,13,15,16,18,19,20,21,22,23,29,30> PP3300_DX
<4,5,6,7,9,11,13,14,22,30> PP1800_PCH
<18,20,30> +5V



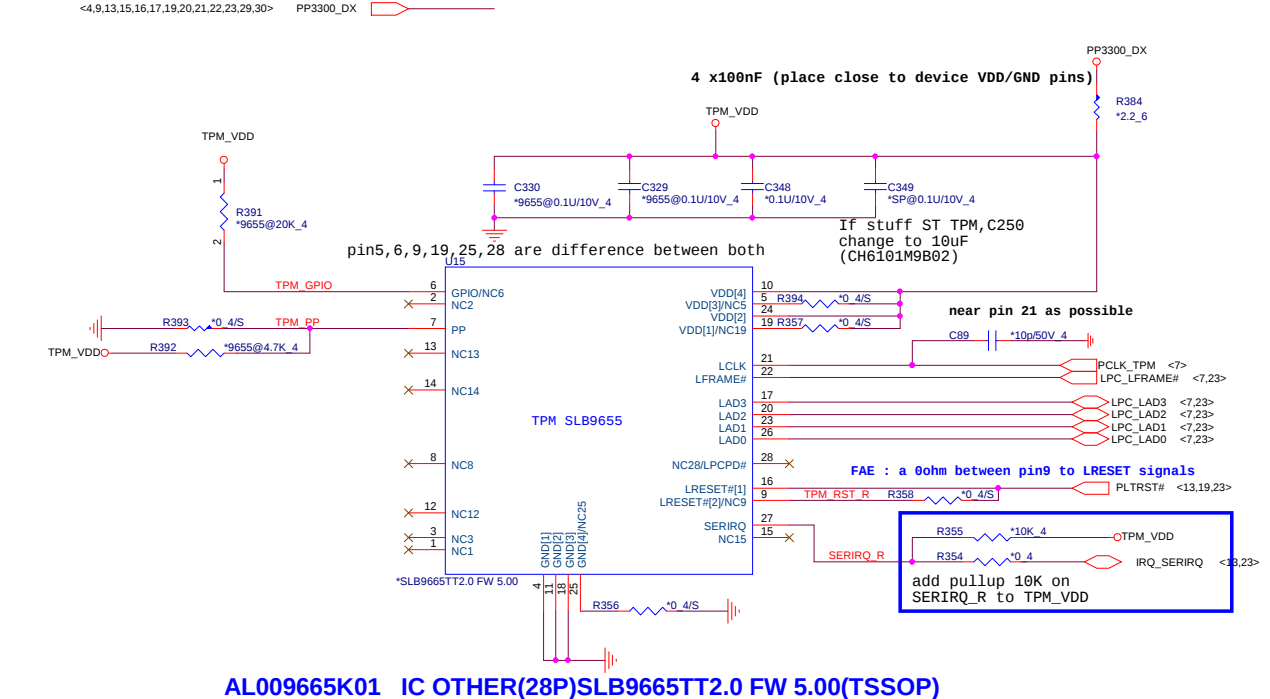
PROJECT : Y0A
Quanta Computer Inc.

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HDMI		
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Mini Card WLAN/BT

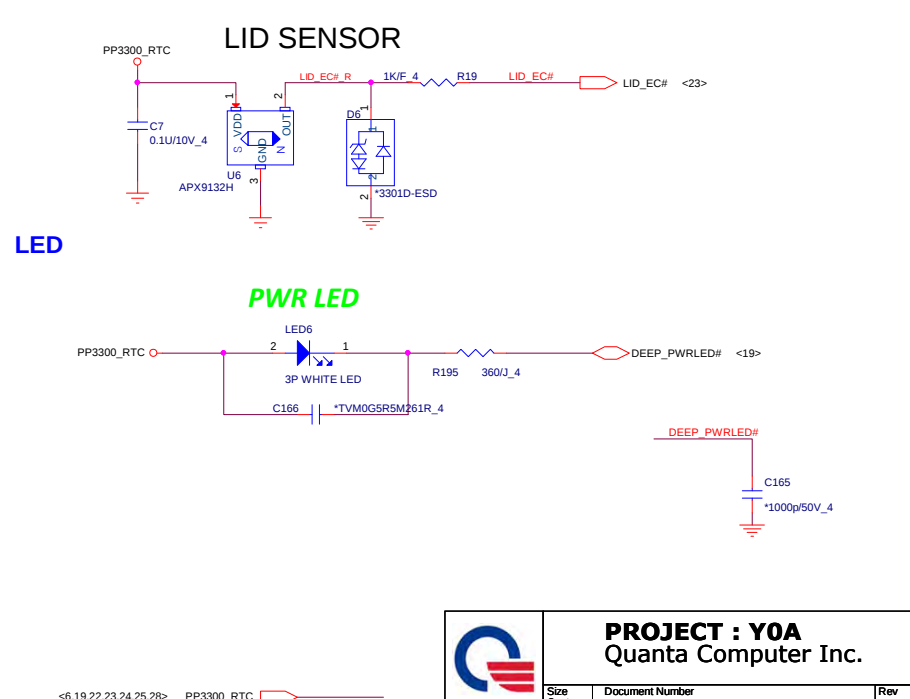


TPM (CLG)



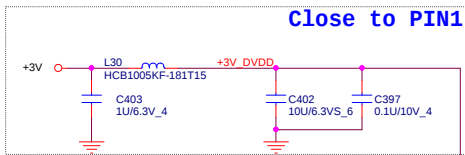
AL009665K01 IC OTHER(28P)SLB9665TT2.0 FW 5.00(TSSOP)

LID

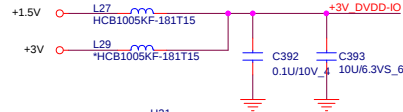


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	WLAN/TPM/LED/LID/SW		
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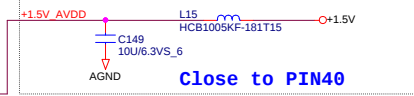
Close to PIN1



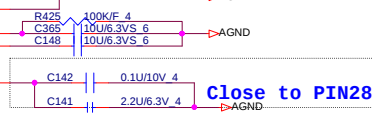
>40mils trace



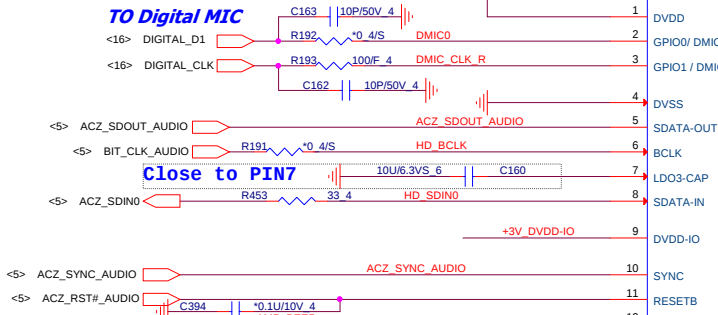
Close to PIN26



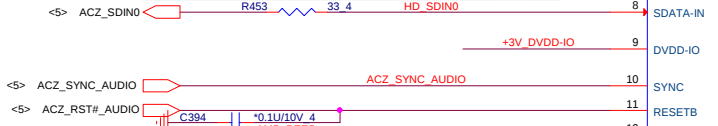
Close to PIN28



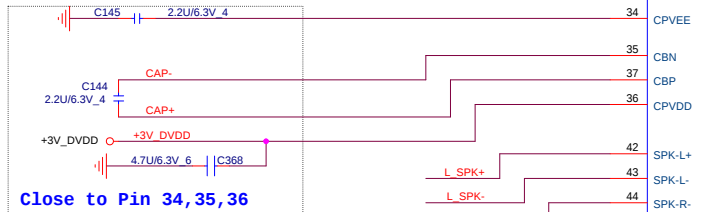
TO Digital MIC



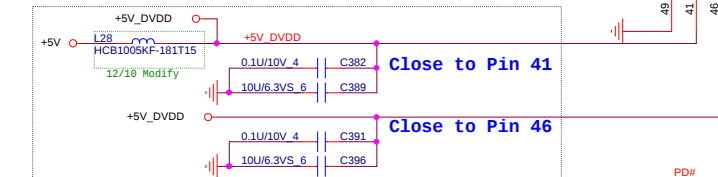
Close to PIN7



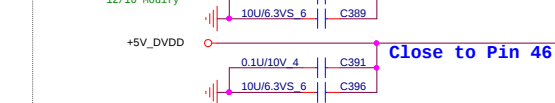
Close to Pin 34,35,36



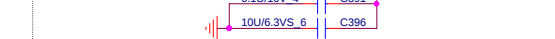
TO Internal Speakers



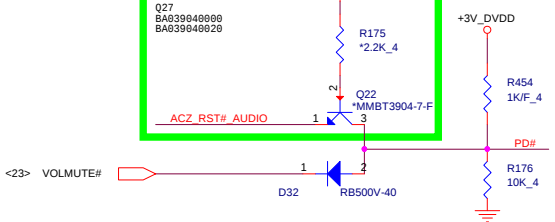
Close to Pin 41



Close to Pin 46

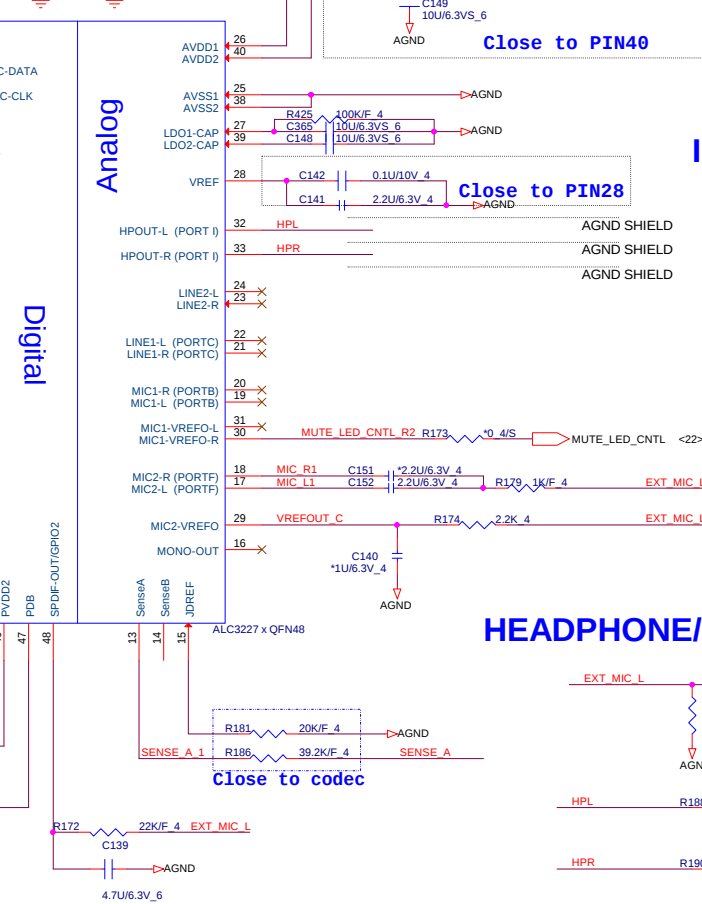


for intel HSW ULT



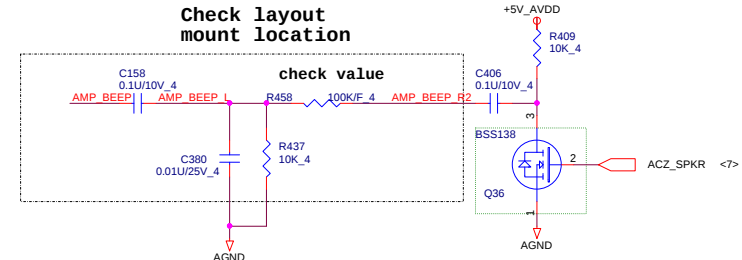
Analog

Digital

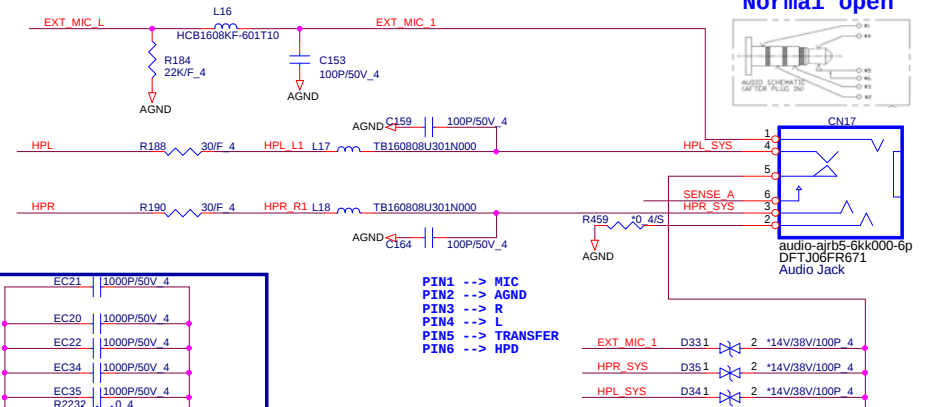


Internal Speaker

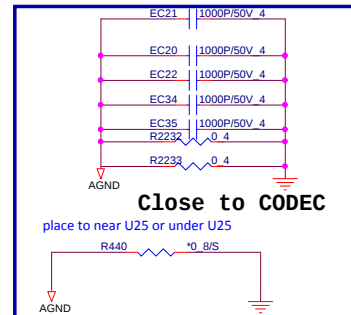
40mil for each signal



HEADPHONE/Mic combo(ADO)

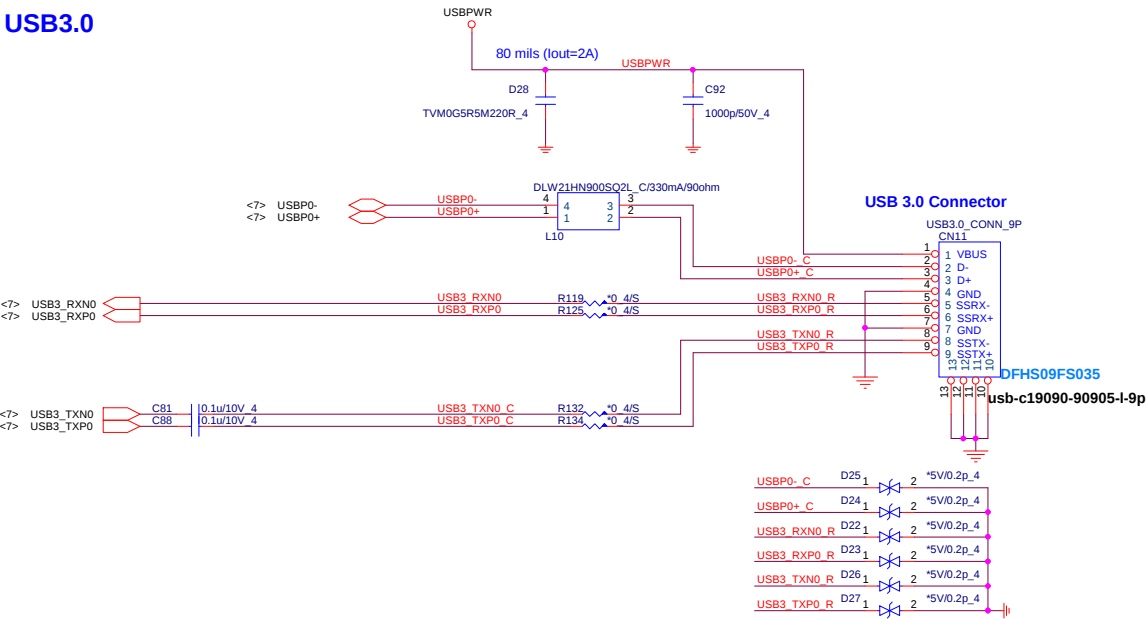


Close to CODEC

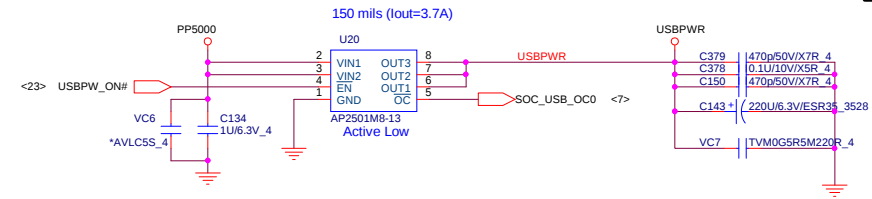


ESD 2'nd CY00G050B00
ESD 2'nd CY00G050B00

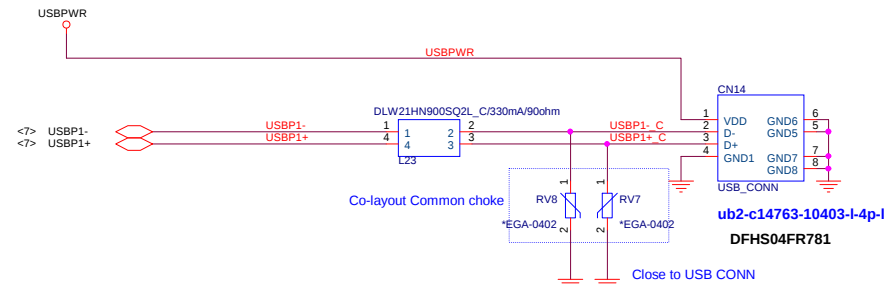
USB3.0



USB POWER SWITCH

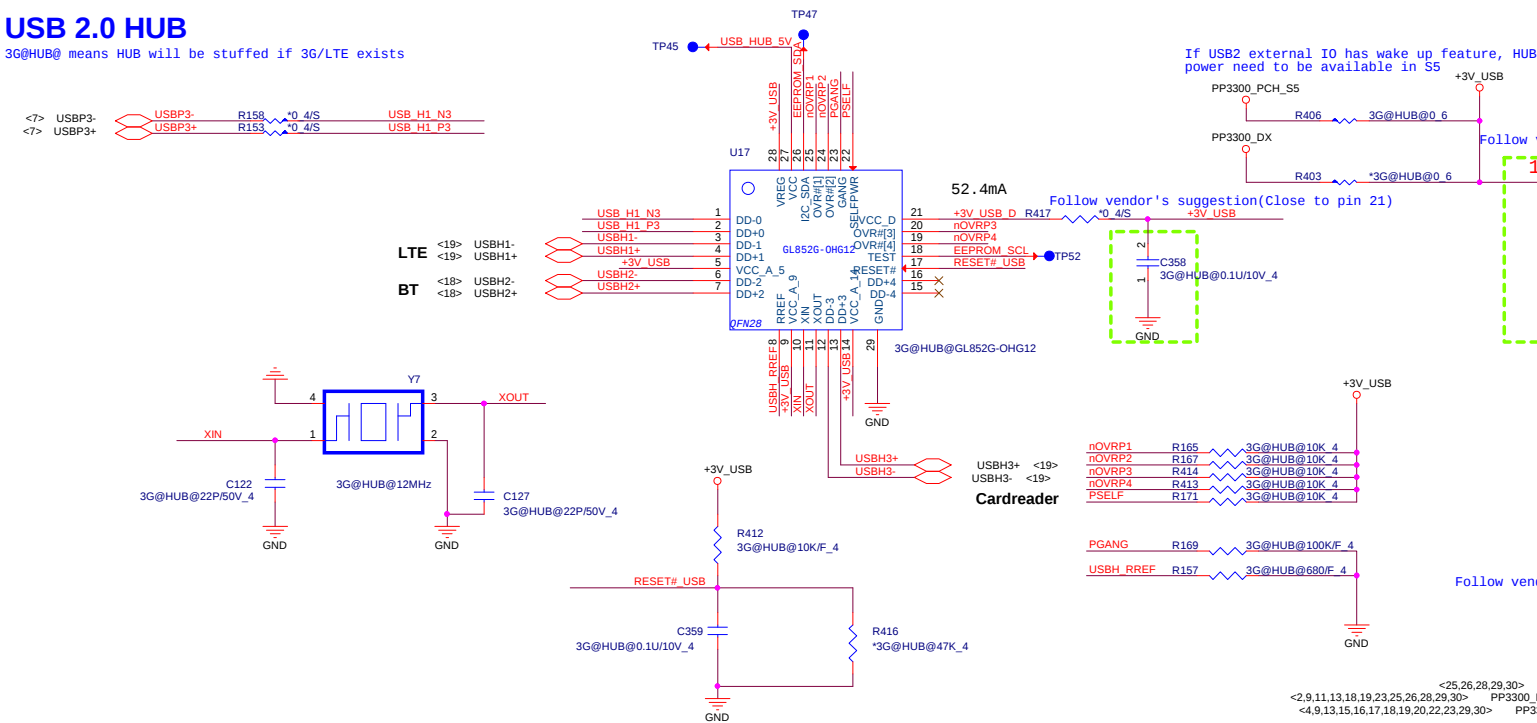


USB 2.0



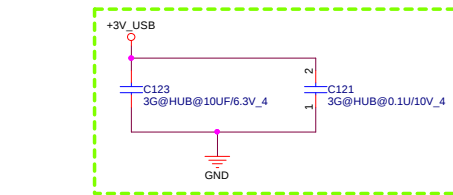
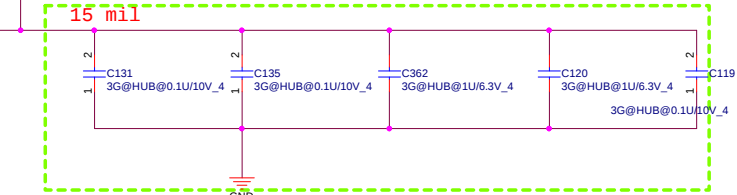
USB 2.0 HUB

3G@HUB@ means HUB will be stuffed if 3G/LTE exists



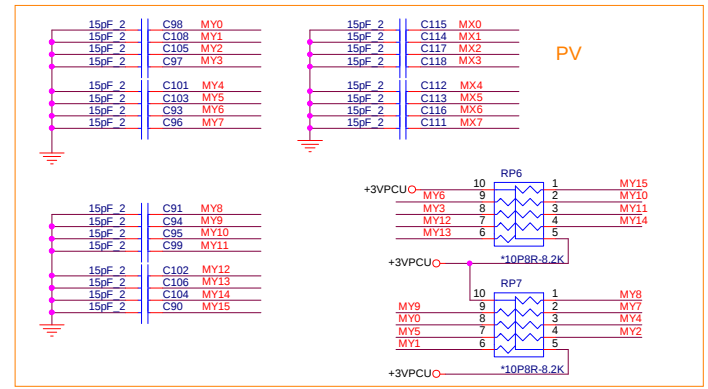
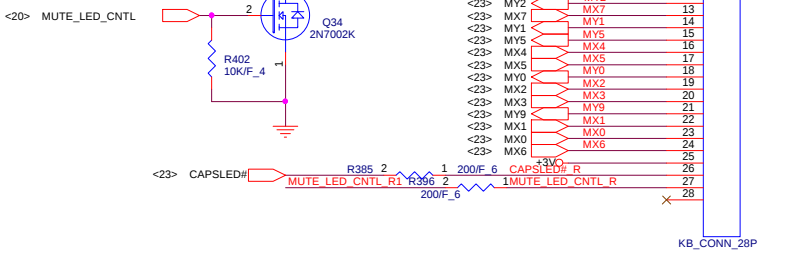
If USB2 external IO has wake up feature, HUB power need to be available in S5

Follow vendor's suggestion(Close to GL850G-31)

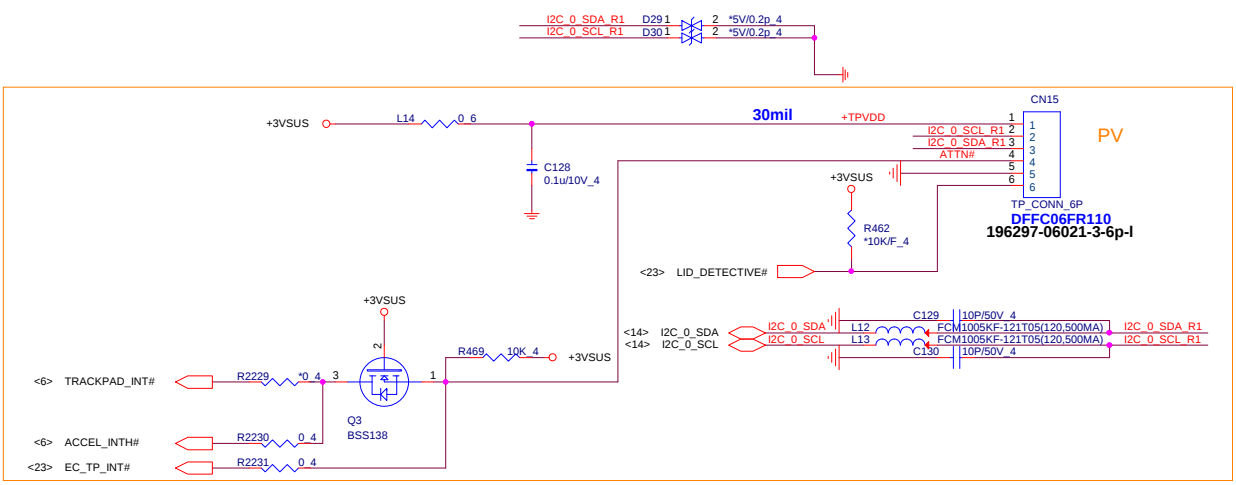


Follow vendor's suggestion(Close to pin 28)

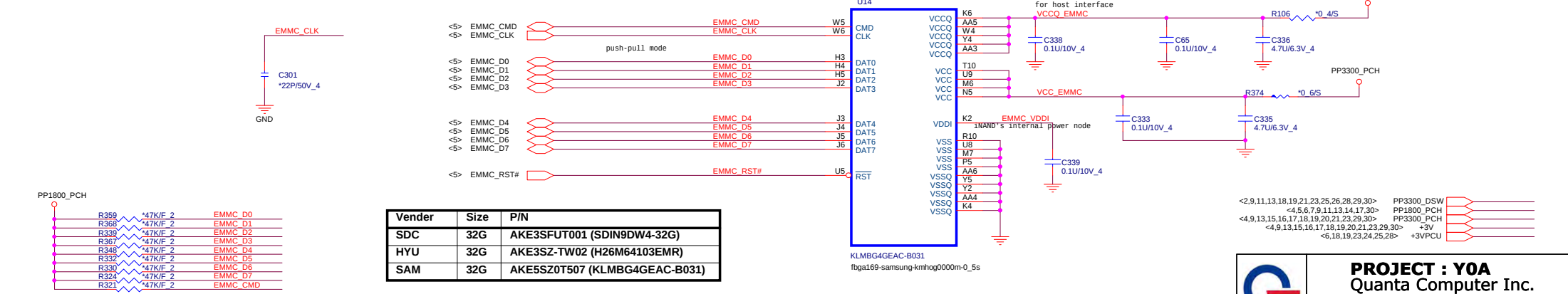
K/B (KBC) NEW Change



Track PAD BOARD CONN (TPD)



EMMC (CBS)

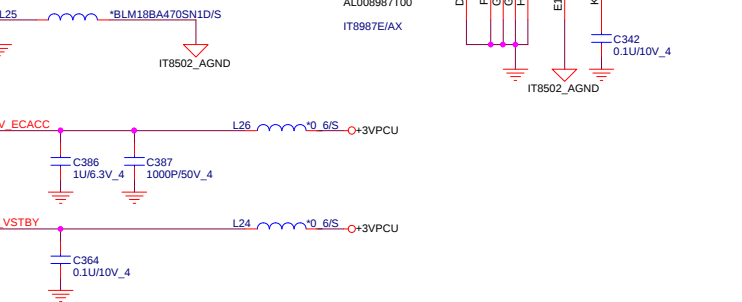
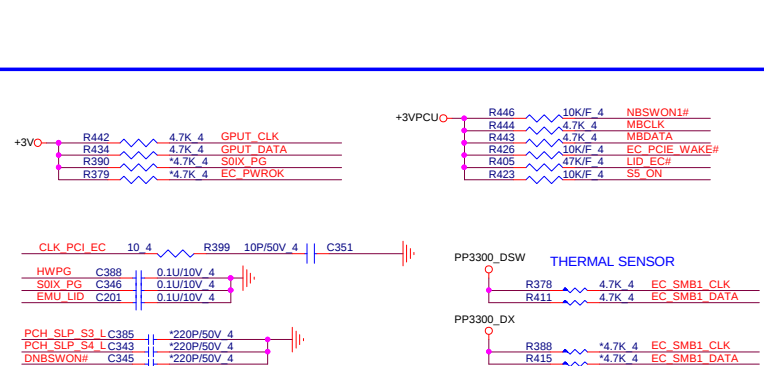
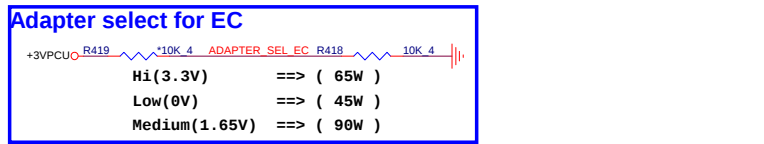
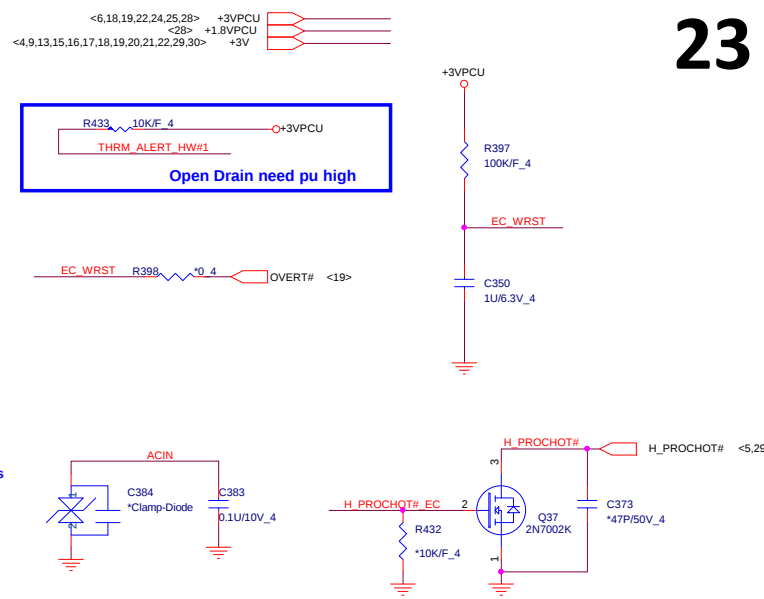
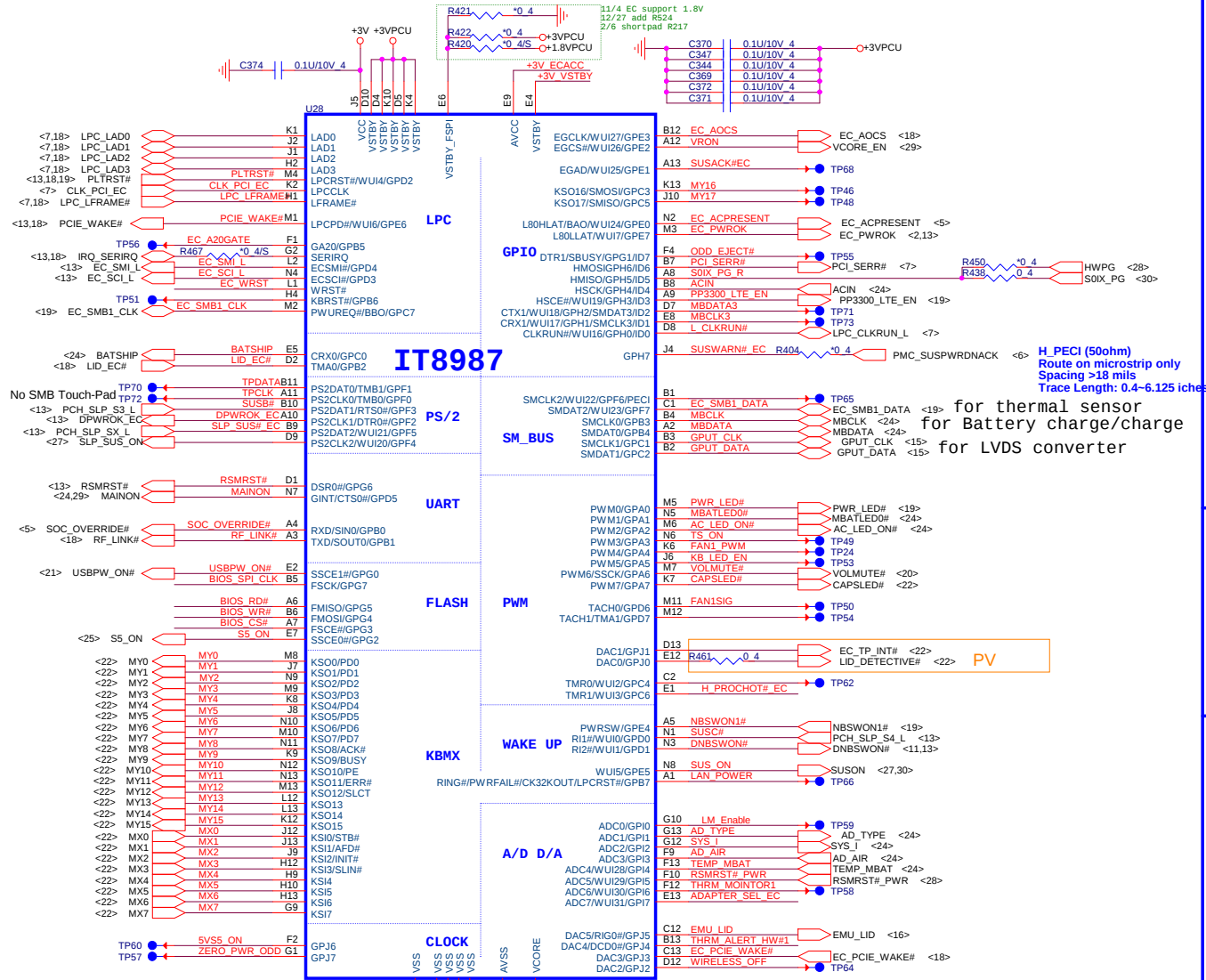


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Quanta Computer Inc.

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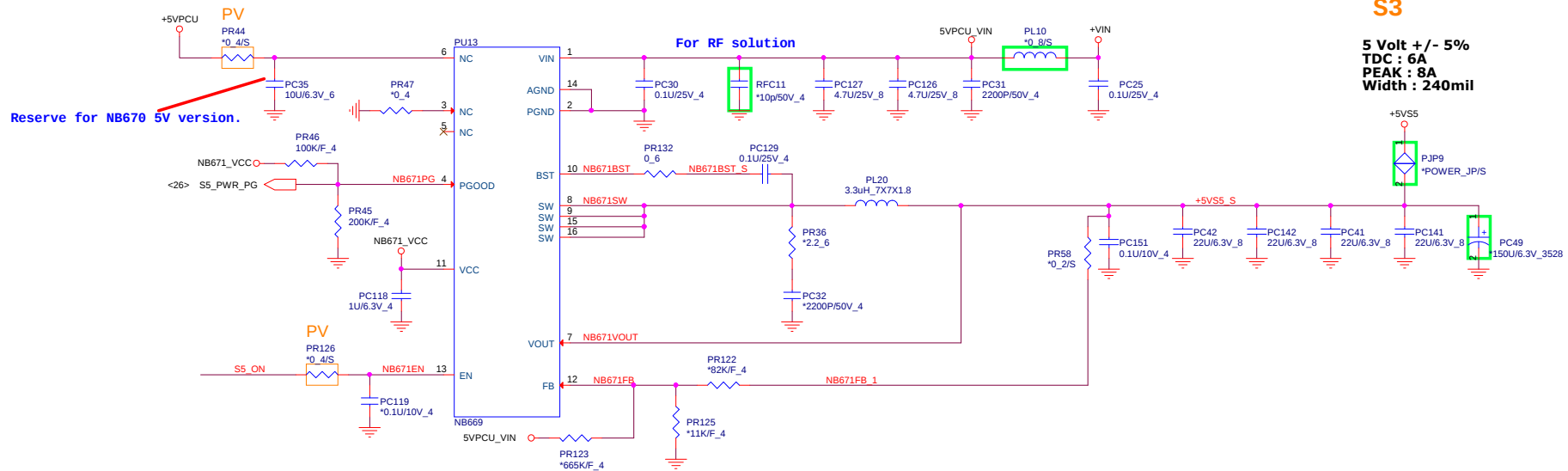
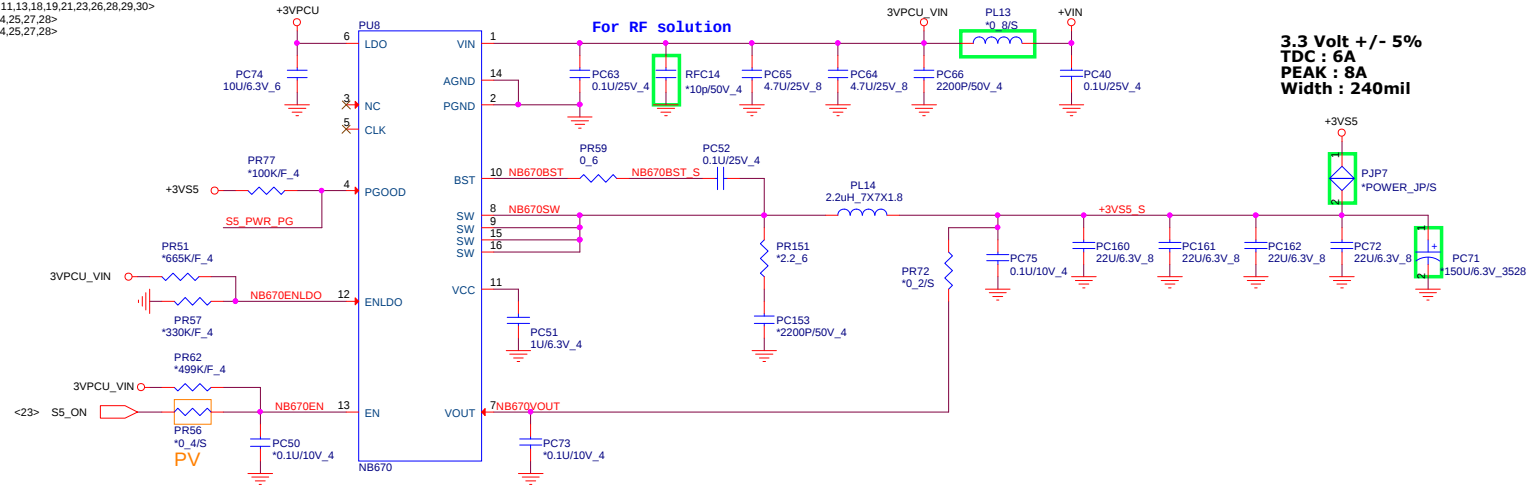
FBGA EC P/N:AL008987T00 IC CTRL(128P)IT8987VG/BX(VFBGA)

23



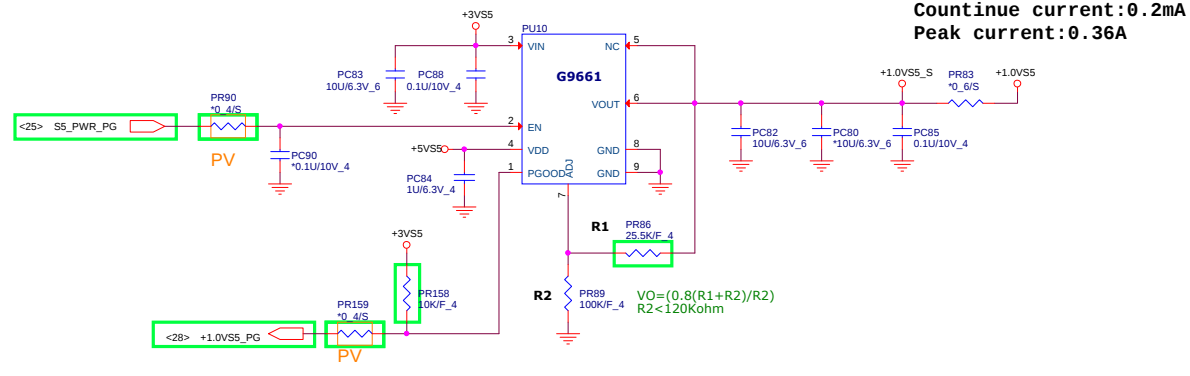
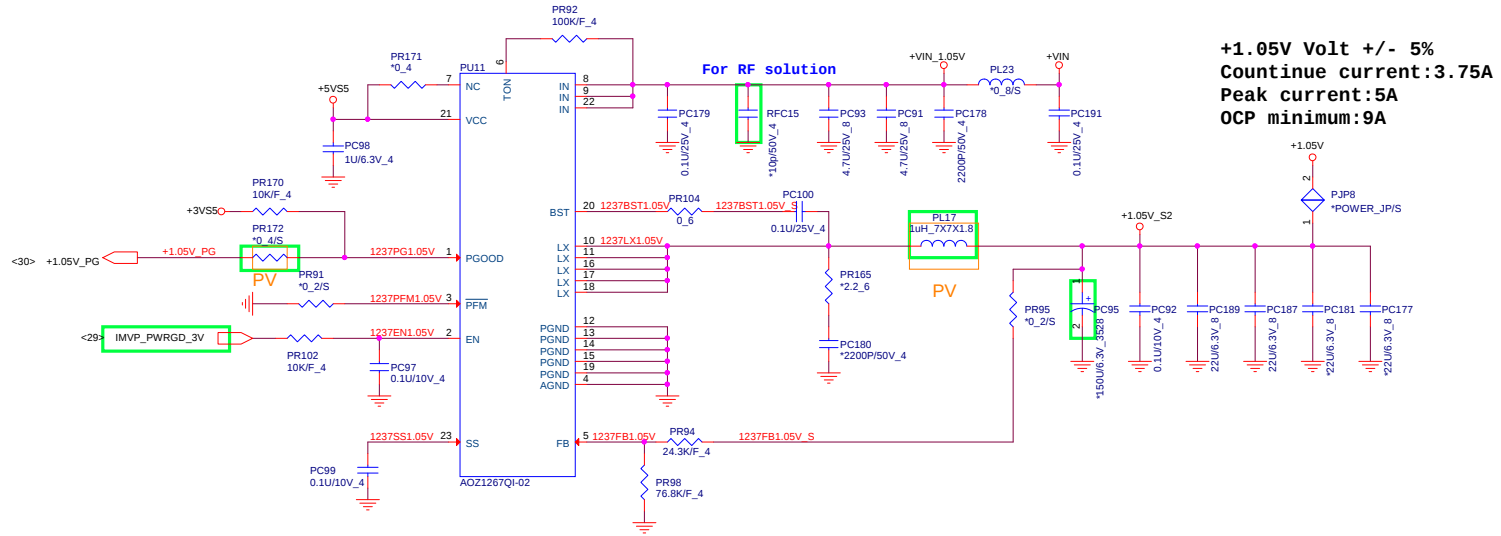
45/65W DC_JACK

+VIN <16,19,24,26,27,29,30,31>
 +3VPCU <6,18,19,22,23,24,28>
 +3VS5 <2,9,11,13,18,19,21,23,26,28,29,30>
 +5VPCU <24,25,27,28>
 +5VS5 <24,25,27,28>

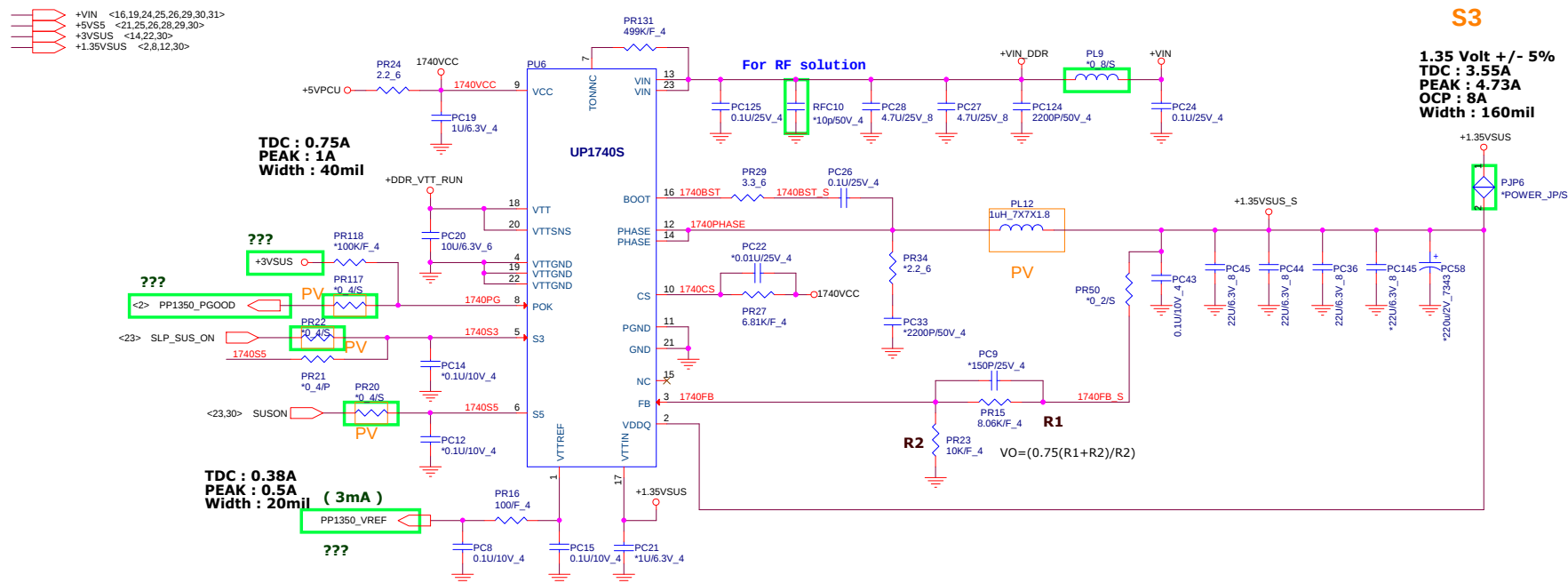


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+VIN <16,19,24,25,27,29,30,31>
+3VS5 <2,9,11,13,18,19,21,23,25,28,29,30>
+5VS5 <21,25,28,29,30>
+1.0VS5 <9,11>
+1.05V <5,6,9,29>

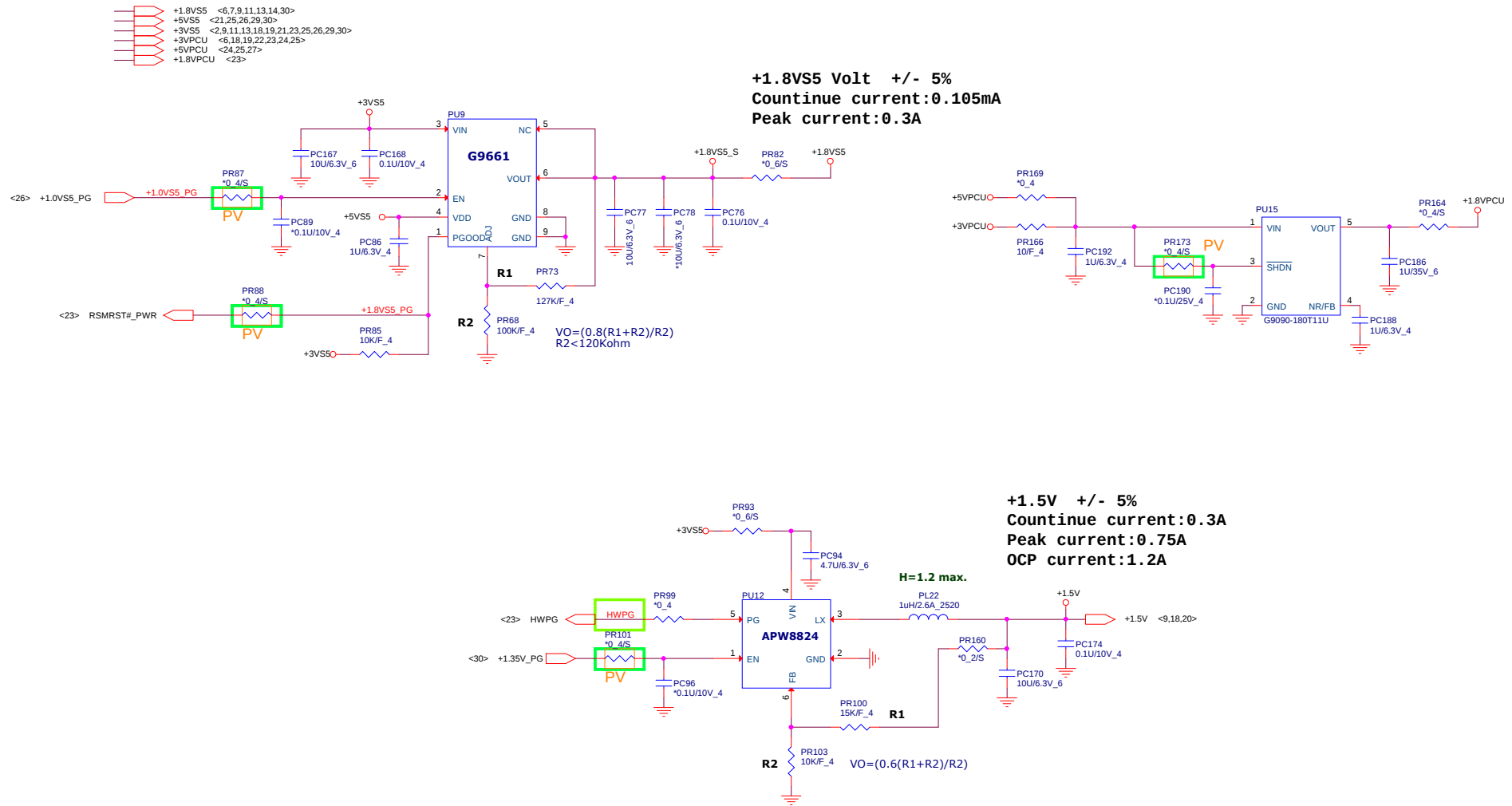


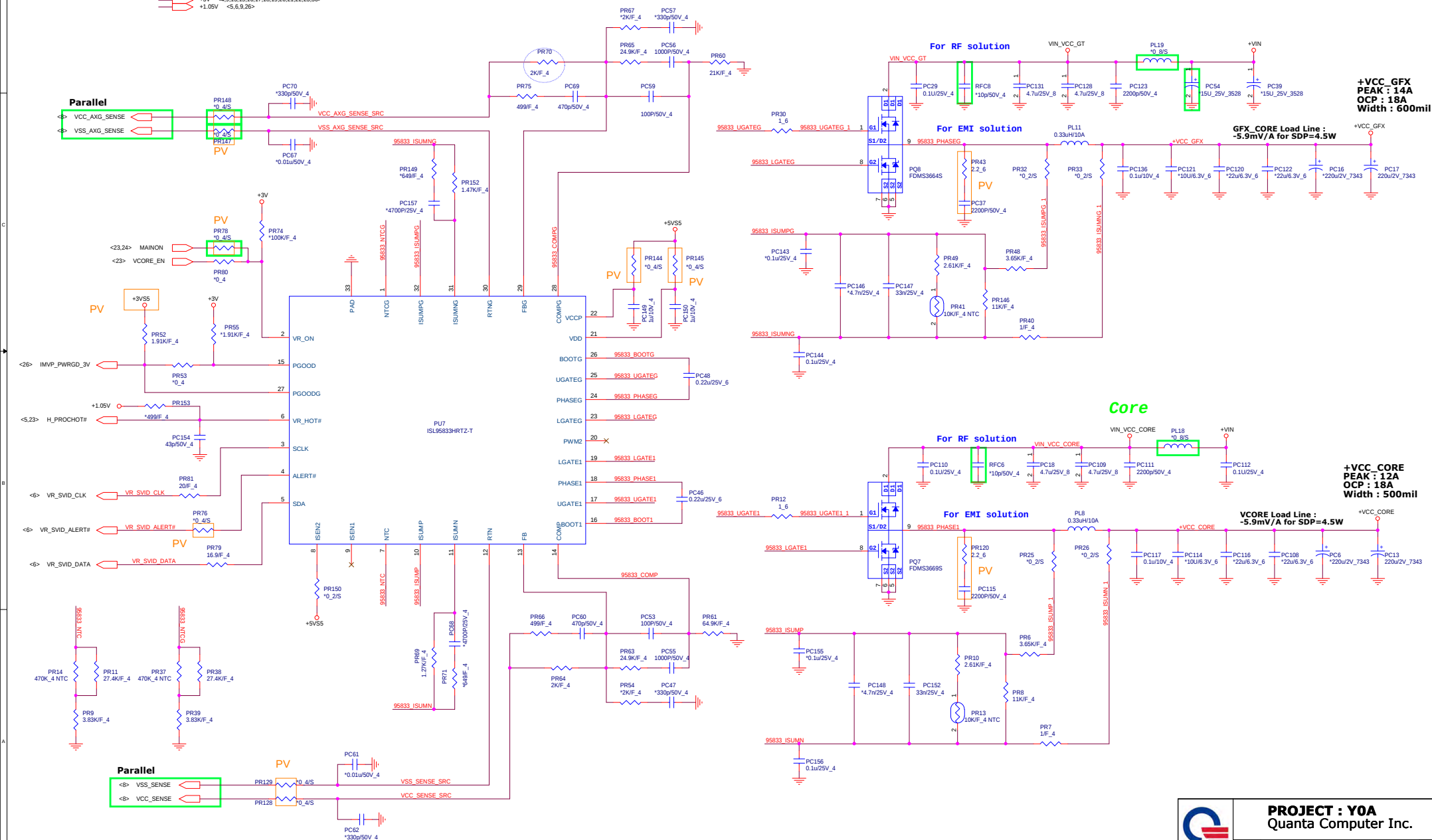
	S3	S5	+1.35VSUS	REF	VTT
S0	1	1	ON	ON	ON
S3 (main on off)	0	1	ON	ON	OFF
S4/S5	0	0	OFF	OFF	OFF

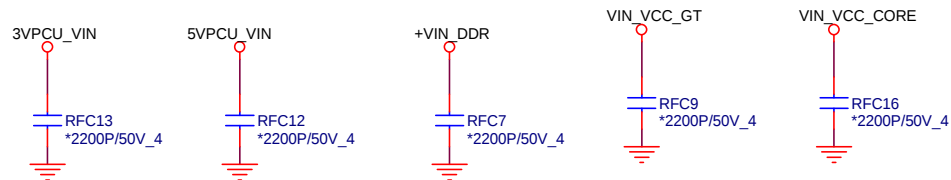


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TOP



BOTTOM

