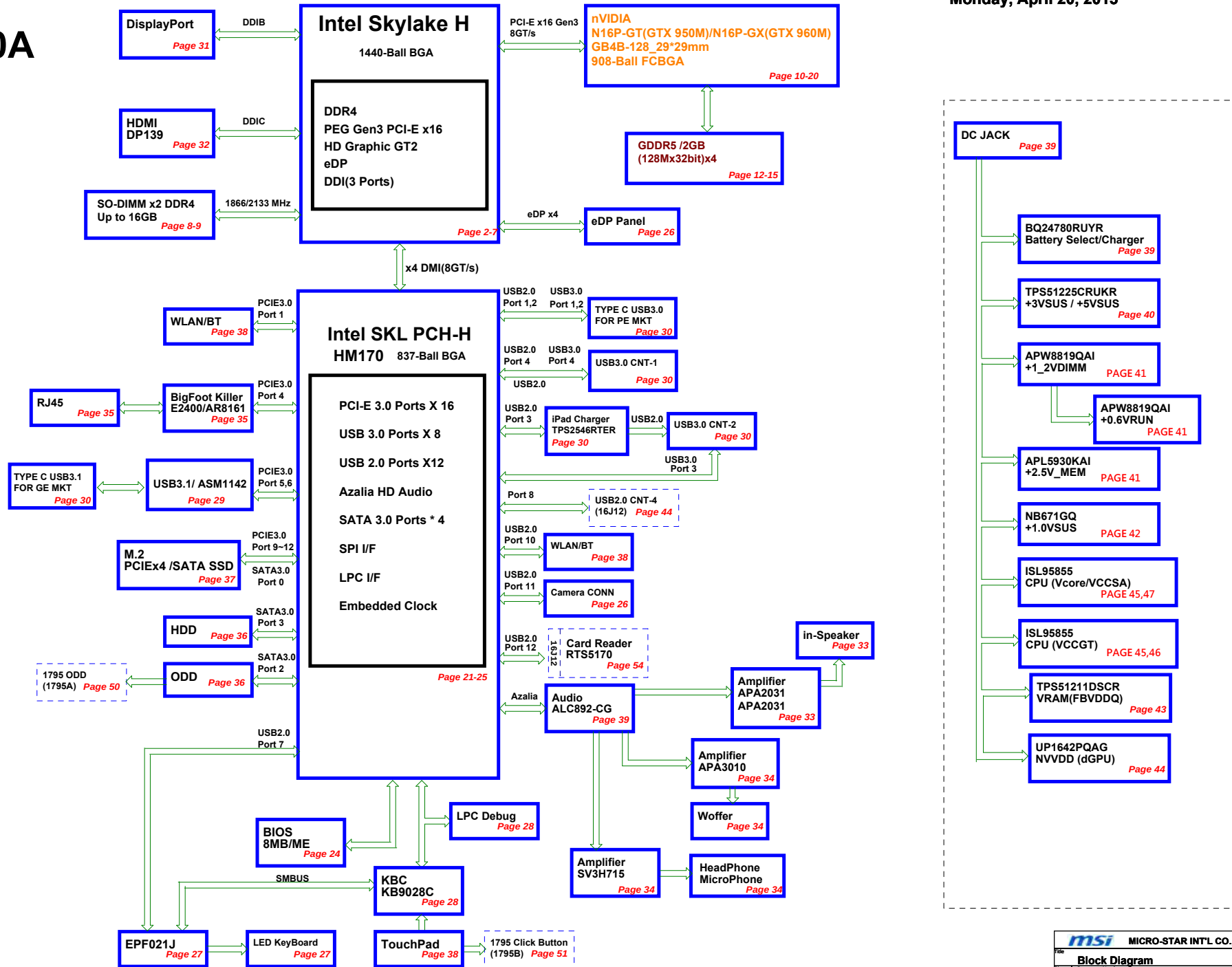


MS-16J5/MS-1795

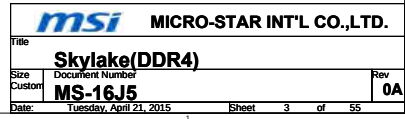
Intel Skylake Mobile

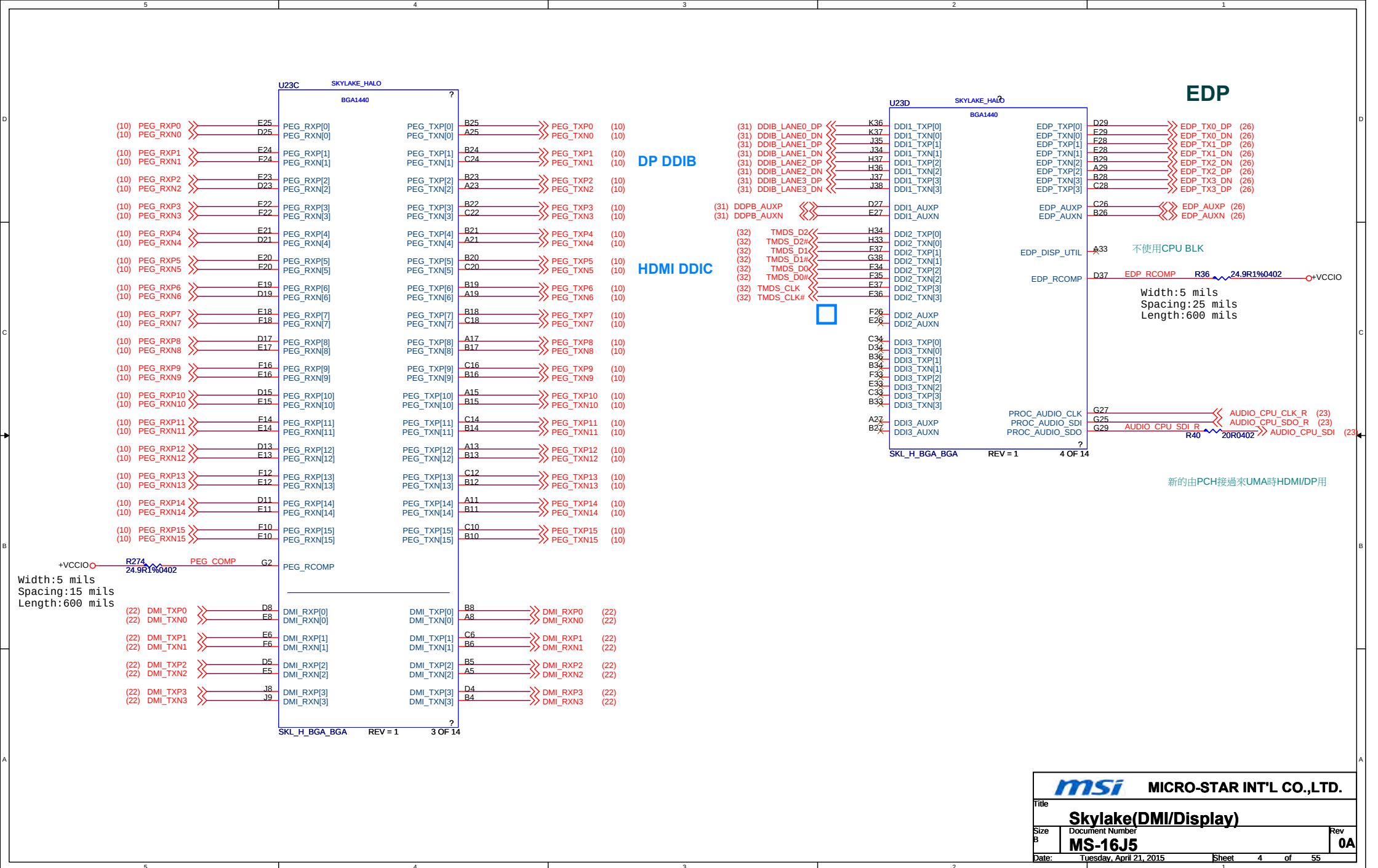
Ver:0A

Monday, April 20, 2015



DDR Channel B







少63顆0201 1uF電容
10uF 換幾顆1uF 靠近BALL C11-105A312-M09(X6S)
10uF 更改FOOTPRINT

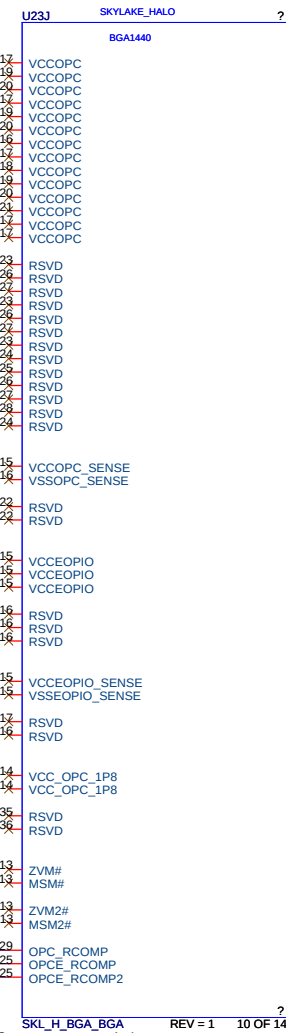
Unconnected for Processors without OPC. EDS.P121

VCCGtx, VCCOPC, VCCOPC_1p8, VCCEOP10 is only applicable to SKUs with OPC

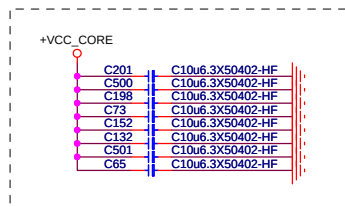
GT4 Power

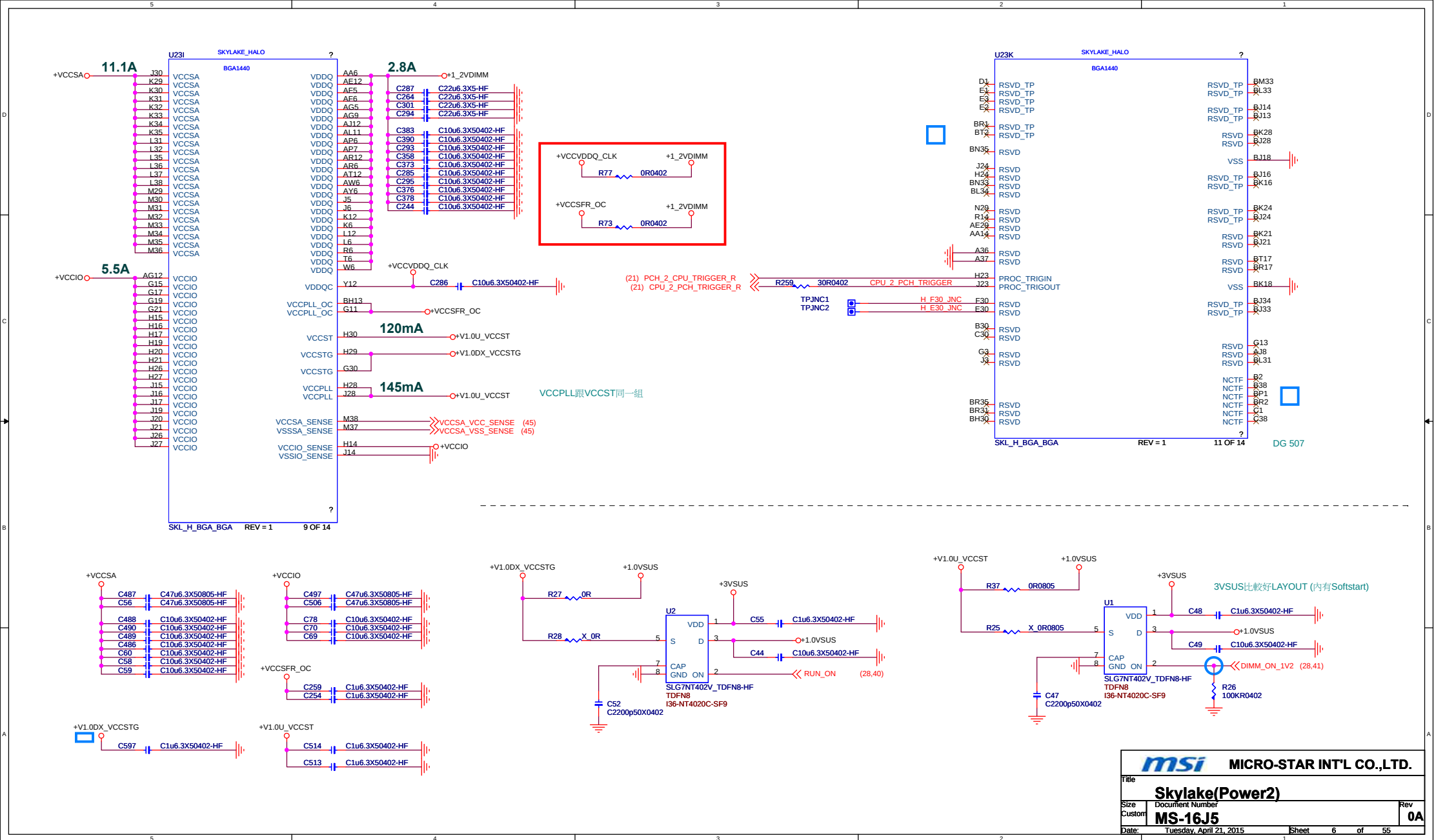
GT4 Power

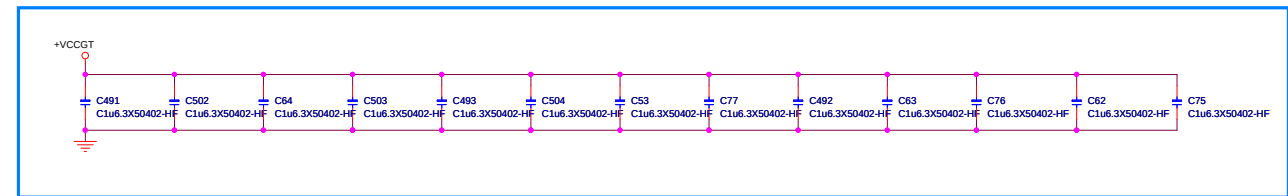
GT4 Power



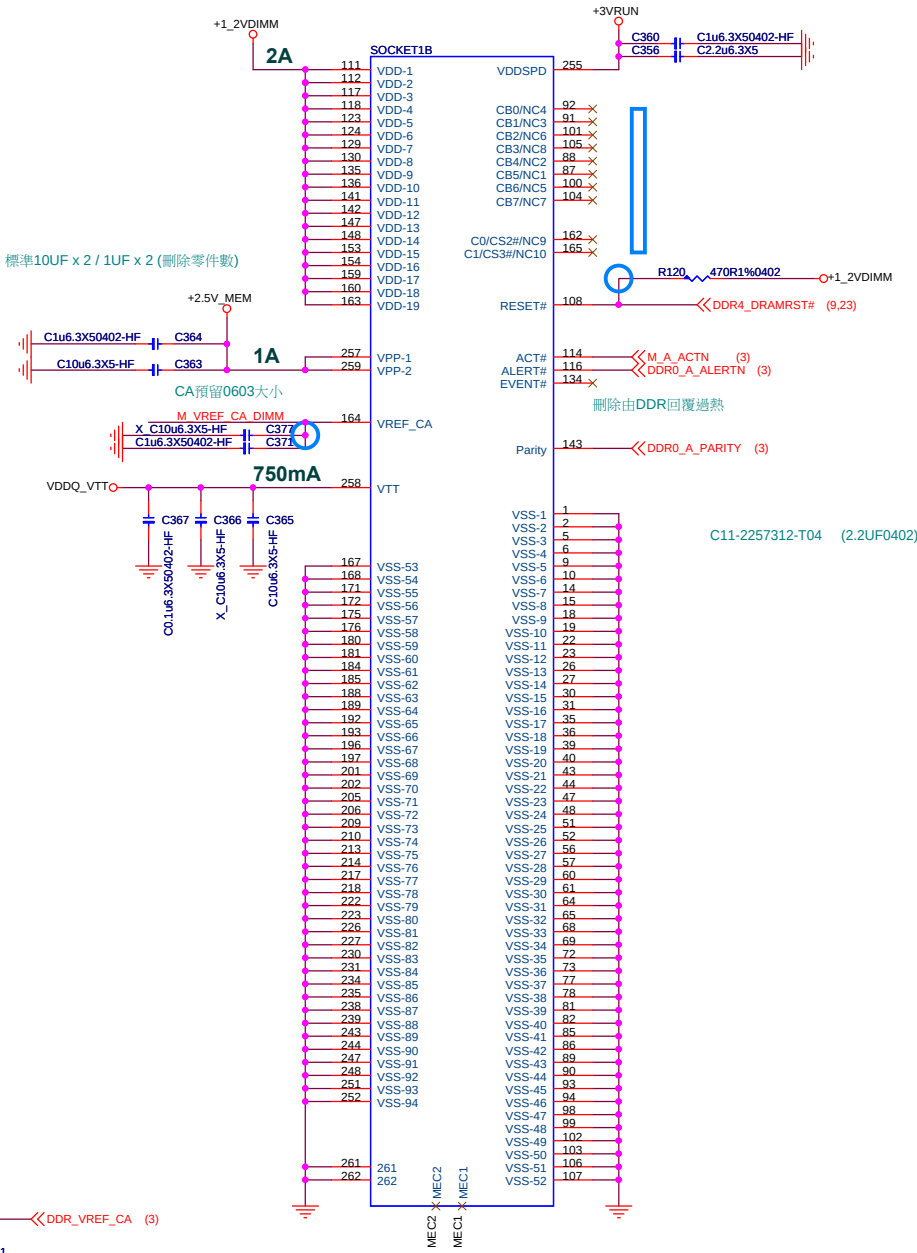
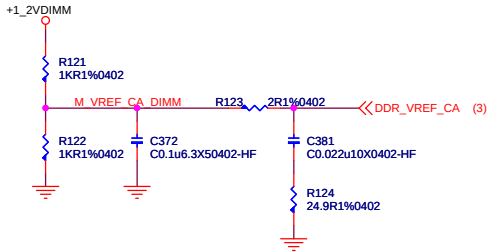
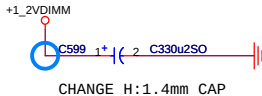
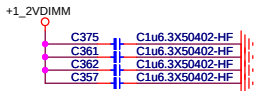
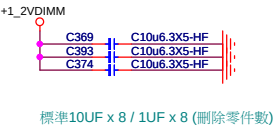
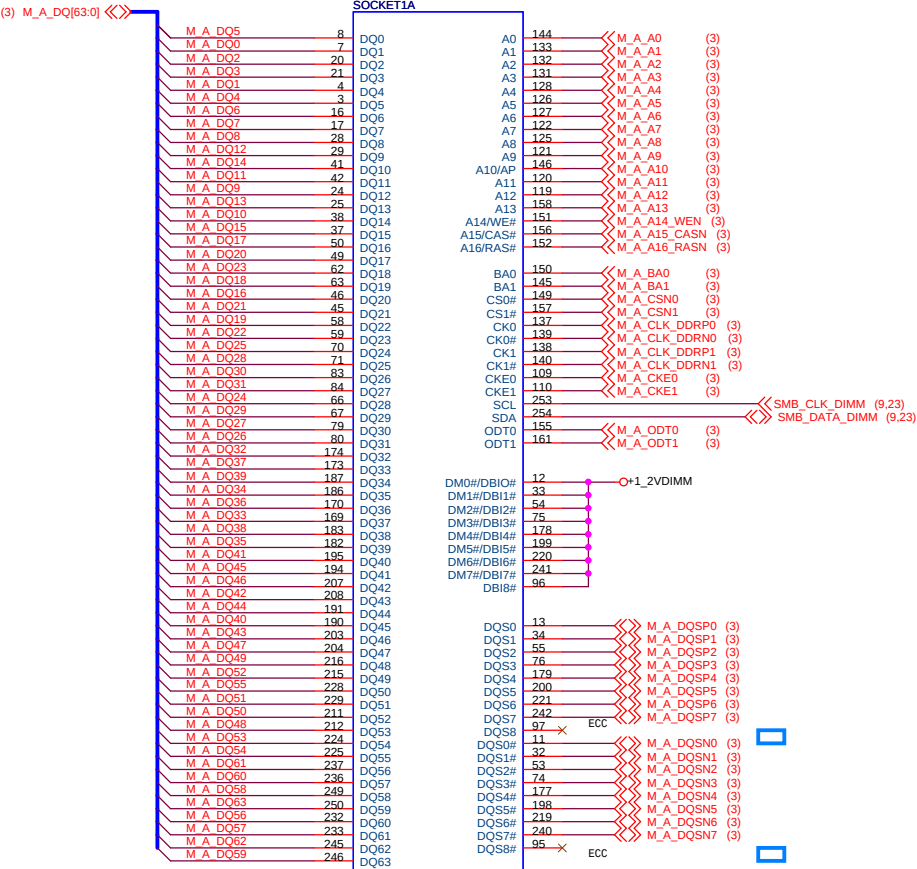
SKL_H_BGA_BGA REV = 1 10 OF 14





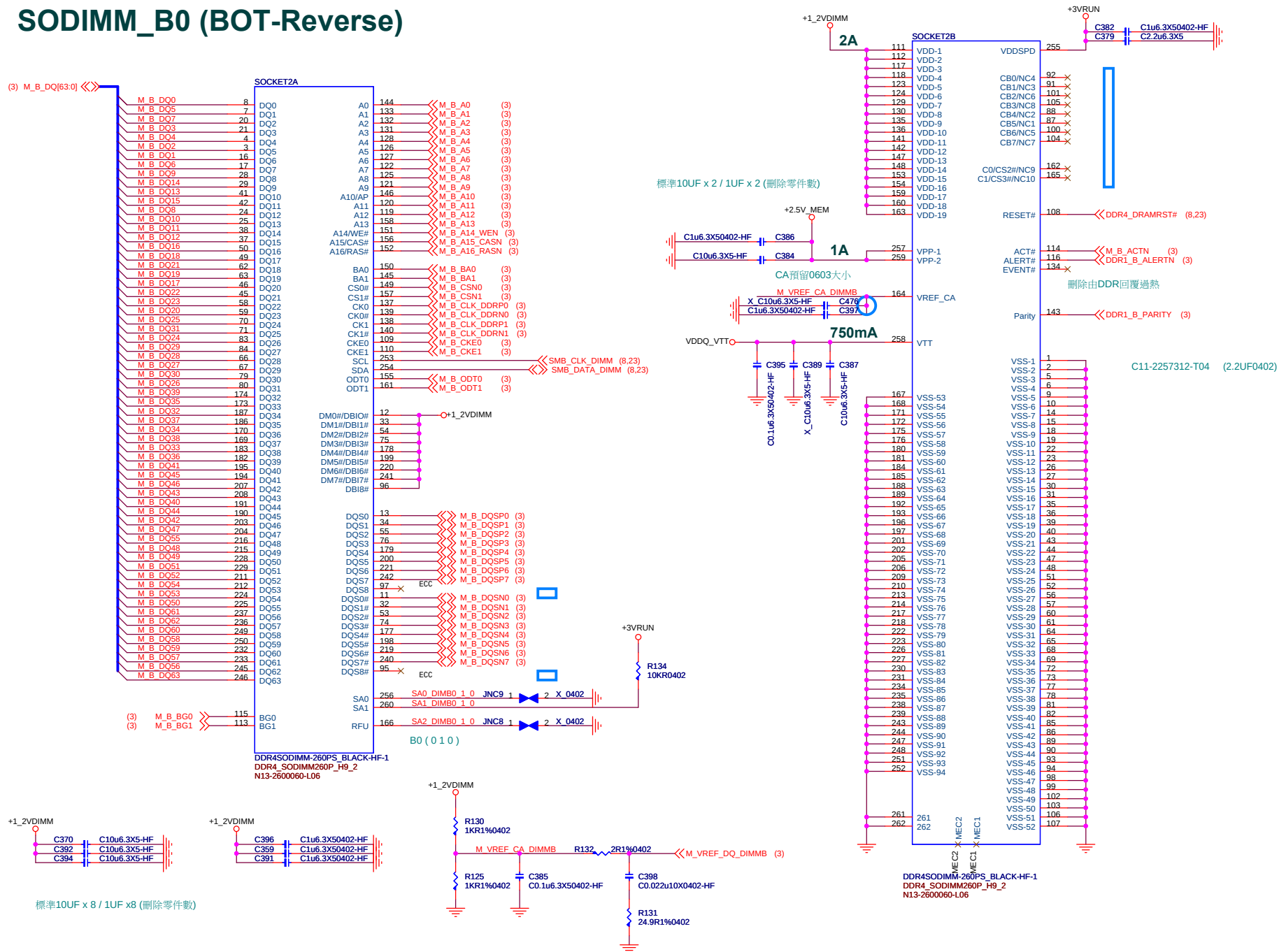


SODIMM_A0 (BOT-Reverse)

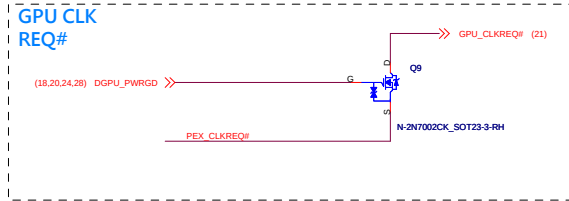


msi MICRO-STAR INT'L CO.,LTD.		
Title		
DDR4 SODIMM A0		
Size	Document Number	Rev
Custom	MS-16J5	0A
Date:	Tuesday, April 21, 2015	Sheet 8 of 55

SODIMM_B0 (BOT-Reverse)



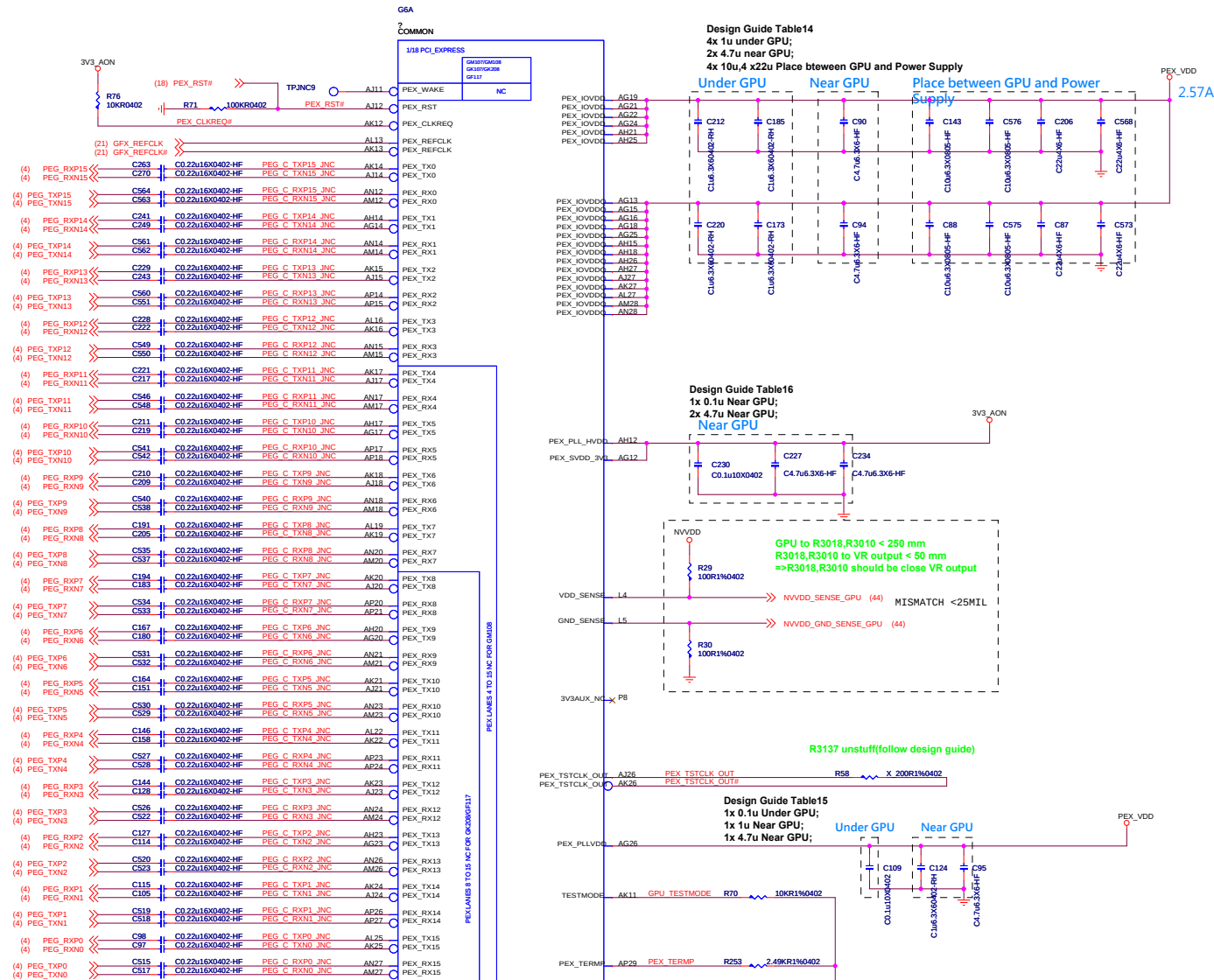
N16P-GX(PCI-Express Gen3 x16 Interface)



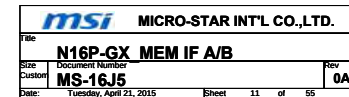
GTX950

B03-0N16P05-N08

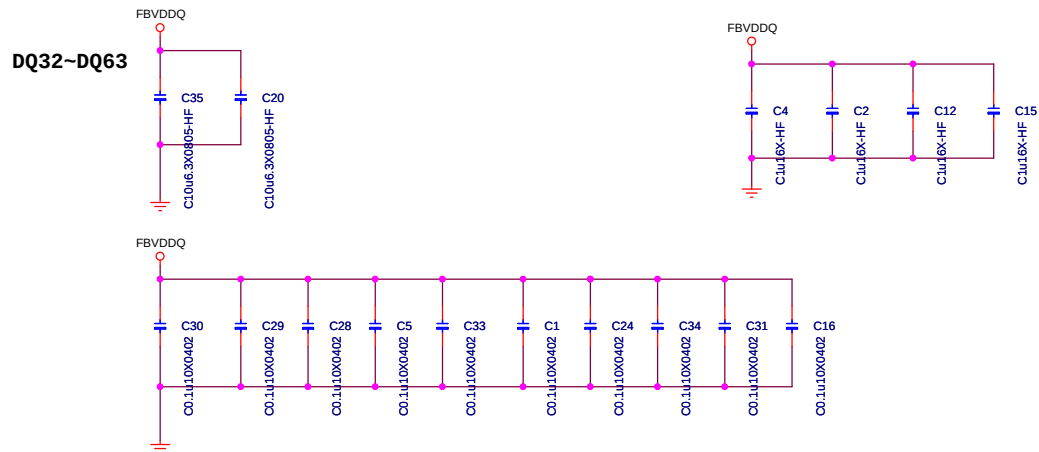
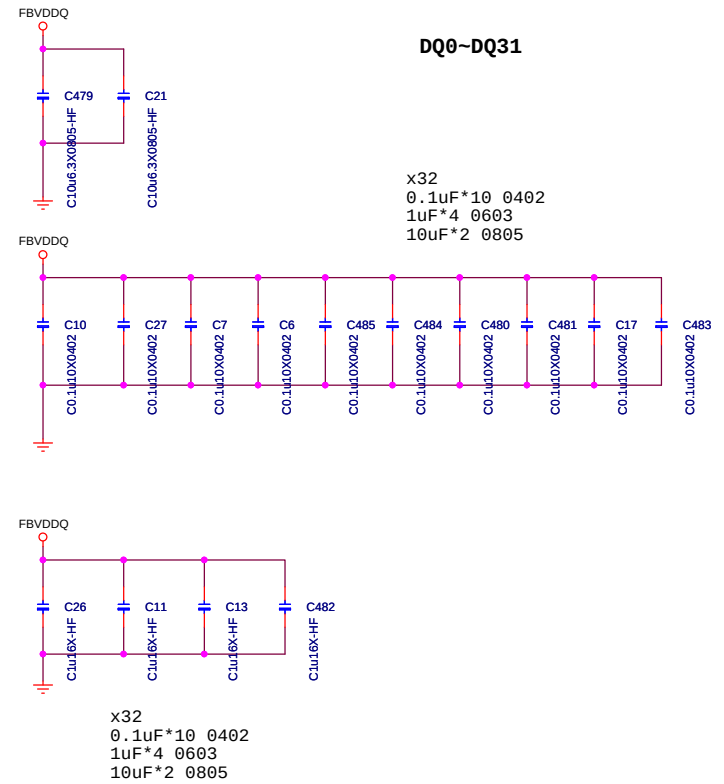
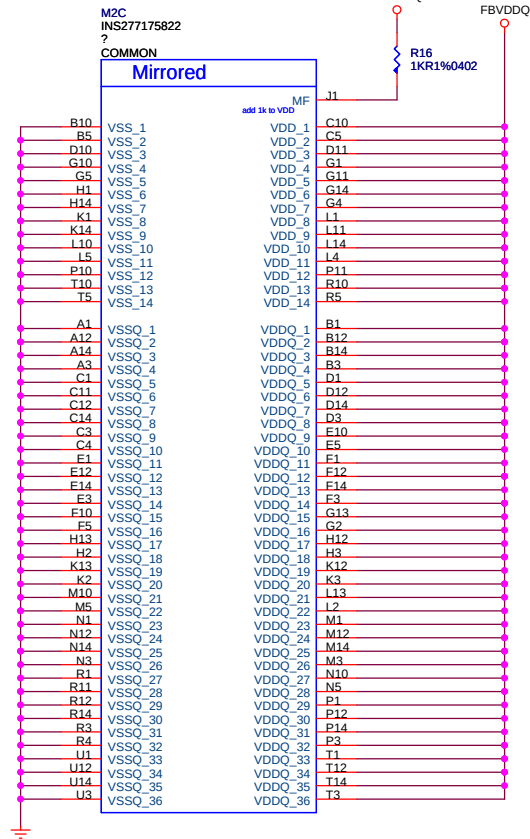
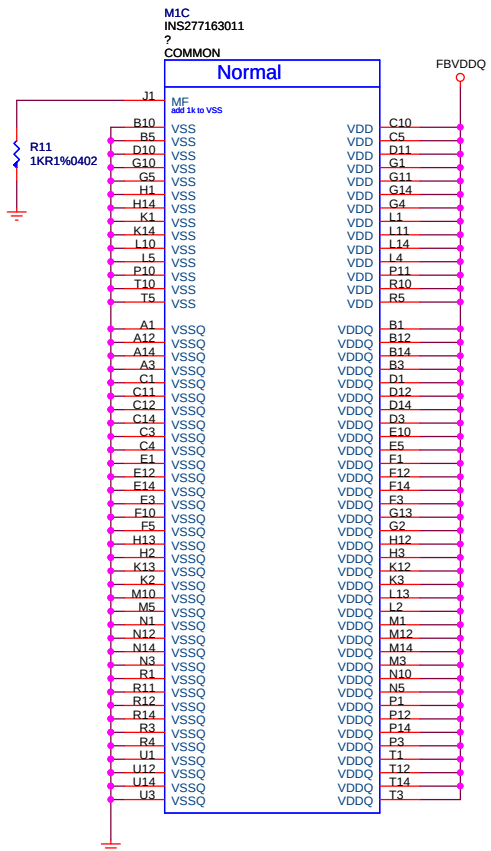
X_N16P-GT-A2



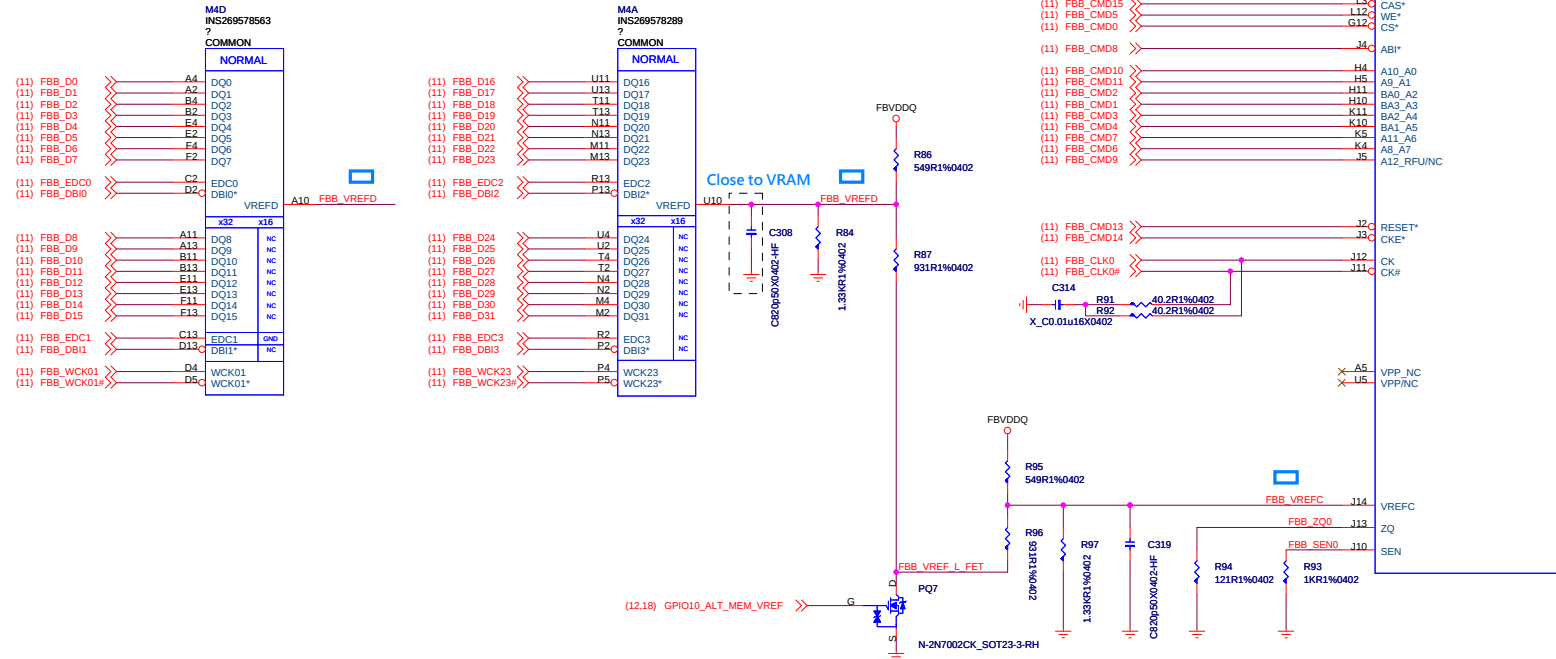
GC6M is GC6 2.0 that it is for N15x or later GPU
So we use CRB GC6M design



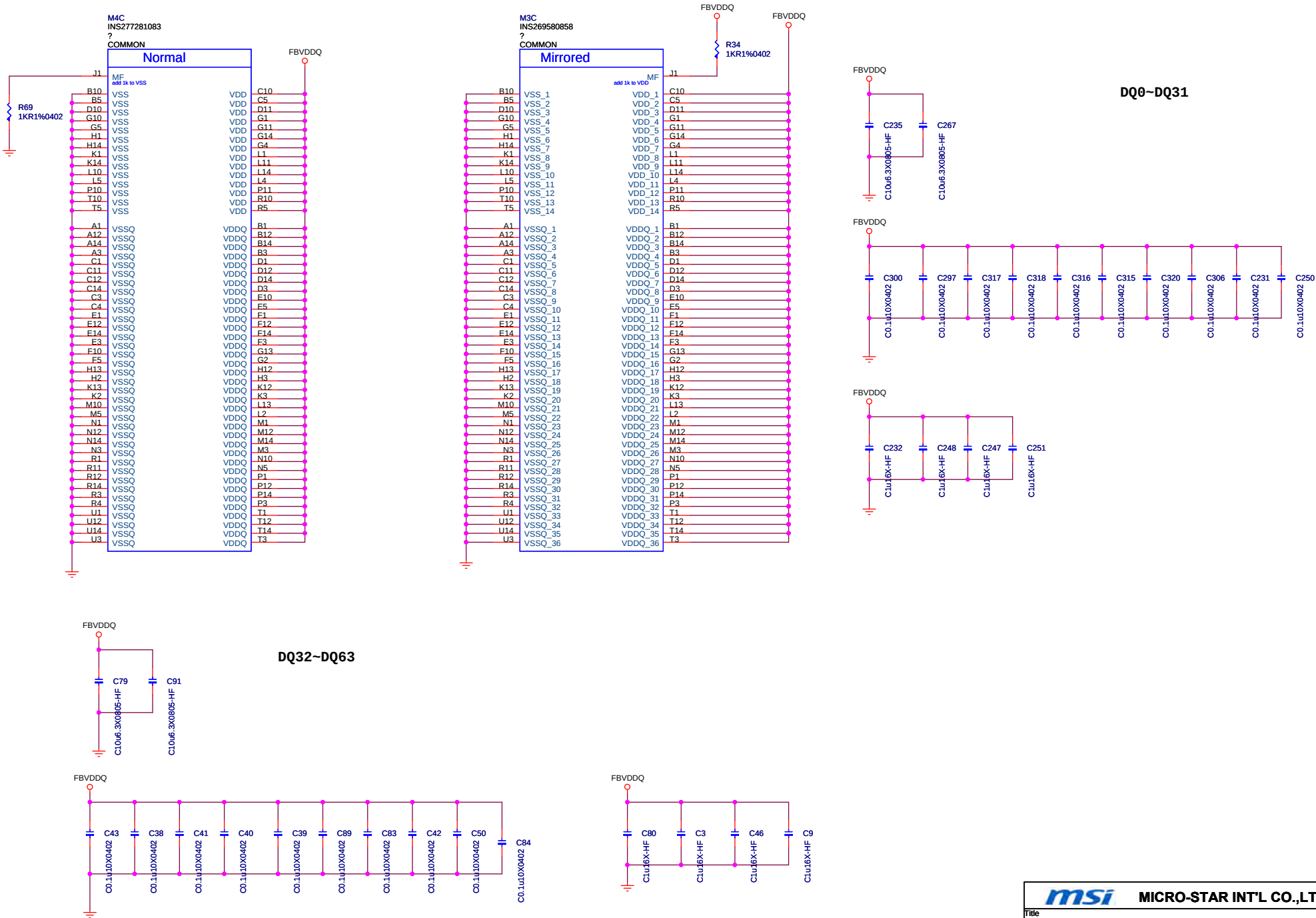
N16P-GX(GDDR5 Frame A-2)



N16P-GX(GDDR5 Frame B-1)

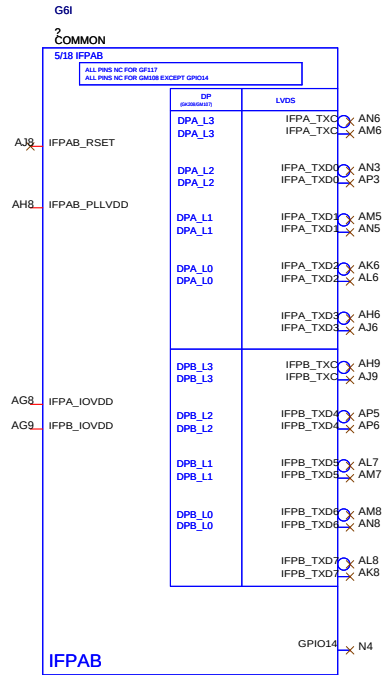


N16P-GX(GDDR5 Frame B-2)

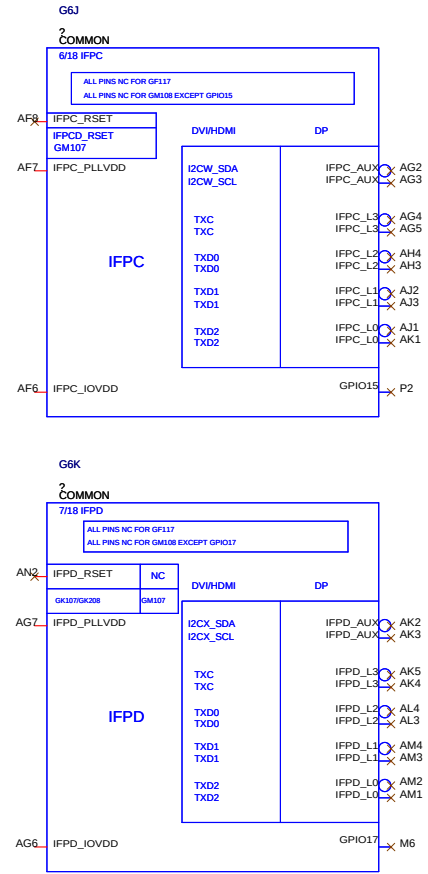


N16P-GX(Display IF)

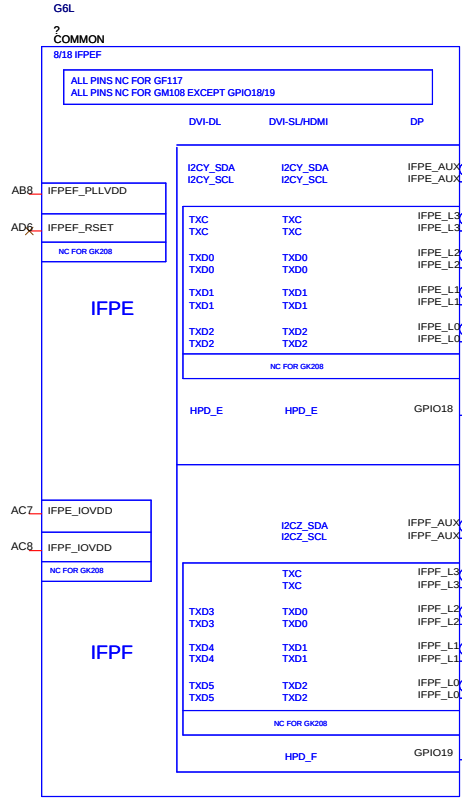
IFP A/B LVDSDual Link



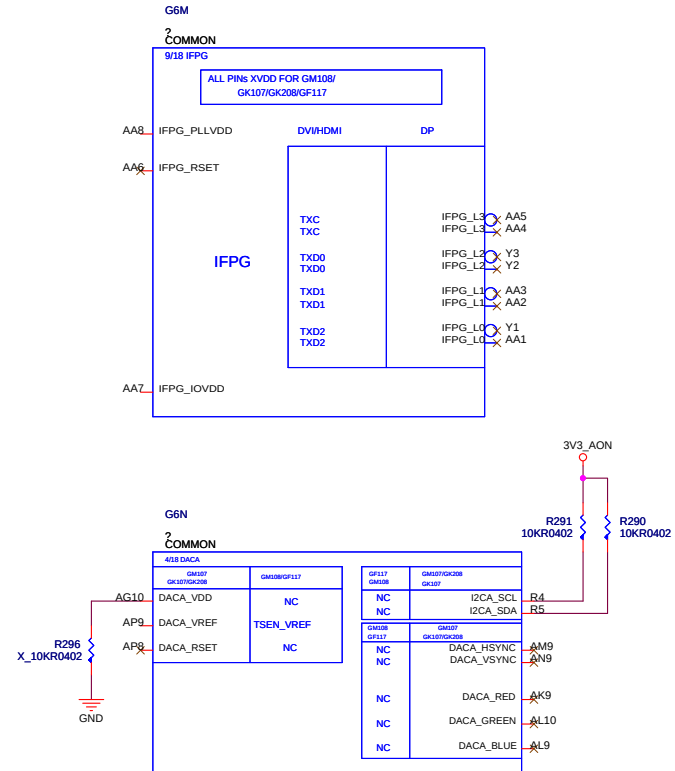
IFP C Native HDMI OR DP



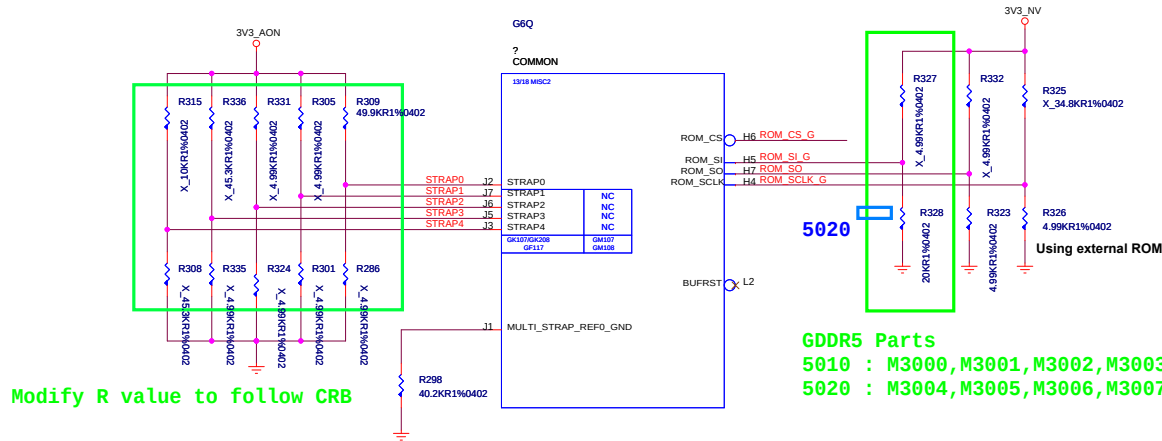
IFP E/F Dual Link TMDS DVI-I



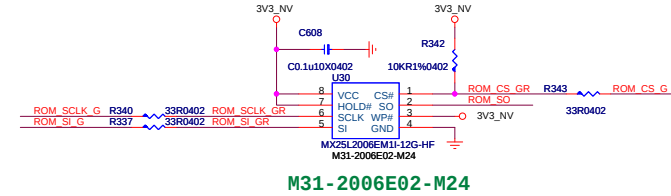
DAC A VGA



ROM, MULTI-LEVEL STRAPS

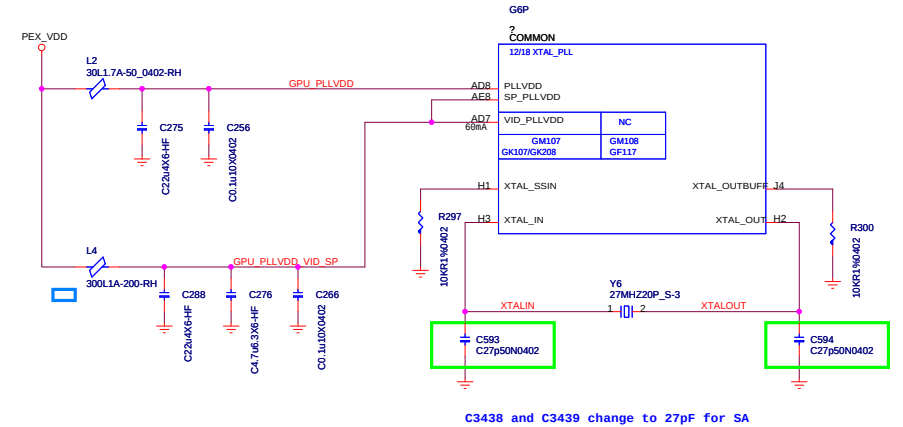


External EEPROM



DEFAULT SETTING		BOT 記得轉階層	
ROM_S2	Samsung	V_BOT5	5020
☐ 128Mx32bit		☐ M12-4132525-S02	
R11-0203T12-W08		X_K4G41325FC-HC03	
X_20KR1%0402			
ROM_H2	Hynix	V_BOT4	5020
☐ 128Mx32bit		☐ M12-5GC4H05-H23	
R11-0153T12-W08		X_H5GC4H24MFR-T2C	
X_15KR1%0402			
ROM_M2	Micron	V_BOT6	5020
☐ 128Mx32bit		☐ M12-4032B05-E59	
R11-2492T12-W08		X_EDW4032BABG-60-F-HF	
X_24.9KR1%0402			
ROM_H3	Hynix	V_BOT7	5020
☐ 128Mx32bit		☐ M12-5GC4H65-H23	
R11-3482T12-W08		X_H5GC4H24AJR-T2C	
X_34.8KR1%0402			

ROM_SI	RAM_CFG[3:0]	
Micron GDDR5 : M12-4032B05-E59 0x4 25K PD Micron 128Mx32bit Samsung GDDR5 : M12-4132525-S02 0x3 20K PD Samsung 128Mx32bit Hynix GDDR5 : M12-5GC4H05-H23 0x2 15K PD Hynix 128Mx32bit Hynix GDDR5 : H5GC4H24AJR-T2C 0x6 35K PD		
ROM_SO	DEVID_SEL PCIE_CFG SMB_ALT_ADDR VGADEVICE	5K PD
ROM_SCLK	SOR_EXposed[3:0]	5K PD
STRAP0		50K PU 3V3_AON
STRAP1		Reserved
STRAP2		Reserved
STRAP3		Reserved
STRAP4		Reserved



GND		3V3
5K	0000	1000
10K	0001	1001
15K	0010	1010
20K	0011	1011
25K	0100	1100
30K	0101	1101
35K	0110	1110
45K	0111	1111
	PD	PU

change 100K to follow design guide

add C3 for signal modify

CRB有兩種接法
一種接3V3_NV
一種接3V3_AON

NO GC6 NO STUFF

EC_PROTECT_PWR , EC_OVERT# 先前的16J2是預留給GPU保護機制 16J5 不需要

From Battery

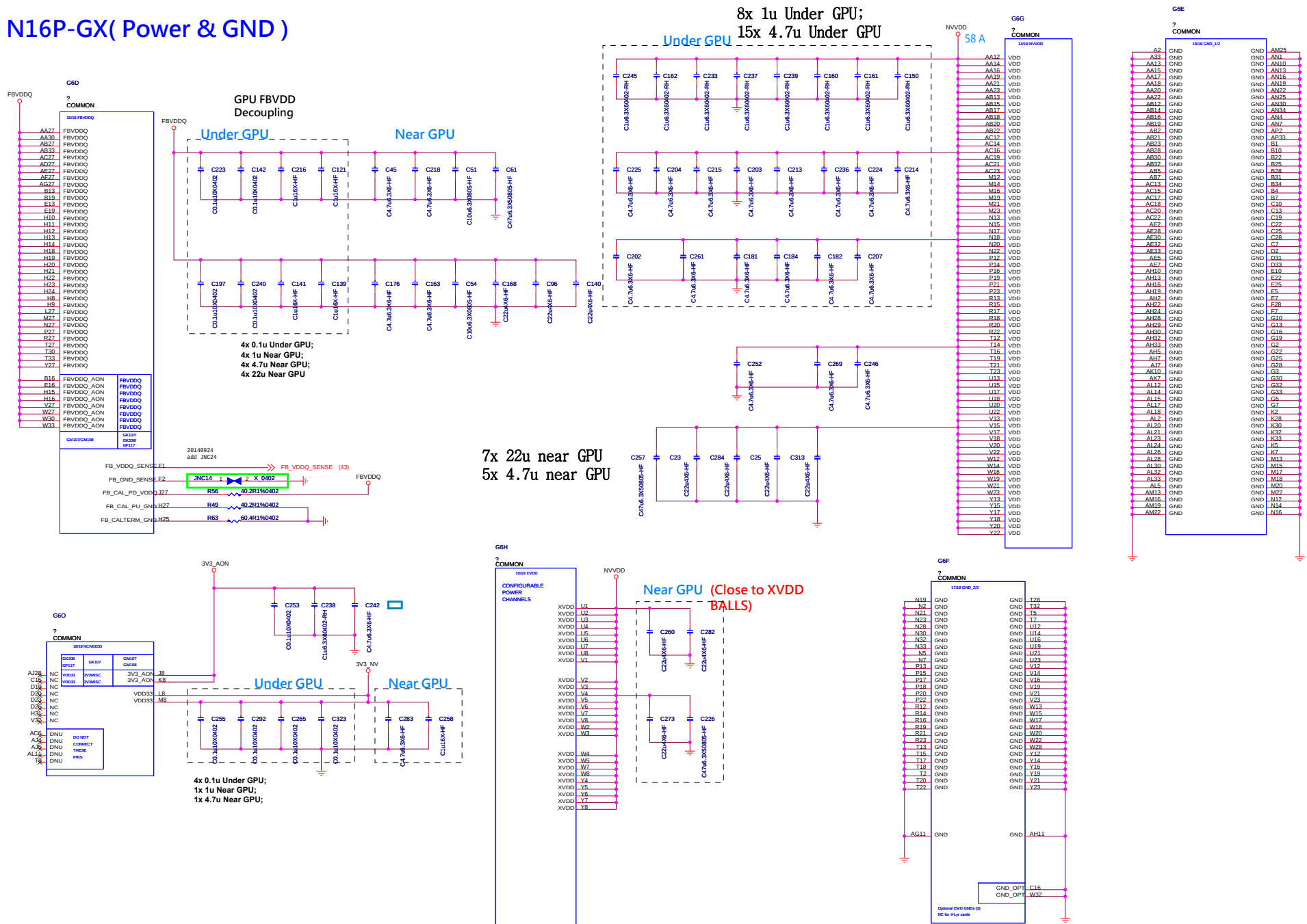
From EC

R111 MOVE TO PAGE25

msi MICRO-STAR INT'L CO.,LTD.

Title			N16P-GX Thermal & GPIO
Size	Document Number	Rev	
Custom	MS-16J5	0A	
Date:	Tuesday, April 21, 2015	Sheet	18 of 55

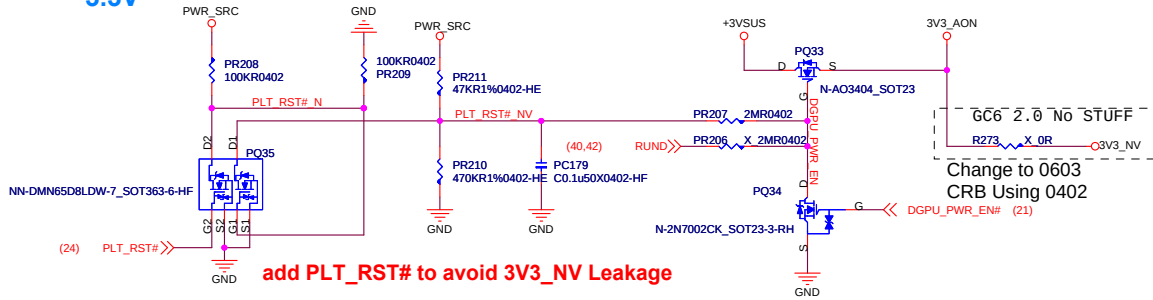
N16P-GX(Power & GND)



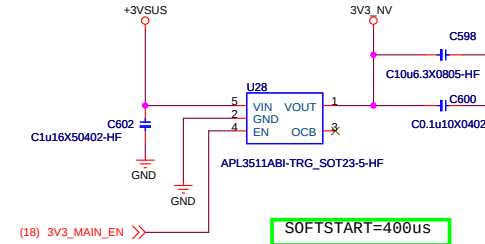
N16P-GX(Power Control)

nVIDIA Power Sequence Control 3V3_AON -> 3V3_NV -> NVVDD -> PEX_VDD -> FBVDDQ -> DGPUPWRGD

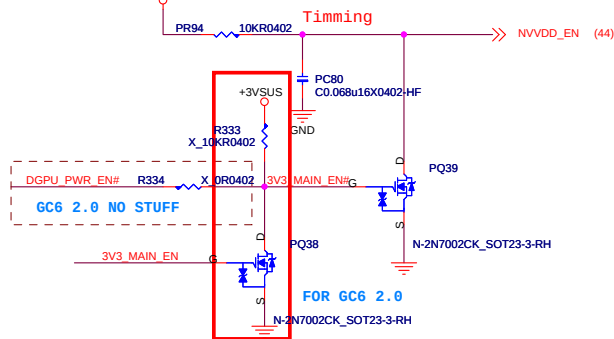
3.3V



GC6 2.0 STUFF



NVVDD

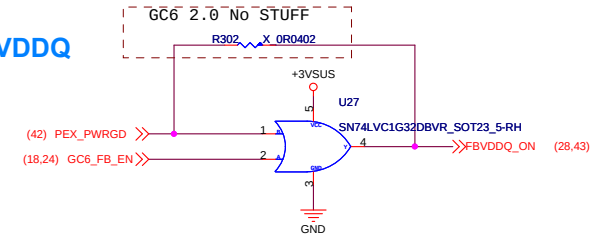


PEX_VDD

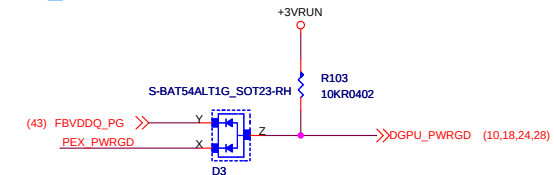
EDP Design Guide:
N16P-GX
NVVDD : 51.1A ; Peak 87A
3.3VRUN : 0.34A
PEX_VDD : 2.57A
FBVDDQ : 8.75A

EDP Design Guide:
N16P-GT
NVVDD : 30.2A ; Peak 53A
3.3VRUN : 0.34A
PEX_VDD : 2.57A
FBVDDQ : 8.75A

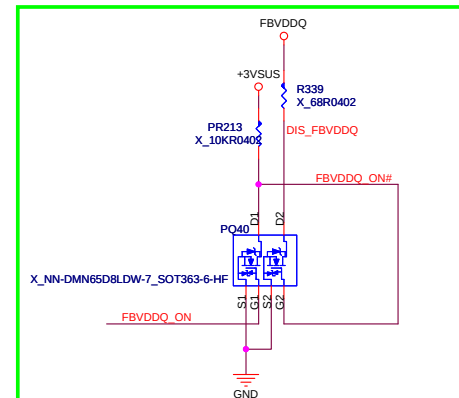
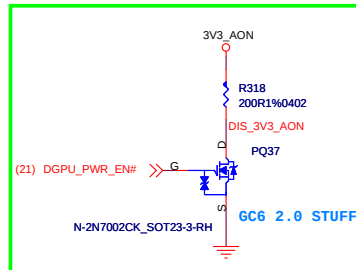
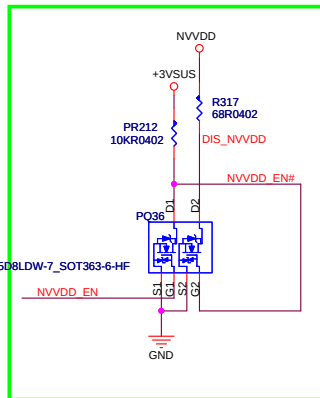
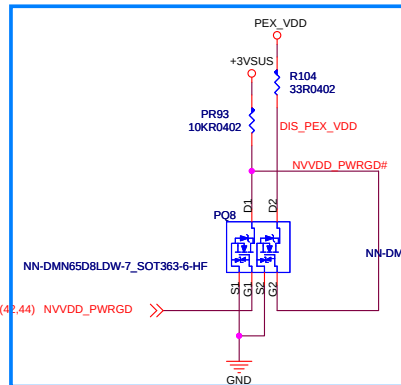
FBVDDQ



DGPU_PWRGD

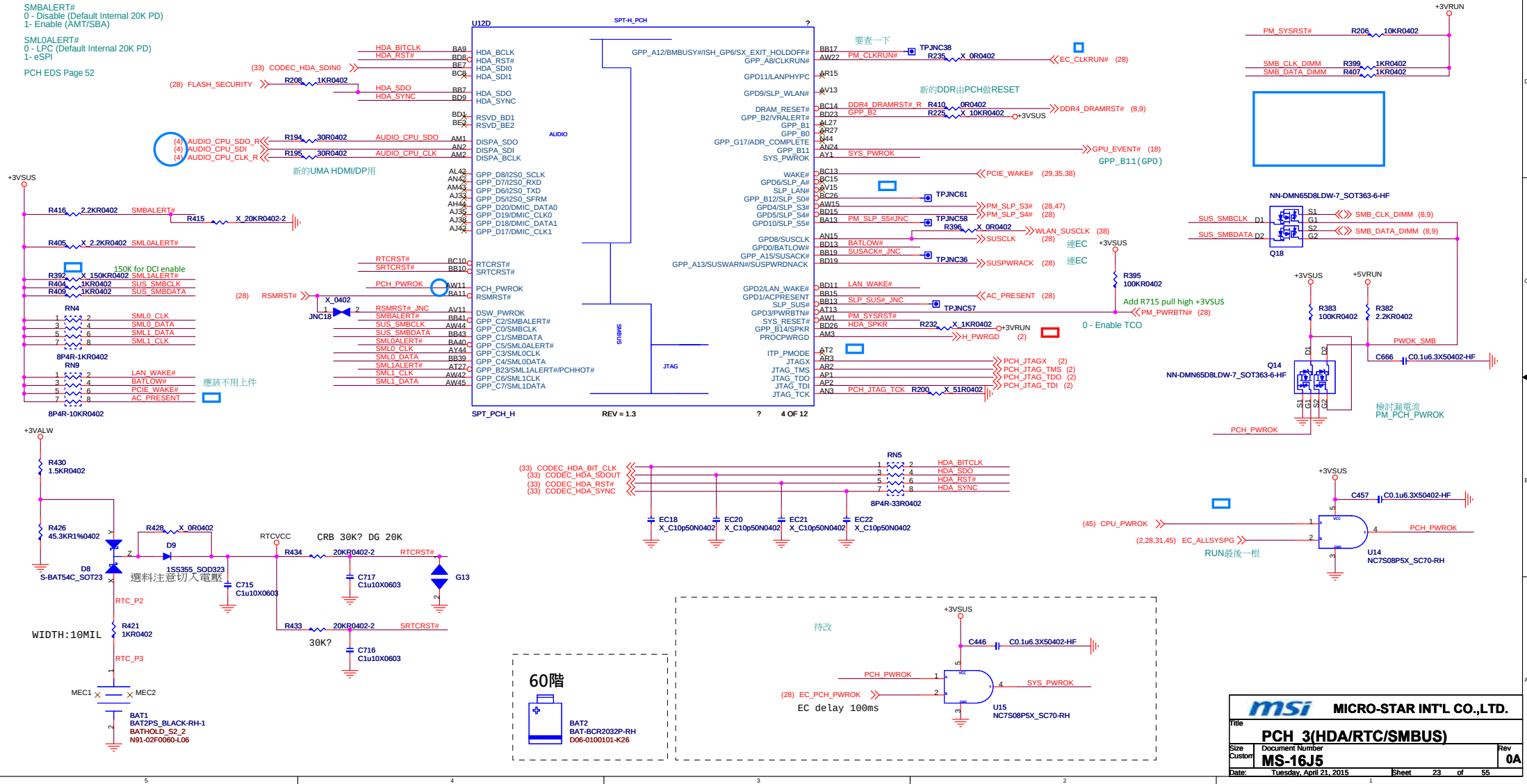


Discharge Circuit

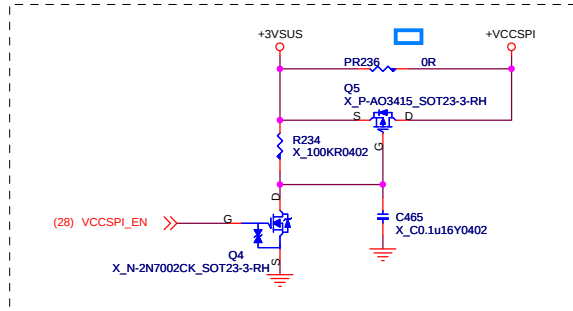
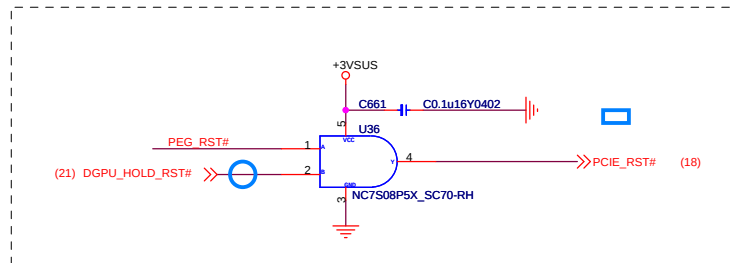
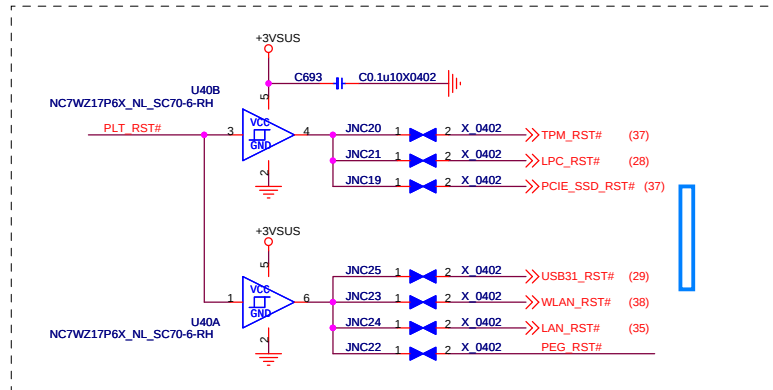
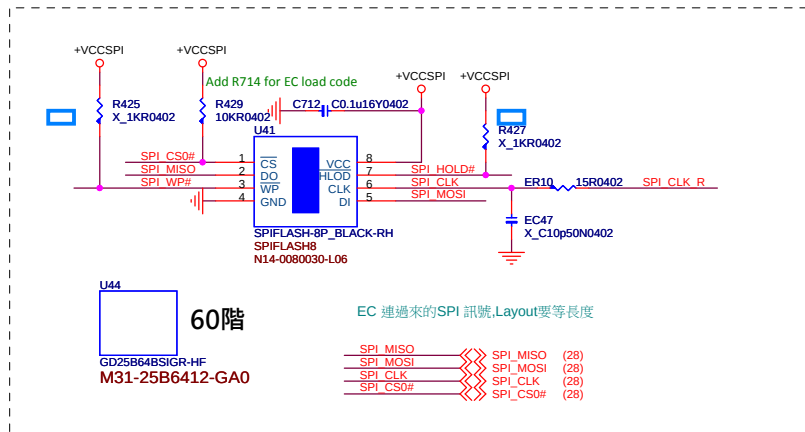
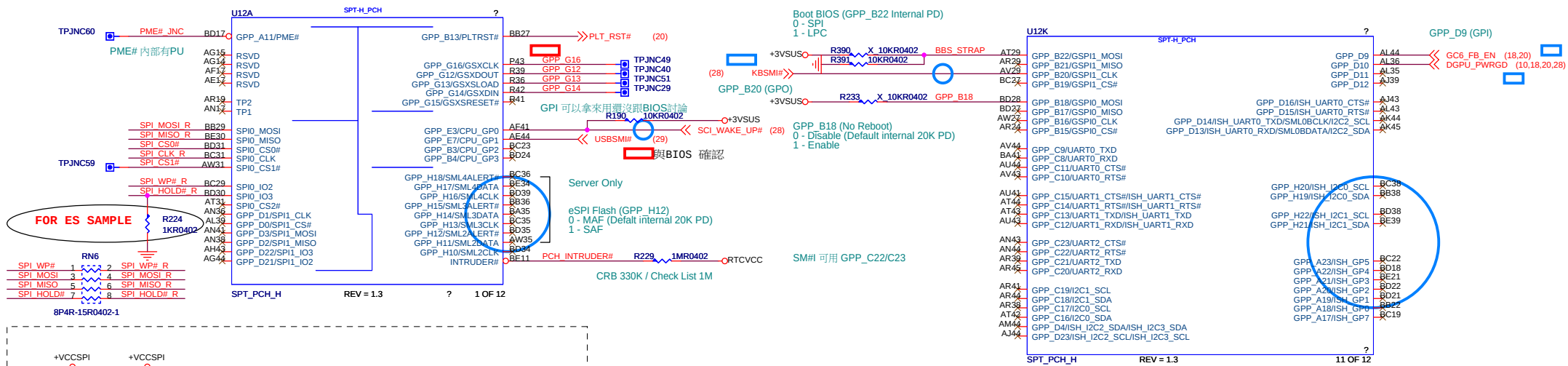


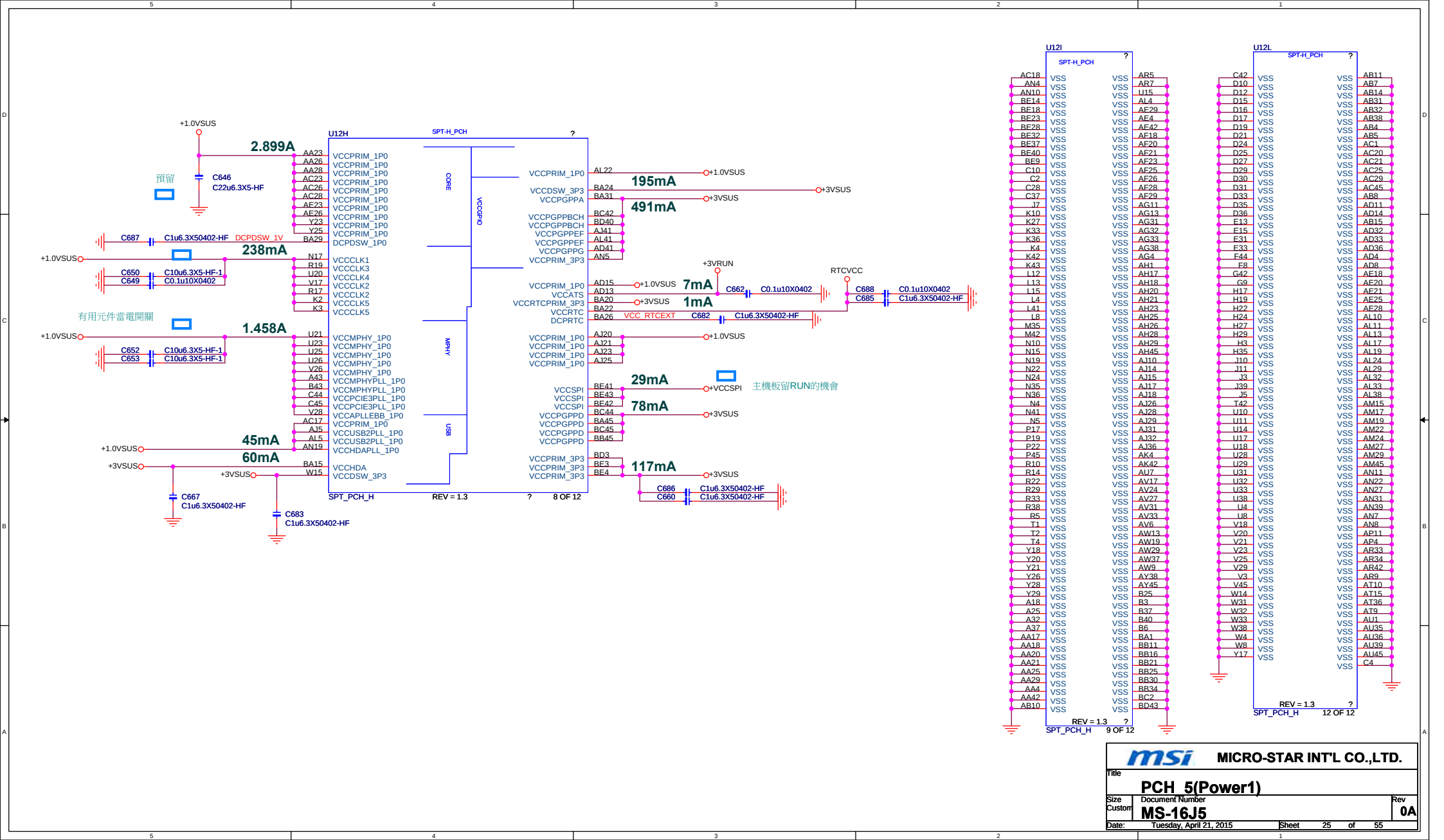
4/13更換料號
D03-7992D18-D07 轉到 D03-65D8L09-D07

SMBALERT#
0 - Disable (Default Internal 20K PD)
1 - Enable (AMT/SSA)
SML0ALERT#
0 - LPC (Default Internal 20K PD)
1 - eSPI
PCH EDS Page 52



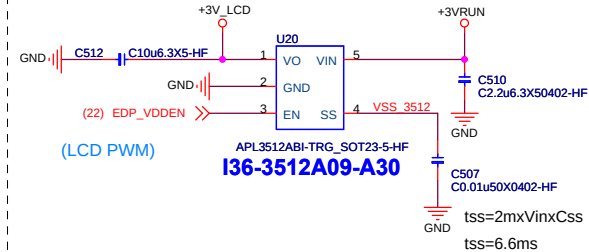
msi MICRO-STAR INT'L CO.,LTD.			
Title PCH 3(HDA/RTC/SMBUS)			
Size	Document Number	Rev	
Custom	MS-16J5	0A	
Date:	Tuesday, April 21, 2015	Sheet	23 of 55



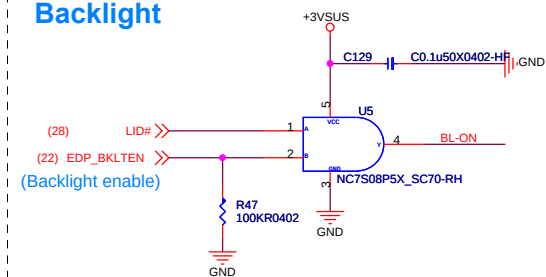


eDP

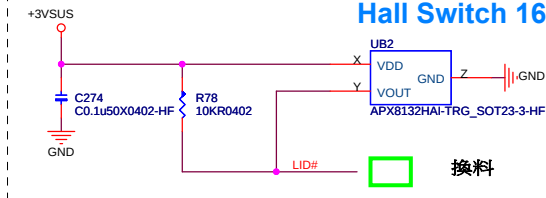
Panel Device Logic Power



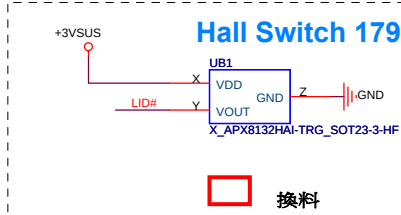
Backlight



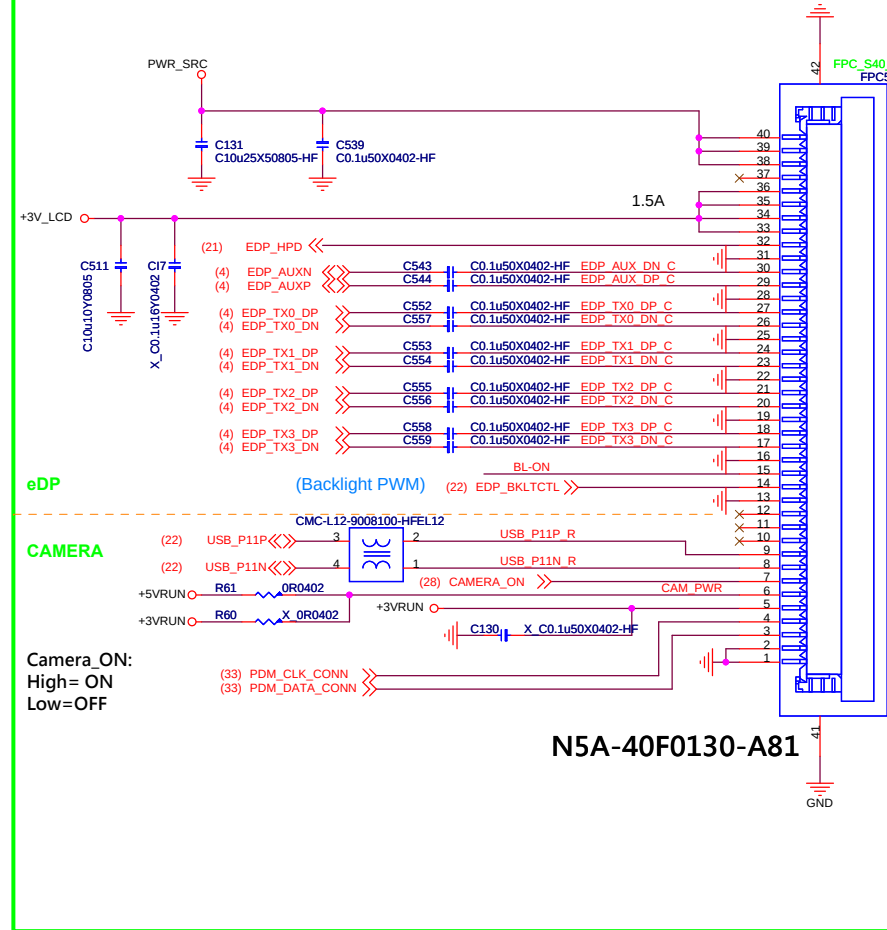
Hall Switch 16J5



Hall Switch 1795

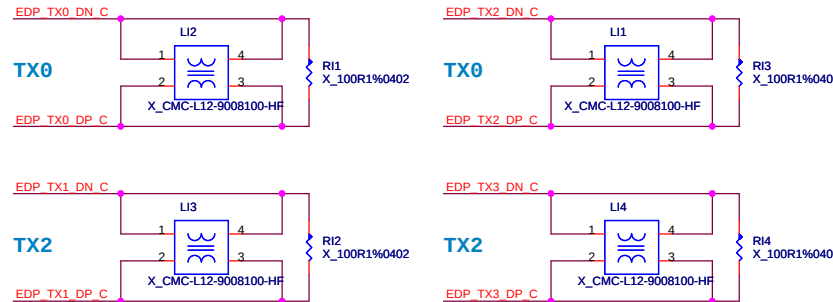


eDP CONN CAMERA



LCD Module Pin Define

Pin No	Symbol	Description
1	WP	EEPROM Write Protect(Keep open)
2	H_GND	High Speed Ground(0V)
3	eDP_Rx_3N	Complement Signal Link Lane 3
4	eDP_Rx_3P	True Signal Link Lane 3
5	H_GND	High Speed Ground(0V)
6	eDP_Rx_2N	Complement Signal Link Lane 2
7	eDP_Rx_2P	True Signal Link Lane 2
8	H_GND	H_GND
9	eDP_Rx_1N	Complement Signal Link Lane 1
10	eDP_Rx_1P	True Signal Link Lane 1
11	H_GND	H_GND
12	eDP_Rx_0N	Complement Signal Link Lane 0
13	eDP_Rx_0P	True Signal Link Lane 0
14	H_GND	H_GND
15	eDP_AUX_CH_P	True Signal Aux Channel
16	eDP_AUX_CH_N	Complement Signal Aux Channel
17	H_GND	H_GND
18	LCD_VCC	LCD logic and driver power
19	LCD_VCC	LCD logic and driver power
20	LCD_VCC	LCD logic and driver power
21	LCD_VCC	LCD logic and driver power
22	TEST	LCD Test Port
23	LCD_GND	LCD logic and driver ground(0V)
24	LCD_GND	LCD logic and driver ground(0V)
25	LCD_GND	LCD logic and driver ground(0V)
26	LCD_GND	LCD logic and driver ground(0V)
27	eDP_HPD	HPD signal pin
28	BL_GND	Backlight ground(0V)
29	BL_GND	Backlight ground(0V)
30	BL_GND	Backlight ground(0V)
31	BL_GND	Backlight ground(0V)
32	BL_ENABLE	Backlight enable
33	BL_PWM_DIM	System PWM signal input
34	SDA	I2C-bus Data
35	SCL	I2C-bus Clock
36	BL_PWR	Backlight power (5~21V)
37	BL_PWR	Backlight power (5~21V)
38	BL_PWR	Backlight power (5~21V)
39	BL_PWR	Backlight power (5~21V)
40	HSYNC	HSYNC output from Tcon

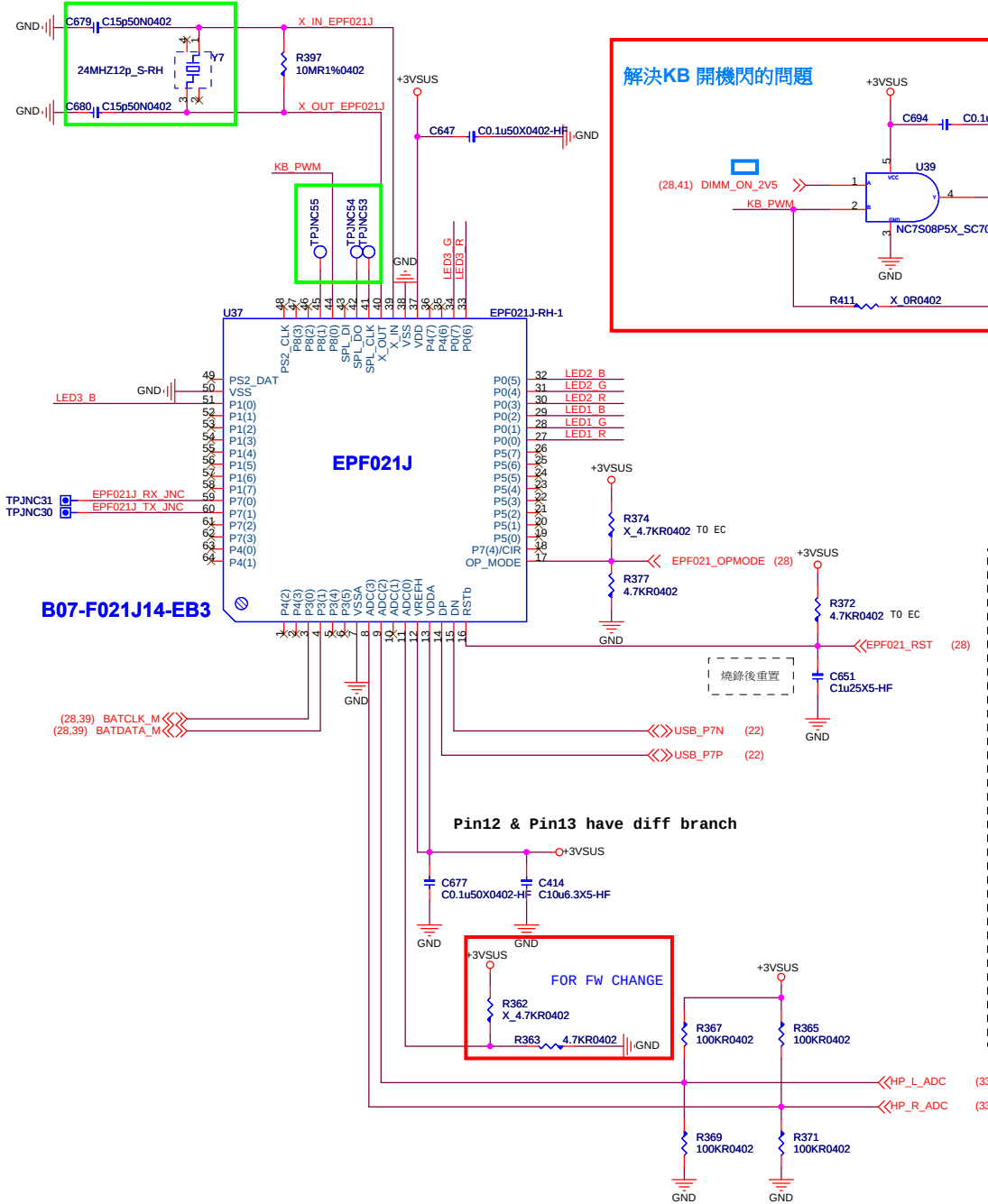


Place Close eDP Connector

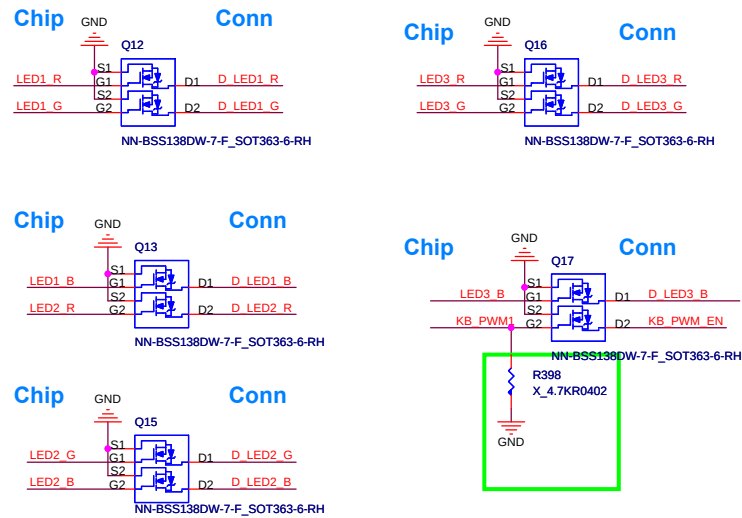
Reserve for EMI

LED 8051 Controller

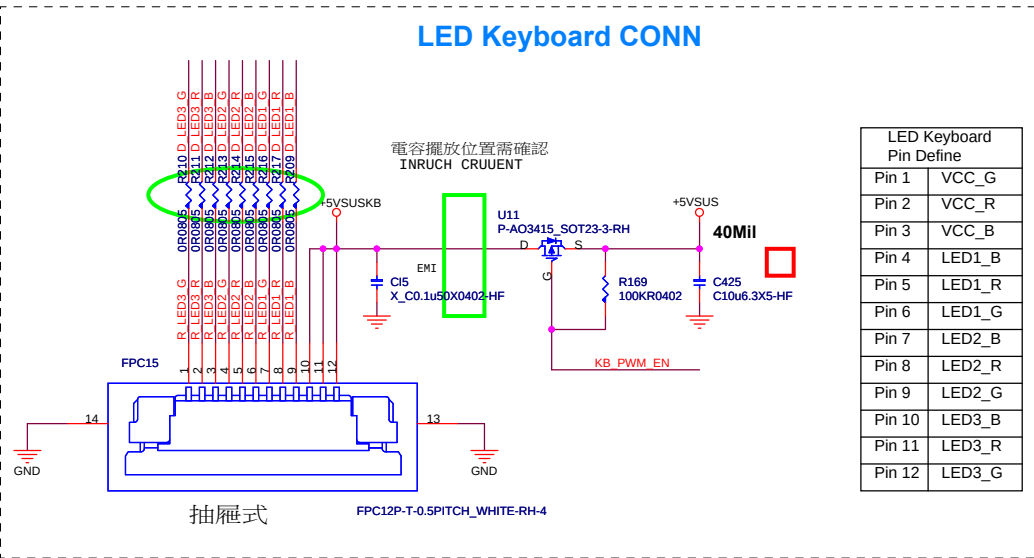
C749 and C750 change to 15pF for SA



EPF021J Sink current not enough, only using BSS138 (0.22A)



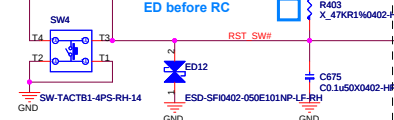
LED Keyboard CONN



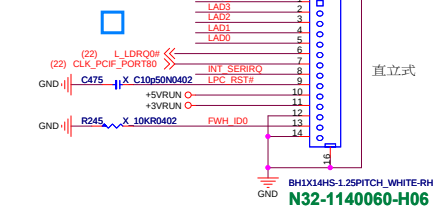
LED Keyboard Pin Define	
Pin 1	VCC_G
Pin 2	VCC_R
Pin 3	VCC_B
Pin 4	LED1_B
Pin 5	LED1_R
Pin 6	LED1_G
Pin 7	LED2_B
Pin 8	LED2_R
Pin 9	LED2_G
Pin 10	LED3_B
Pin 11	LED3_R
Pin 12	LED3_G

KBC/EC/uP (ENE9028)

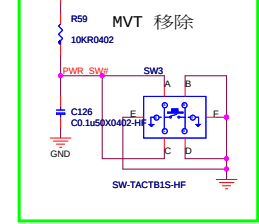
Hardware Reset



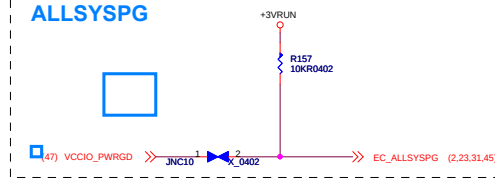
SW Debug (LPC)



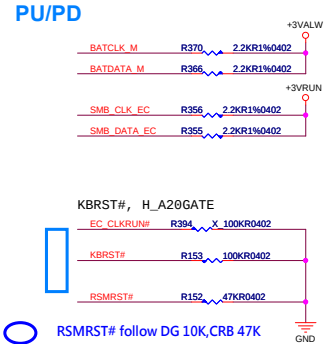
HW Debug



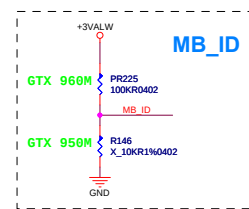
ALLSYSPG



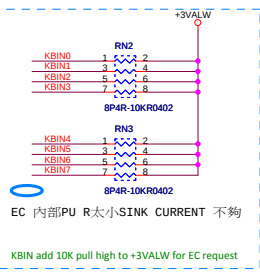
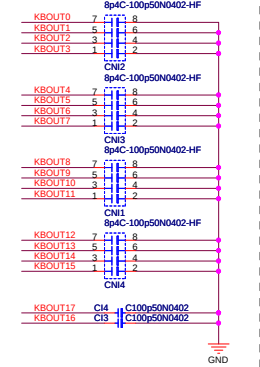
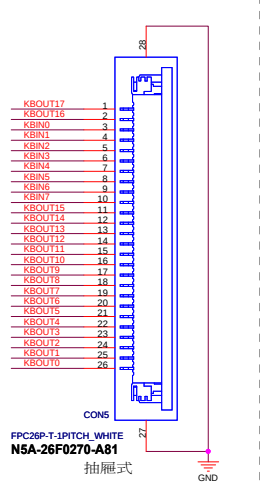
PU/PD



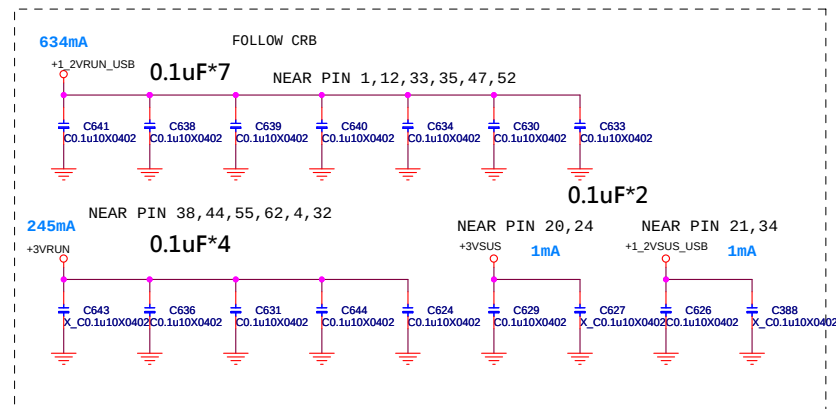
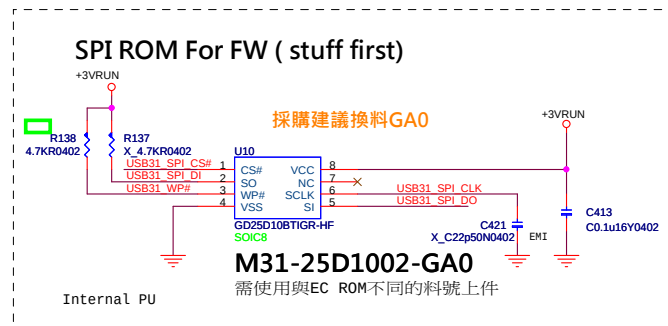
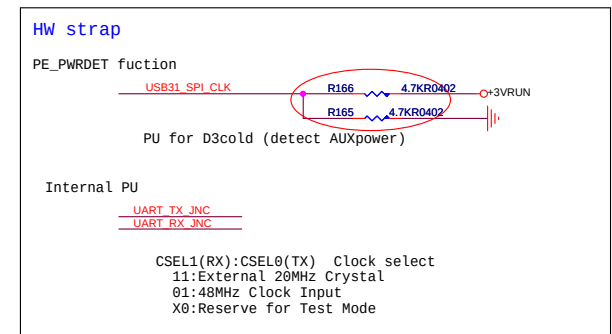
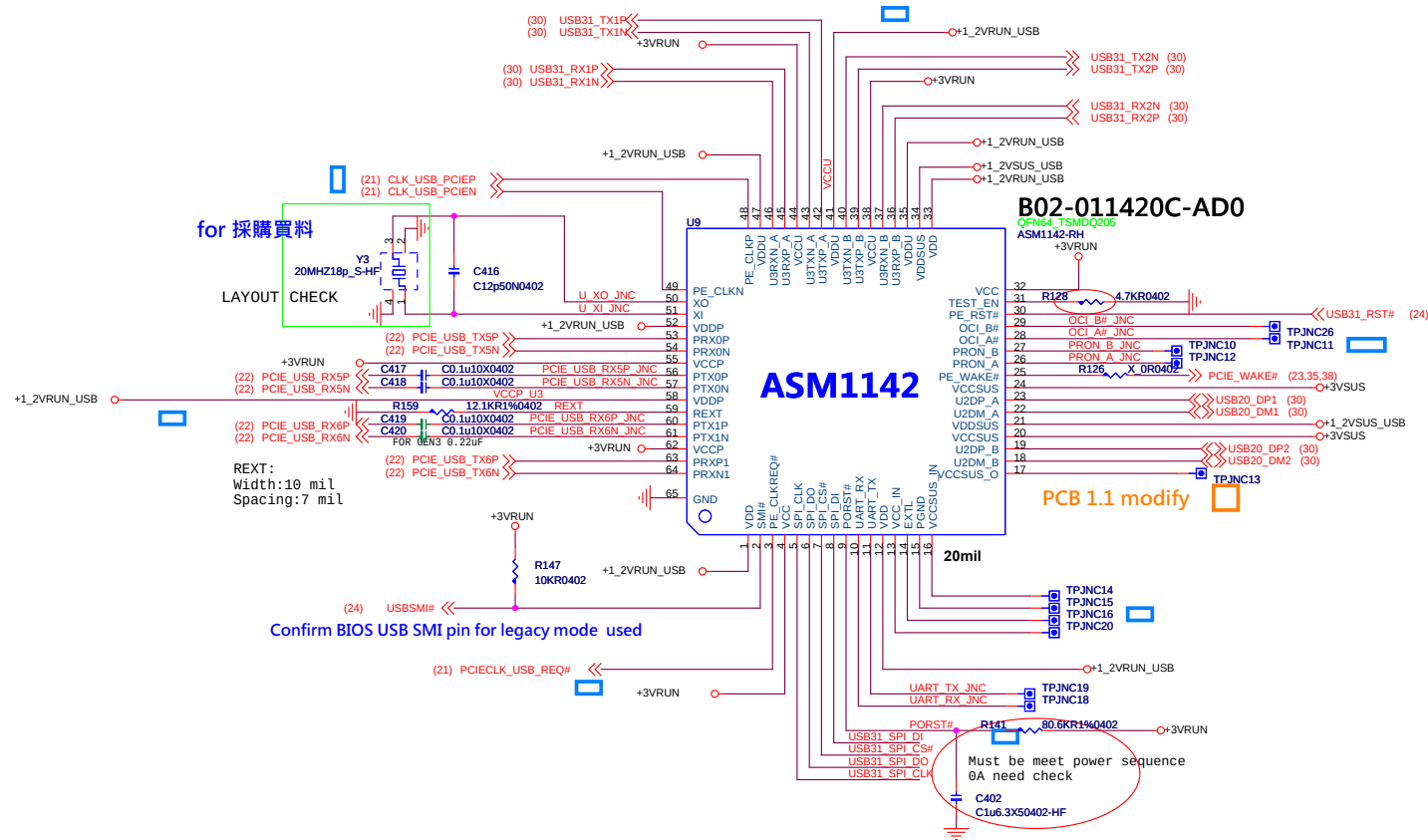
MB_ID



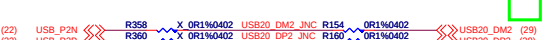
Keyboard conn



USB 3.0/ USB.3.1



52 X 0R1%0402 USB31 TX2P JNC R142 0R1%0402
51 X 0R1%0402 USB31 TX2N JNC R140 0R1%0402

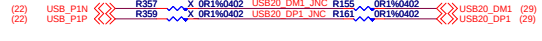


PCH ASM1142

```

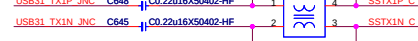
R368      X 0R1%0402  USB31 TX1P  JNC R168  0R1%0402
R364      X 0R1%0402  USB31 TX1N  JNC R167  0R1%0402

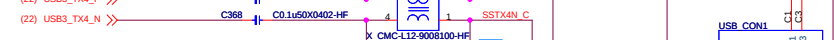
```



4

USB31 TX2P JNC C637 C0.22u16X50402-1





X C0.1u16X7040

7



3



USB_CON2	C1	C3
----------	----	----

9	STDA_SSTX+
1	VBUS
8	STDA_SSTX

3	GN
6	D+
7	STDA_SSRX+

USBM_BLUE



NO. 550021110

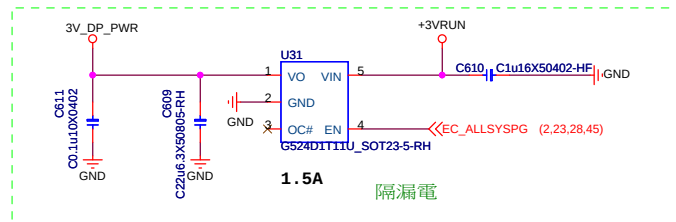


8

1

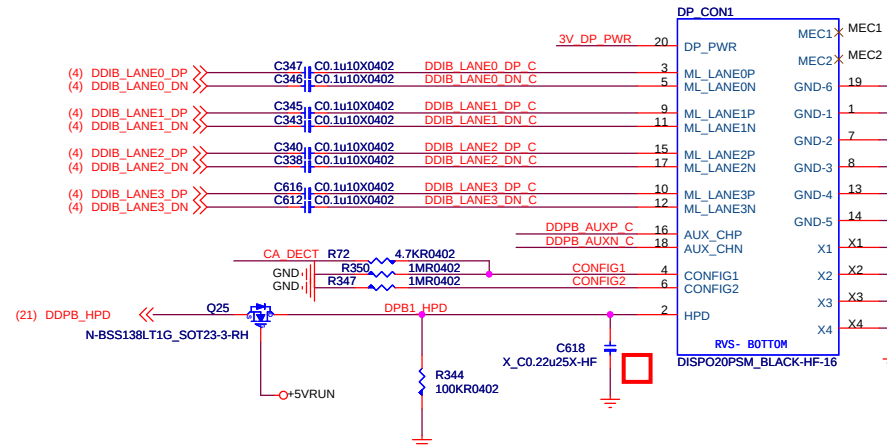
Display Port

The preset trip limit must not exceed 3A at the Upstream device connector DP_PWR pin and 1.5A at the Downstream device connector DP_PWR pin.

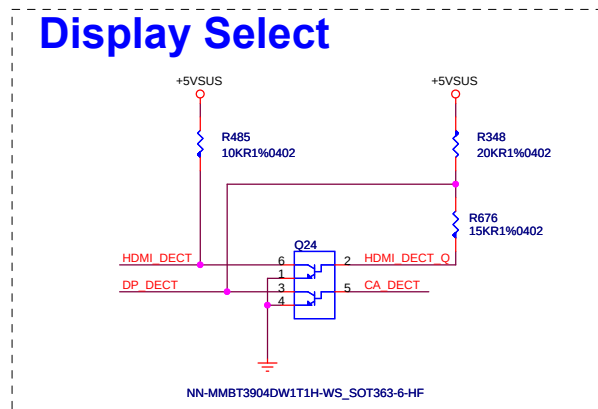


ESD Contact ± 5 KV & Air ± 15 KV

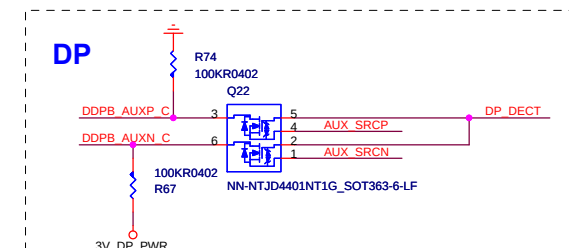
Display Port



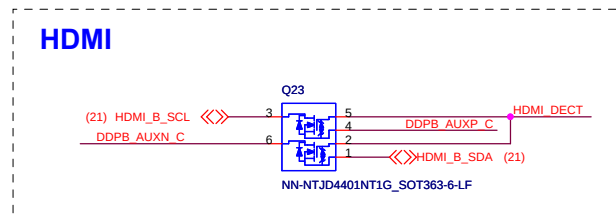
Display Select



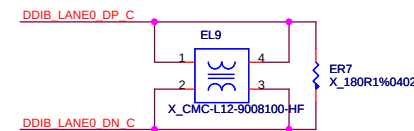
Dual Mode Switch



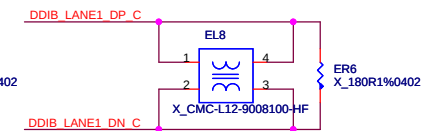
HDMI



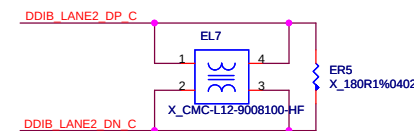
EMI Close Connector



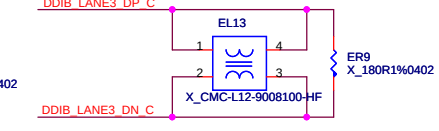
LANE1



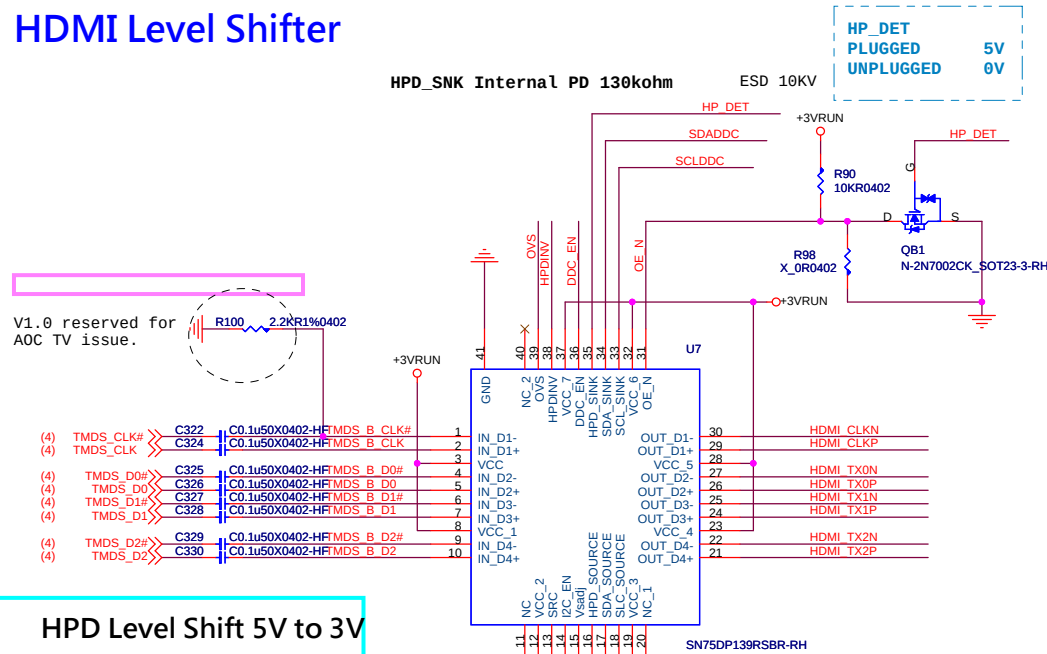
LANE2



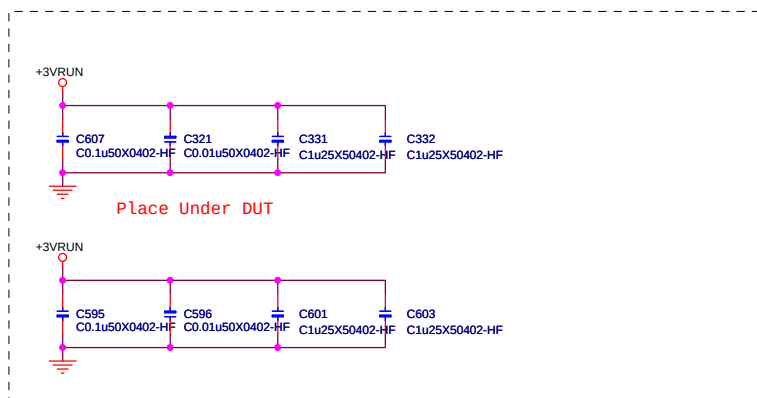
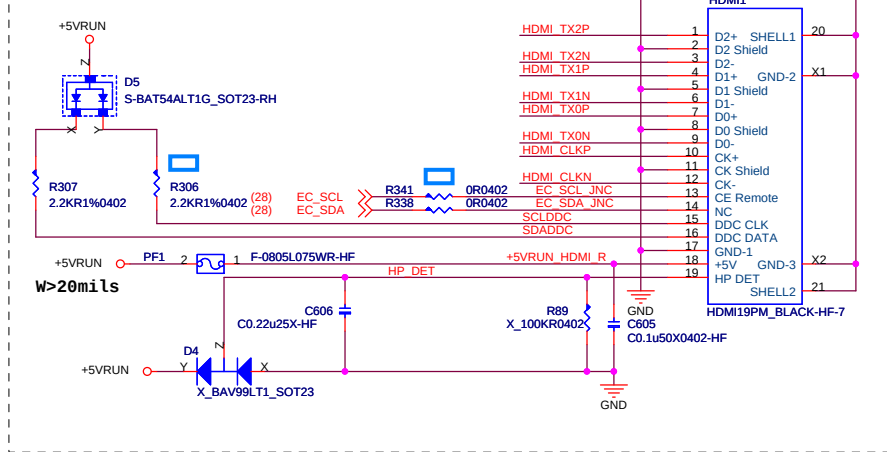
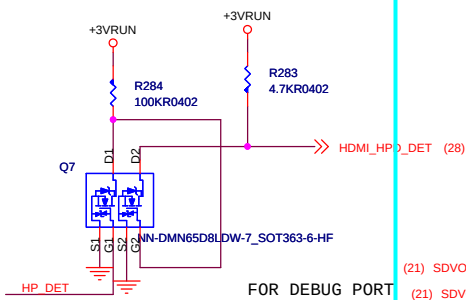
LANE3



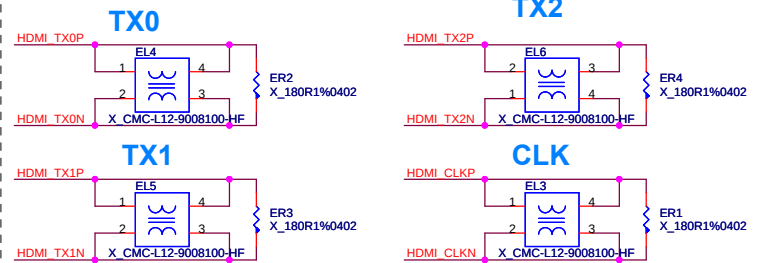
HDMI Level Shifter



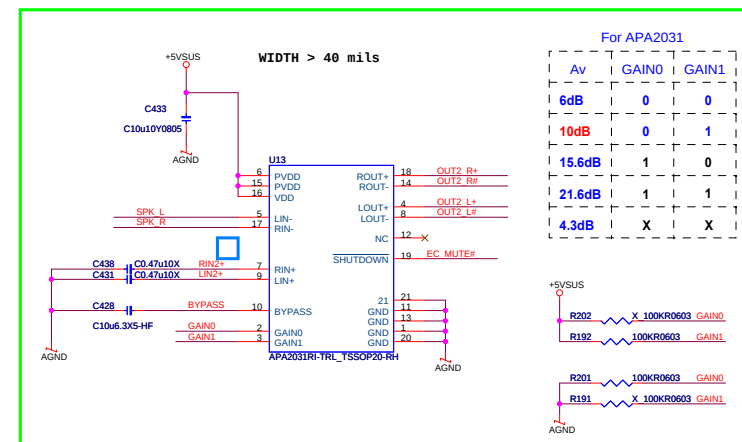
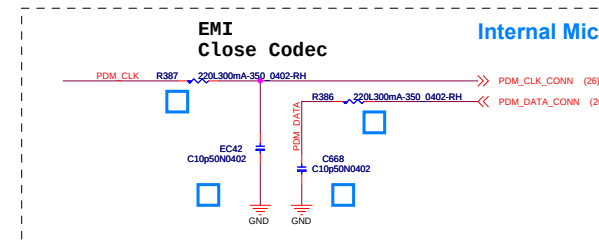
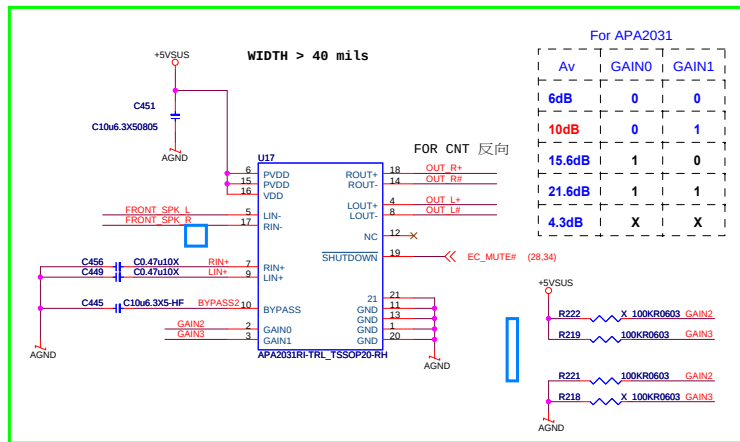
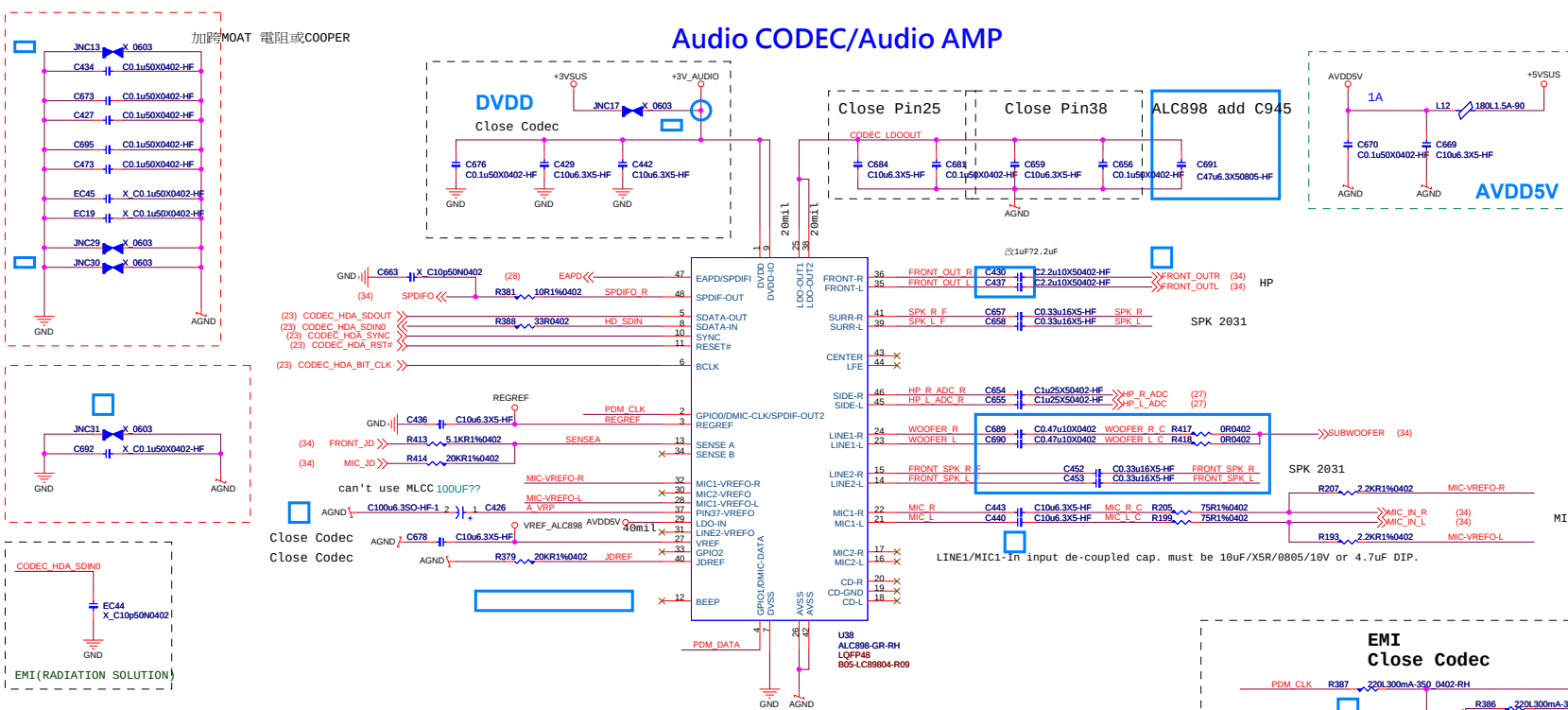
HPD Level Shift 5V to 3V



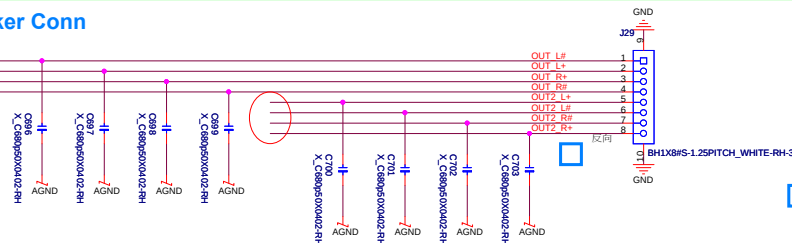
EMI Close Connector



Audio CODEC/Audio AMP



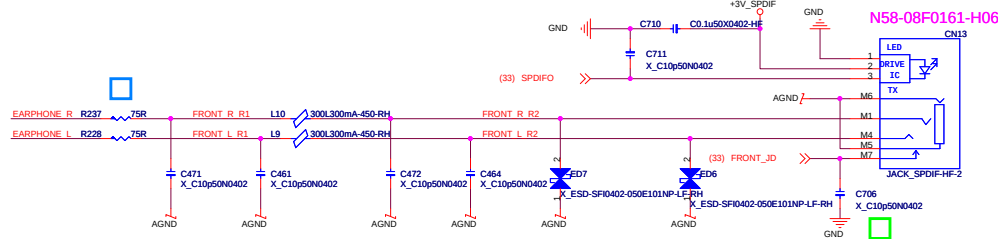
Internal Speaker Conn



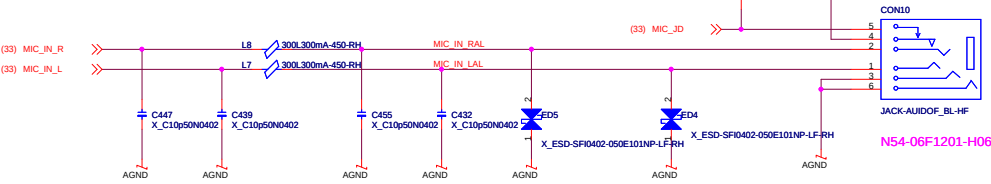
CODEC	喇叭
L	-
L	+
R	+
R	-
L2	+
L2	-
R2	-
R2	+

Audio CONN /Woffler

FRONT OUT



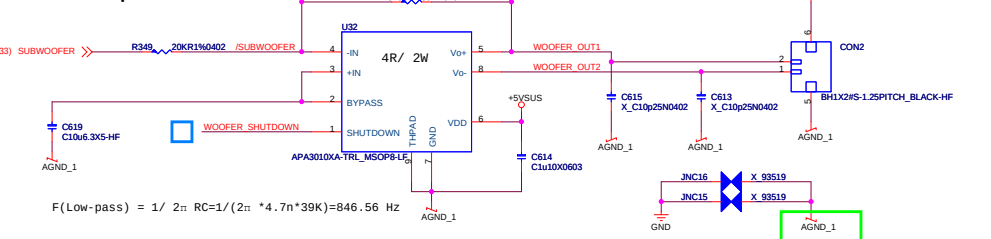
MIC IN



ALC892 SPC MAX 為 1.2Vrms
g_{31n}= -2*(R376/R371)= -2*(40K/20K) = -4
V_{out}= 0.58Vrms *4 = 2.32Vrms , P_o=(2.32*2.32)/3.8=1.42W

Woofer SPEC
YG 3.8ohm / normal 3W,Max 3.5W
FG 4ohm/ normal 3W,Max 3.5W

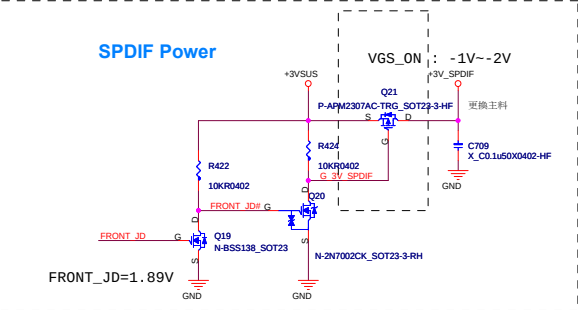
Bass Speaker



$$F(\text{Low-pass}) = 1 / 2\pi RC = 1 / (2\pi * 4.7n * 39K) = 846.56 \text{ Hz}$$

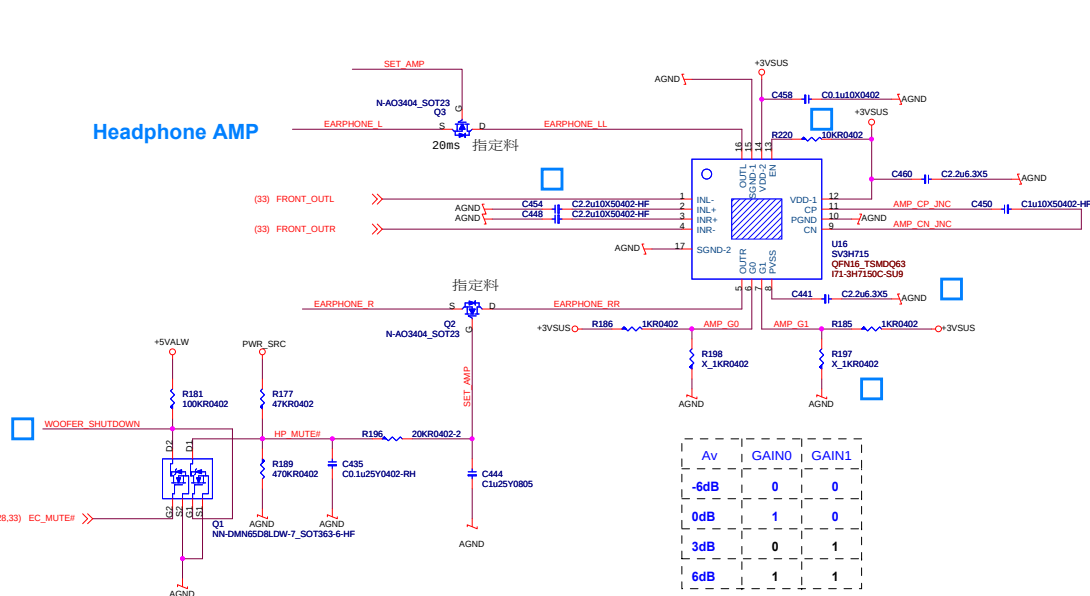
WOOFER AMP 擺太遠獨立切AGND_1

SPDIF Power



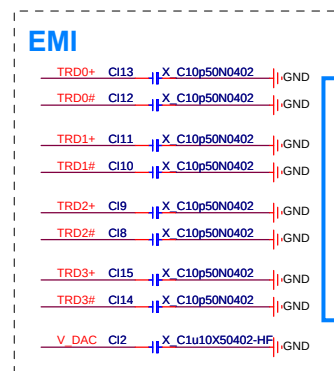
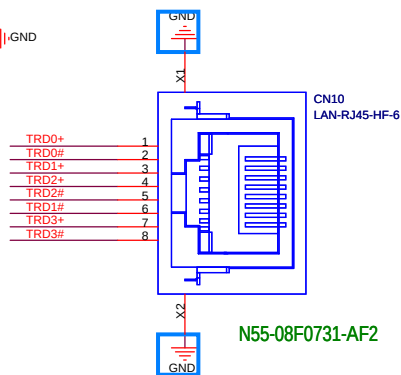
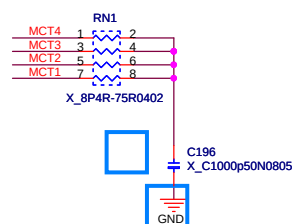
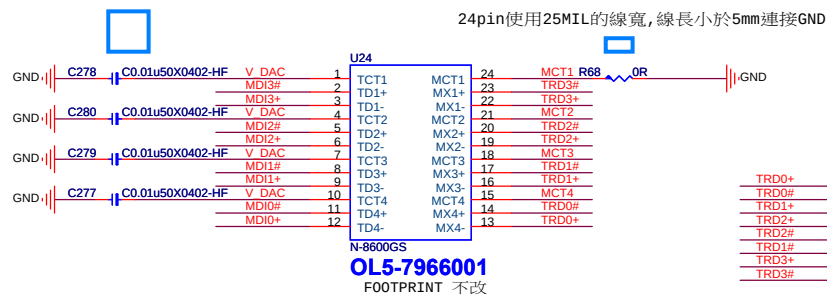
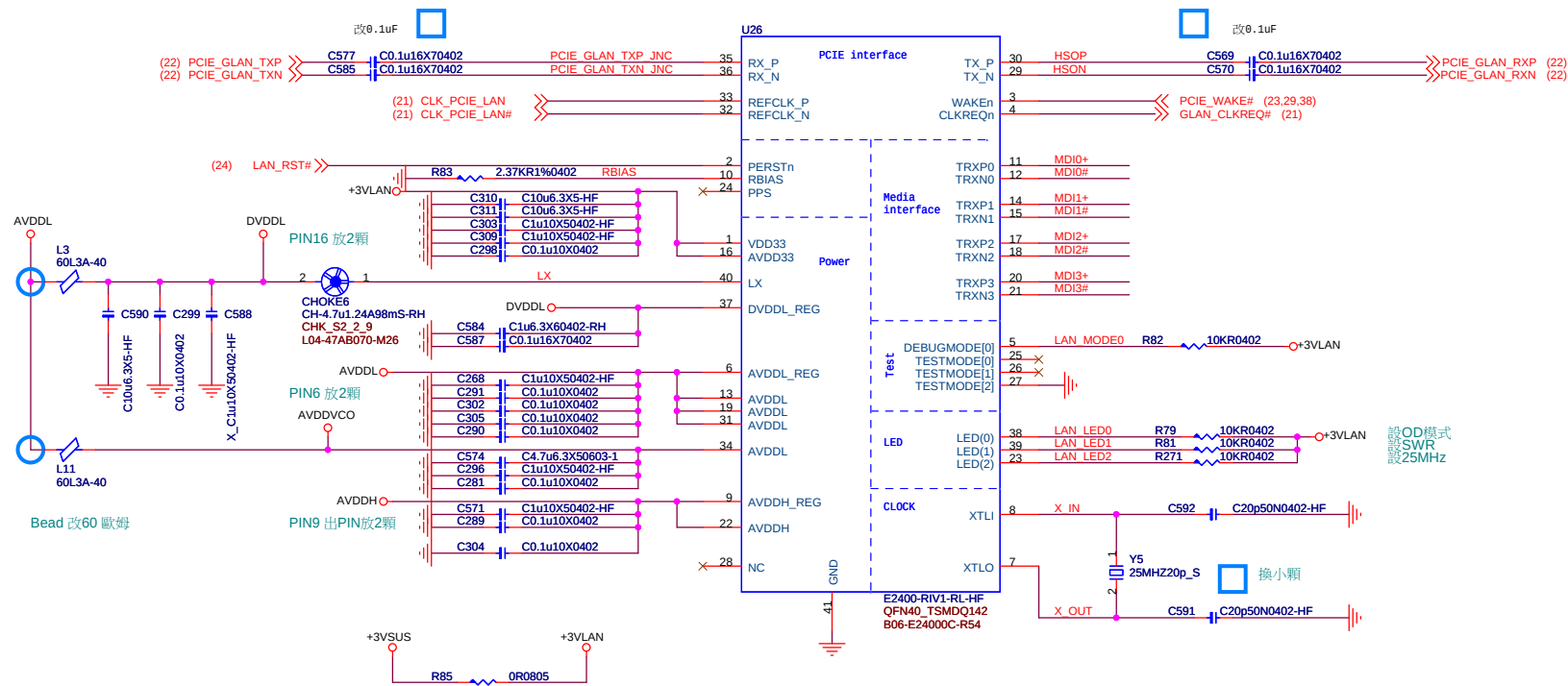
FRONT_JD=1.89V

Headphone AMP

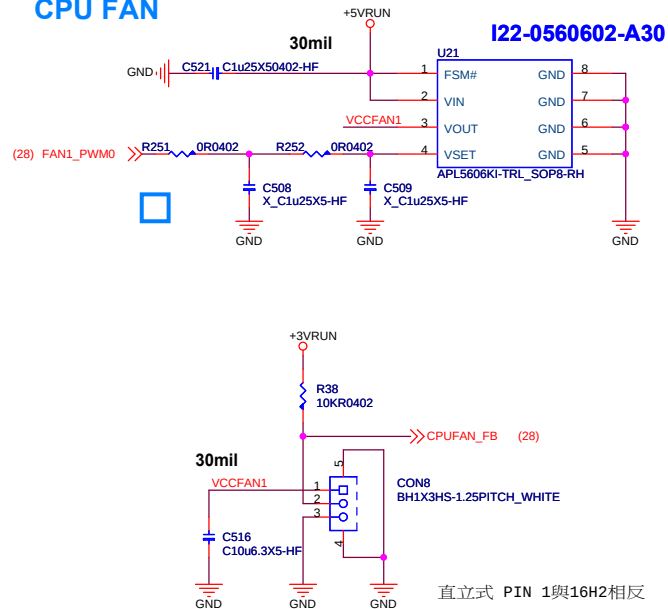


Av	GAIN0	GAIN1
-6dB	0	0
0dB	1	0
3dB	0	1
6dB	1	1

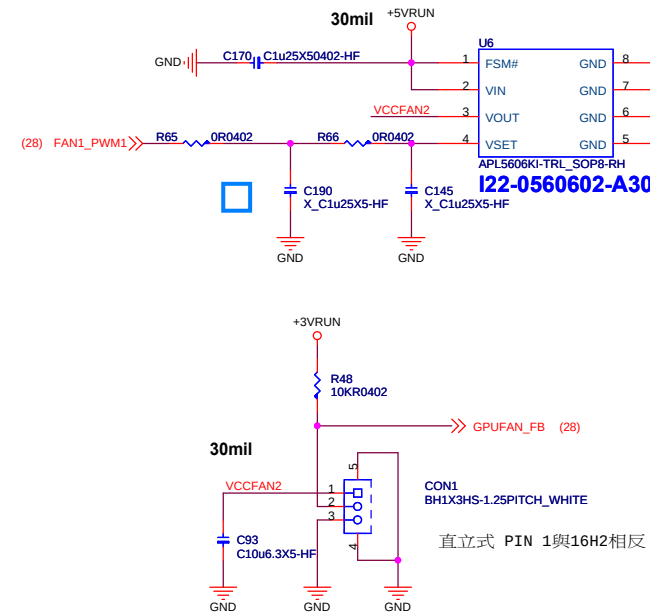
GIGA LAN(BigFoot BFN2400B)



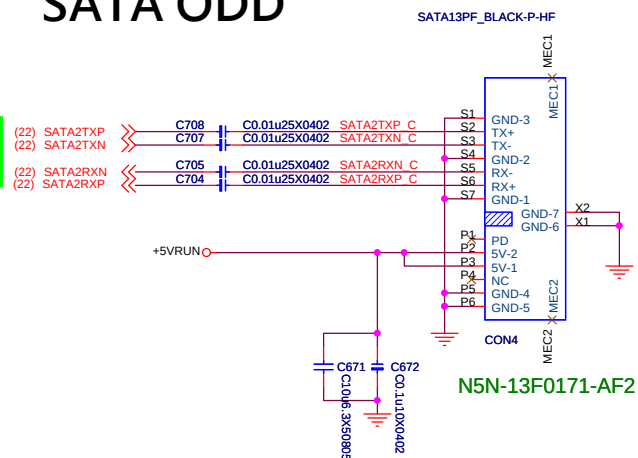
CPU FAN



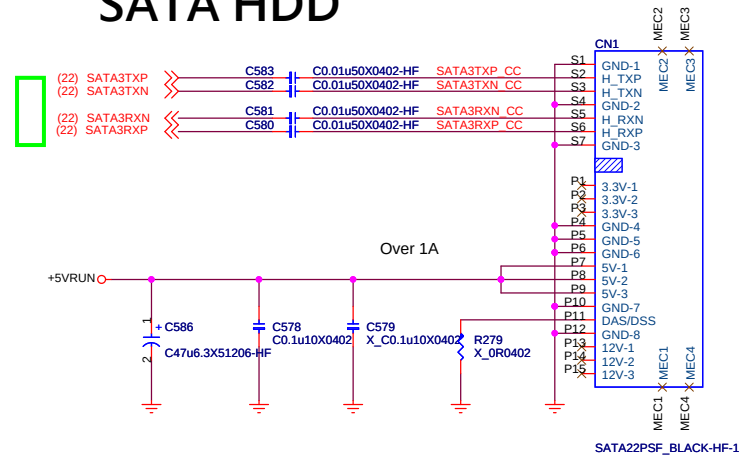
DGPU FAN



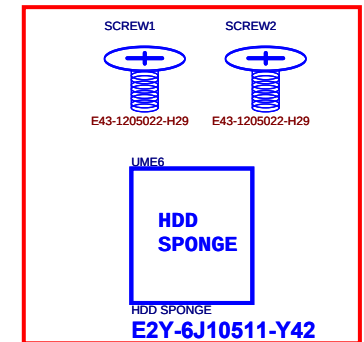
SATA ODD



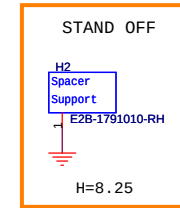
SATA HDD



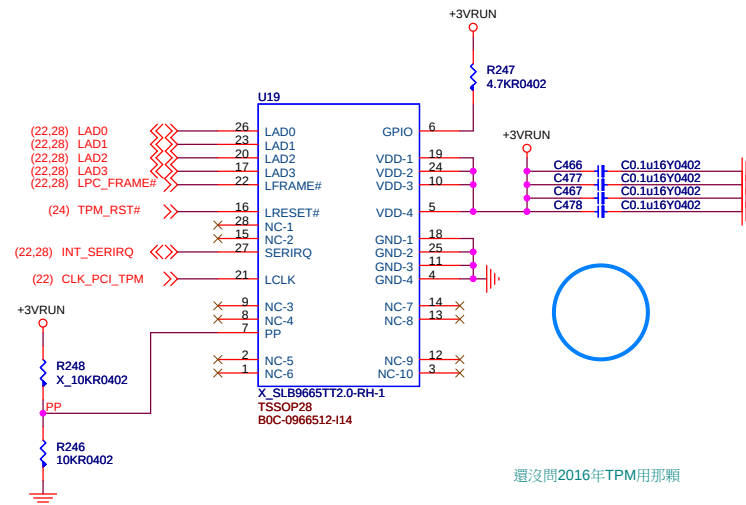
22. GND
21. TX
20. TX#
19. GND
18. RX#
17. RX
16. GND
15. V33
14. V33
13. V33
12. GND
11. GND
10. GND
9. V5
8. V5
7. V5
6. GND
5. Reserved
4. GND
3. V12
2. V12
1. V12



This schematic diagram illustrates the electrical connections for a PCIe/SATA SSD3 module. The module is connected to a host system via a PCIe4x /mSATA Co-lay SSD SIGNAL interface. The diagram shows the internal components of the SSD, including the controller (C723, C722, C721, C720, C719, C718, C714, C713), memory (C470, C470A, C470B, C470C, C470D, C470E, C470F, C470G, C470H, C470I, C470J, C470K, C470L, C470M, C470N, C470O, C470P, C470Q, C470R, C470S, C470T, C470U, C470V, C470W, C470X, C470Y, C470Z, C470AA, C470AB, C470AC, C470AD, C470AE, C470AF, C470AG, C470AH, C470AI, C470AJ, C470AK, C470AL, C470AM, C470AN, C470AO, C470AP, C470AQ, C470AR, C470AS, C470AT, C470AU, C470AV, C470AW, C470AX, C470AY, C470AZ, C470BA, C470BB, C470BC, C470BD, C470BE, C470BF, C470BG, C470BH, C470BI, C470BJ, C470BK, C470BL, C470BM, C470BN, C470BO, C470BP, C470BQ, C470BR, C470BS, C470BT, C470BU, C470BV, C470BW, C470BX, C470BY, C470BZ, C470CA, C470CB, C470CC, C470CD, C470CE, C470CF, C470CG, C470CH, C470CI, C470CJ, C470CK, C470CL, C470CM, C470CN, C470CO, C470CP, C470CQ, C470CR, C470CS, C470CT, C470CU, C470CV, C470CW, C470CX, C470CY, C470CZ, C470DA, C470DB, C470DC, C470DD, C470DE, C470DF, C470DG, C470DH, C470DI, C470DJ, C470DK, C470DL, C470DM, C470DN, C470DO, C470DP, C470DQ, C470DR, C470DS, C470DT, C470DU, C470DV, C470DW, C470DX, C470DY, C470DZ, C470EA, C470EB, C470EC, C470ED, C470EE, C470EF, C470EG, C470EH, C470EI, C470EJ, C470EK, C470EL, C470EM, C470EN, C470EO, C470EP, C470EQ, C470ER, C470ES, C470ET, C470EU, C470EV, C470EW, C470EX, C470EY, C470EZ, C470FA, C470FB, C470FC, C470FD, C470FE, C470FF, C470FG, C470FH, C470FI, C470FJ, C470FK, C470FL, C470FM, C470FN, C470FO, C470FP, C470FQ, C470FR, C470FS, C470FT, C470FU, C470FV, C470FW, C470FX, C470FY, C470FZ, C470GA, C470GB, C470GC, C470GD, C470GE, C470GF, C470GG, C470GH, C470GI, C470GJ, C470GK, C470GL, C470GM, C470GN, C470GO, C470GP, C470GQ, C470GR, C470GS, C470GT, C470GU, C470GV, C470GW, C470GX, C470GY, C470GZ, C470HA, C470HB, C470HC, C470HD, C470HE, C470HF, C470HG, C470HH, C470HI, C470HJ, C470HK, C470HL, C470HM, C470HN, C470HO, C470HP, C470HQ, C470HR, C470HS, C470HT, C470HU, C470HV, C470HW, C470HX, C470HY, C470HZ, C470IA, C470IB, C470IC, C470ID, C470IE, C470IF, C470IG, C470IH, C470II, C470IJ, C470IK, C470IL, C470IM, C470IN, C470IO, C470IP, C470IQ, C470IR, C470IS, C470IT, C470IU, C470IV, C470IW, C470IX, C470IY, C470IZ, C470JA, C470JB, C470JC, C470JD, C470JE, C470JF, C470JG, C470JH, C470JI, C470JJ, C470JK, C470JL, C470JM, C470JN, C470JO, C470JP, C470JQ, C470JR, C470JS, C470JT, C470JU, C470JV, C470JW, C470JX, C470JY, C470JZ, C470KA, C470KB, C470KC, C470KD, C470KE, C470KF, C470KG, C470KH, C470KI, C470KJ, C470KK, C470KL, C470KM, C470KN, C470KO, C470KP, C470KQ, C470KR, C470KS, C470KT, C470KU, C470KV, C470KW, C470KX, C470KY, C470KZ, C470LA, C470LB, C470LC, C470LD, C470LE, C470LF, C470LG, C470LH, C470LI, C470LJ, C470LK, C470LL, C470LM, C470LN, C470LO, C470LP, C470LQ, C470LR, C470LS, C470LT, C470LU, C470LV, C470LW, C470LX, C470LY, C470LZ, C470MA, C470MB, C470MC, C470MD, C470ME, C470MF, C470MG, C470MH, C470MI, C470MJ, C470MK, C470ML, C470MM, C470MN, C470MO, C470MP, C470MQ, C470MR, C470MS, C470MT, C470MU, C470MV, C470MW, C470MX, C470MY, C470MZ, C470NA, C470NB, C470NC, C470ND, C470NE, C470NF, C470NG, C470NH, C470NI, C470NJ, C470NK, C470NL, C470NM, C470NN, C470NO, C470NP, C470NQ, C470NR, C470NS, C470NT, C470NU, C470NV, C470NW, C470NX, C470NY, C470NZ, C470OA, C470OB, C470OC, C470OD, C470OE, C470OF, C470OG, C470OH, C470OI, C470OJ, C470OK, C470OL, C470OM, C470ON, C470OO, C470OP, C470OQ, C470OR, C470OS, C470OT, C470OU, C470OV, C470OW, C470OX, C470OY, C470OZ, C470PA, C470PB, C470PC, C470PD, C470PE, C470PF, C470PG, C470PH, C470PI, C470PJ, C470PK, C470PL, C470PM, C470PN, C470PO, C470PP, C470PQ, C470PR, C470PS, C470PT, C470PU, C470PV, C470PW, C470PX, C470PY, C470PZ, C470QA, C470QB, C470QC, C470QD, C470QE, C470QF, C470QG, C470QH, C470QI, C470QJ, C470QK, C470QL, C470QM, C470QN, C470QO, C470QP, C470QQ, C470QR, C470QS, C470QT, C470QU, C470QV, C470QW, C470QX, C470QY, C470QZ, C470RA, C470RB, C470RC, C470RD, C470RE, C470RF, C470RG, C470RH, C470RI, C470RJ, C470RK, C470RL, C470RM, C470RN, C470RO, C470RP, C470RQ, C470RR, C470RS, C470RT, C470RU, C470RV, C470RW, C470RX, C470RY, C470RZ, C470SA, C470SB, C470SC, C470SD, C470SE, C470SF, C470SG, C470SH, C470SI, C470SJ, C470SK, C470SL, C470SM, C470SN, C470SO, C470SP, C470SQ, C470SR, C470SS, C470ST, C470SU, C470SV, C470SW, C470SX, C470SY, C470SZ, C470TA, C470TB, C470TC, C470TD, C470TE, C470TF, C470TG, C470TH, C470TI, C470TJ, C470TK, C470TL, C470TM, C470TN, C470TO, C470TP, C470TQ, C470TR, C470TS, C470TT, C470TU, C470TV, C470TW, C470TX, C470TY, C470TZ, C470UA, C470UB, C470UC, C470UD, C470UE, C470UF, C470UG, C470UH, C470UI, C470UJ, C470UK, C470UL, C470UM, C470UN, C470UO, C470UP, C470UQ, C470UR, C470US, C470UT, C470UU, C470UV, C470UW, C470UX, C470UY, C470UZ, C470VA, C470VB, C470VC, C470VD, C470VE, C470VF, C470VG, C470VH, C470VI, C470VJ, C470VK, C470VL, C470VM, C470VN, C470VO, C470VP, C470VQ, C470VR, C470VS, C470VT, C470VU, C470VV, C470VW, C470VX, C470VY, C470VZ, C470WA, C470WB, C470WC, C470WD, C470WE, C470WF, C470WG, C470WH, C470WI, C470WJ, C470WK, C470WL, C470WM, C470WN, C470WO, C470WP, C470WQ, C470WR, C470WS, C470WT, C470WU, C470WV, C470WW, C470WX, C470WY, C470WZ, C470XA, C470XB, C470XC, C470XD, C470XE, C470XF, C470XG, C470XH, C470XI, C470XJ, C470XK, C470XL, C470XM, C470XN, C470XO, C470XP, C470XQ, C470XR, C470XS, C470XT, C470XU, C470XV, C470XW, C470XX, C470XY

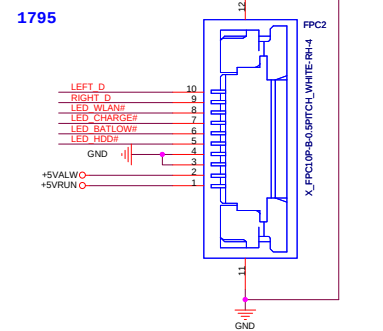
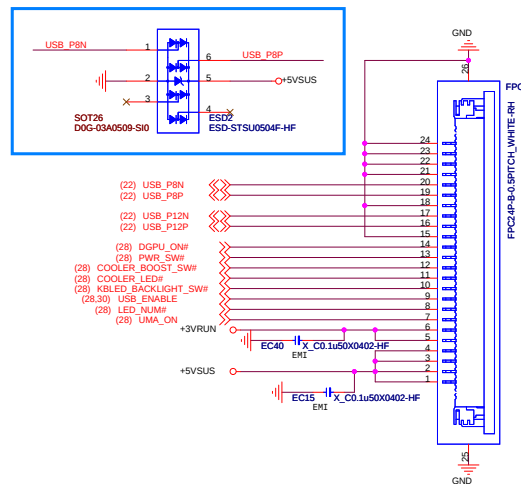
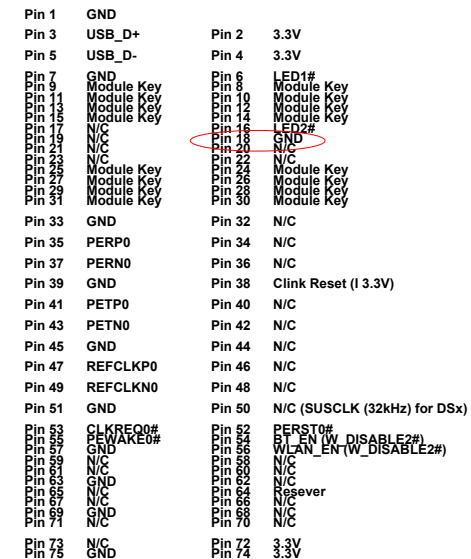


TPM



還沒問2016年TPM用那顆

WLAN/LED



EMI_C0.1u50X0402-HF

+3VRUN

R436 R435

RIGHT_D LEFT_D

(28) TP_DATA (28) TP_CLK

+5VRUN

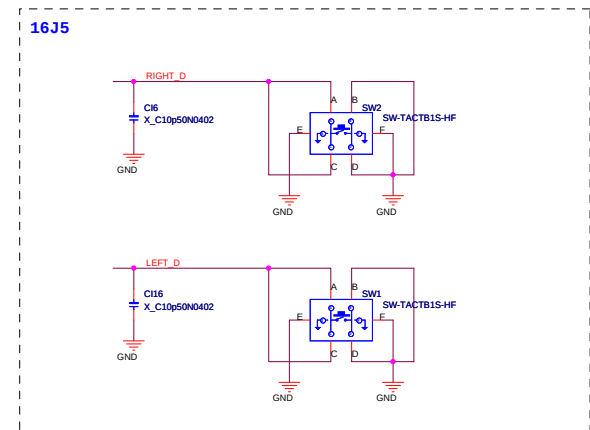
EMI_C0.1u50X0402-HF

EC49

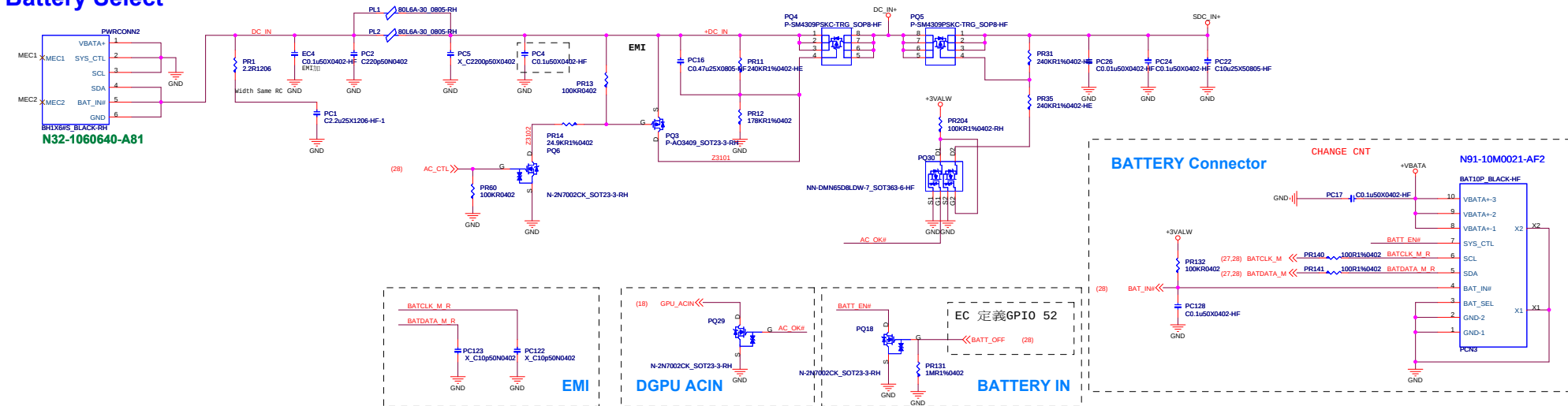
FPC6P-B-0.5PITCH_WHITE-RH-5

FPC4

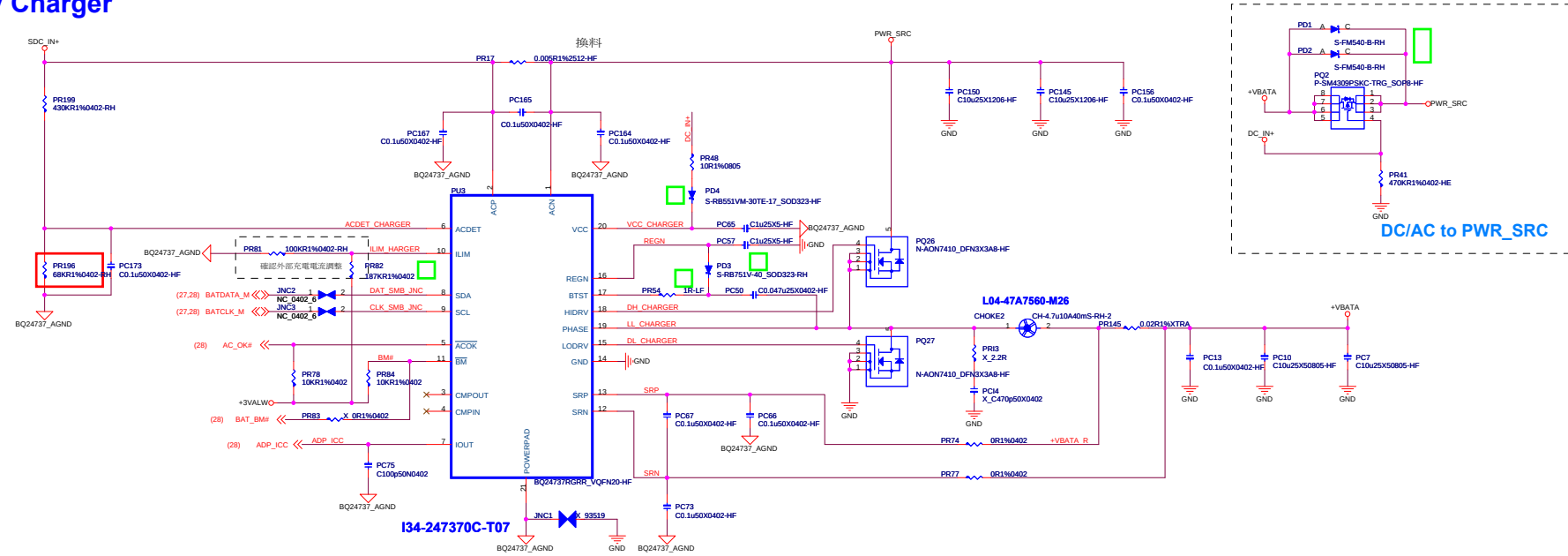
CNT pin 與TP module pin定義相反



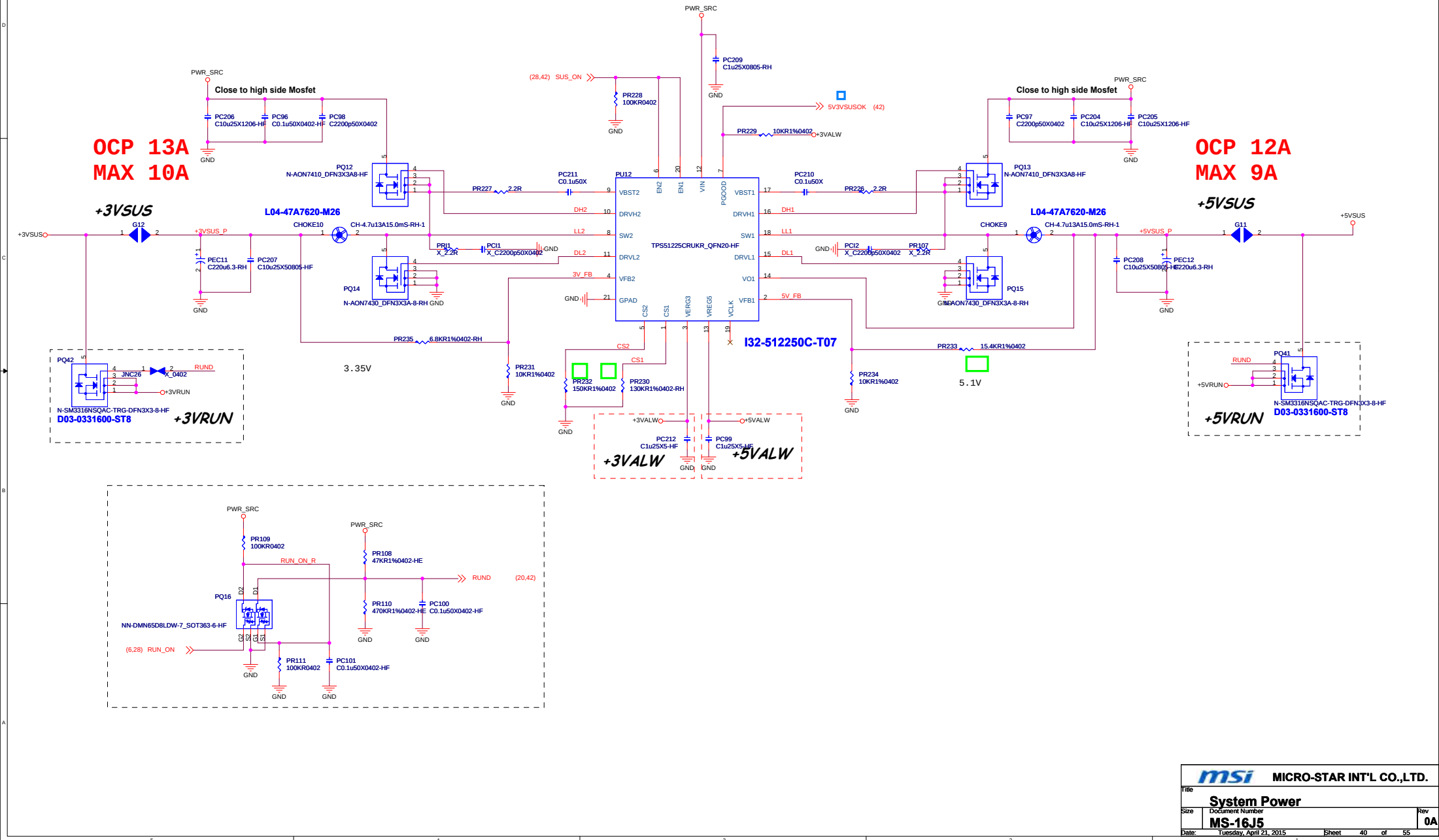
Battery Select

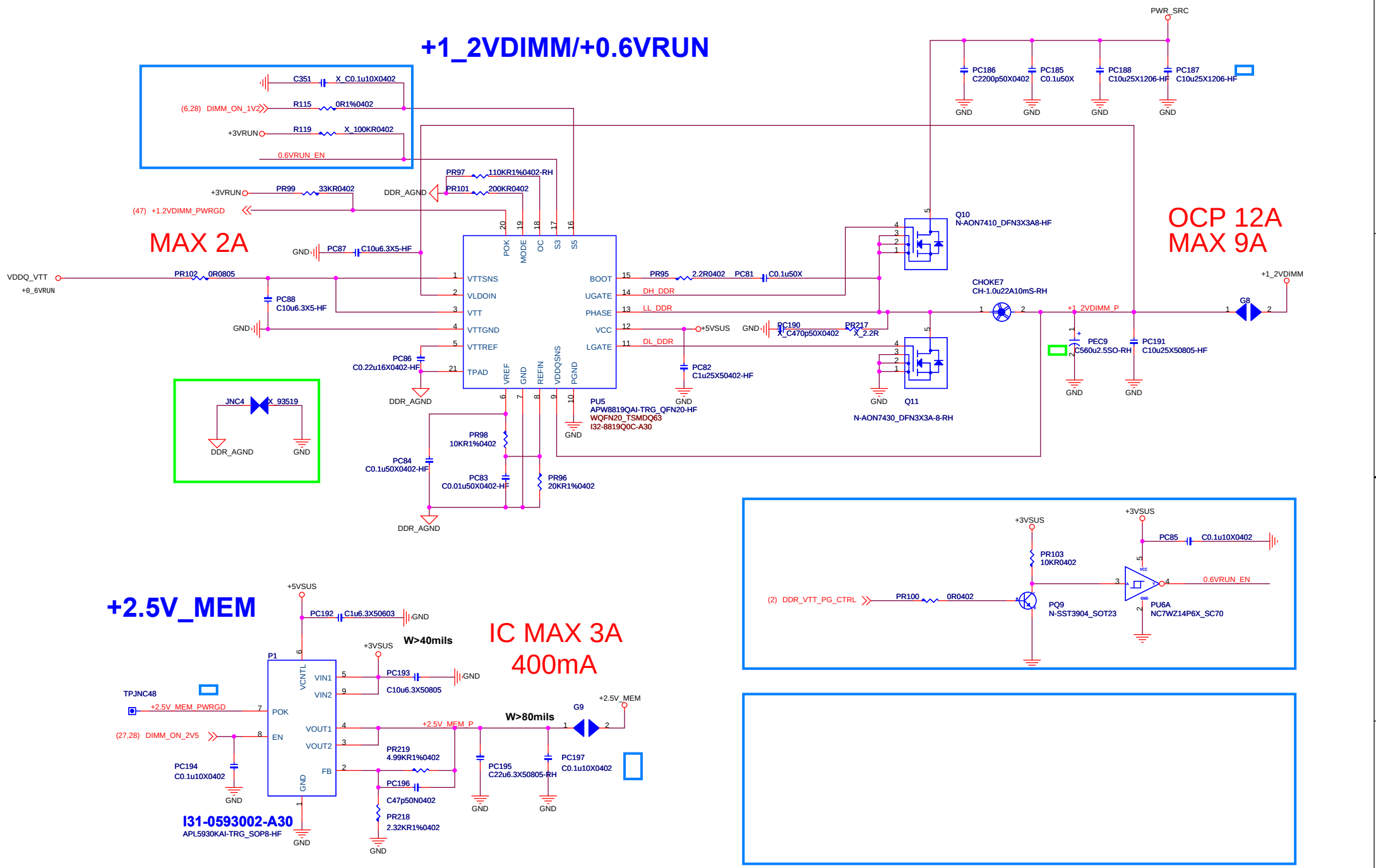


Battery Charger

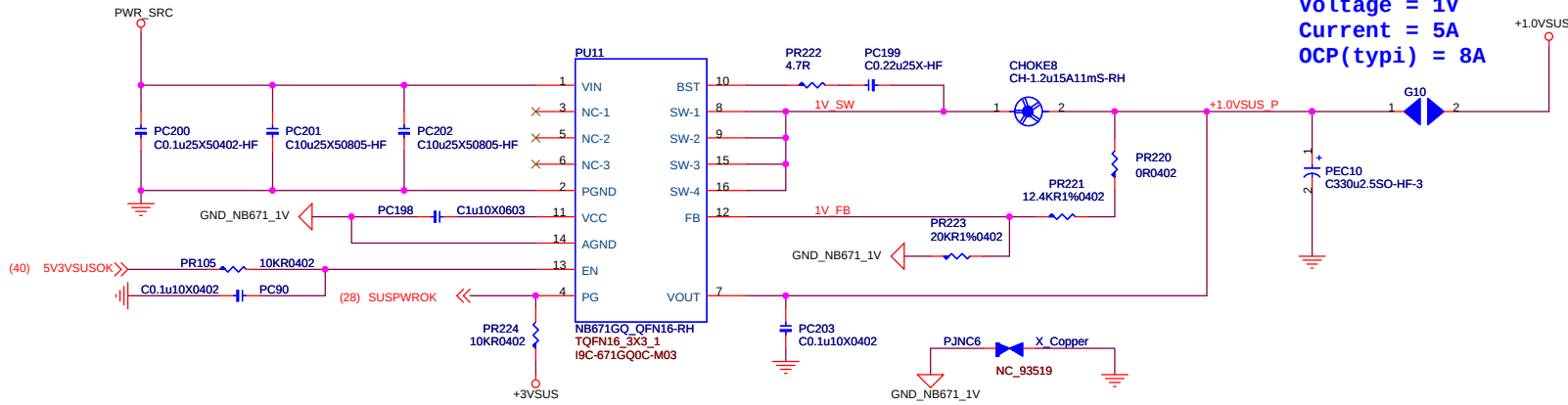


System Power

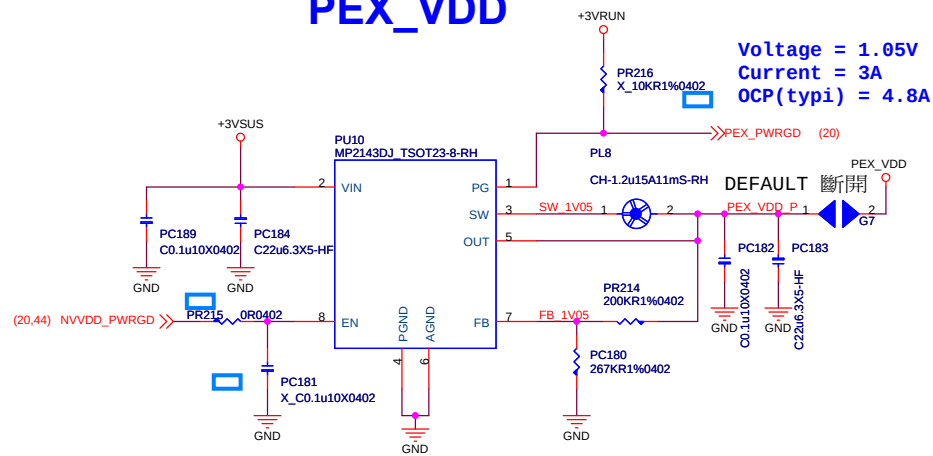




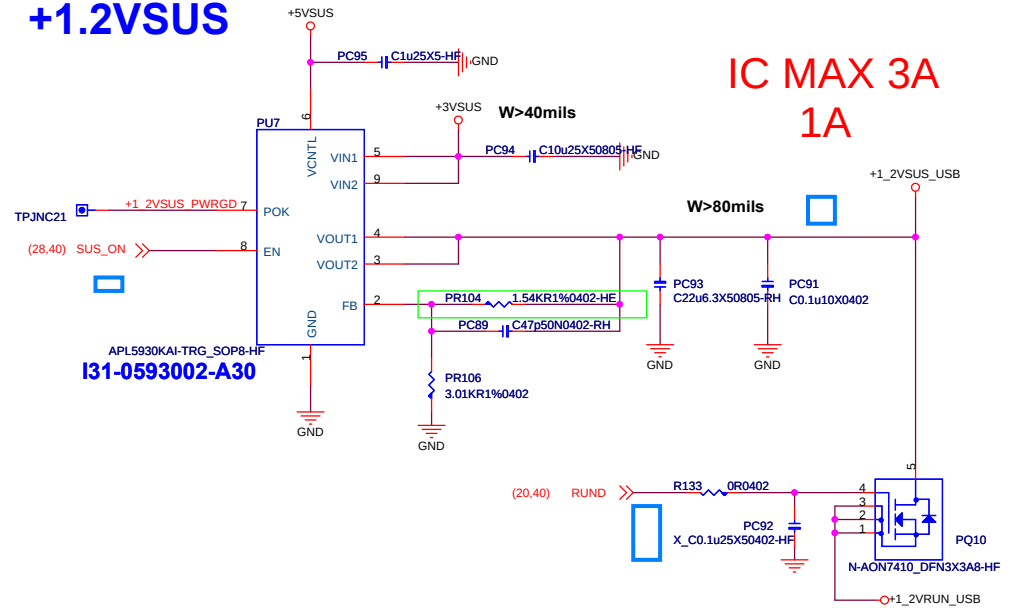
+1VSUS

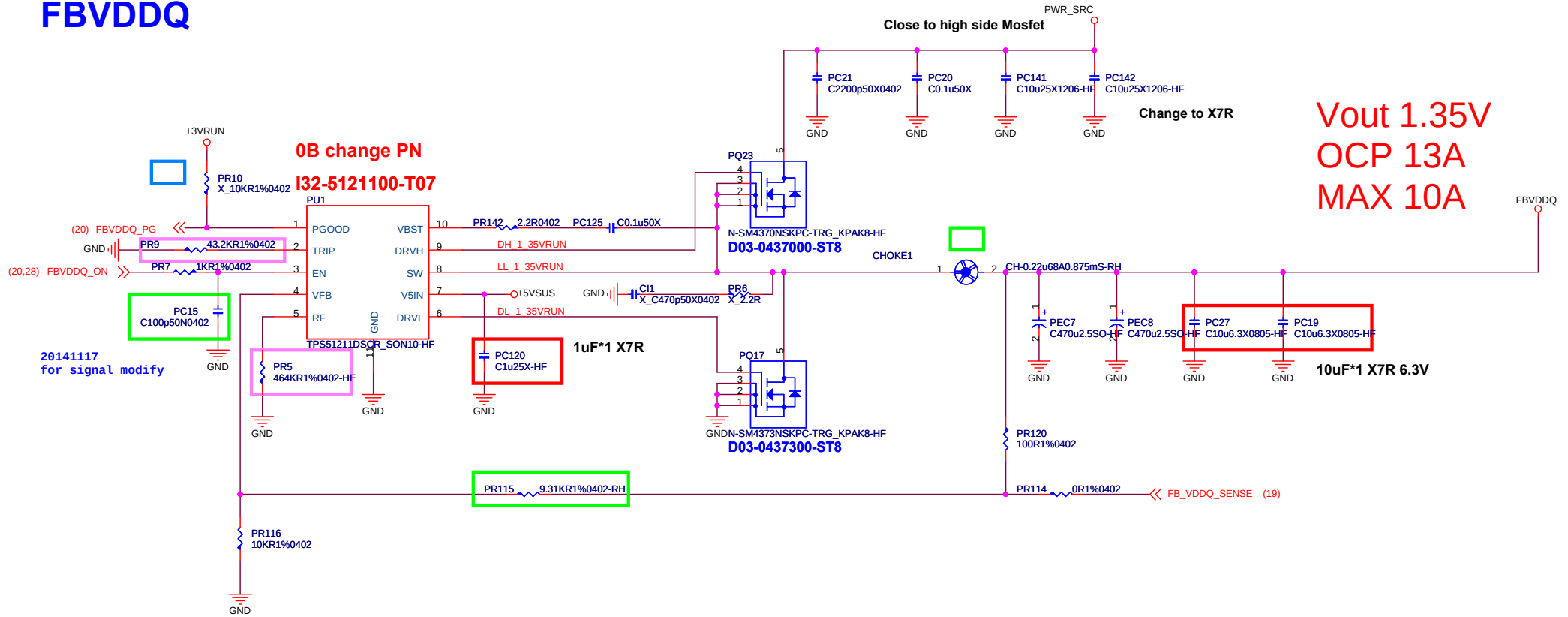


PEX_VDD



+1.2VSUS

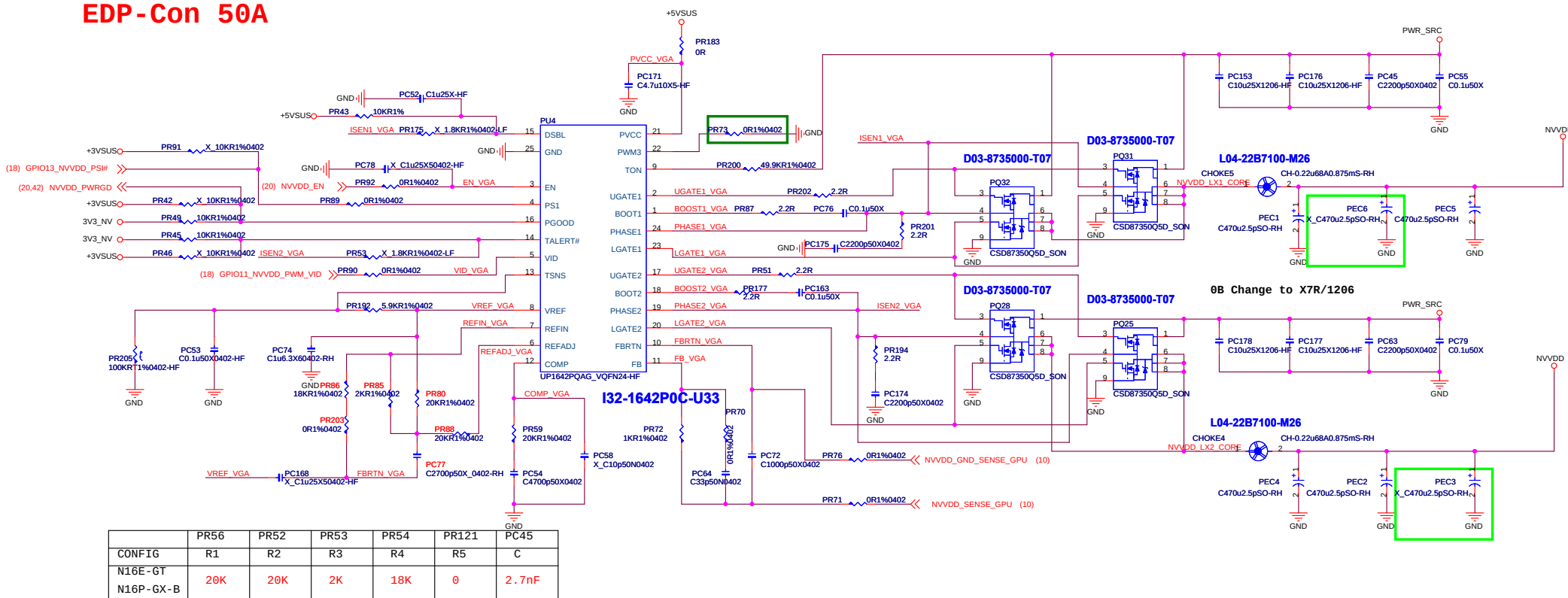


FBVDDQ

Vout 1.35V
OCP 13A
MAX 10A

EDP-Peak 87A
EDP-Con 50A

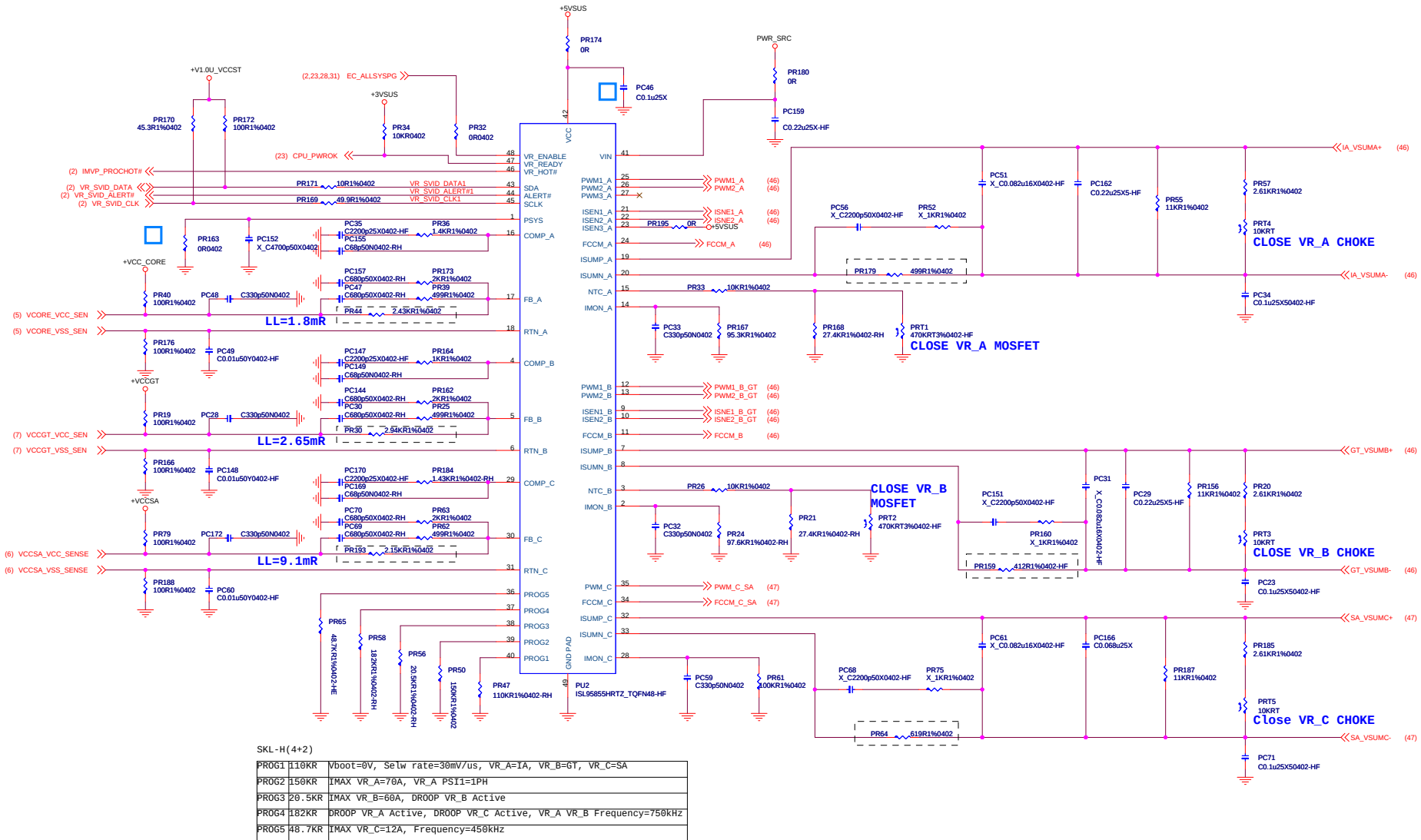
Vmin:0.6V / Vmax:1.2V

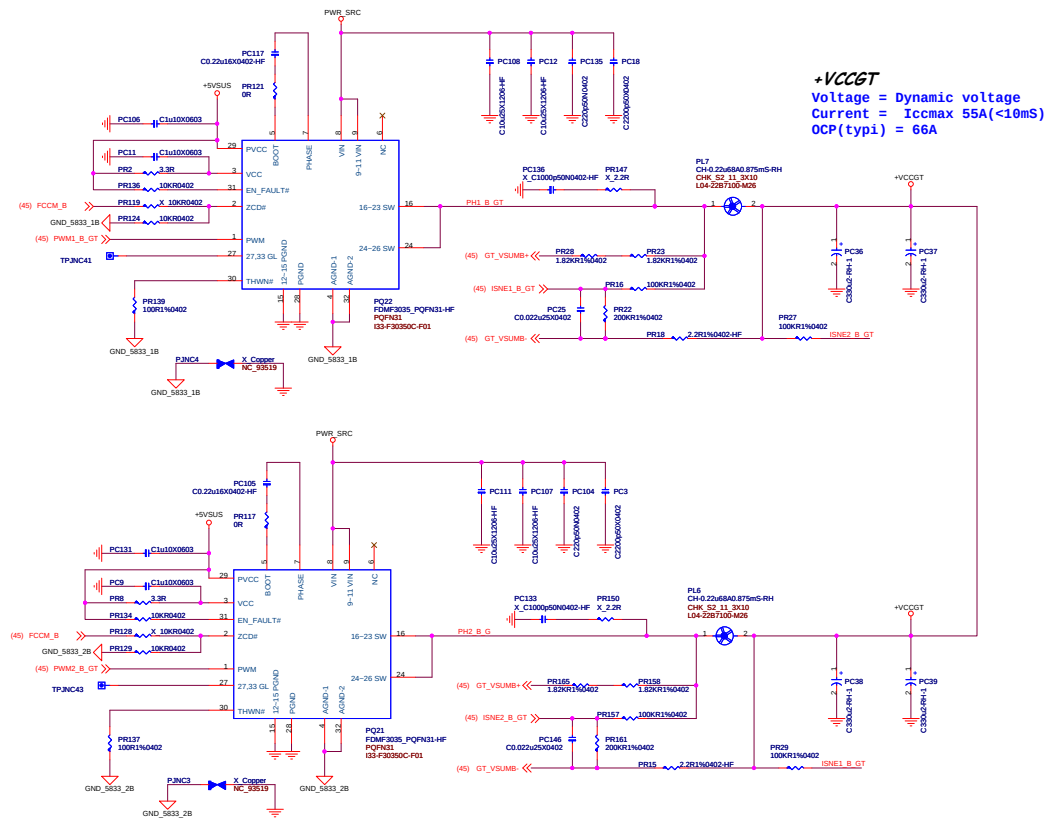
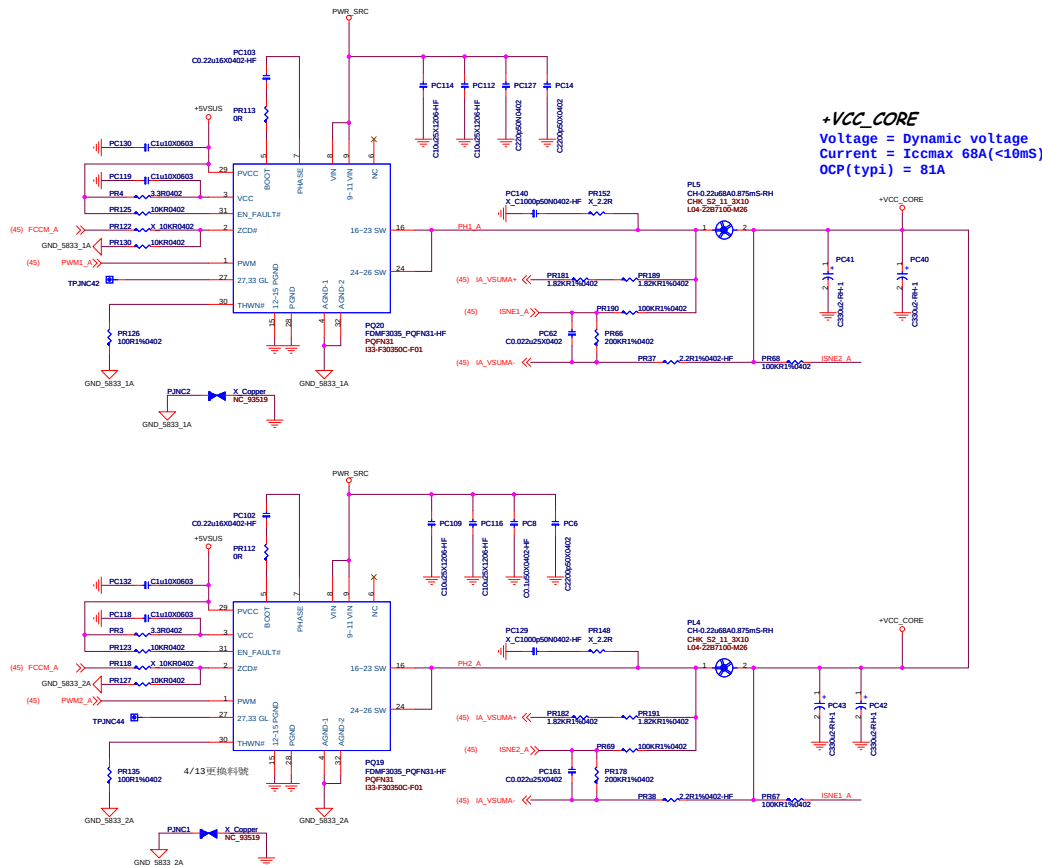


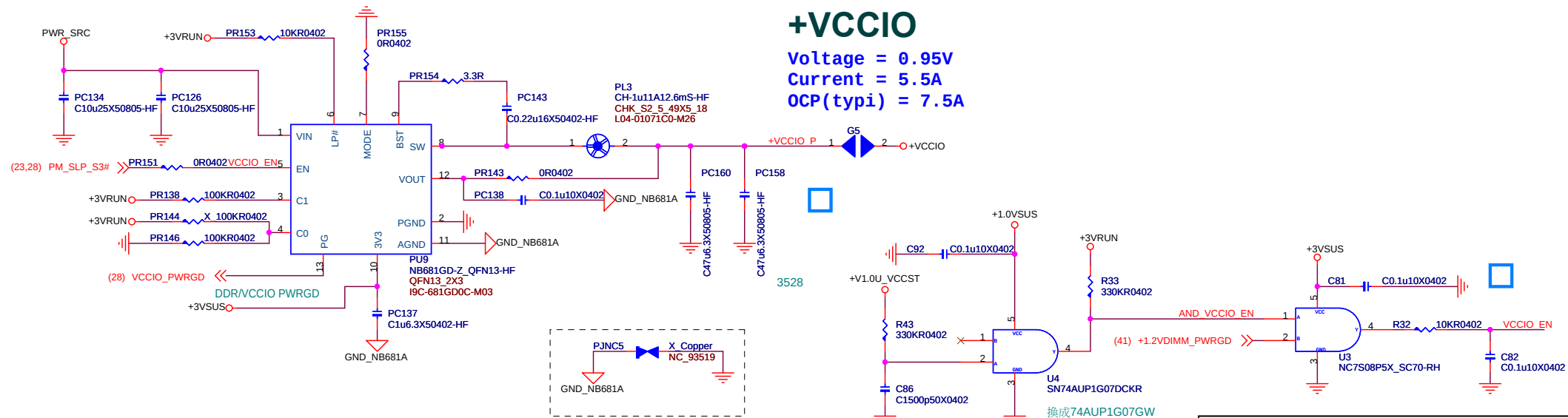
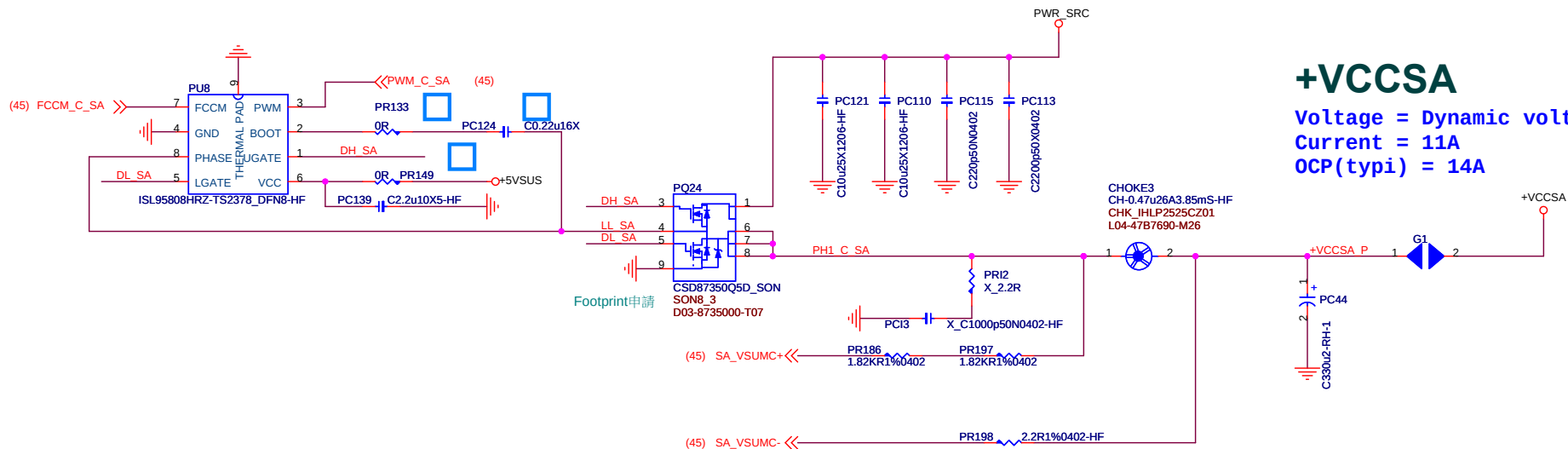
PC45: 1.5nF to 2.7nF

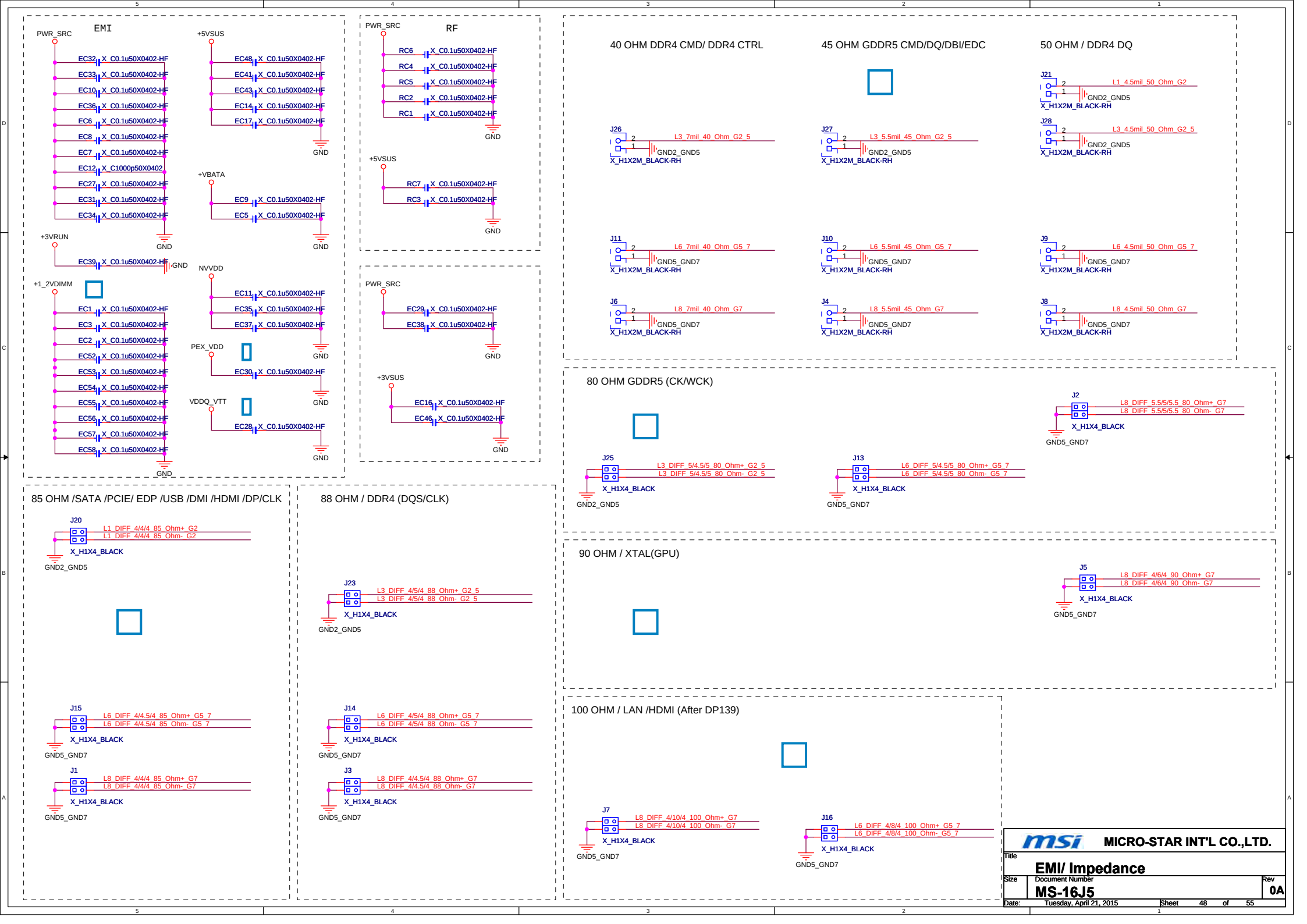
	PR56	PR52	PR53	PR54	PR121	PC45
CONFIG	R1	R2	R3	R4	R5	C
N16E-GT N16P-GX-B	20K	20K	2K	18K	0	2.7nF

Skylake H-line 42 45W ISL95855









CPU/GPU Holes

MCPU4
H_R276D169_PB

MCPU2
H_R276D169_PB

MCPU3
H_R276D169_PB

MCPU1
H_R276D169_PB

GND

X_CPU_HOLE

GND

X_GPU_HOLE

GND

X_CPU_HOLE

GND

X_GPU_HOLE

MGPU2
H_R276D169_PB

MGPU4
H_R276D169_PB

MGPU1
H_R276D169_PB

MGPU3
H_R276D169_PB

GND

X_CPU_HOLE

GND

X_GPU_HOLE

GND

X_CPU_HOLE

GND

X_GPU_HOLE

Diagram illustrating the locations of screw holes (MH7, MH8, MH9, MH5) and their corresponding ground connections. Each hole is marked with a red ground symbol and a note indicating a modification: "修改內徑3mm" (Modify inner diameter 3mm).

- MH7:** H_R19TD118_PT, X_ME_SCREW HOLE
- MH8:** H_R19TD118_PT, X_ME_SCREW HOLE
- MH9:** H_R19TD91, X_ME_SCREW HOLE
- MH5:** H_R19TD91, X_ME_SCREW HOLE

Figure 1: Schematic diagrams of the test setup for the M41 and M42 modules. The M41 diagram shows a module with a single screw hole (X_ME_SCREW_HOLE) connected to ground. The M42 diagram shows a module with eight screw holes (X_8x8) connected to ground. The M42 diagram also shows a red square indicating the location of the ground connection.

The diagram illustrates the electrical connections for the E23-1011040-CA7 board. It features three HS-Ms1011-RH components (PAD7, PAD2, PAD8) connected to a central ground point. Each component is labeled with 'X_HS-MS1011-RH' and 'ATE_C006_106'. The components are connected to a central ground point via a network of resistors and capacitors. The board is labeled 'E23-1011040-CA7' in the center.

Diagram illustrating the connection of two pads, PAD4 and PAD6, to ground. Both pads are connected to a common ground symbol. The pads are labeled with the part number X_H5-MS1011-RH and the test point ATE_C006_106. A red square is shown to the right of the pads.