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HSF Property: ROHS or Halogen-Free(5L3?)

CALVIN DIS

SI BUILD

FINAL

2011.08.02

21.OCT.2002		
DATE	CHANGE NO.	REV

	EE	DATE	POWER	DATE	INVENTEC			
DRAWER								
DESIGN								
CHECK								
RESPONSIBLE								
SIZE				VER				
FILE NAME					SIZE	CODE	DOC NUMBER	REV
PIN				XXXX	2	CS	13110XXXX-0-0	201
					SHEET			
					1 of 01			

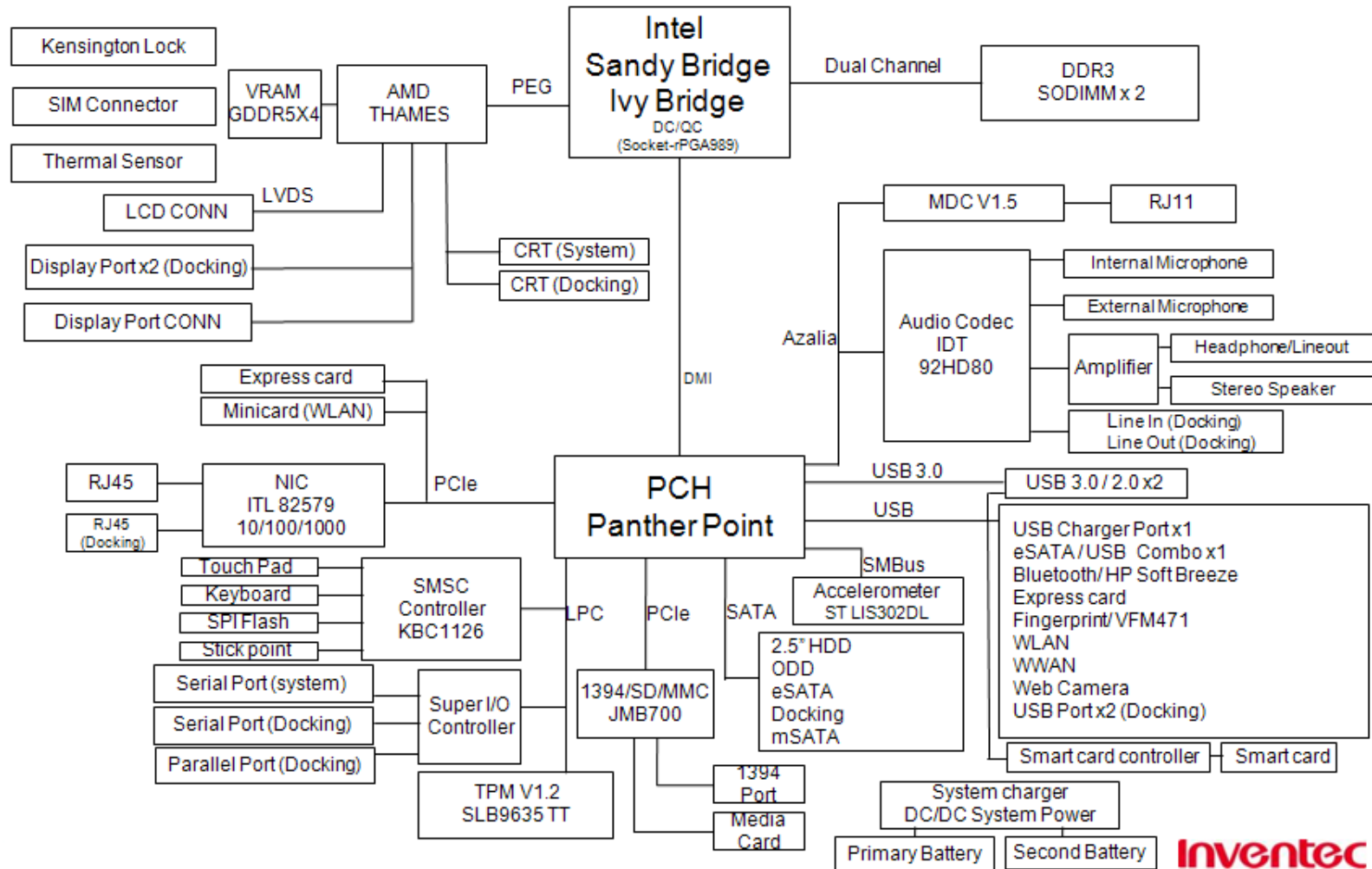
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TITLE			
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SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
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CALVIN Discrete Block Diagram



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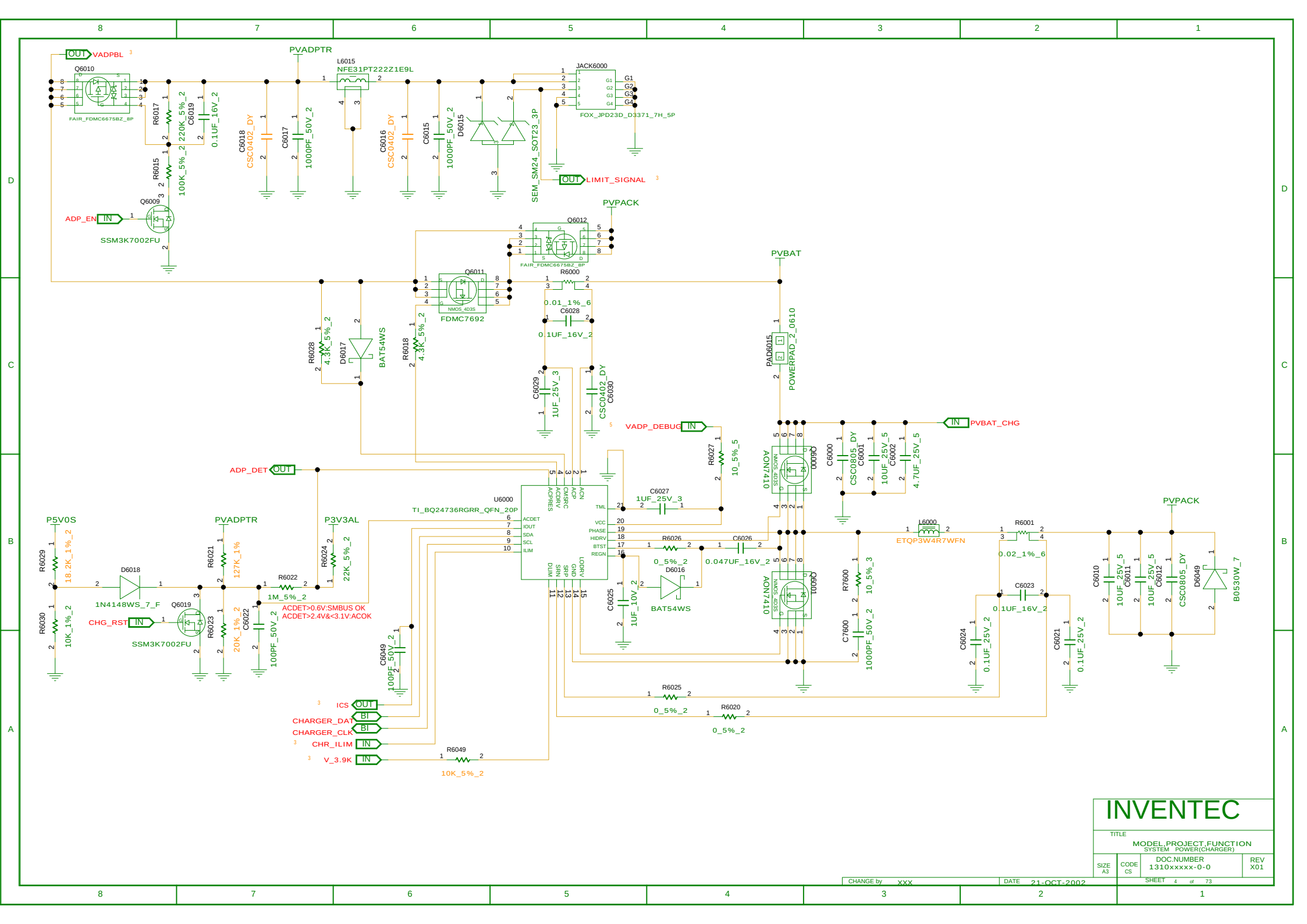
TITLE MODEL, PROJECT, FUNCTION

BLOCK DIAGRAM

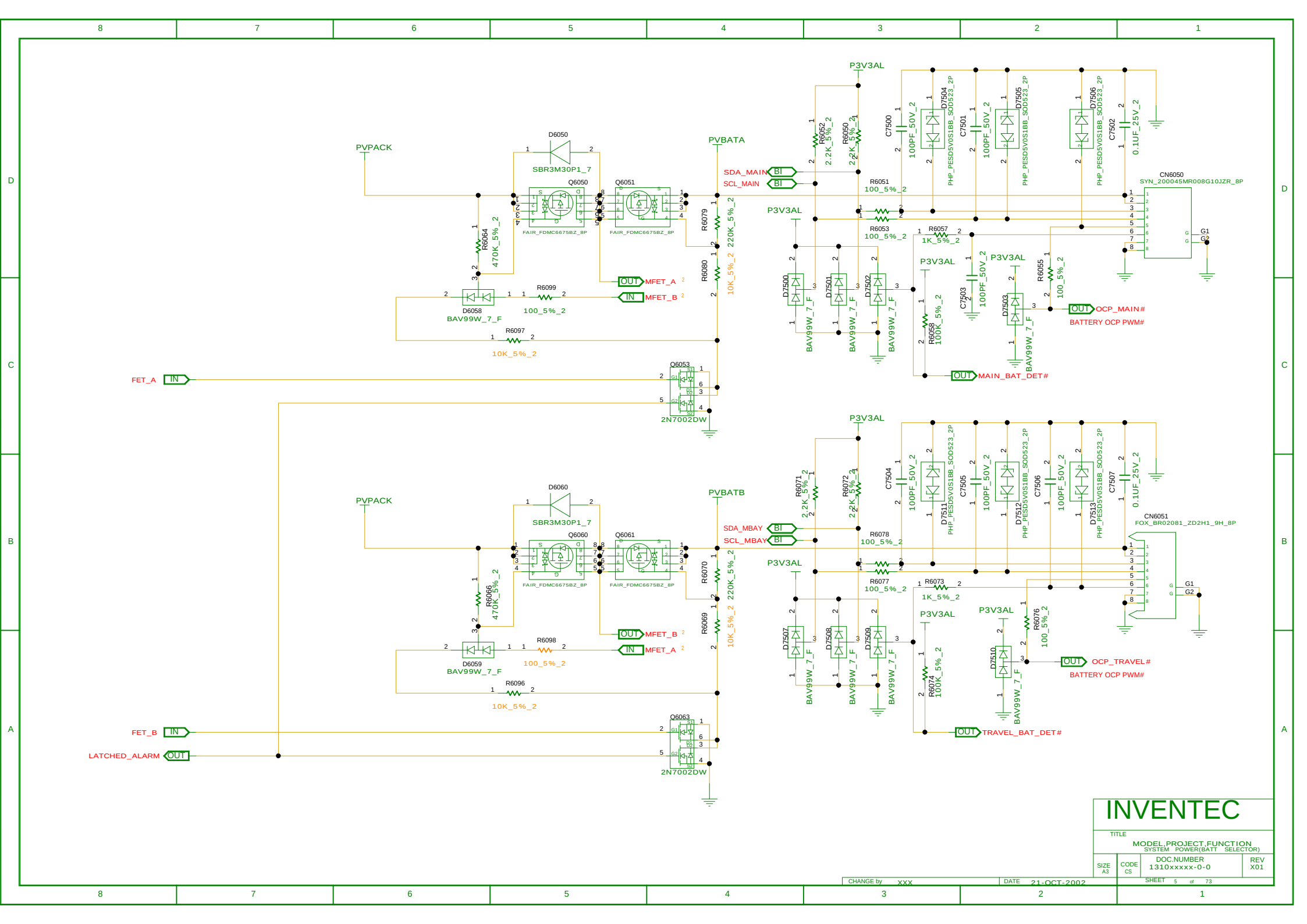
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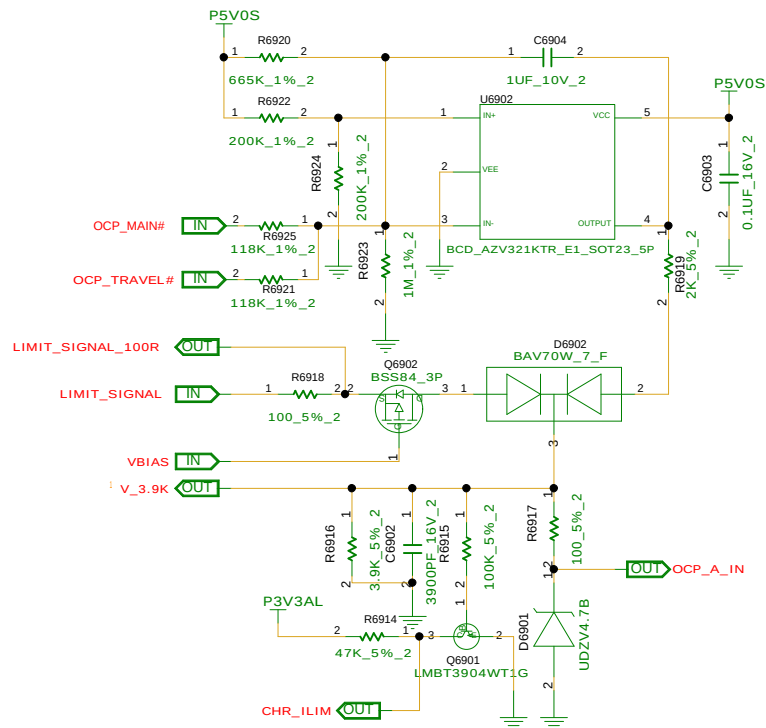
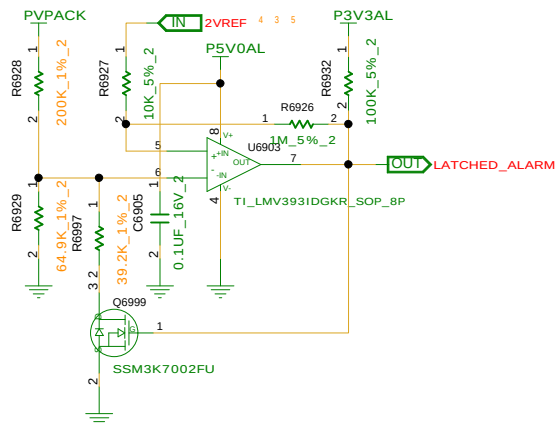
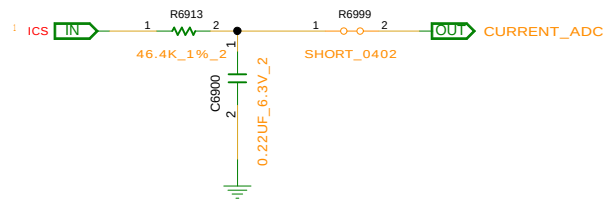
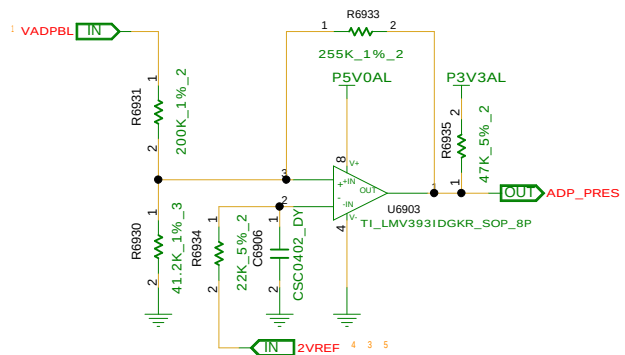
CHANGE BY XXX DATE 21-OCT-2002



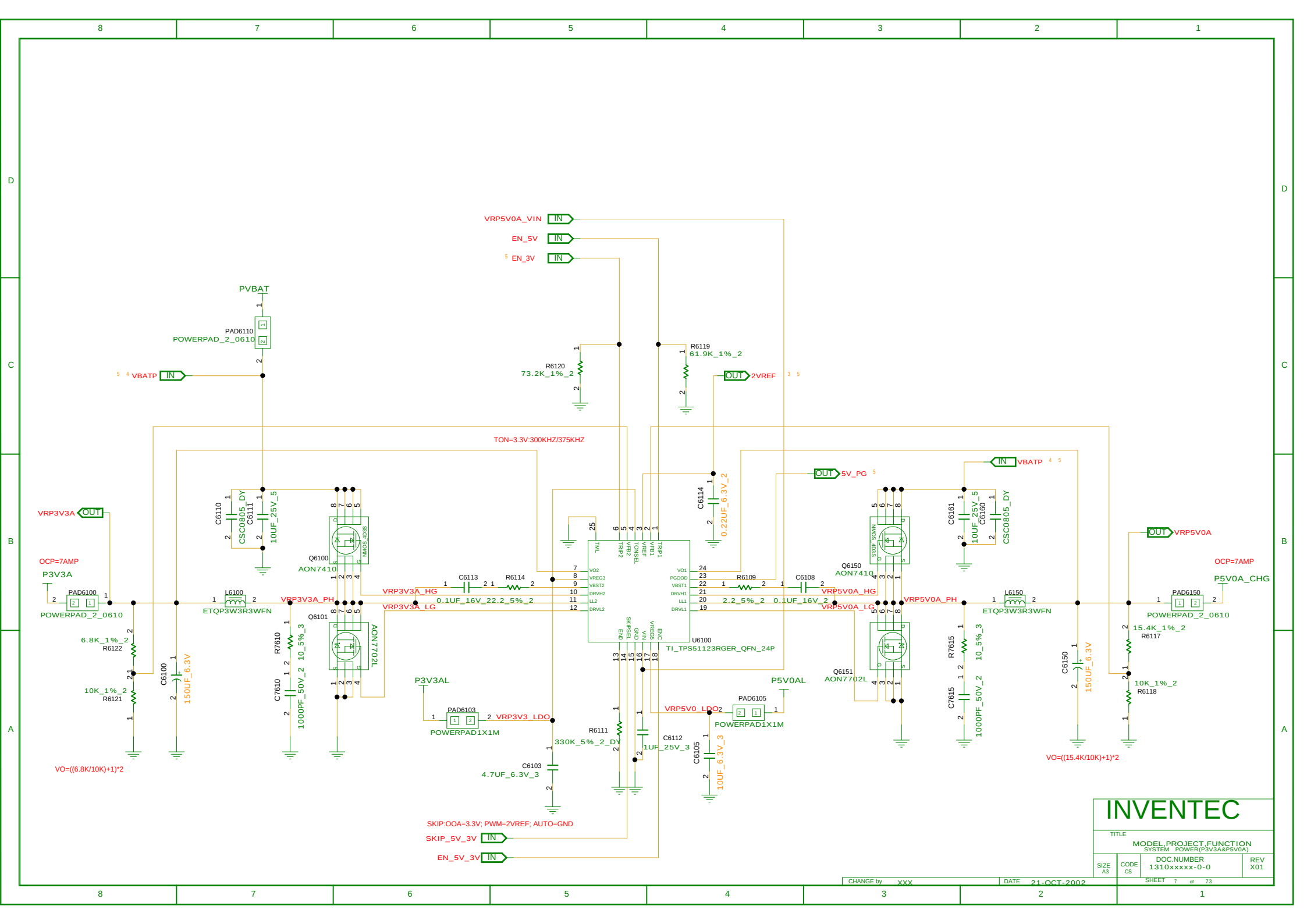
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MODEL,PROJECT,FUNCTION			
SYSTEM POWER(CHARGER)			
SIZE A3	CODE CS	DOC.NUMBER	REV X01
		1310xxxxx-0-0	
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1			



INVENTEC			
TITLE			
MODEL PROJECT FUNCTION SYSTEM POWER(BATT_SELECTOR)			
DOC NUMBER 1310xxxxx-0-0		REV X01	
SIZE A3	CODE CS	SHEET 5 of 73	

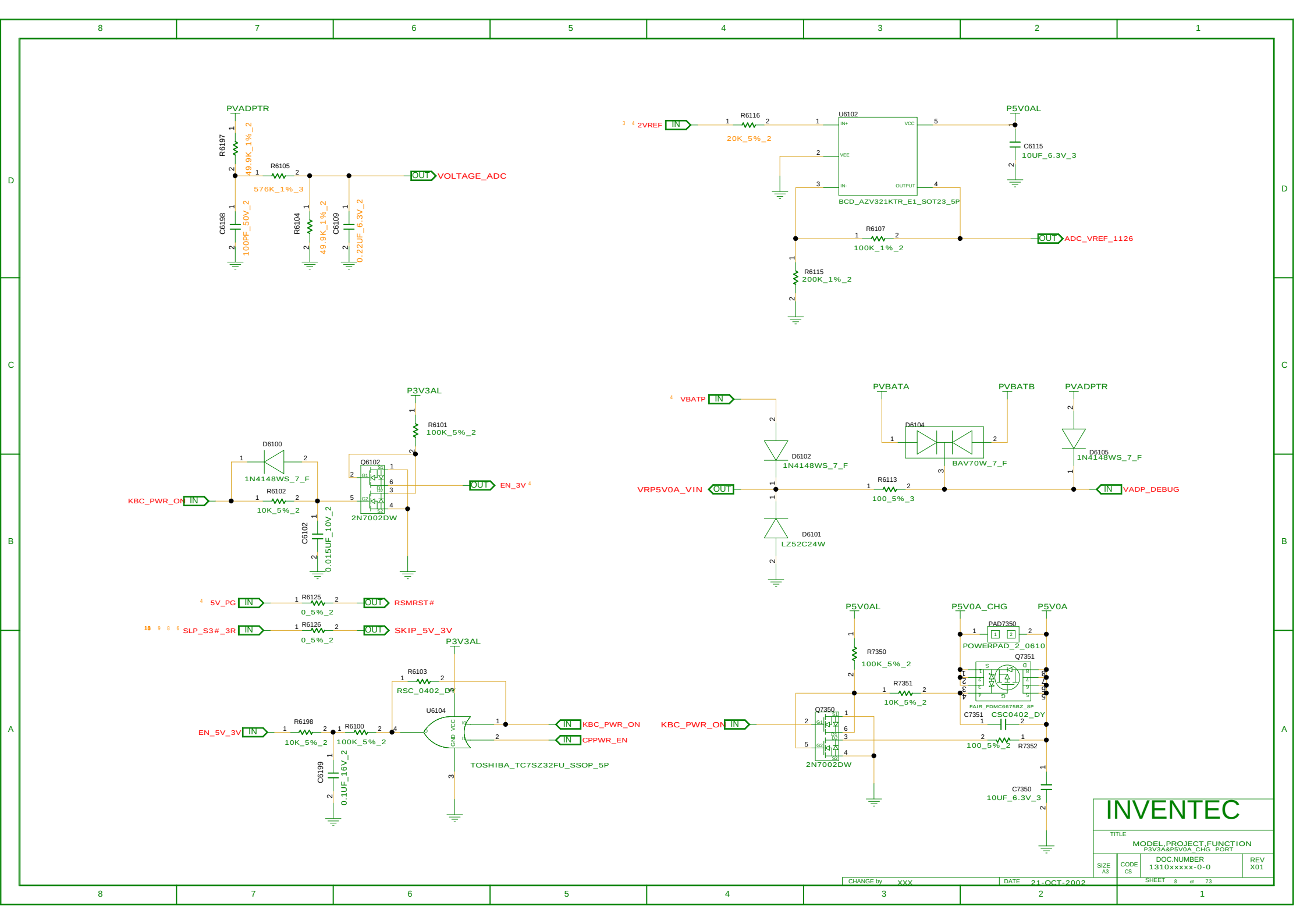


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TITLE			
MODEL PROJECT FUNCTION			
SYSTEM POWER(OCP)			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
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TITLE			
MODEL PROJECT FUNCTION			
SYSTEM POWER(P3V3A&P5V0A)			
DOC NUMBER			
1310xxxxx-0-0			
REV			
X01			
SIZE	CODE	SHEET	
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INVENTEC

TITLE MODEL PROJECT FUNCTION

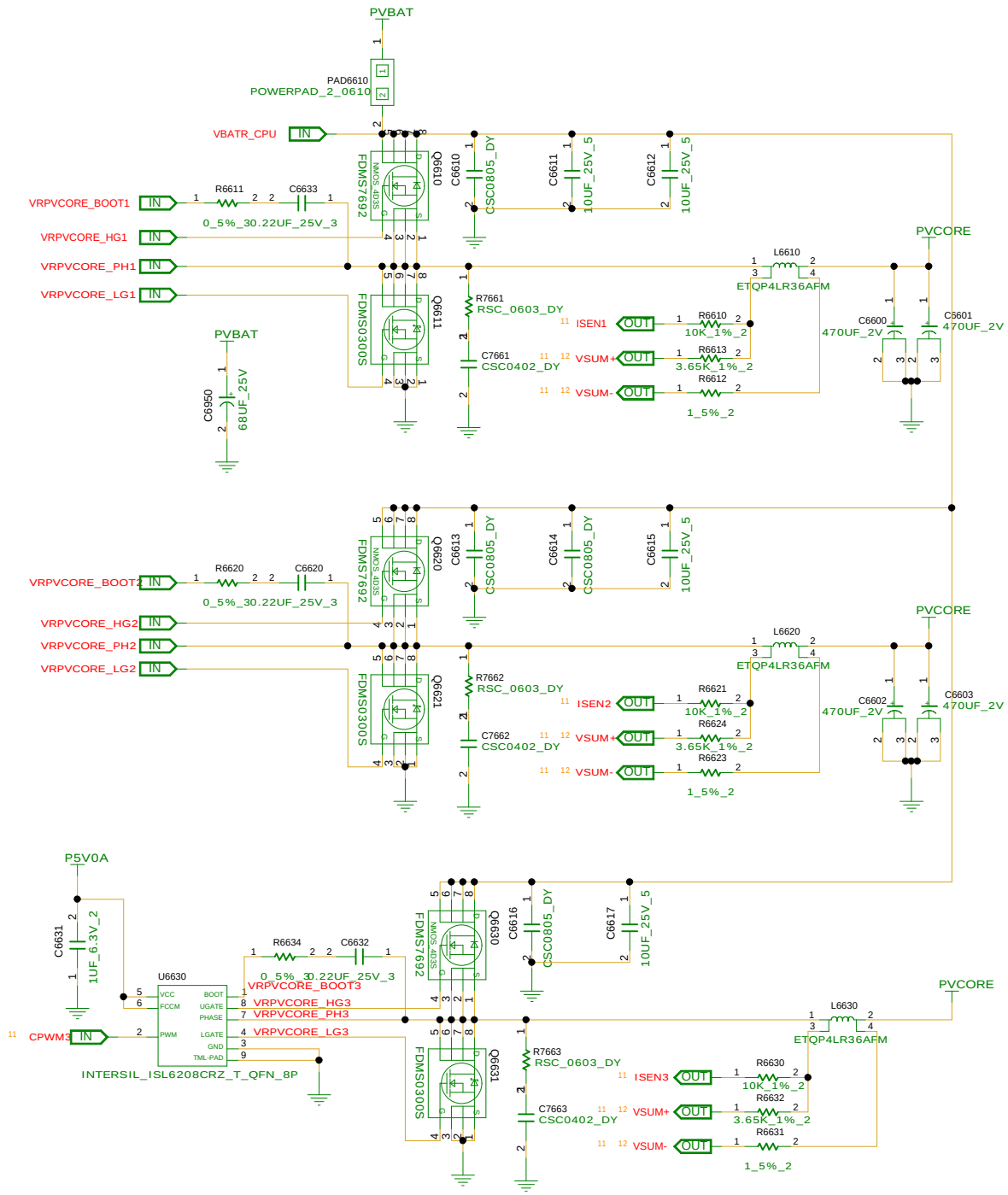
P3V3A&P5V0A_CHG_PORT

DOC NUMBER 1310xxxxx-0-0

REV X01

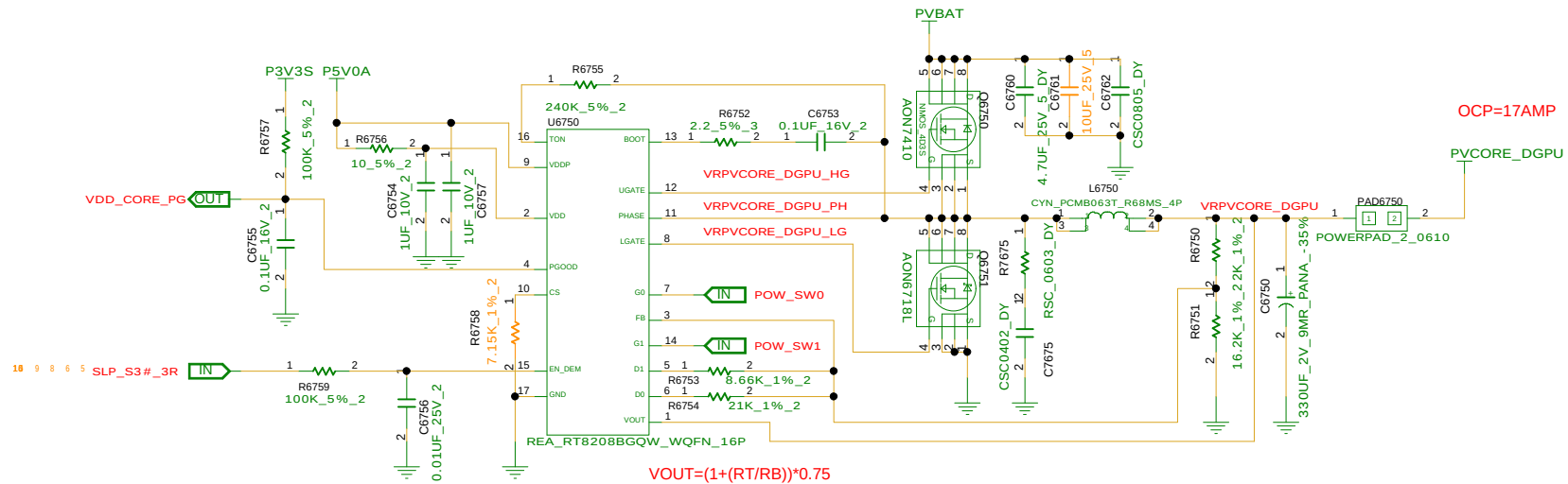
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CHANGE by XXX DATE 21-OCT-2002



PVCORE OCP=112A

INVENTEC			
TITLE MODEL,PROJECT,FUNCTION SYSTEM POWER(PVCORE)			
SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01
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VOUT	SW1	SW0
1.10	0	0
1.00	0	1
0.90	1	0
0.85	1	1

INVENTEC

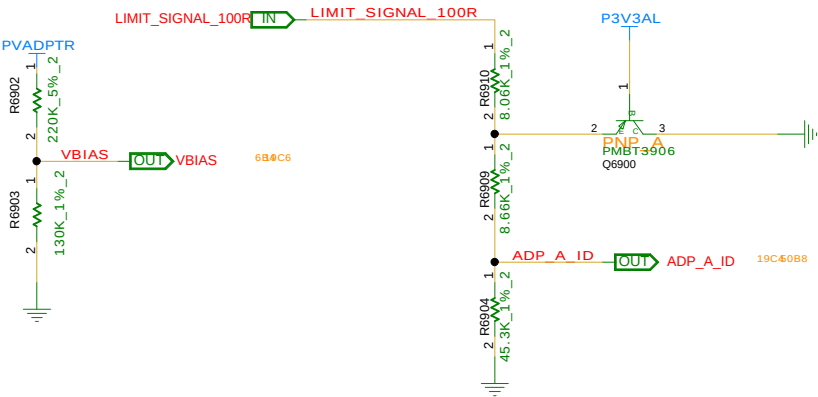
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SYSTEM POWER(PVCORE_DGPU)

DOC NUMBER 1310xxxxx-0-0 REV X01

CHANGE BY XXX DATE 21-OCT-2002

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REFERENCE NUMBER : 6900~6999



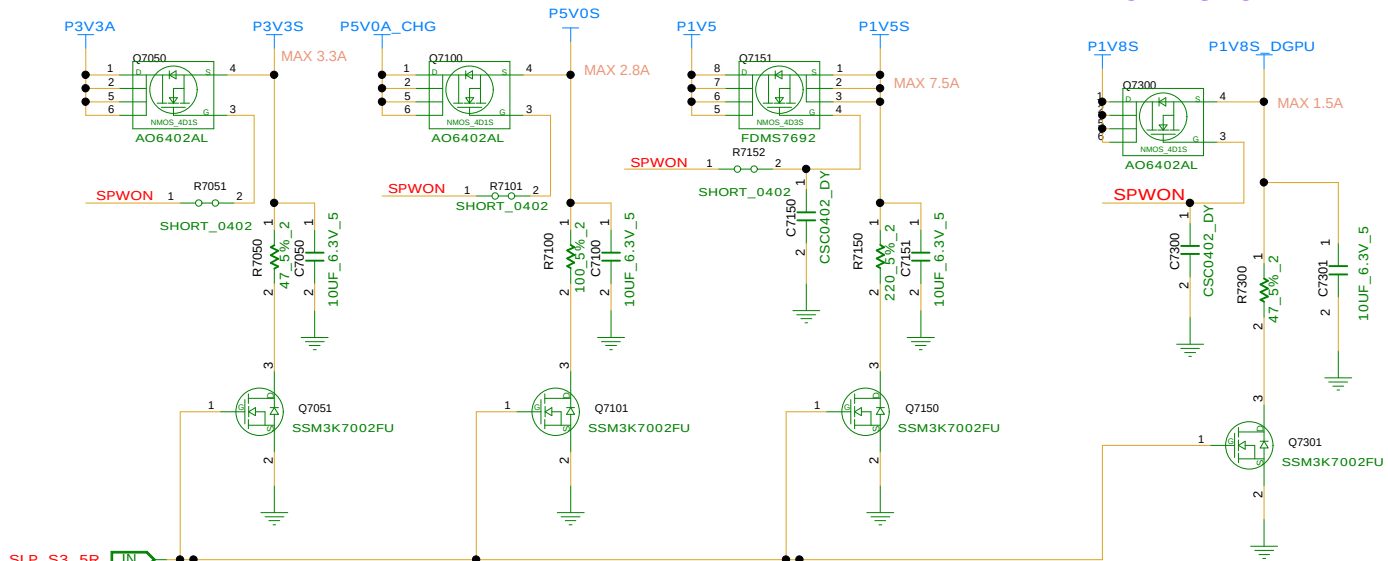
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TITLE			
MODEL,PROJECT,FUNCTION			
HP_OCP			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

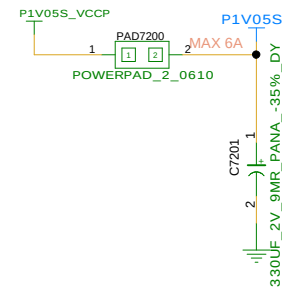
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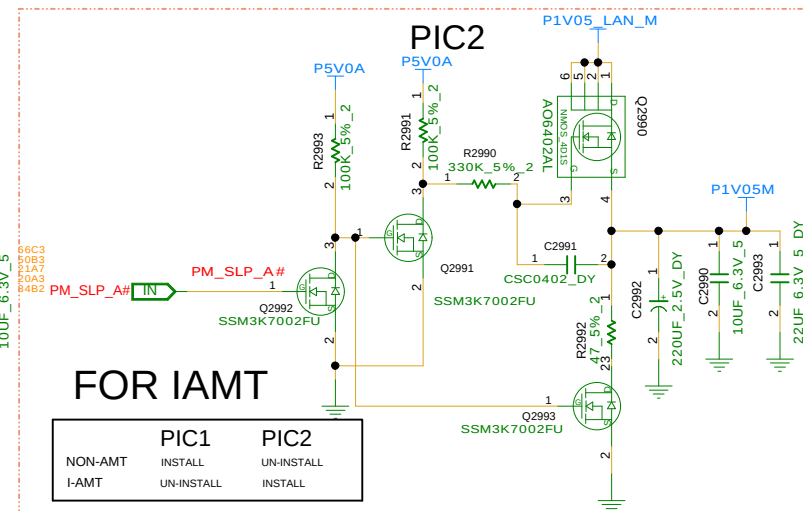
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FOR DGPU



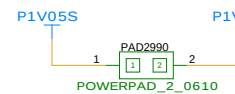
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FOR IAMT

	PIC1	PIC2
NON-AMT	INSTALL	UN-INSTALL
I-AMT	UN-INSTALL	INSTALL

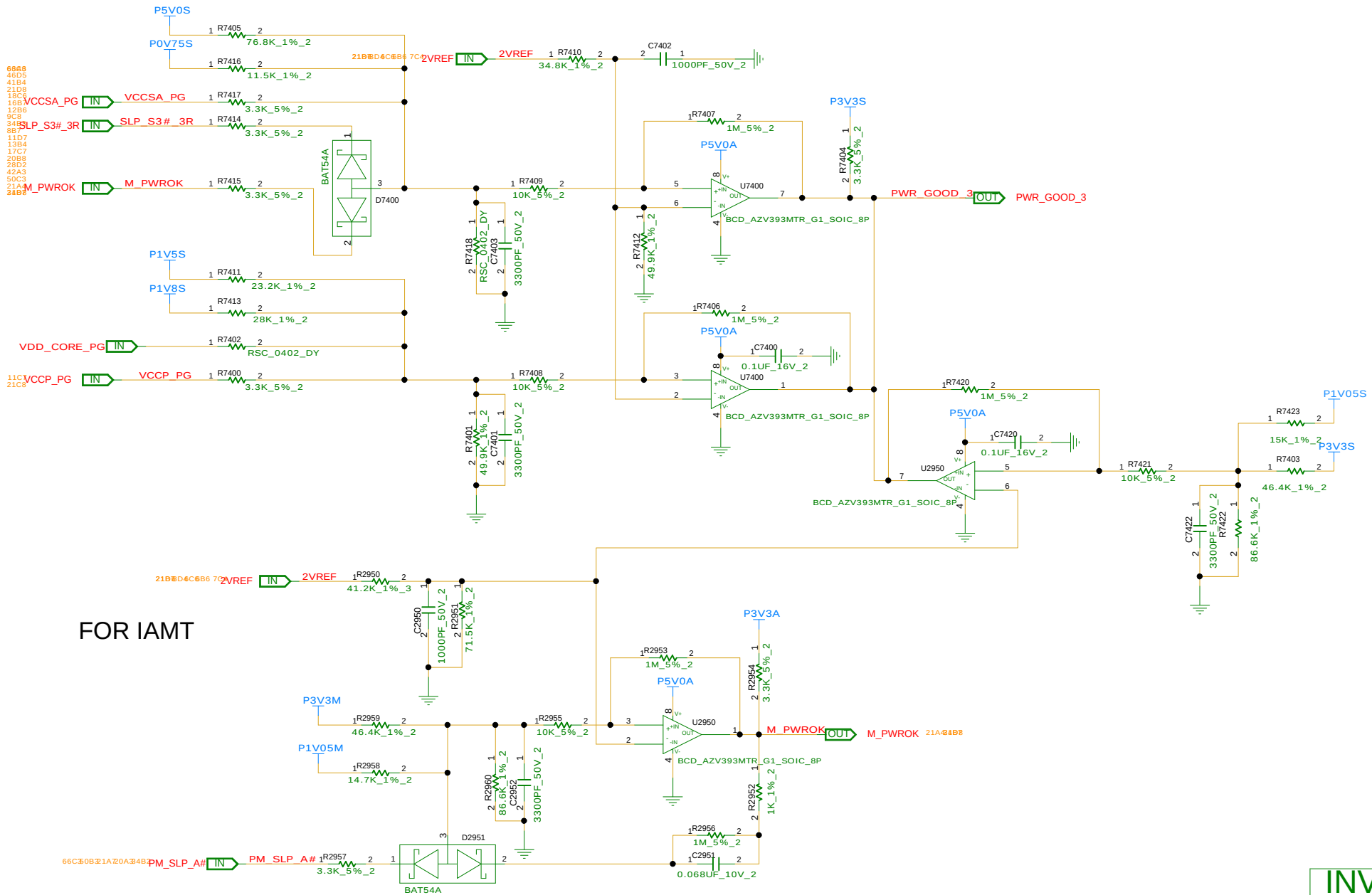
PIC1



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION POWER(SLEEP)			
SIZE A3	CODE CS	DOC. NUMBER 1310xxxxx-0-0	REV X01

REFERENCE NUMBER : 7400~7450



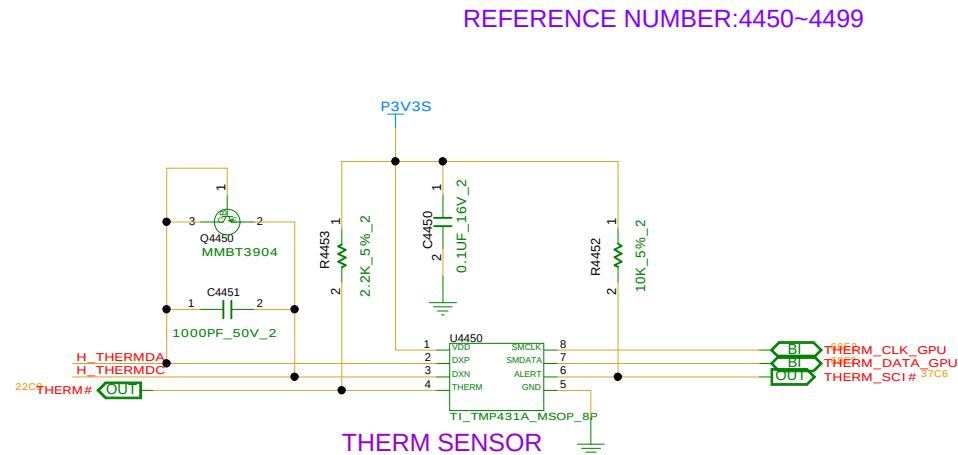
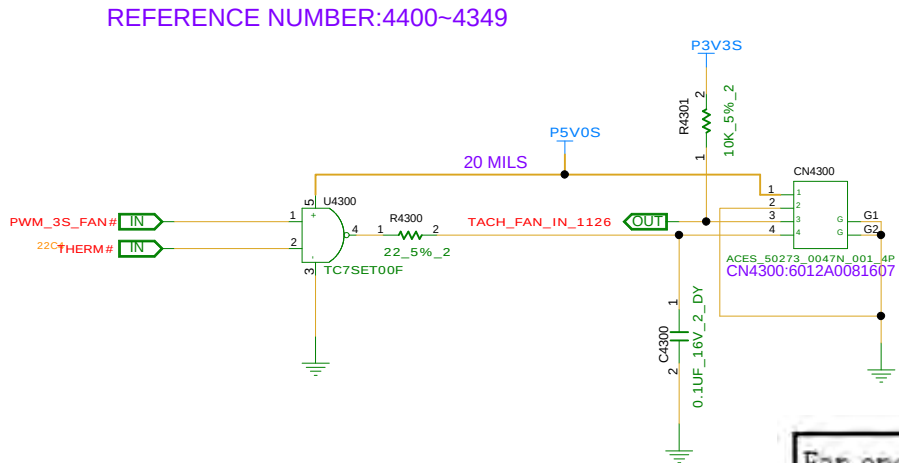
FOR IAMT

REFERENCE NUMBER : 2950~2999

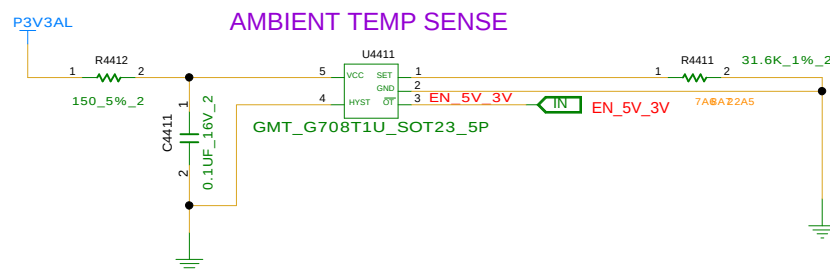
INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION POWER(SEQUENCE)			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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CHANGE by	xxx	DATE	21-OCT-2002
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Fan speeds (RPM) :	2500	3300	3800	4300	5000	0	0	0
CPU temperature trip points :	45	57	68	80	91	0	0	0



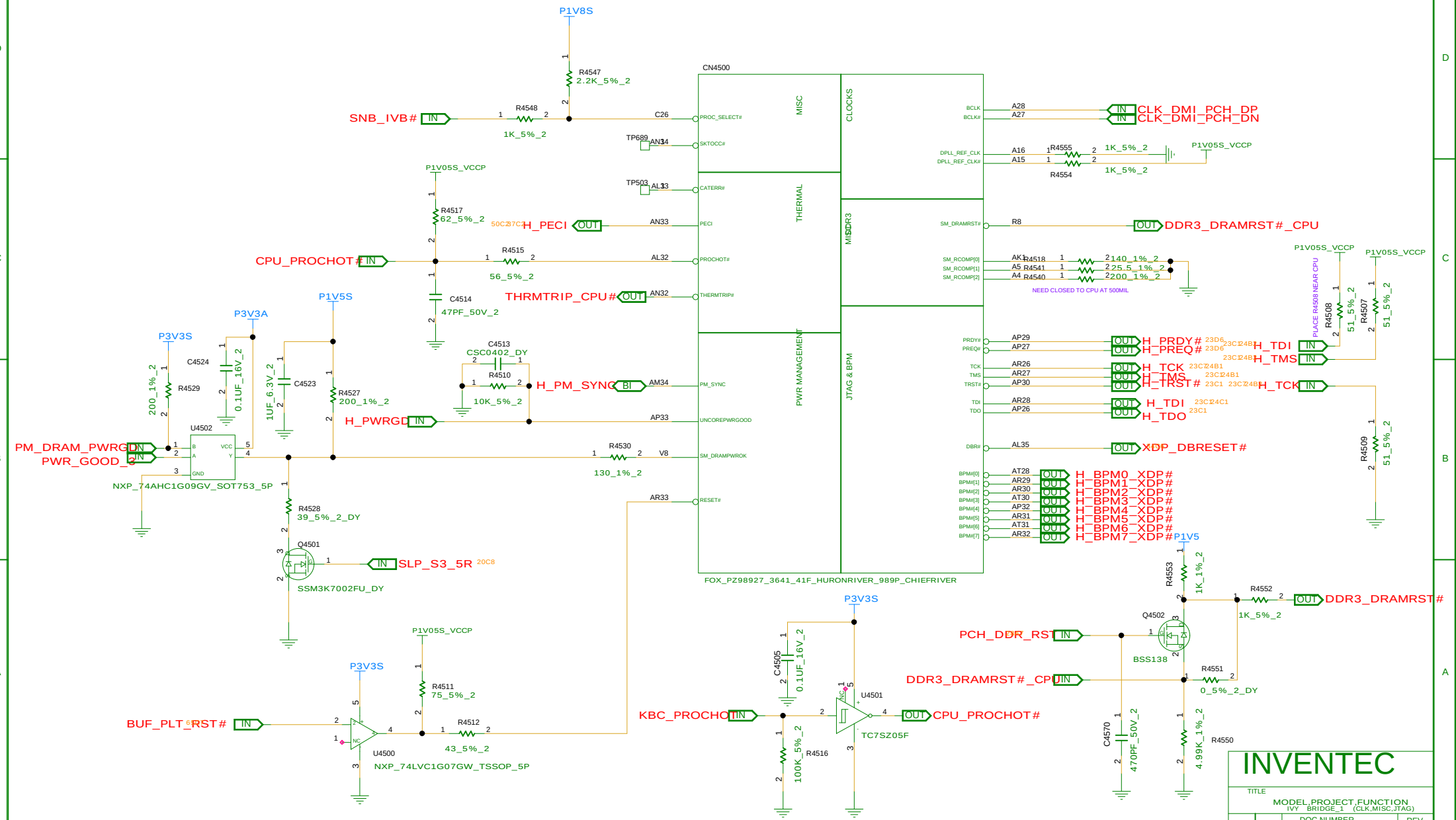
RSET (K OHM) = 0.0012T^2 - 0.9308T + 96.147
THERMAL SHUTDOWN AT 77 DEG. FOR B SERIES

REFERENCE NUMBER:4411~4449

INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
FAN & THERMAL			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
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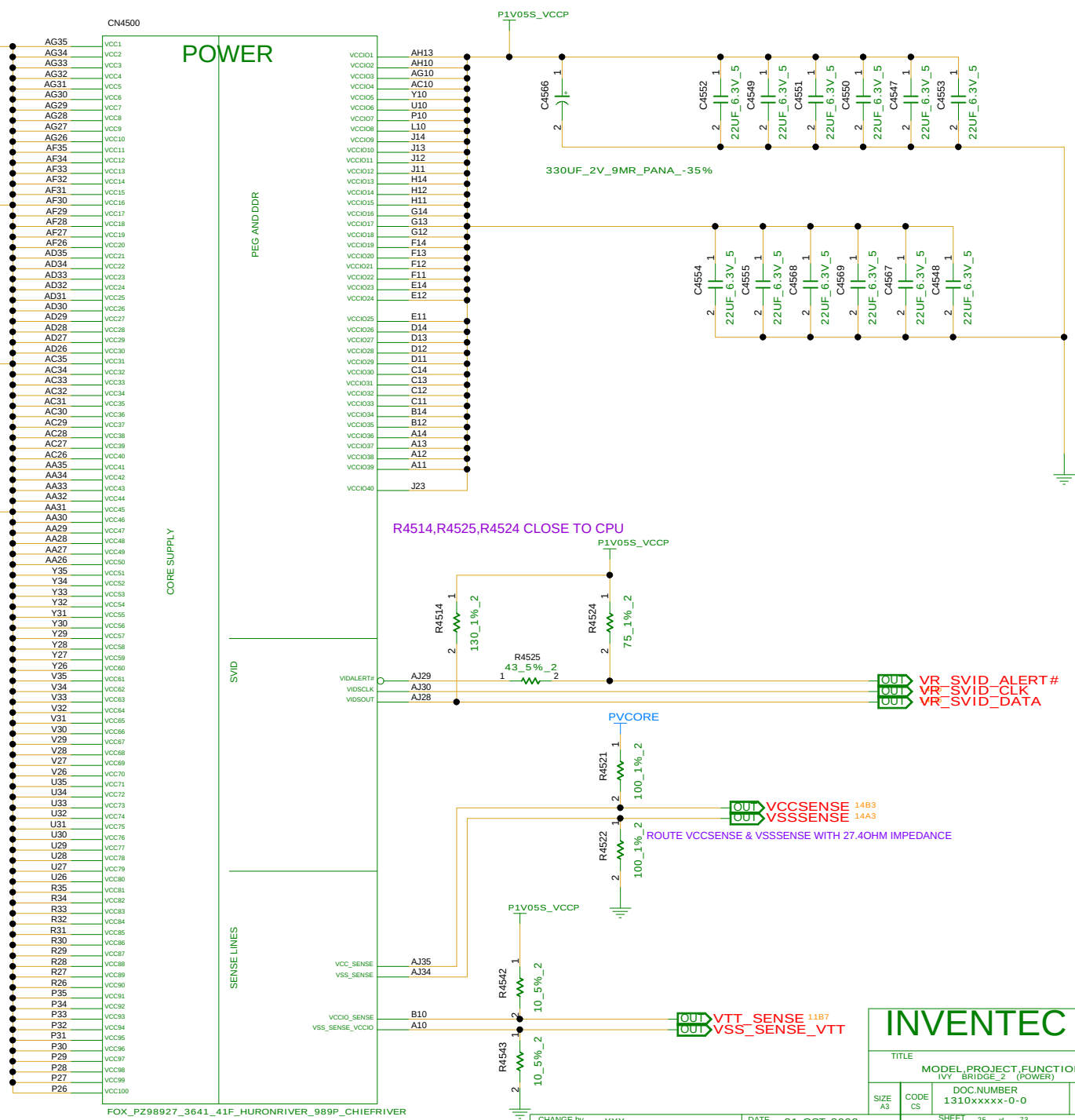
CHANGE by: XXX DATE: 21-OCT-2002

REFERENCE NUMBER:4500~4699



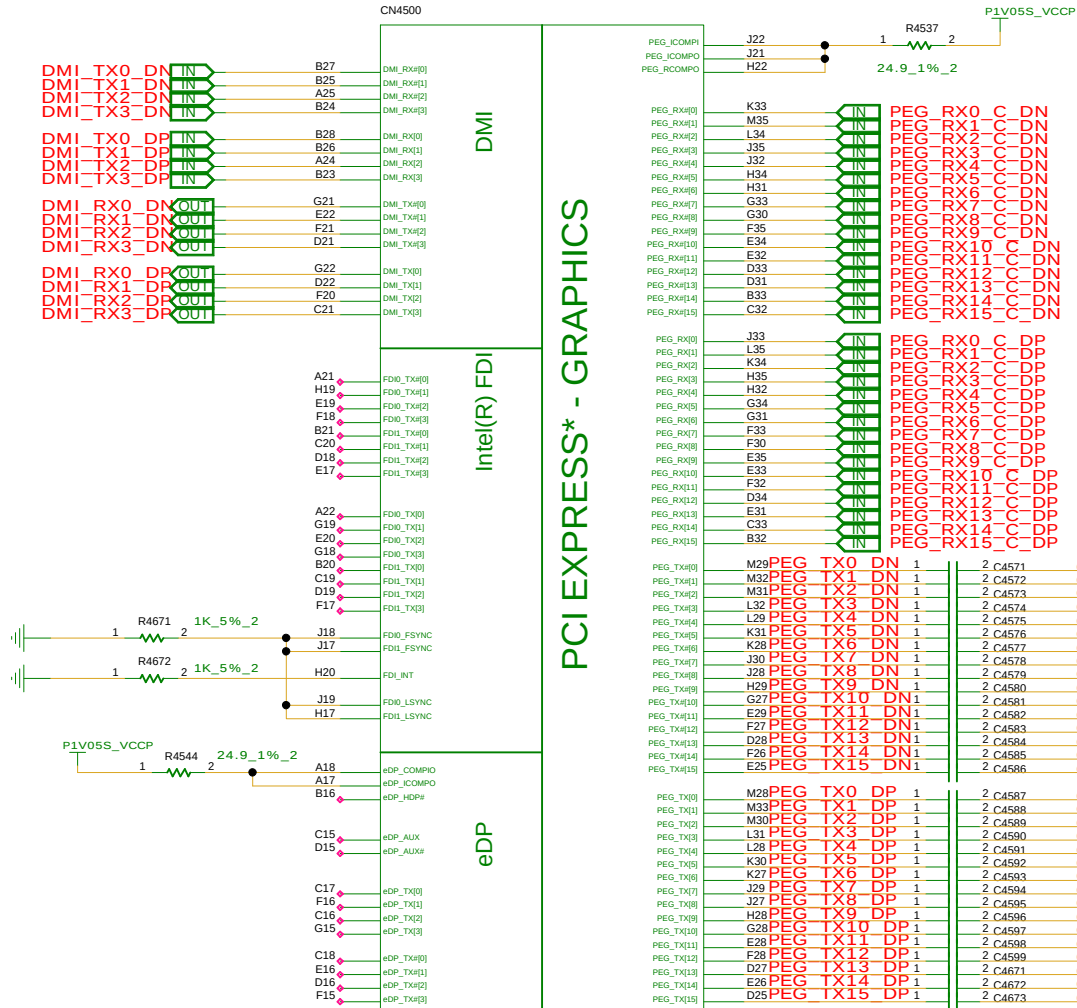
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TITLE			
MODEL PROJECT FUNCTION			
IVY BRIDGE-1 (CLK,MISC,JTAG)			
DOC NUMBER			
1310xxxx-0-0			
REV			
X01			
SIZE	CODE	SHEET 24 of 73	
A3	CS		

REFERENCE NUMBER:4500~4699



REFERENCE NUMBER:4500~4699

PCI EXPRESS* - GRAPHICS



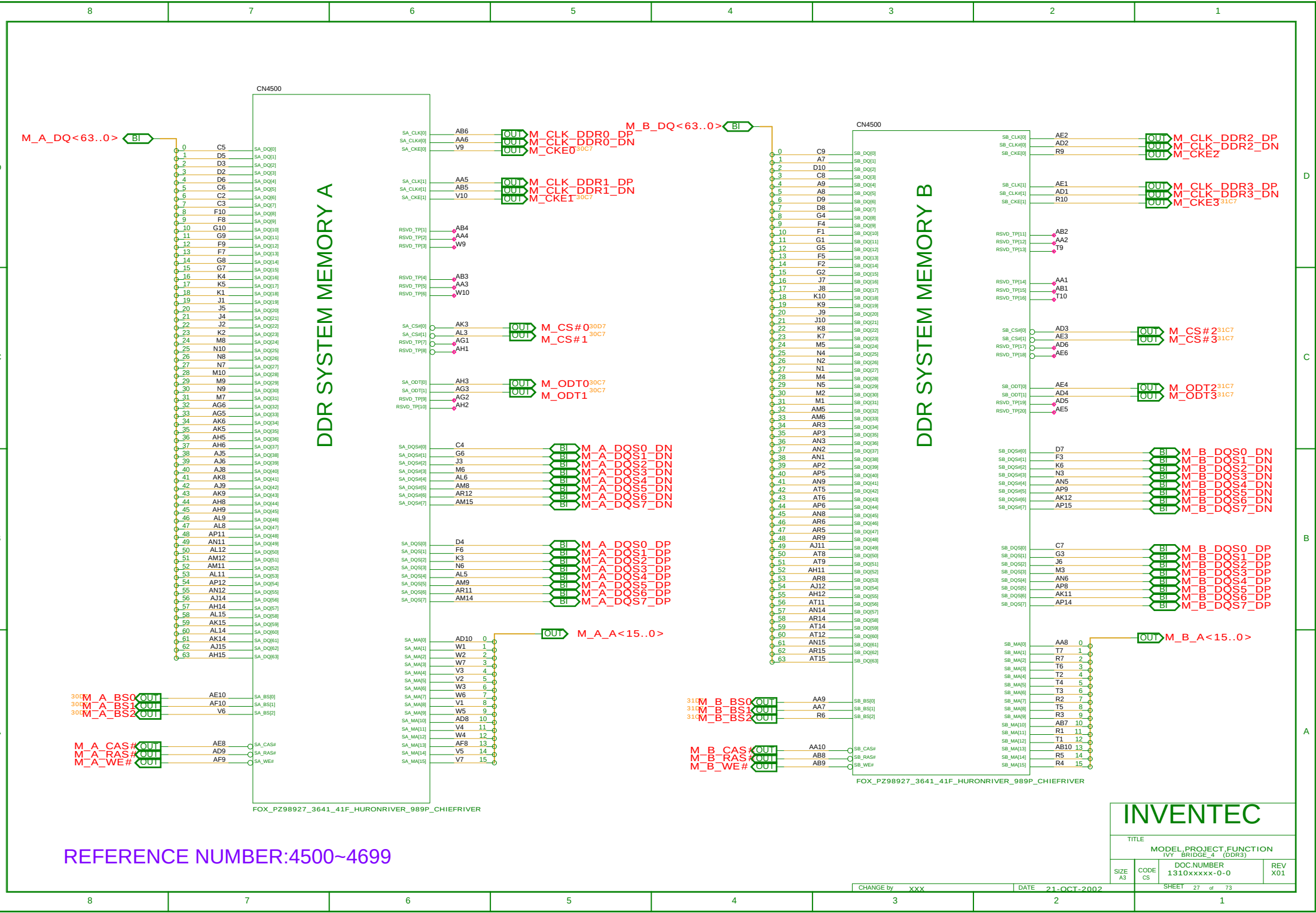
ALL CLOSED TO CPU

INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
IVY BRIDGE_3 (DMI,DP,PEG,FDI)			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by: XXX DATE: 21-OCT-2002

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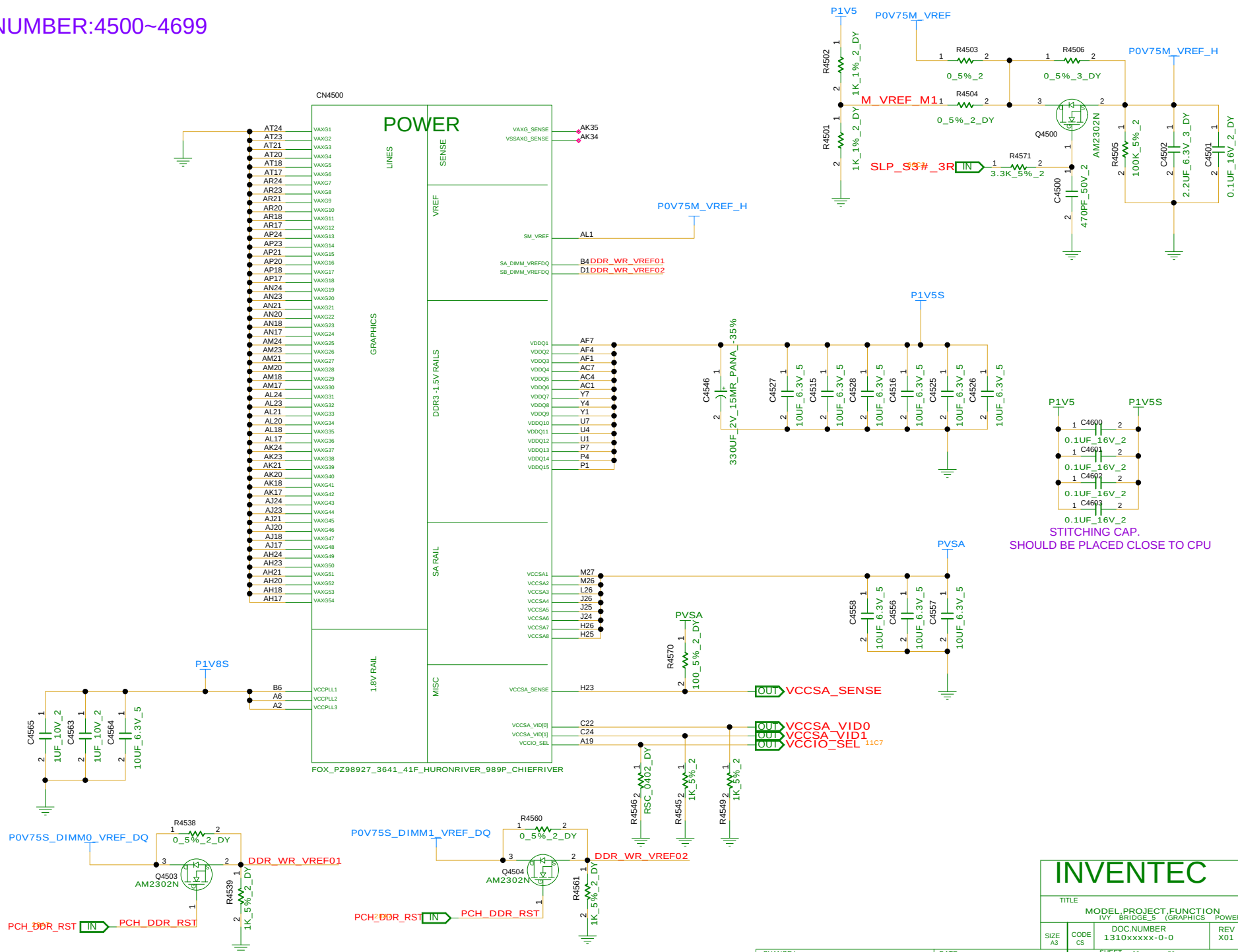


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INVENTEC

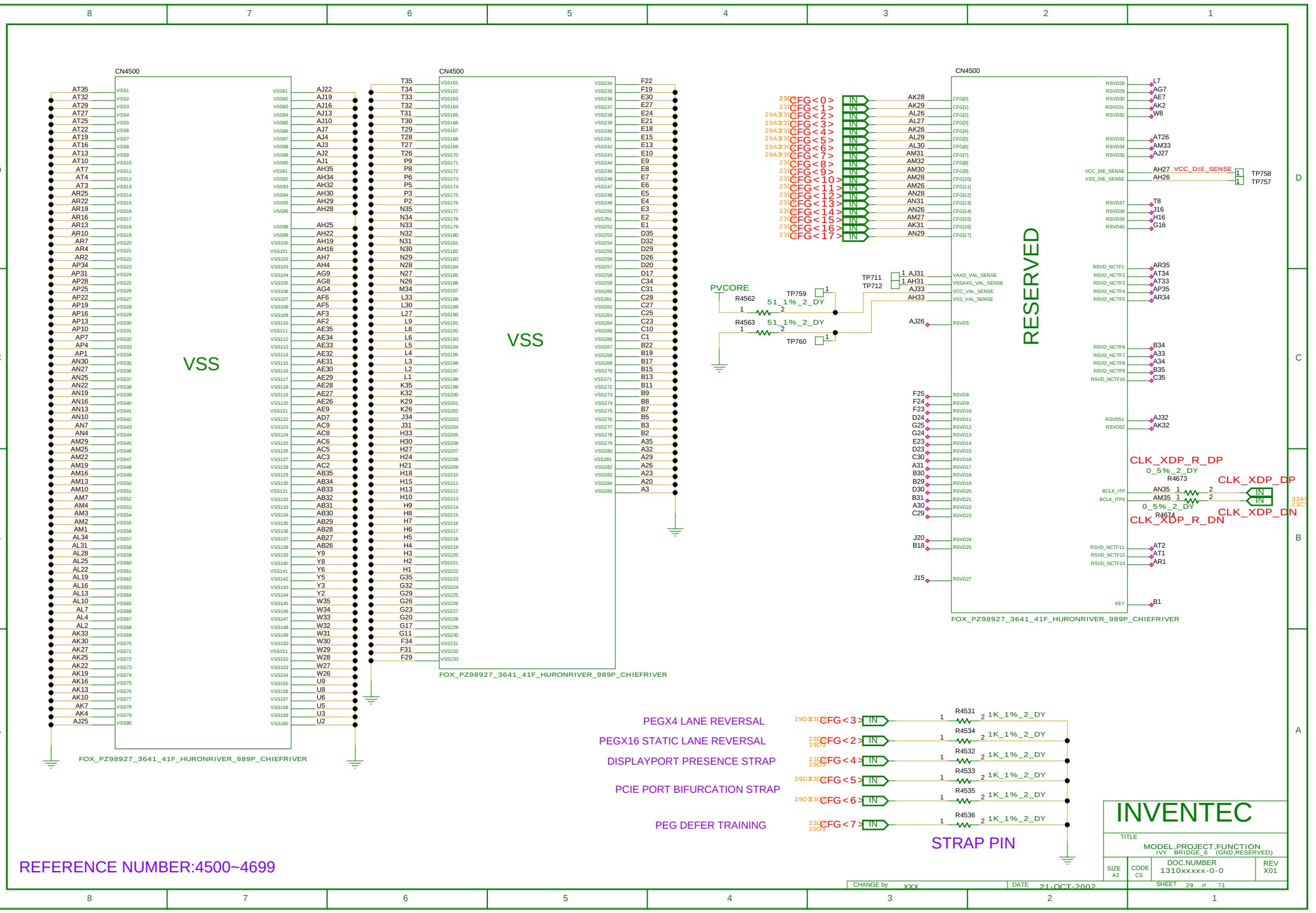
TITLE			
MODEL PROJECT FUNCTION IVY BRIDGE-4 (DDR3)			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	
		REV X01	

REFERENCE NUMBER:4500~4699



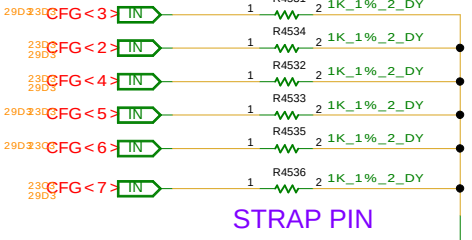
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TITLE			
MODEL,PROJECT,FUNCTION IVY BRIDGE_5 (GRAPHICS POWER)			
SIZE A2	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01

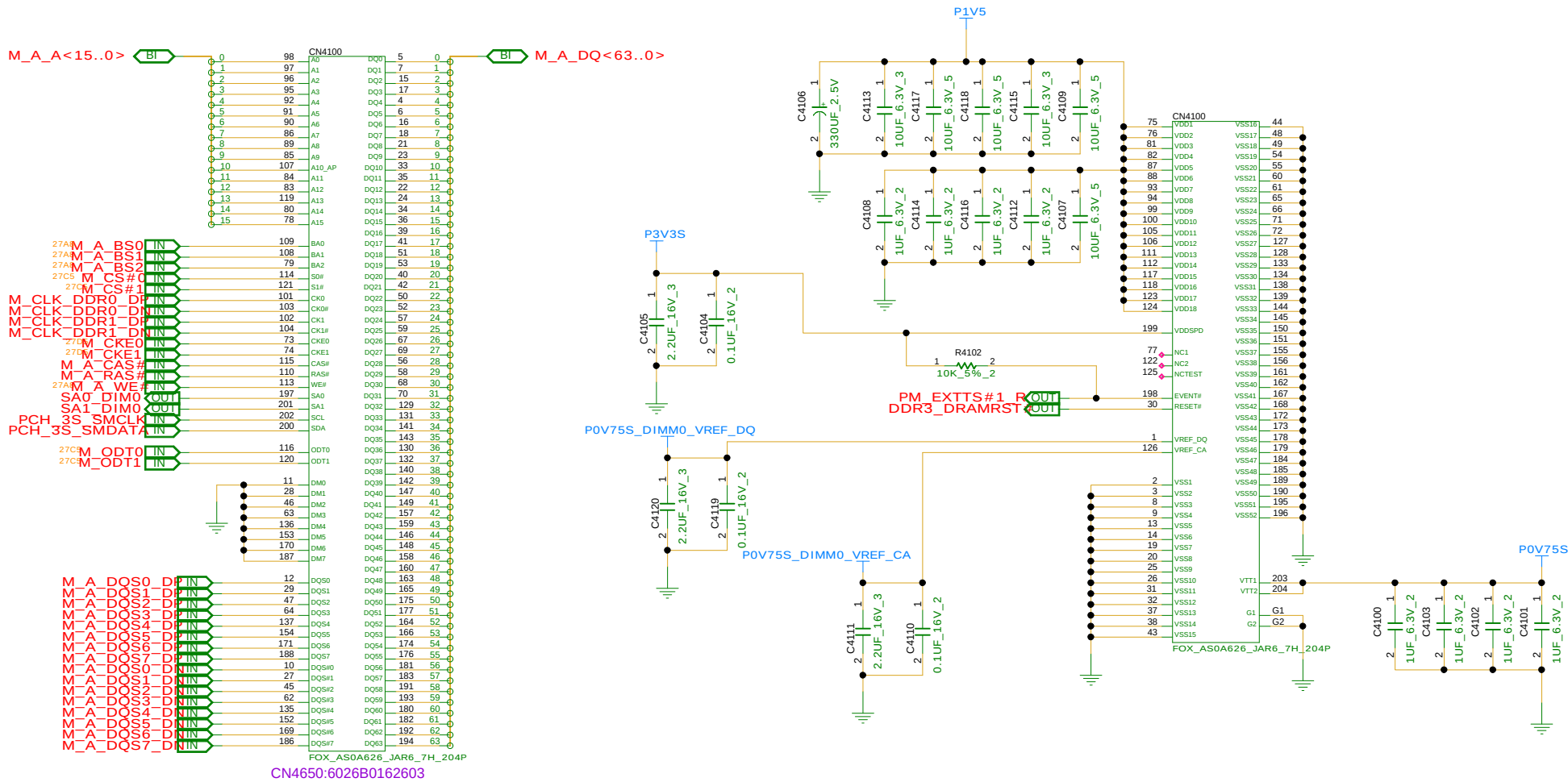


REFERENCE NUMBER:4500~4699

PEGX4 LANE REVERSAL
PEGX16 STATIC LANE REVERSAL
DISPLAYPORT PRESENCE STRAP
PCIE PORT BIFURCATION STRAP
PEG DEFER TRAINING



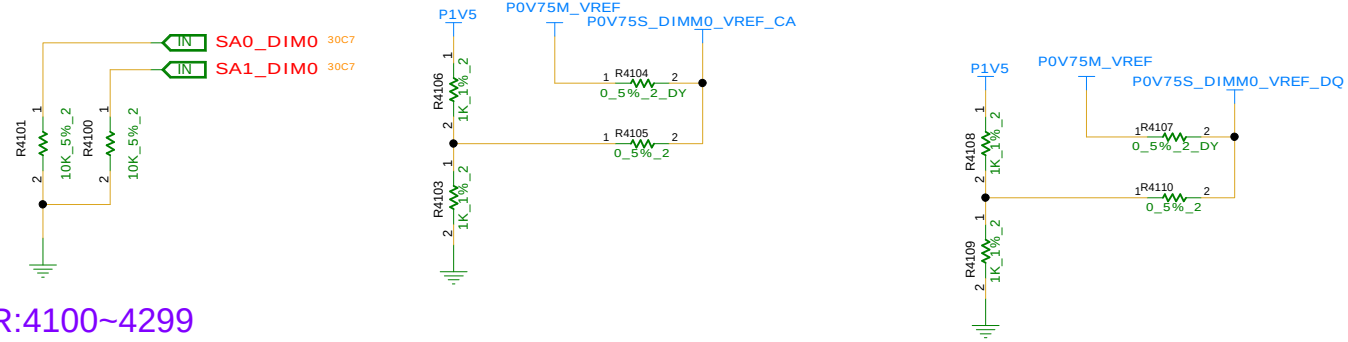
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TITLE			
MODEL PROJECT.FUNCTION			
IVY BRIDGE_6 (GND,RESERVED)			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET		29 of 73	



NOTE:
IF SA0_DIM0=0, SA1_DIM0=0
SO-DIMMA SPD ADDRESS IS 0XA0
SO-DIMMA TS ADDRESS IS 0X30

IF SA0_DIM0=1, SA1_DIM0=0
SO-DIMMA SPD ADDRESS IS 0XA2
SO-DIMMA TS ADDRESS IS 0X32

REFERENCE NUMBER:4100~4299



INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
DDR3_SO_DIMM0			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX

DATE 21-OCT-2002

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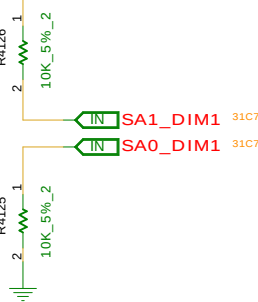
M_B_A<15..0> BI M_B_DQ<63..0> BI

M_B_BS0
M_B_BS1
M_B_BS2
M_CS#3
M_CS#3
M_CLK_DDR2-DP
M_CLK_DDR3-DP
M_CLK_DDR3-DN
M_CKE2
M_CKE3
M_B_CAS#
M_B_RAS#
M_B_WE#
SA0_DIM1
SA1_DIM1
PCH_3S_SMCLK
PCH_3S_SMDATA

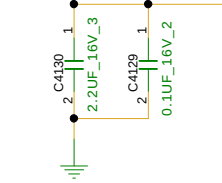
M_ODT2
M_ODT3

M_B_BS0
M_B_BS1
M_B_BS2
M_CS#3
M_CS#3
M_CLK_DDR2-DP
M_CLK_DDR3-DP
M_CLK_DDR3-DN
M_CKE2
M_CKE3
M_B_CAS#
M_B_RAS#
M_B_WE#
SA0_DIM1
SA1_DIM1
PCH_3S_SMCLK
PCH_3S_SMDATA

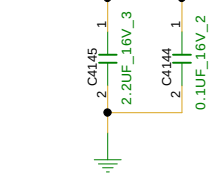
P3V3S



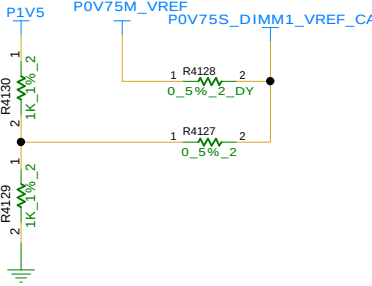
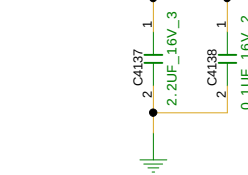
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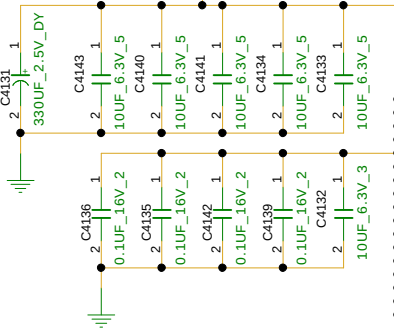
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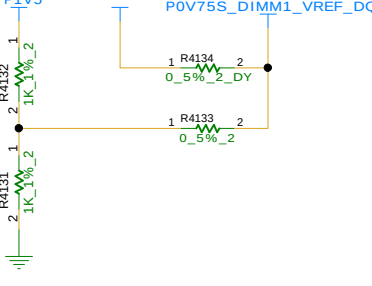
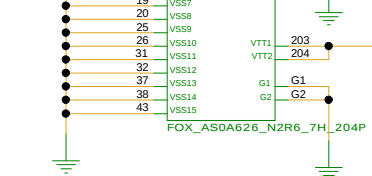
P0V75S_DIMM1_VREF_CA



P1V5



PM_EXTTTS#1 ROUT
DDR3_DRAMRST ROUT



FOX_AS0A626_N2R6_7H_204P

VTT1
VTT2
G1
G2

VREF_DQ
VREF_CA

VSS1
VSS2
VSS3
VSS4
VSS5
VSS6
VSS7
VSS8
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VSS97
VSS98
VSS99
VSS100

INVENTEC

TITLE

MODEL PROJECT FUNCTION

DDR3_SO_DIMM1

DOC NUMBER

1310xxxxx-0-0

REV

X01

SIZE

A3

CODE

CS

CHANGE BY

XXX

DATE

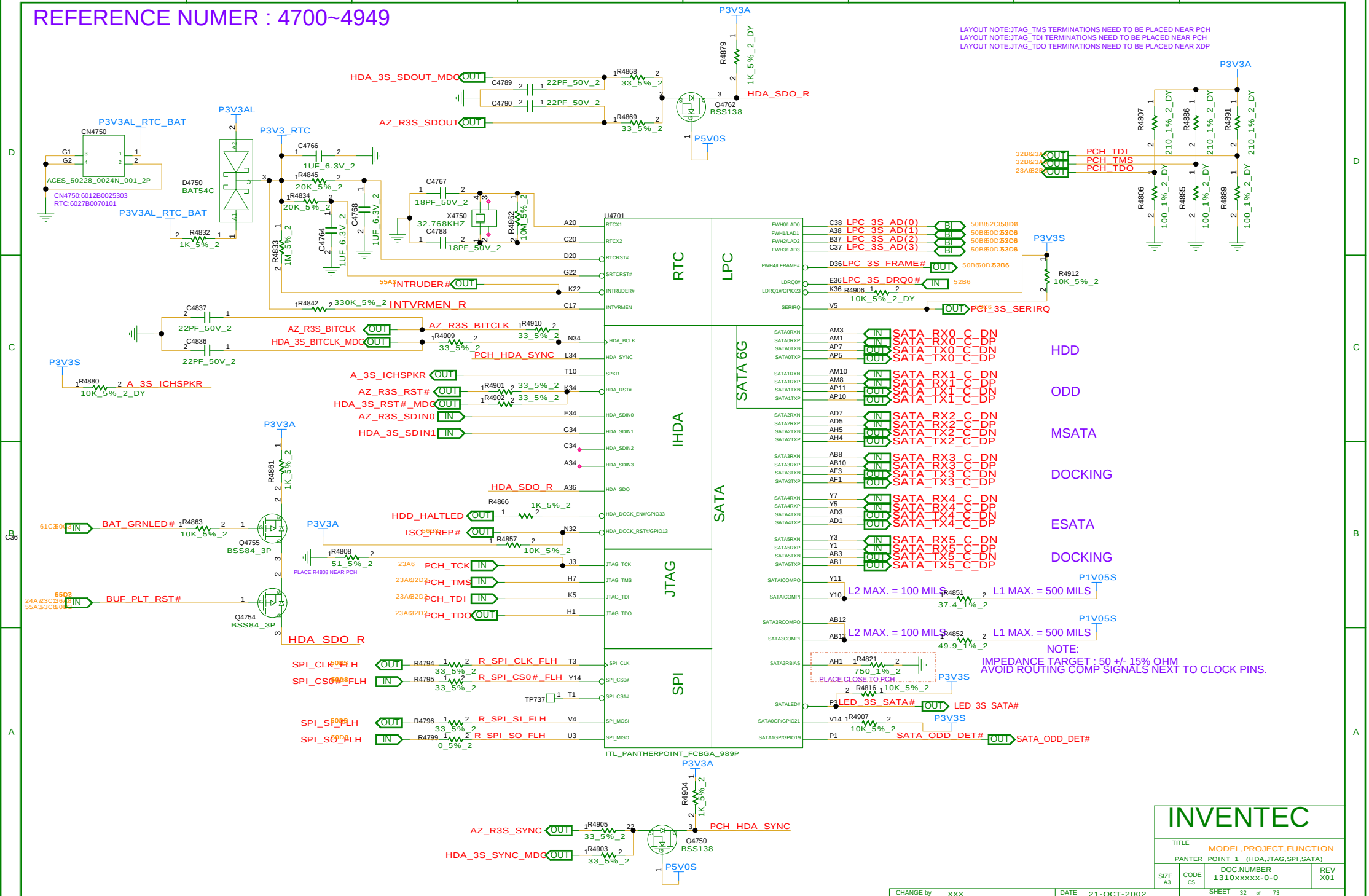
21-OCT-2002

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REFERENCE NUMBER:4100~4299

REFERENCE NUMBER : 4700~4949



INVENTEC

TITLE		MODEL,PROJECT,FUNCTION	
PANTER		POINT_1 (HDA,JTAG,SPI,SATA)	
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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P3V3S

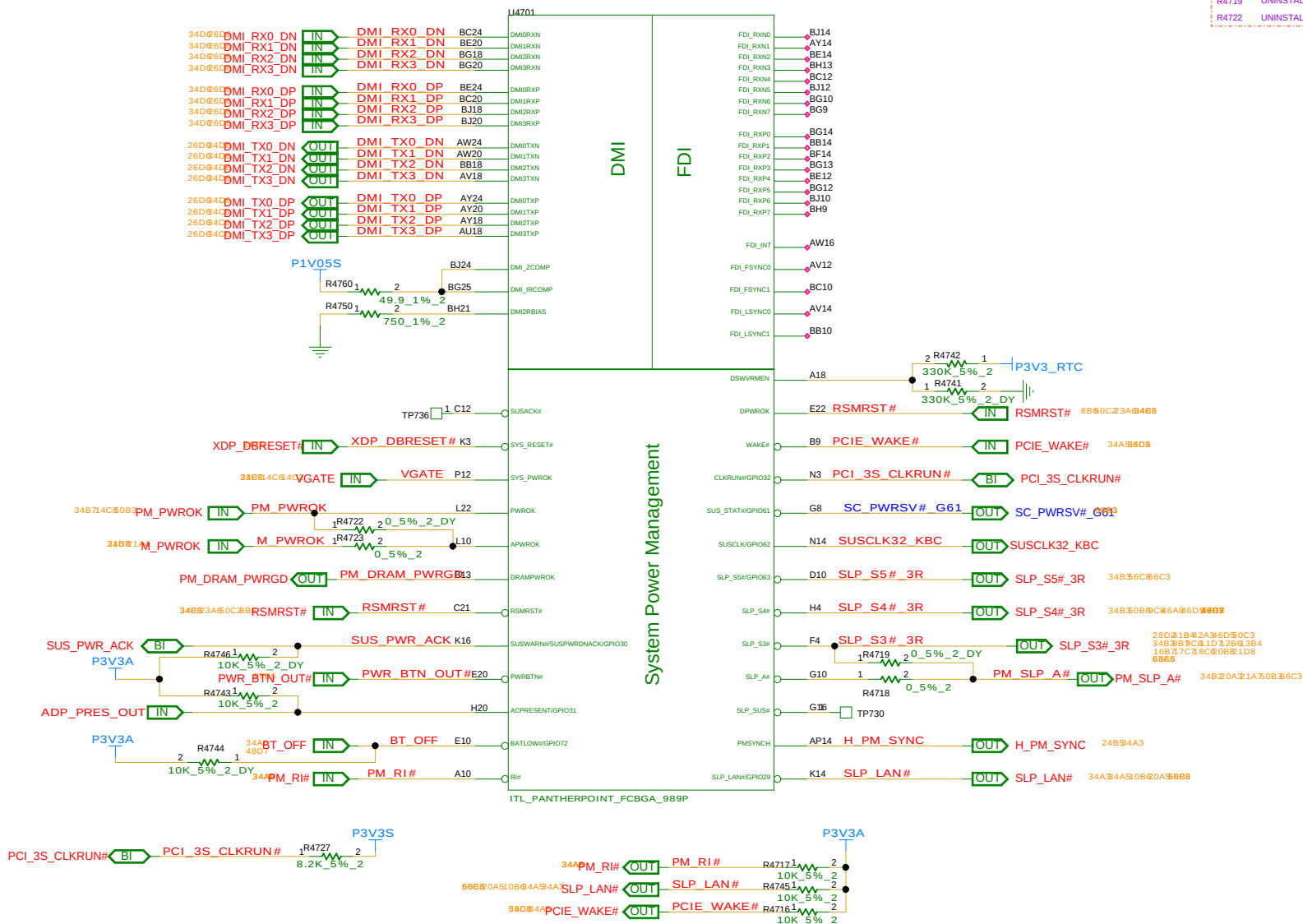


REFERENCE NUMBER : 4700~4949

NOTE:

- 1.SLP_SUS AND SUSACK# ARE NC IF DSW IS NOT SUPPORTED
- 2.DPWROK SHOULD CONNECT TO RSMRST# IF DSW NOT SUPPORTED
- 3.PCH DPWROK PULL UP TO +V3S ENABLES DSW WUPPORT. NO INSTALL R5261 TO DISABLE DSW

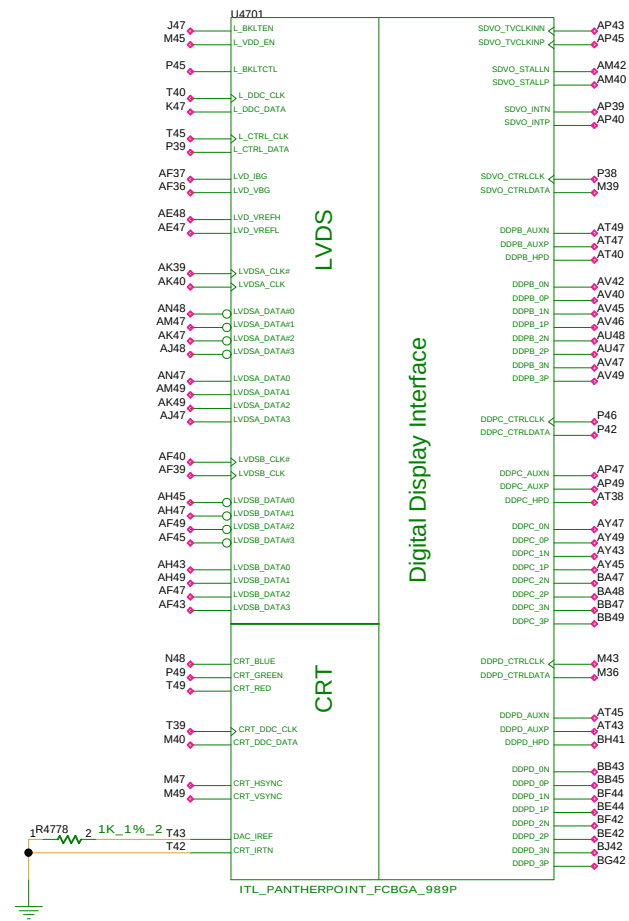
	IAMT	NON-IAMT
R4718	INSTALL	UNINSTALL
R4723	INSTALL	UNINSTALL
R4719	UNINSTALL	INSTALL
R4722	UNINSTALL	INSTALL



INVENTEC

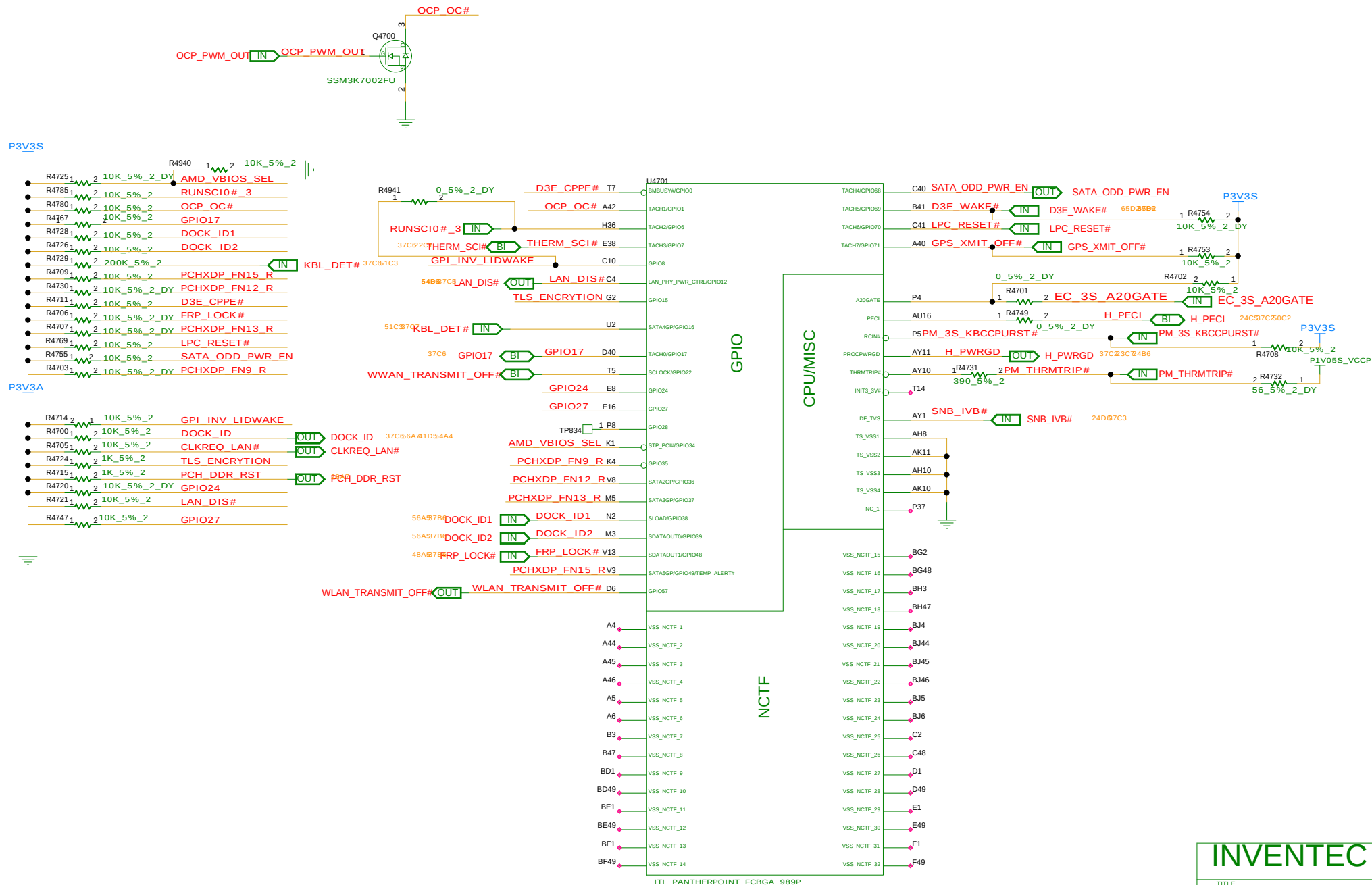
TITLE			
MODEL,PROJECT,FUNCTION PANTER POINT_3 (DMI,FDI,GPIO)			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	R X

REFERENCE NUMER : 4700~4949



INVENTEC				
TITLE				
MODEL PROJECT FUNCTION PANTHER POINT_4 (LVDS,DDI)				
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0		REV X01
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REFERENCE NUMBER : 4700~4949



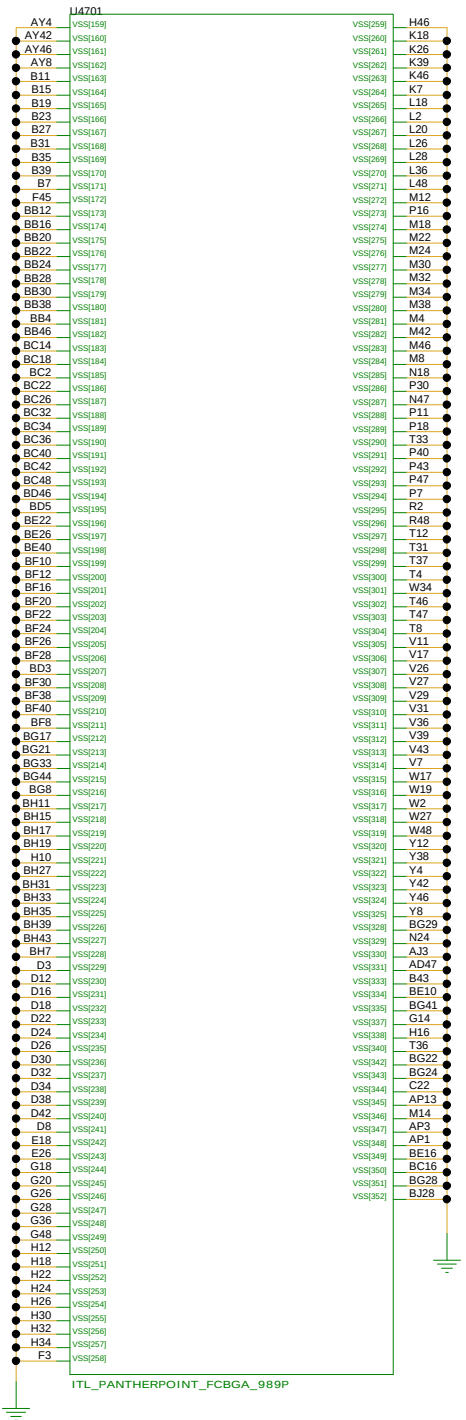
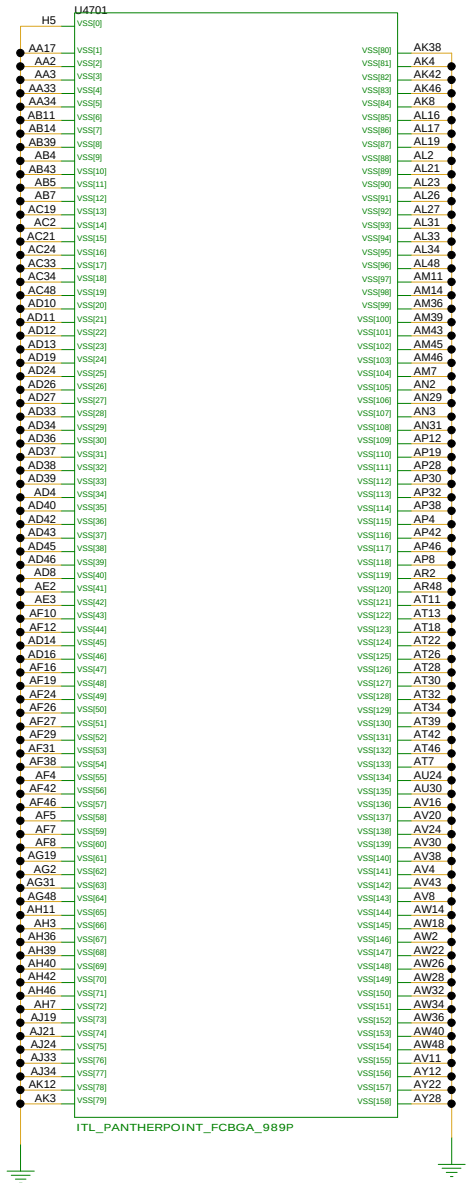
INVENTEC

TITLE			
PANTHER	MODEL	PROJECT	FUNCTION
POINT_5	POINT_5	POINT_5	POINT_5
SIZE A3	CODE CS	DOC NUMBER 1310XXXX-0-0	REV X01

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REFERENCE NUMBER : 4700~4949



INVENTEC

TITLE
MODEL, PROJECT, FUNCTION
PANTHER POINT_9 (GND)

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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SHEET 40 of 73

CHANGE by XXX DATE 21-OCT-2002

REFERENCE NUMBER:3680~3699

REFERENCE NUMBER:3050~3099

INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
VGA SWITCH / CRT			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

SHEET 41 of 73

CHANGE by XXX DATE 21-OCT-2002

1

2

3

4

5

6

7

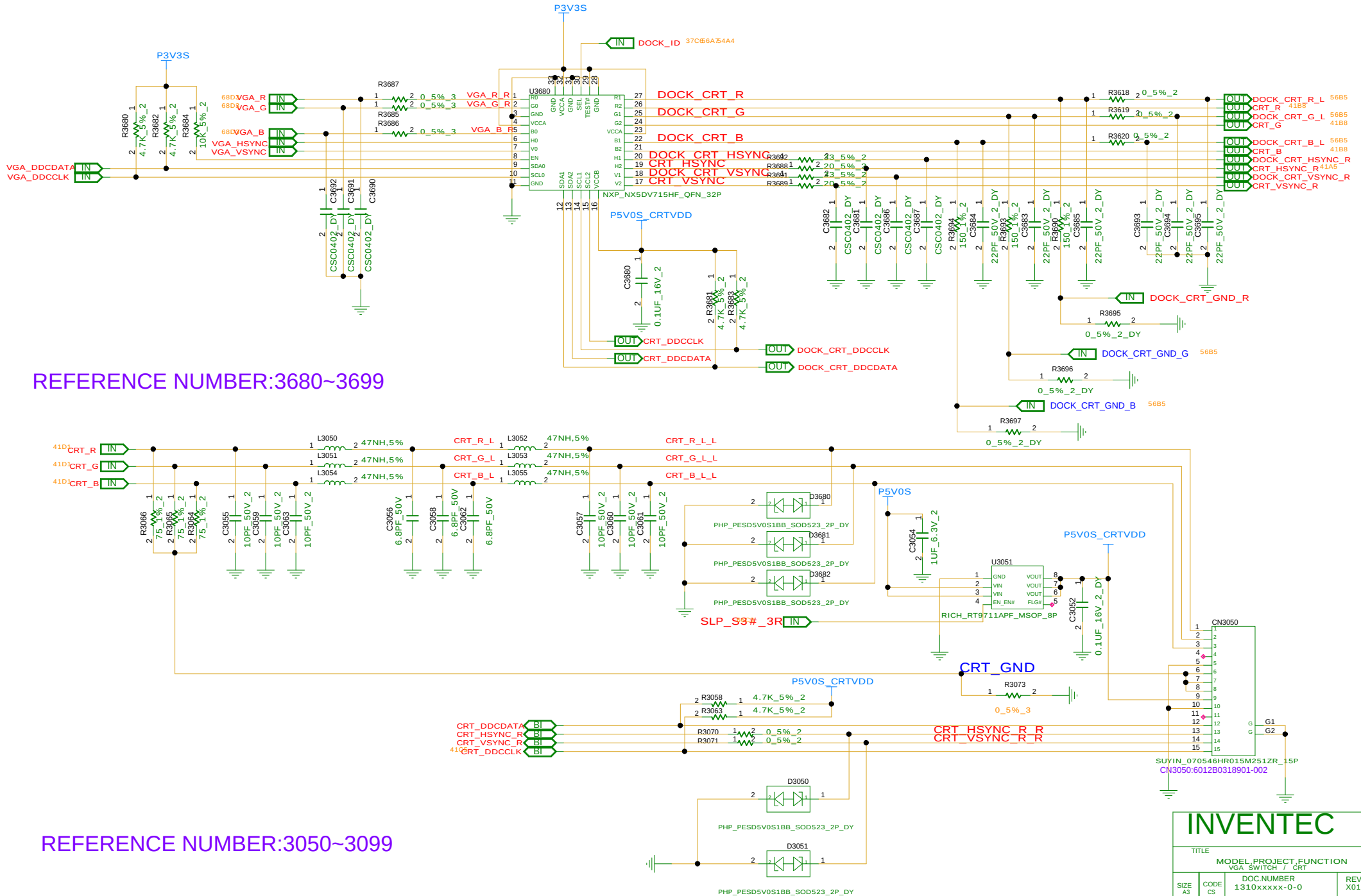
8

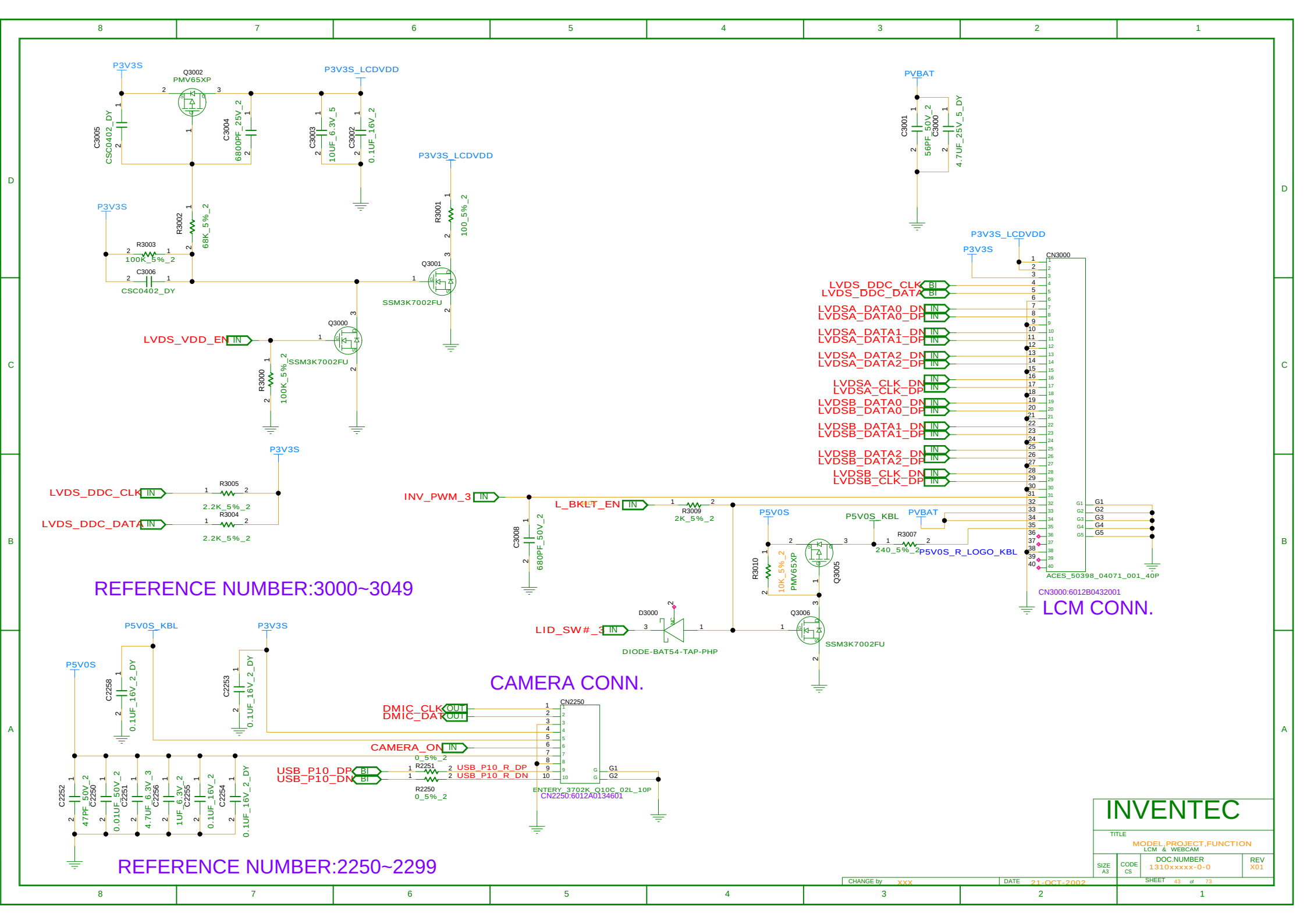
A

B

C

D





REFERENCE NUMBER:3000~3049

CAMERA CONN.

LCM CONN.

REFERENCE NUMBER:2250~2299

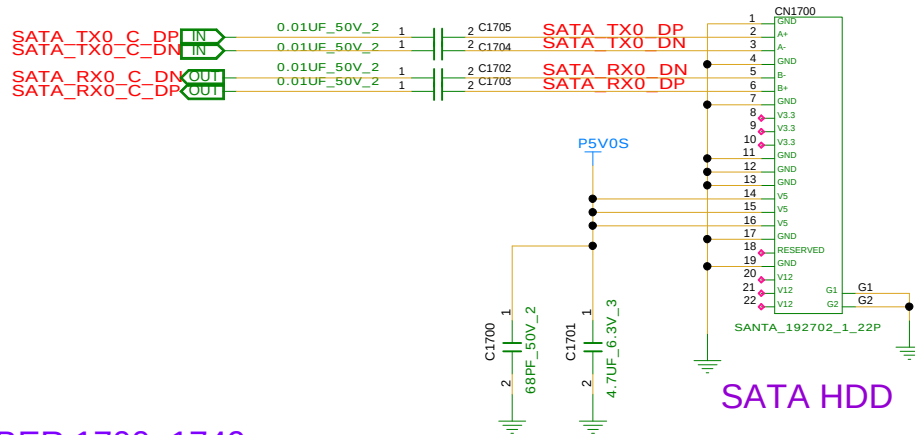
INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
LCM & WEBCAM			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 43 of 73			

BLANK

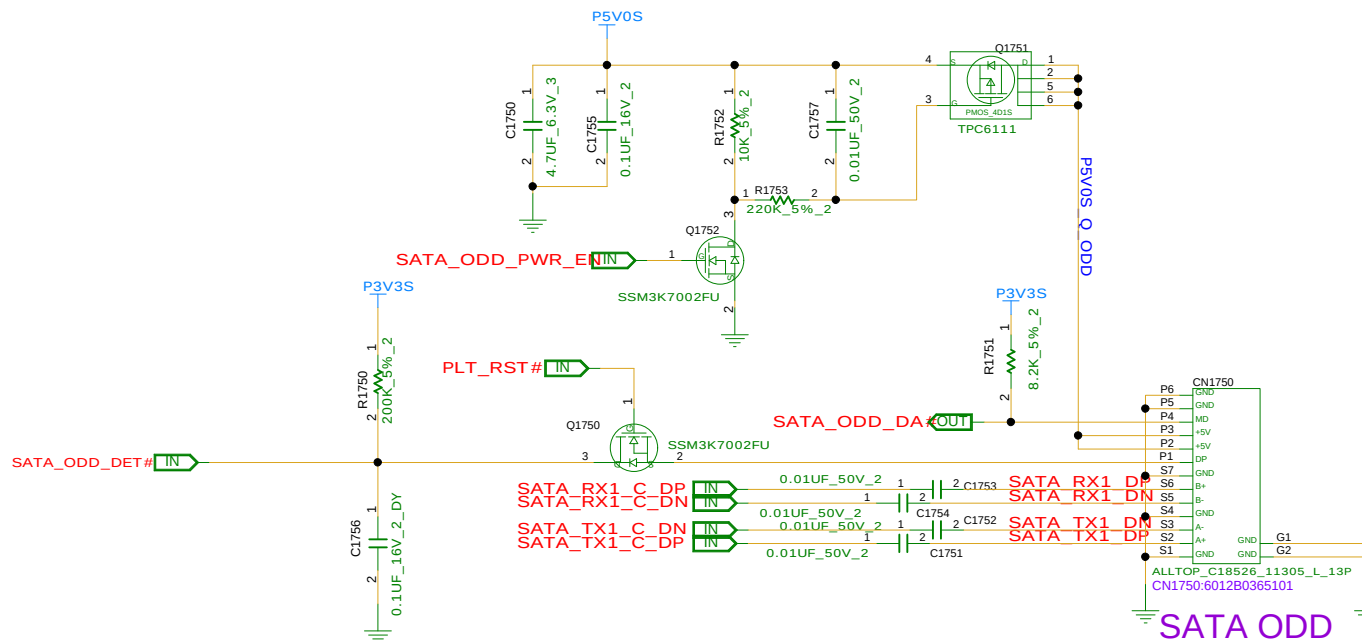
REFERENCE NUMBER:3400~3499

INVENTEC			
TITLE MODEL,PROJECT,FUNCTION EDP TO LVDS			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
SHEET 44 of 73			



SATA HDD

REFERENCE NUMBER:1700~1749

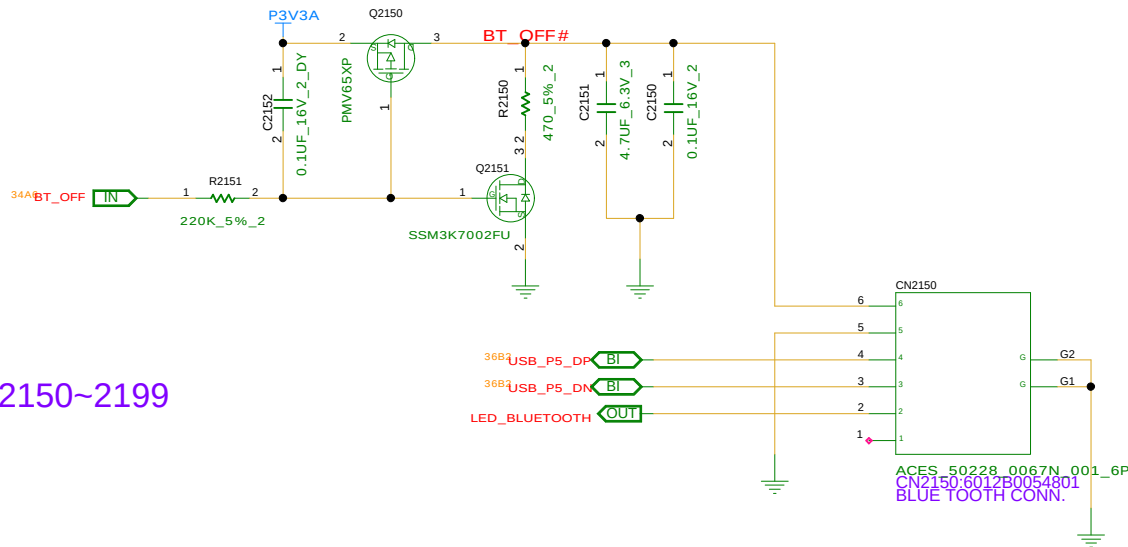


SATA ODD

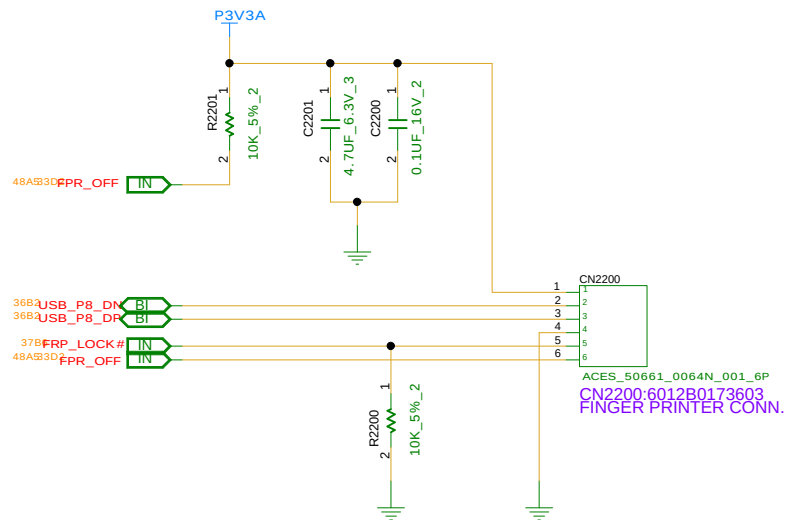
REFERENCE NUMBER:1750~1799

INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
SATA HDD & ODD CNTR			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 45 of 73			

REFERENCE:2150~2199



REFERENCE:2200~2249



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
BLUE TOOTH & FINGER PRINTER CNTR			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

D

C

B

A



C

B

A

INVENTEC

TITLE	MODEL, PROJECT, FUNCTION
ACCELERATOR	

SIZE A3	CODE CS	DOC. NUMBER 1310xxxxx-0-0	F X
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AS	CS	
		SHEET 49 of 73
		1

CHANGE by	XXX	DATE	21-OCT-2002
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xxx

DATE	21-OCT-2002
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21-OCT-2002

SHEET 49 of 73

49 of 73

8

7

6

5

4

3

2

100

REFERENCE NUMBER : 300~399

D

C

B

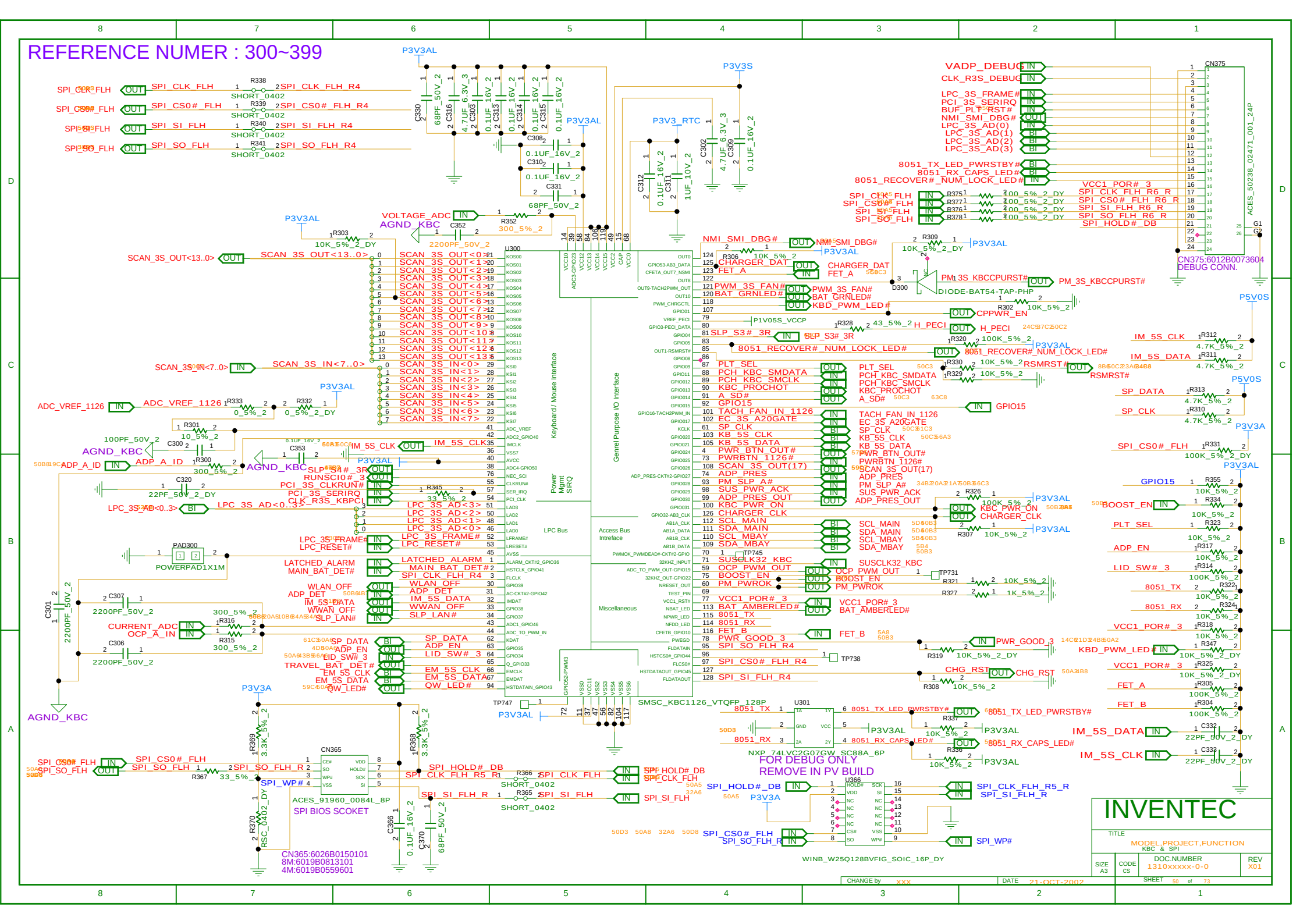
A

D

C

B

A



INVENTEC			
TITLE			
MODEL	PROJECT	FUNCTION	
KBC	SP		
SIZE			
A3	CODE	DOC NUMBER	REV
	CS	1310xxxx-0-0	X01
SHEET 50 of 73			

REFERENCE NUMER : 300~389

D

D

C

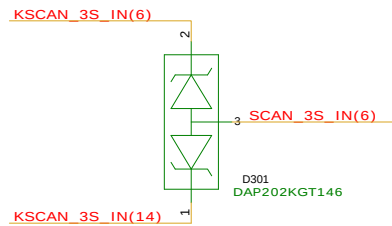
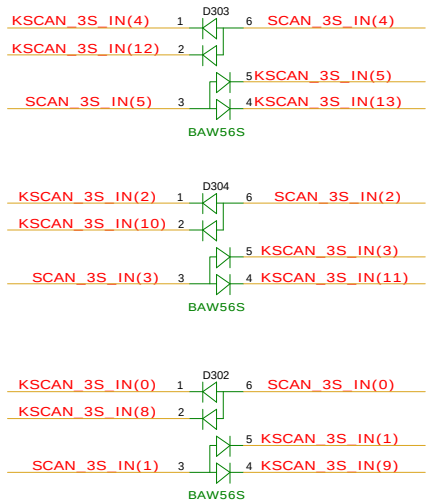
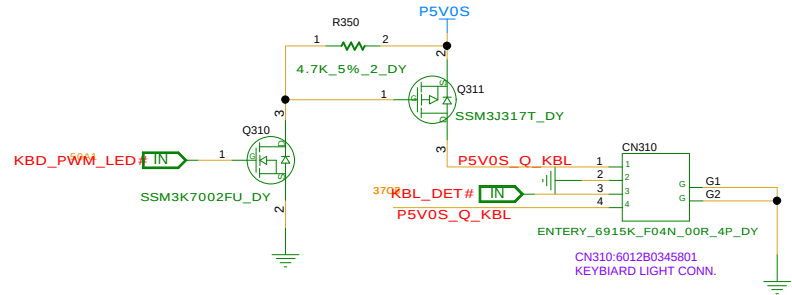
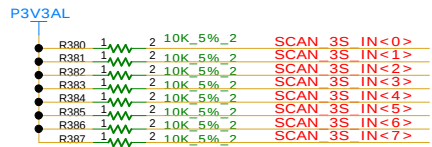
C

B

B

A

A



INVENTEC

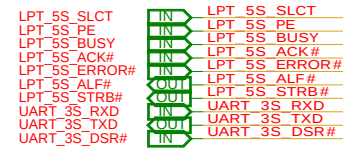
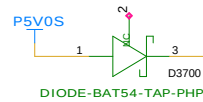
TITLE MODEL,PROJECT,FUNCTION

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

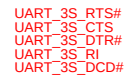
CHANGE by XXX DATE 21-OCT-2002

SHEET 51 of 73

REFERENCE NUMBER : 3700~3799



P3V3S



CLK_R3S_SIO14

LPC 3S AD<0>

LPC 3S AD<1>

LPC 3S AD<2>

LPC 3S AD<3>

LPC 3S FRAME#

LPC 3S DRQ0#

LPC 3S RESET#

LPC 3S LPCPD#

PCI 3S CLKRUN#

CLK_R3S_SIOPCI

PCI 3S SERIRQ

UART 3S RTS#

UART 3S DSR#

UART 3S TXD

UART 3S RXD

UART 3S DCD#

UART 3S RI

UART 3S DTR#

UART 3S CTS

UART 3S RTS#

UART 3S DSR#

UART 3S TXD

UART 3S RXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

UART 3S ERROR#

UART 3S ALF#

UART 3S STRB#

UART 3S RXD

UART 3S TXD

UART 3S DSR#

UART 3S SLCT

UART 3S PE

UART 3S BUSY

UART 3S ACK#

D



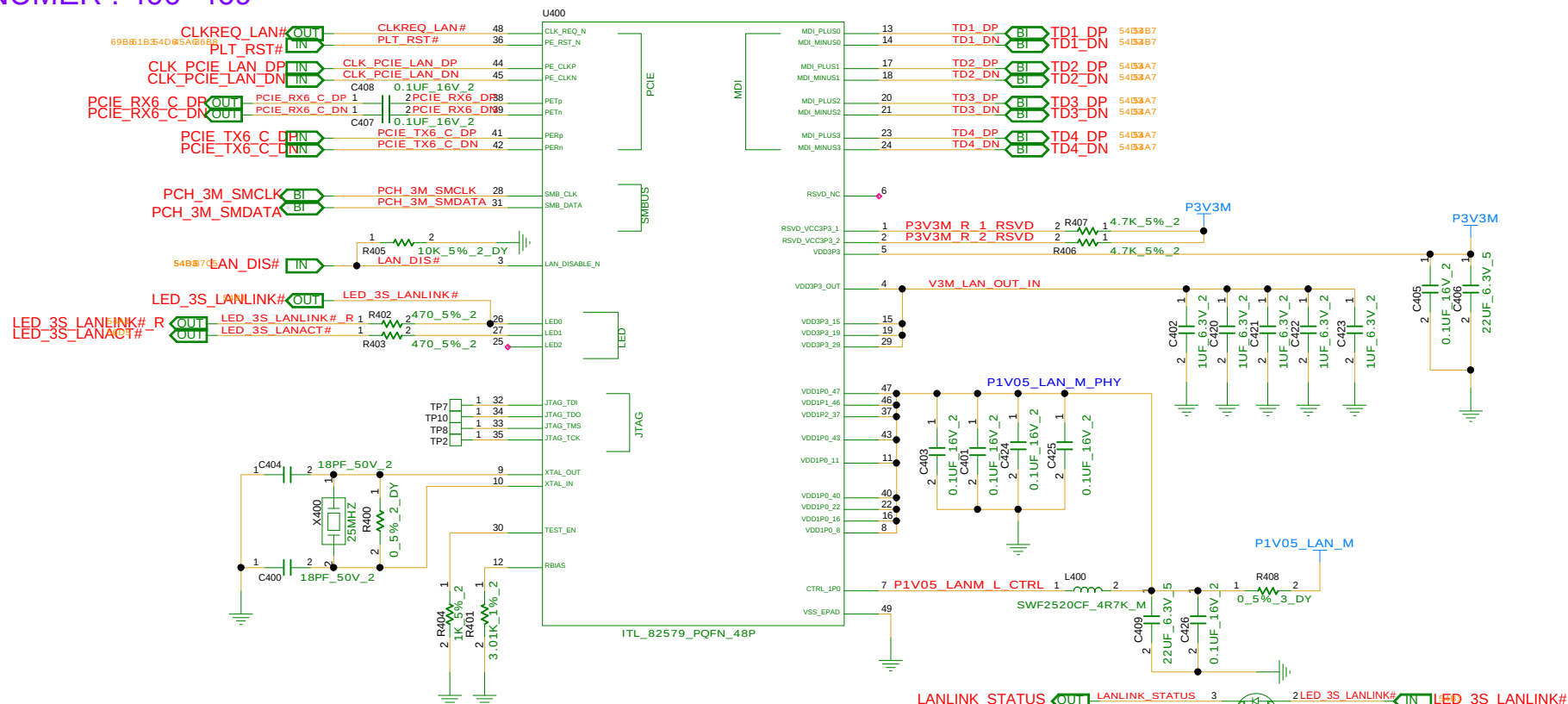
C



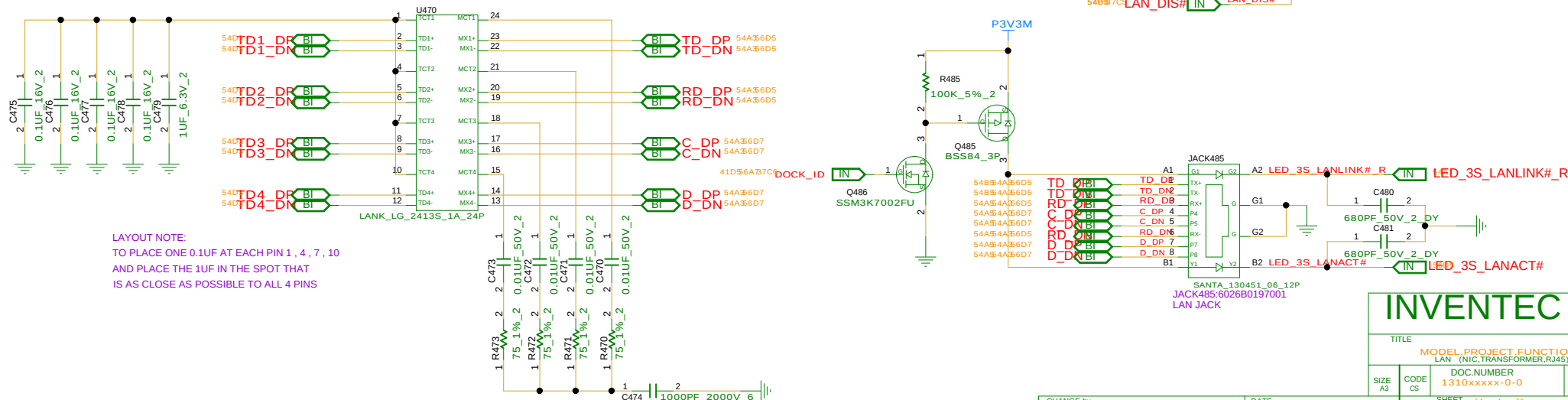
A

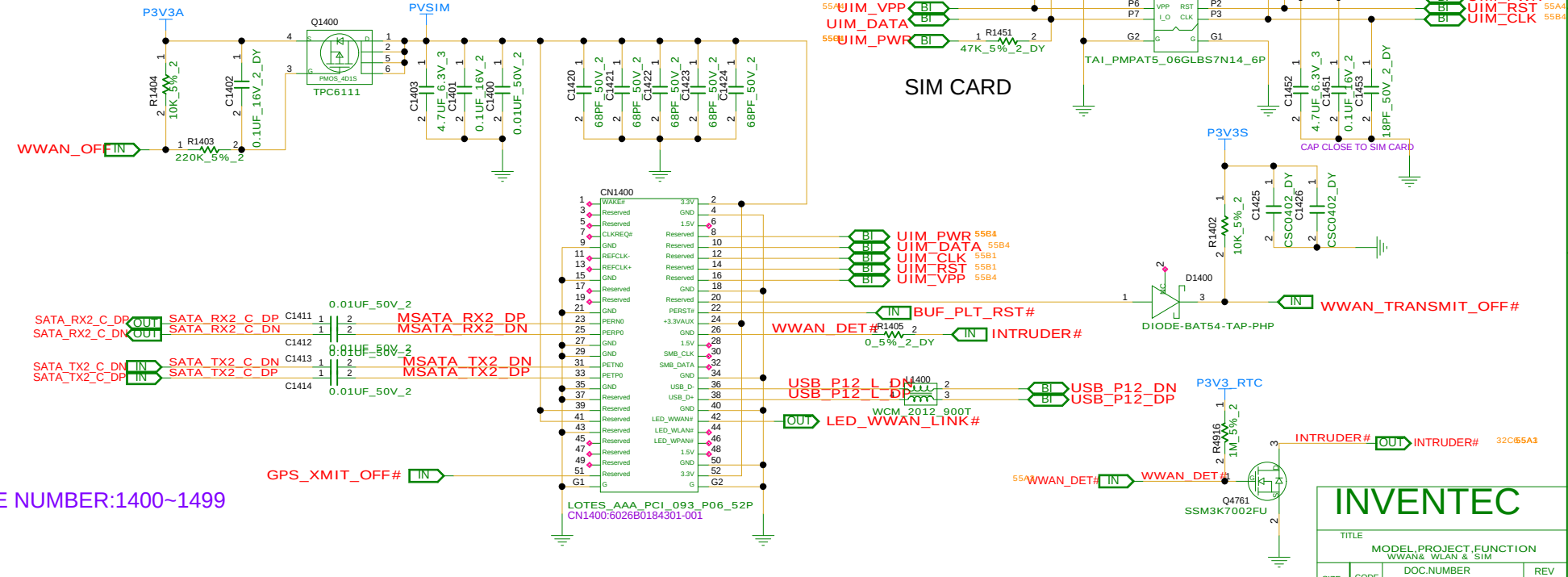
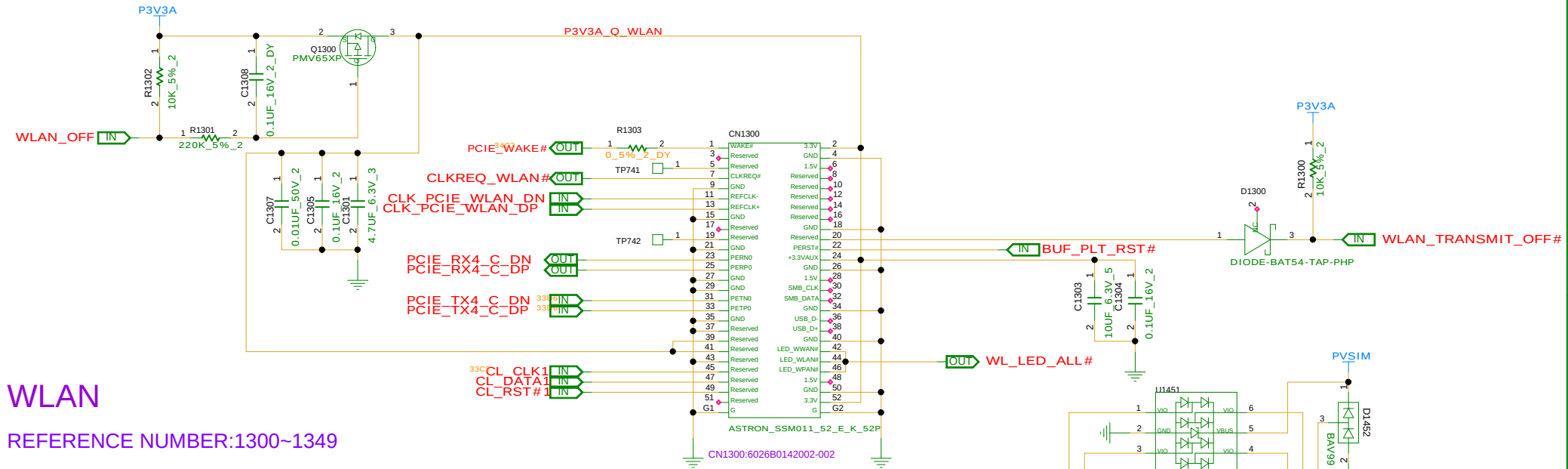
CHANGE by	XXX	DATE	21-OCT-2002
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REFERENCE NUMBER : 400~469

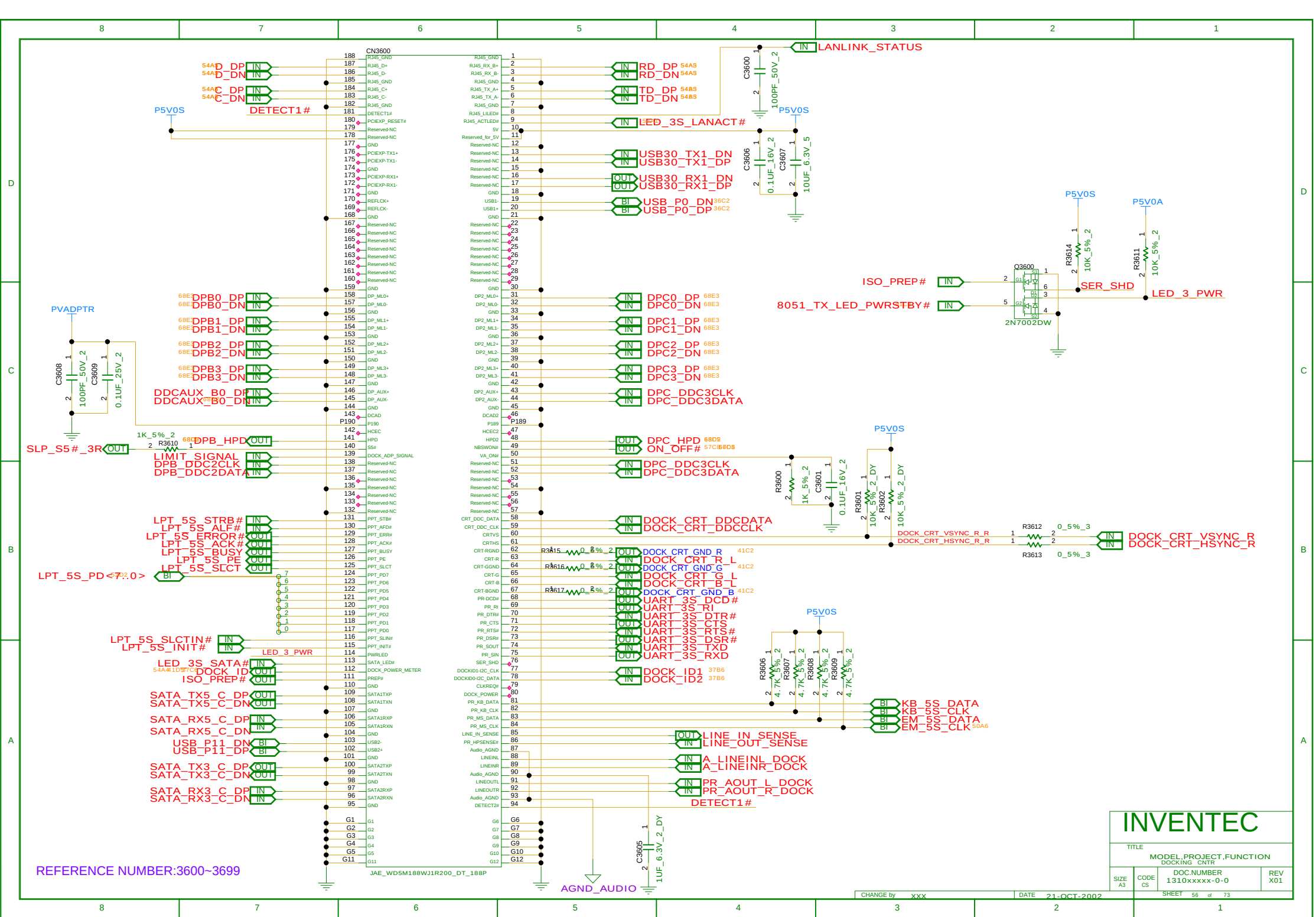


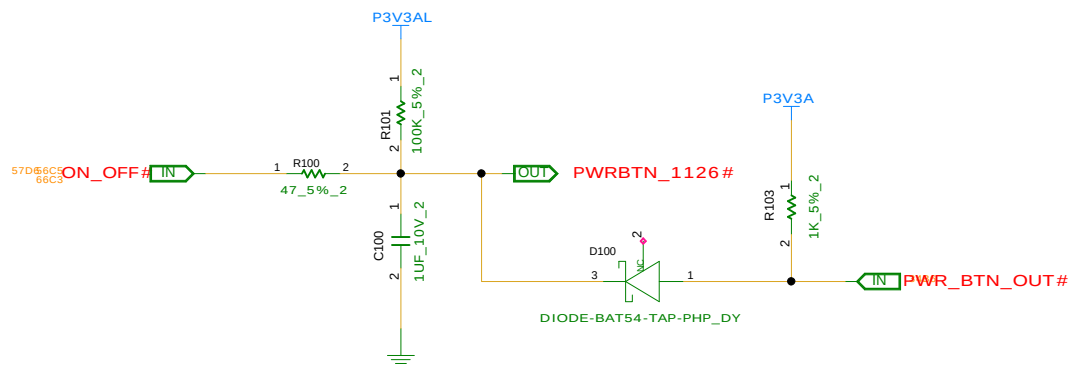
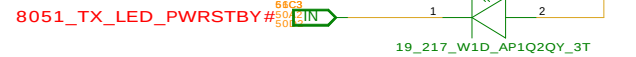
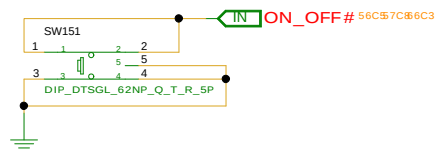
REFERENCE NUMBER : 470~499



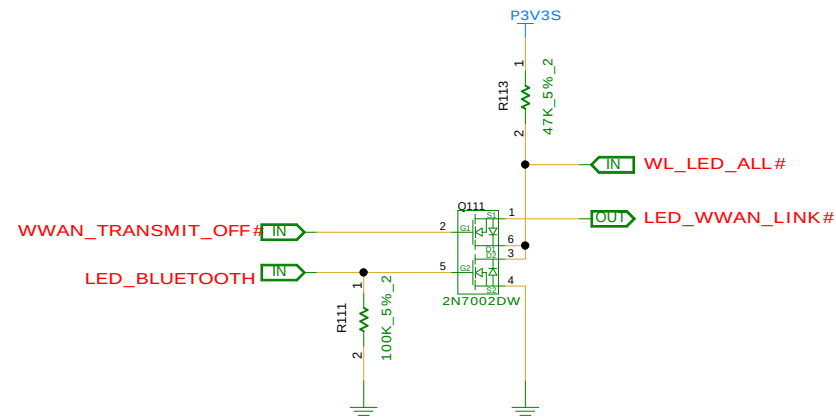


INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
WWAN, WLAN & SIM			
DOC NUMBER			
1310xxxxx-0-0			
REV			
X01			
SIZE	CODE		
A3	CS		





POWER / STANDBY LED



WLAN_WWAN_BLUETOOTH_LED

REFERENCE NUMBER:100~199

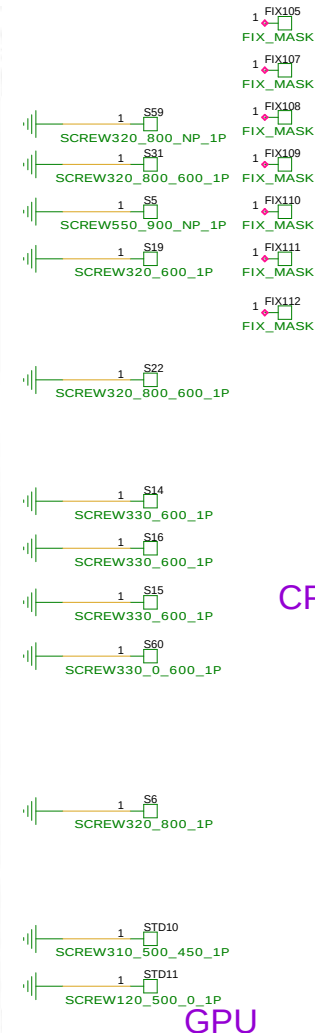
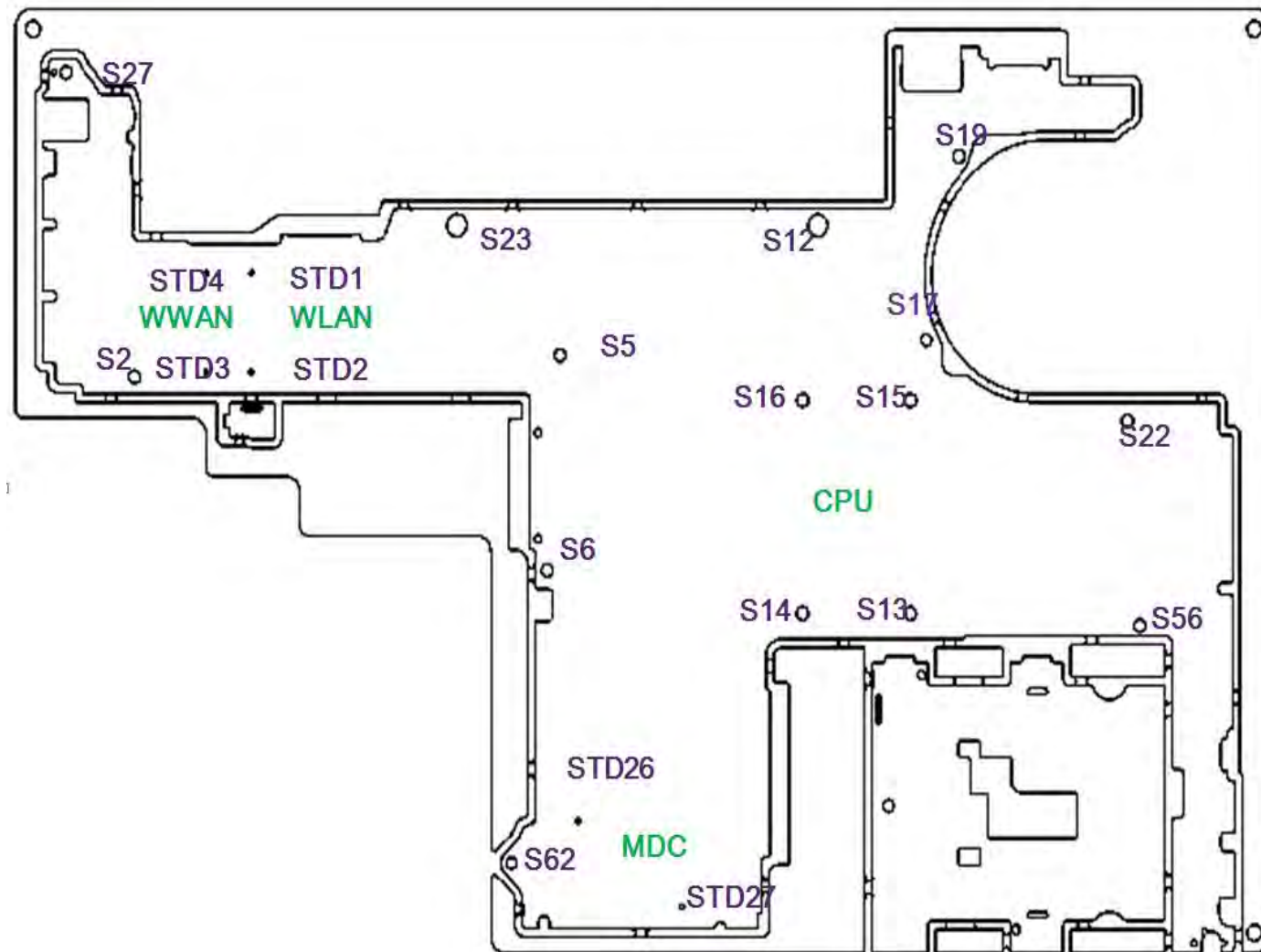
INVENTEC

TITLE			
MODEL/PROJECT/FUNCTION POWER & WLAN_LED			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01

CHANGE by XXX DATE 21-OCT-2002

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SCREW



NOTE :
S9000 .S9001 .STD9000 ON PAGE 60

INVENTEC

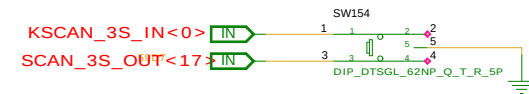
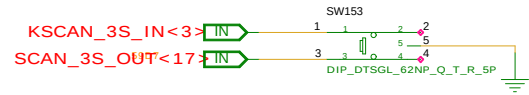
TITLE MODEL,PROJECT,FUNCTION

SIZE CODE DOC NUMBER REV

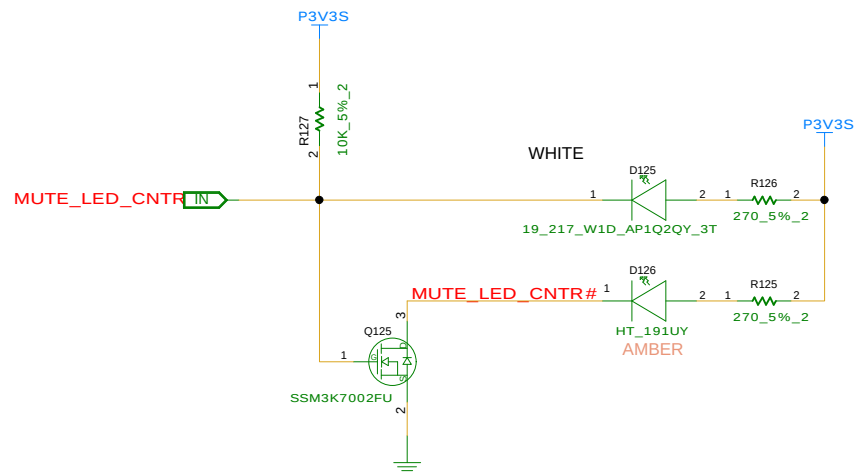
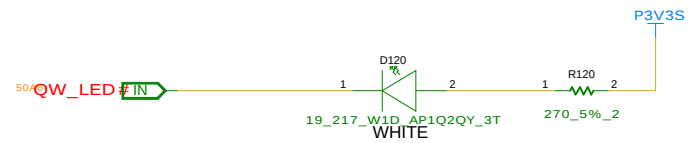
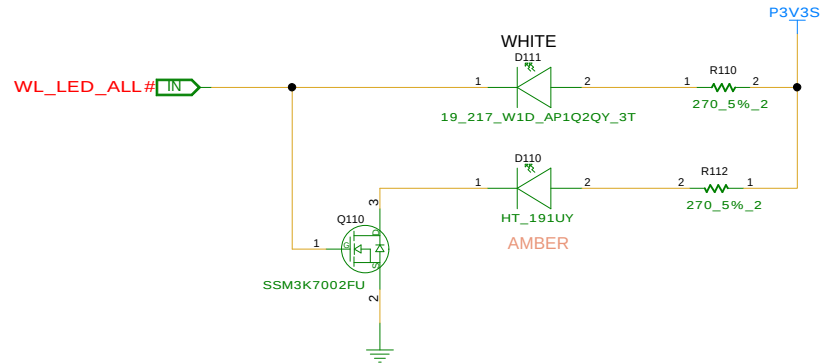
A3 CS 1310xxxxx-0-0 X01

CHANGE BY XXX DATE 21-OCT-2002

SHEET 58 of 73

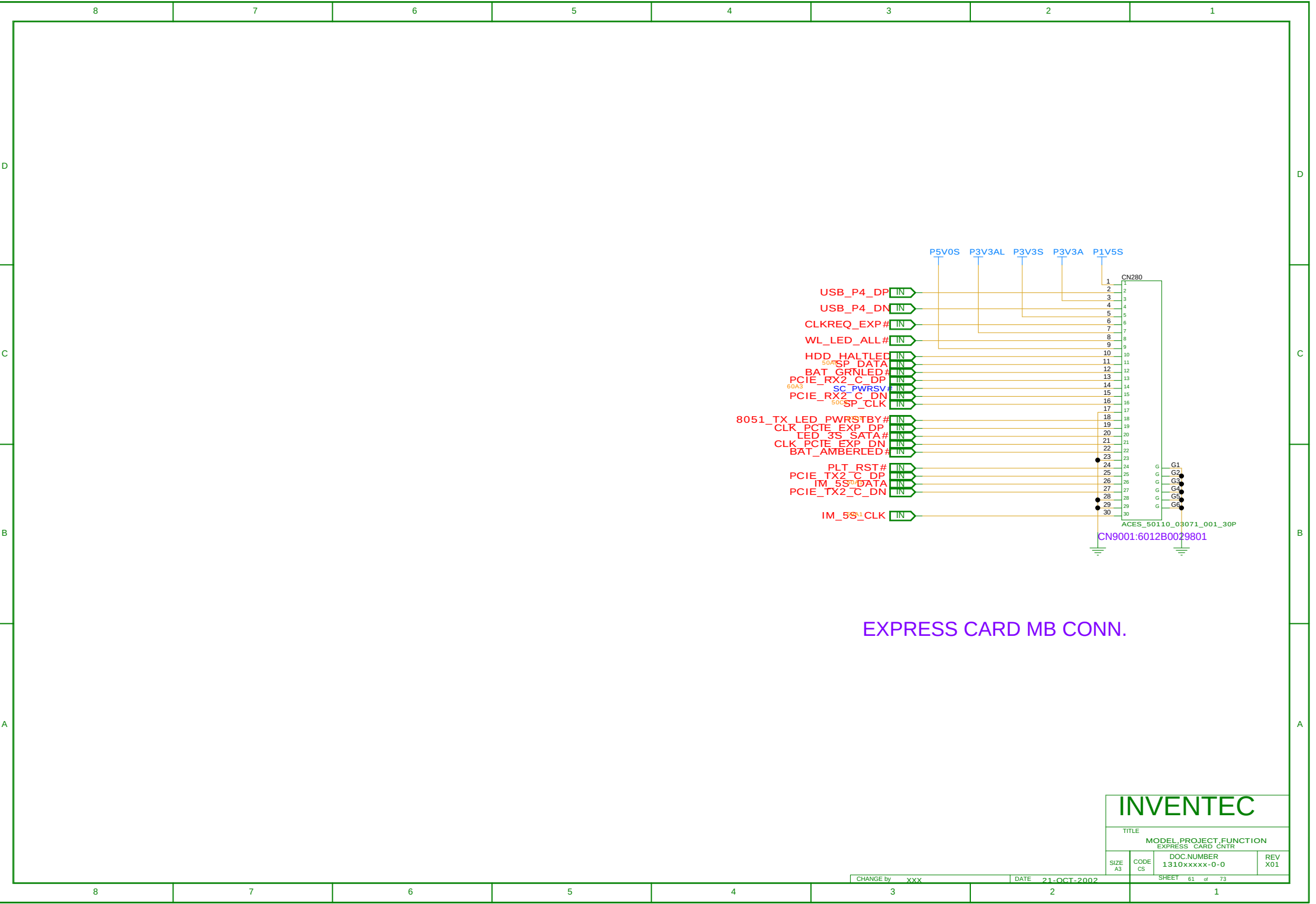


REFERENCE NUMBER:100~199



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
WIRELESS / QUICK WEB / MUTE LED			
DOC NUMBER		REV	
1310xxxxx-0-0		X01	
SIZE	CODE	SHEET	
A3	CS	59 of 73	



EXPRESS CARD MB CONN.

INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION EXPRESS CARD CNTR			
SIZE A3	CODE CS	DOC.NUMBER	REV X01
		1310xxxx-0-0	
SHEET 61 of 73			

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INVENTEC

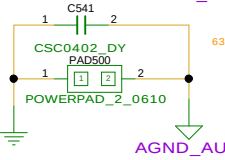
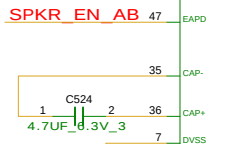
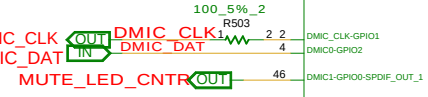
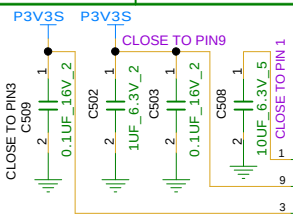
TITLE			
MODEL,PROJECT,FUNCTION BLANK			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01

AZ_R3S_BITCLK
AZ_R3S_SDIN0
AZ_R3S_SDOUT
AZ_R3S_SYNC
AZ_R3S_RST#

A_SD#

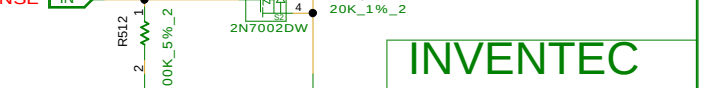
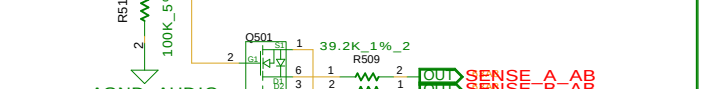
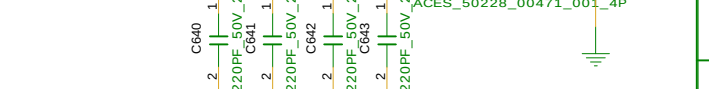
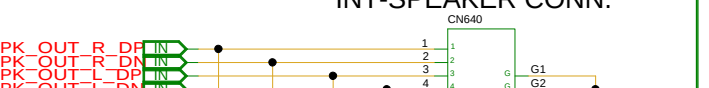
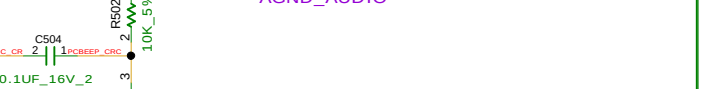
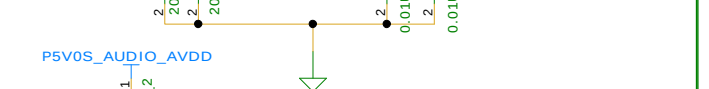
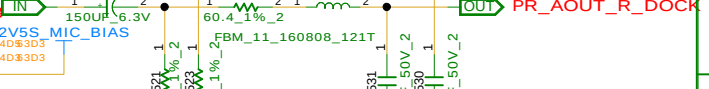
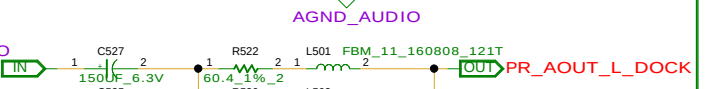
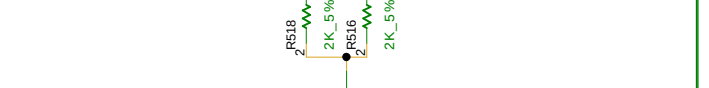
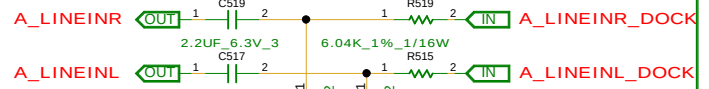
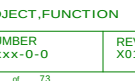
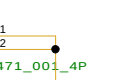
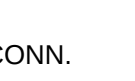
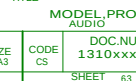
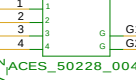
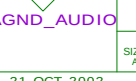
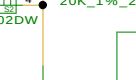
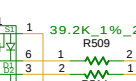
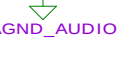
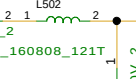
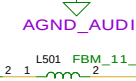
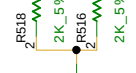
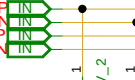
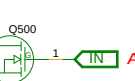
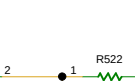
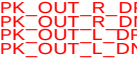
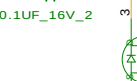
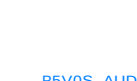
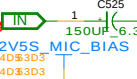
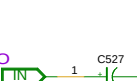
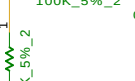
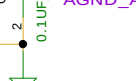
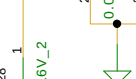
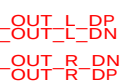
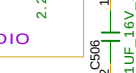
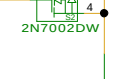
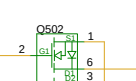
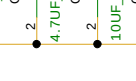
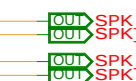
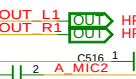
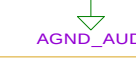
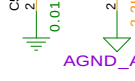
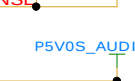
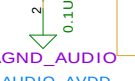
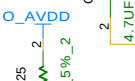
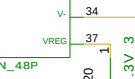
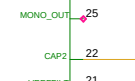
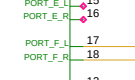
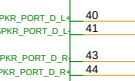
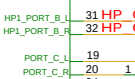
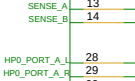
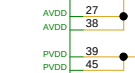
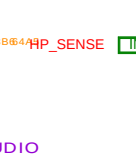
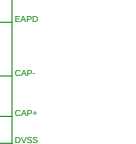
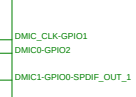
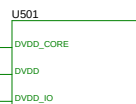
SENSE₃A_AB
SENSE₃B_AB

REFERENCE NUMBER:500~549

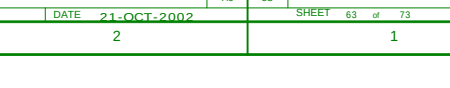
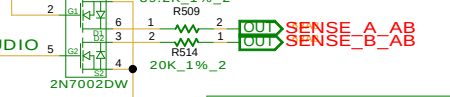
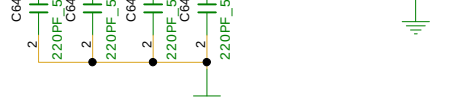
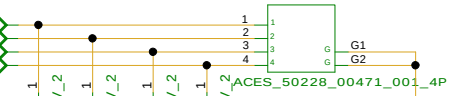


MIC_SENSE

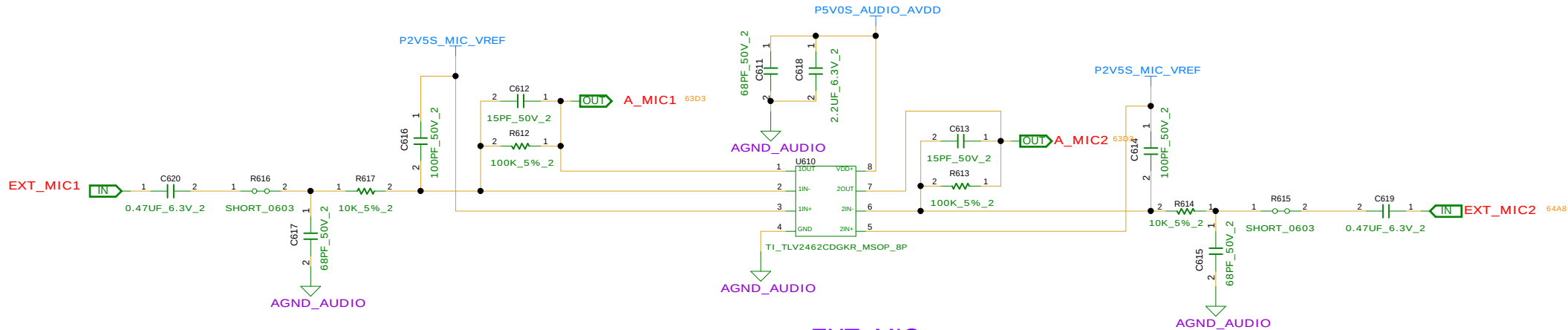
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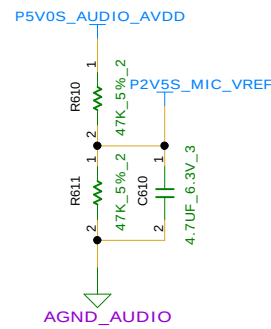
INT-SPEAKER CONN.



INVENTEC				
TITLE				
MODEL PROJECT,FUNCTION				
DOC NUMBER				
1310xxxxx-0-0				
REV				
X01				
SIZE	CODE	SHEET		
A3	CS	63	of	73

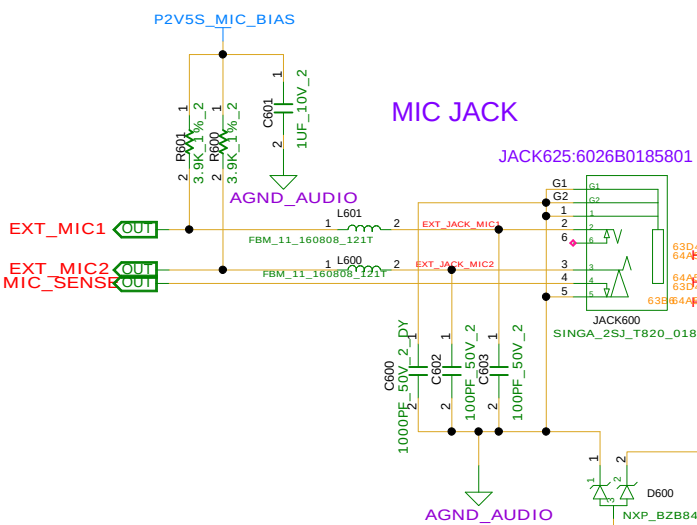


EXT. MIC
REFERENCE NUMBER:610~624

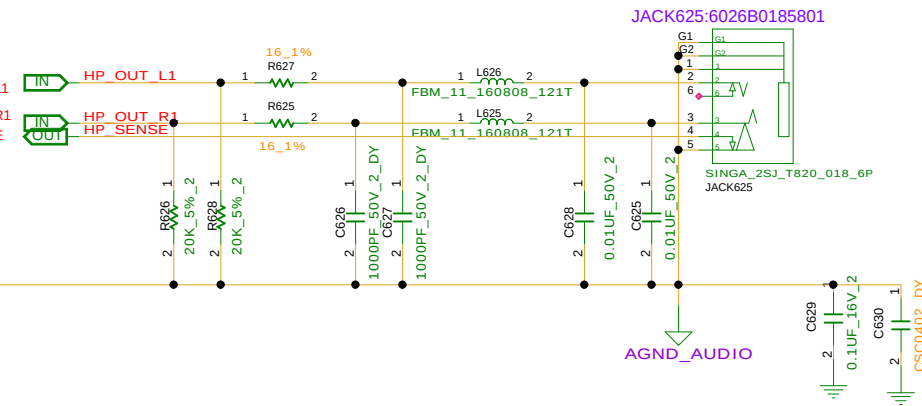


MIC JACK

EARPHONE JACK



REFERENCE NUMBER:600~649



INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
EXT. MIC / MIC / HP			
DOC NUMBER			
1310xxxxx-0-0			
REV			
X01			
SIZE	CODE	SHEET	
A3	CS	64 of 73	

REFERENCE NUMBER:800~899

SD/MMC CONN

1394 CONN.

INVENTEC

TITLE			
MODEL PROJECT FUNCTION		CARD READER CTRL / 1394 CONN / SD CONN	
SIZE A3		DOC NUMBER 1310xxxxx-0-0	
CODE CS		REV X01	

CHANGE by XXX DATE 21-OCT-2002

SHEET 65 of 73

D

D

C

C

B

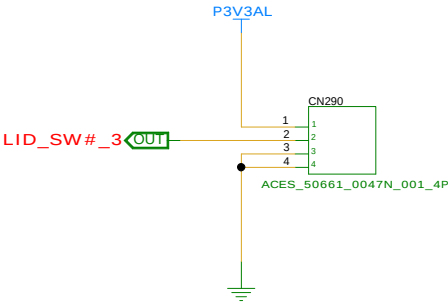
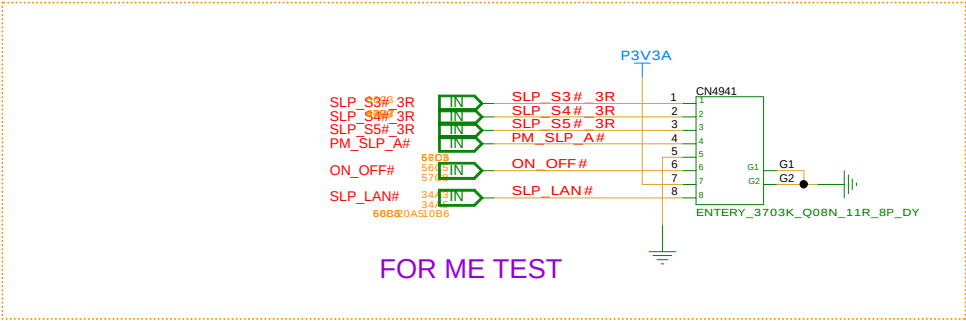
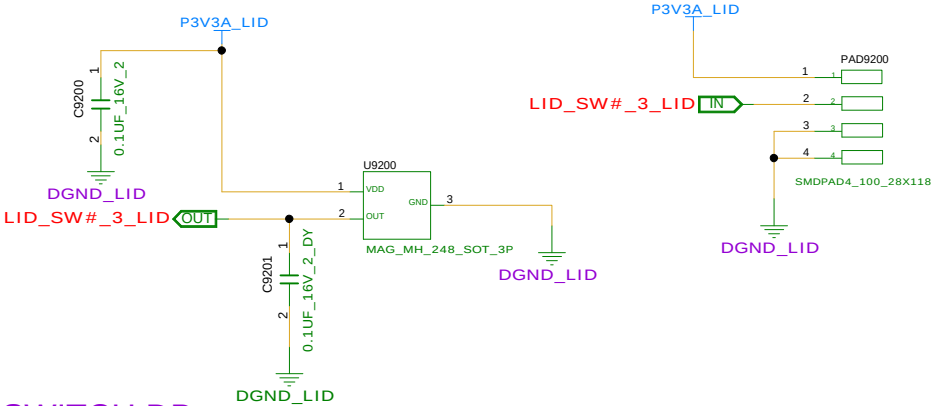
B

A

A

LID SWITCH DB

REFERENCE NUMBER:9200~9299



LID SWITCH MB CONN.

INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
LID SWITCH			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

STRAPS	PIN	DESCRIPTION FOR DEFAULT SETTING	
TX_PWRS_ENB	GPIO0	PCIE FULL TX OUTPUT SWING.	1
TX_DEEMPH_EN	GPIO1	PCIE transmitter de-emphasis enable.	1
BIF_DEBUG_ACCESS	GPIO4	Debug signals muxed out.	0
ROMIDCFG	GPIO[11:13,9]	Memory aperture type.	XXXX

PCH GPIO34		MEM_ID1	MEM_ID0	VENDOR
R4725 (high)	R4940 (low)	R5044	R5043	
In	In	0	0	Hynix Vega die H5GQ2H24MFR-T2C (128Mx16=1G)
In	In	0	1	Samsung C die K4G20325FC-HC04
In	In	1	0	Hynix Gemna die H5GQ2H24AFR-T2C
In	In	1	1	Samsung D die
In	In	0	0	Elpida B die EDW2032BBBG-50-F
In	In	0	1	
In	In	1	0	
in		1	1	

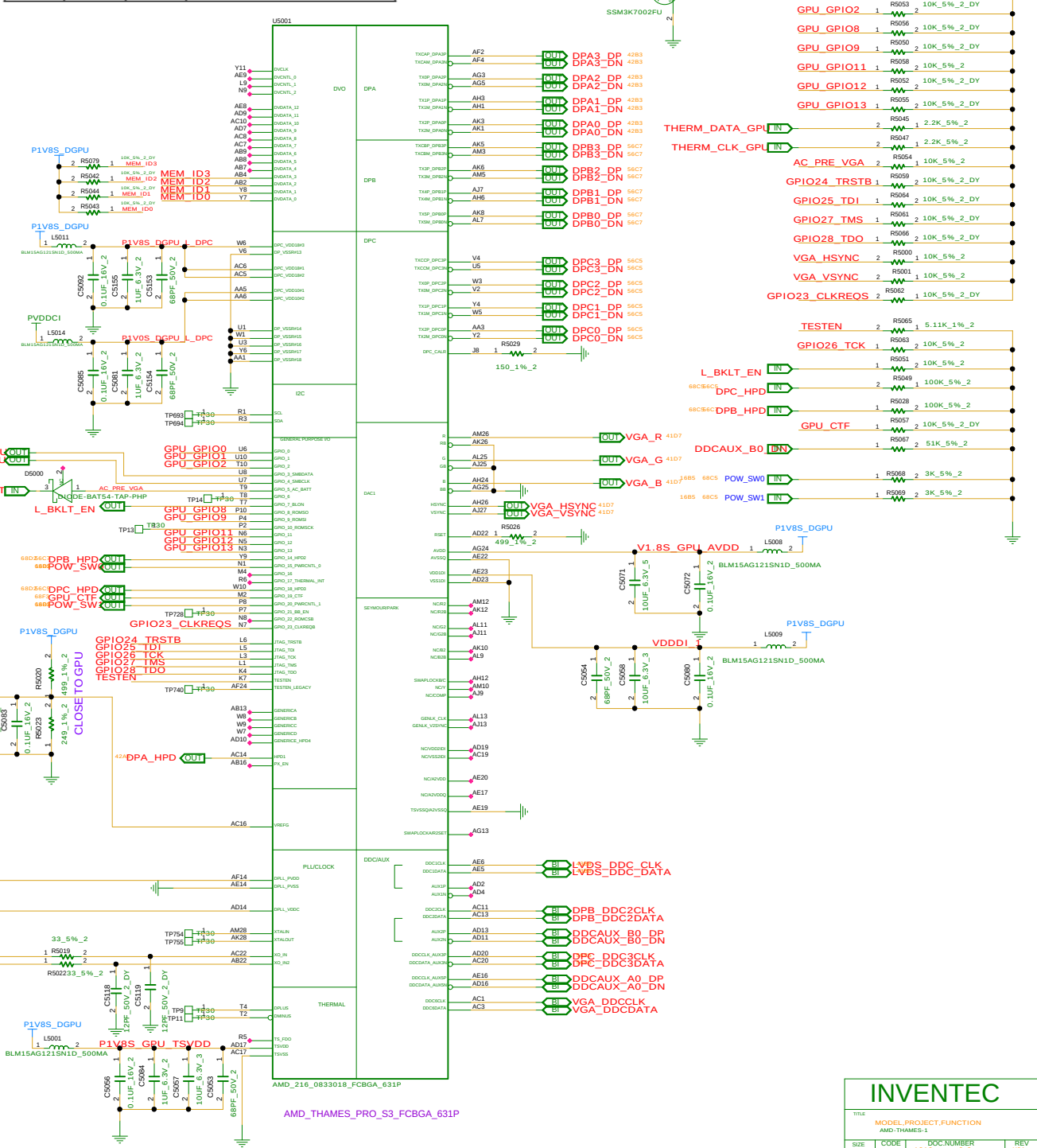
POW_SW1	POW_SW0	PVCORE_DGPU
0	0	1.10V
0	1	1.0V
1	0	0.9V
1	1	0.875V

X = DESIGN DEPENDANT

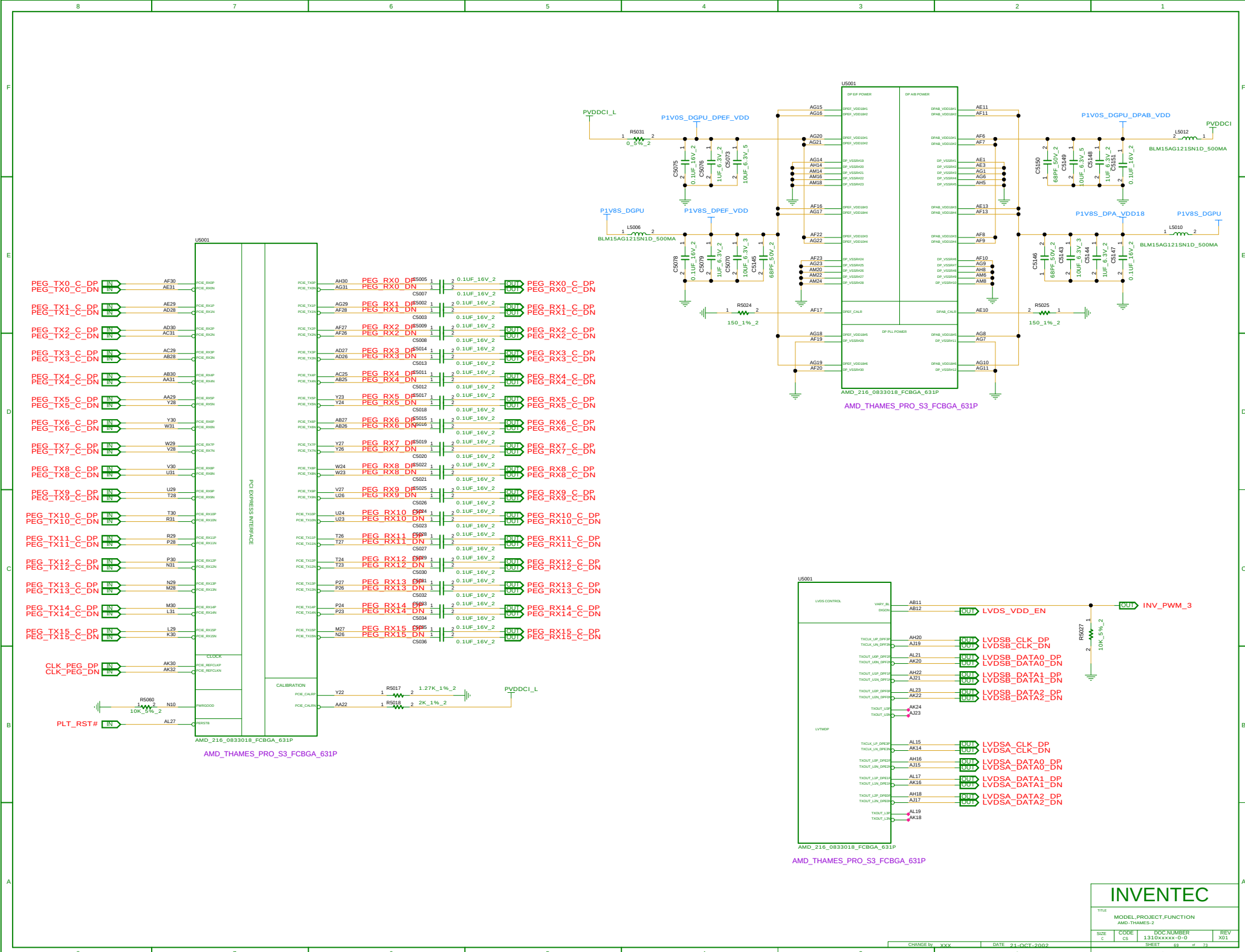
0 = DO NOT INSTALL RESISTOR

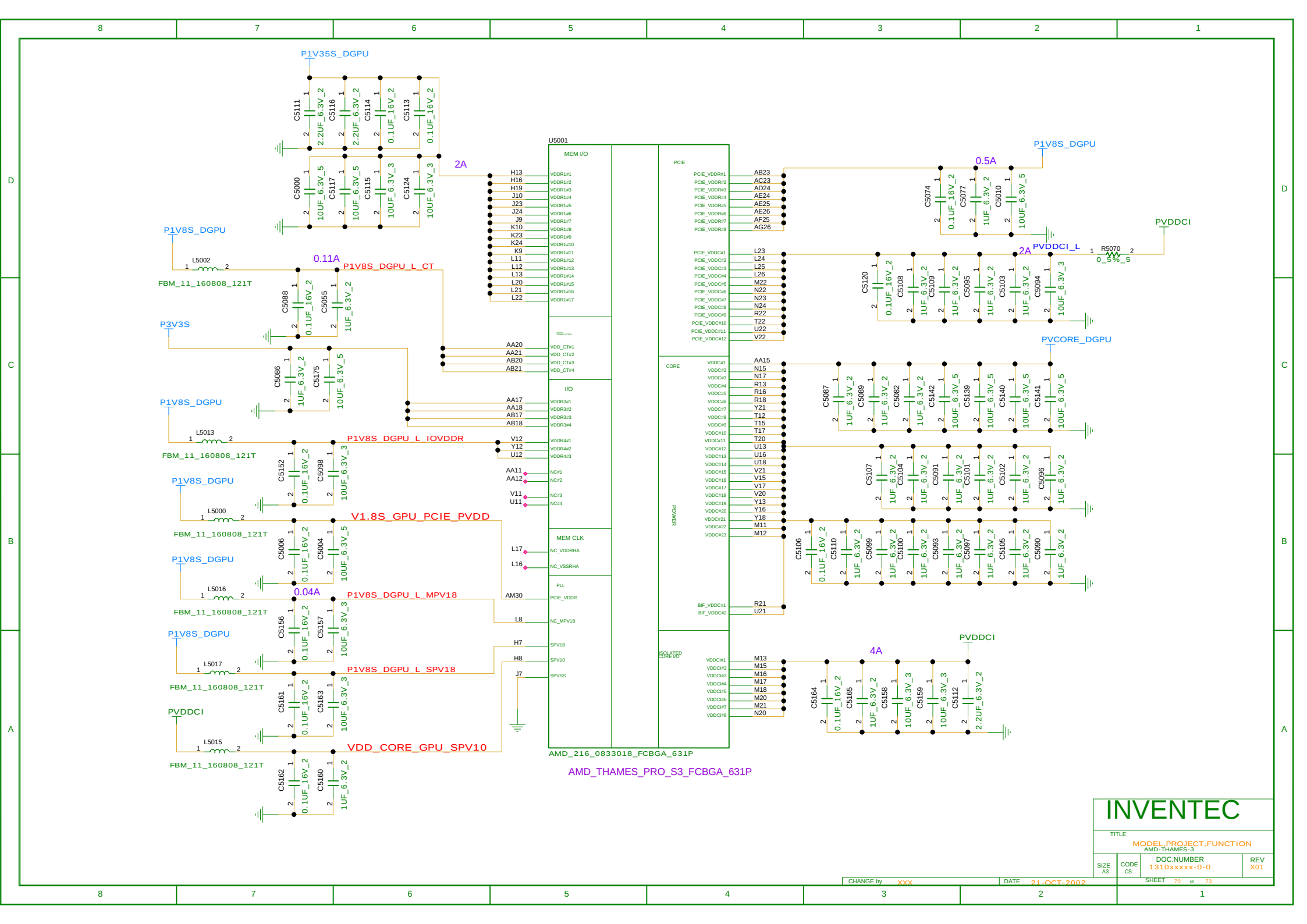
1 = INSTALL 10K RESISTOR

GPIO_13	GPIO_12	GPIO_11	MEMORY APERTURE SIZE
0	0	1	512MB/256MB memory aperture (Default)
1	1	0	Reserved



INVENTEC			
TITLE	MODEL_PROJECT_FUNCTION	AMD_THAMES_1	REV 2021
SIZE	CODE	DOCNUMBER	
E	CS	1313DXXXX-0-0	
	SHEET	84	of 94





INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
AMD-THAMES-3			
DOC.NUMBER			
1310xxxxx-0-0			
REV			
X01			
SIZE	CODE	DATE	
A3	CS	21-OCT-2002	
CHANGE by XXX			
SHEET 70 of 73			

