MODEL NAME : *ZIJI2*

PCB NO : *LA-811P*

BOM P/N : *xxxx*

ZZZ

PCB 11J LA-A811P REV0 M/B
DAA00070000

Compal Confidential

ZIJI2 Schematics Document

Intel Bay Trail

2014-06-03

REV: SOVP

BOM Structure Table

BTO Item	BOM Structure	ASM/NO ASM
@:Nopop Component	@	NO ASM
RF Reserved	@RF	NO ASM
3G WWAN SKU	3G@	ASM
WIFI Only SKU	WIFI@	ASM
EMI/ESD requirement	EMI@	ASM
RF requirement	RF@	ASM
Debug	DBG@	ASM
Debug	DBG@	NO ASM FOR SOVP,MP
Me conn	ME@	ASM
DIGI SKU	DIGI@	ASM
NFC SKU	NFC@	ASM

Revision history

Version	Date	Description
SDV	2013/06/05	* Initial release
SDV-R	2013/07/22	
FVT-2	2013/08/06	
SIT	2013/11/08	
SVT	2013/12/28	
SOVP	2014/03/17	
SOVP_0520	2014/05/20	Cut in ECR 140520_01(Richtek PW IC rework)
SOVP_0603	2014/06/03	Cut in ECR 140603_01(Remove R527)

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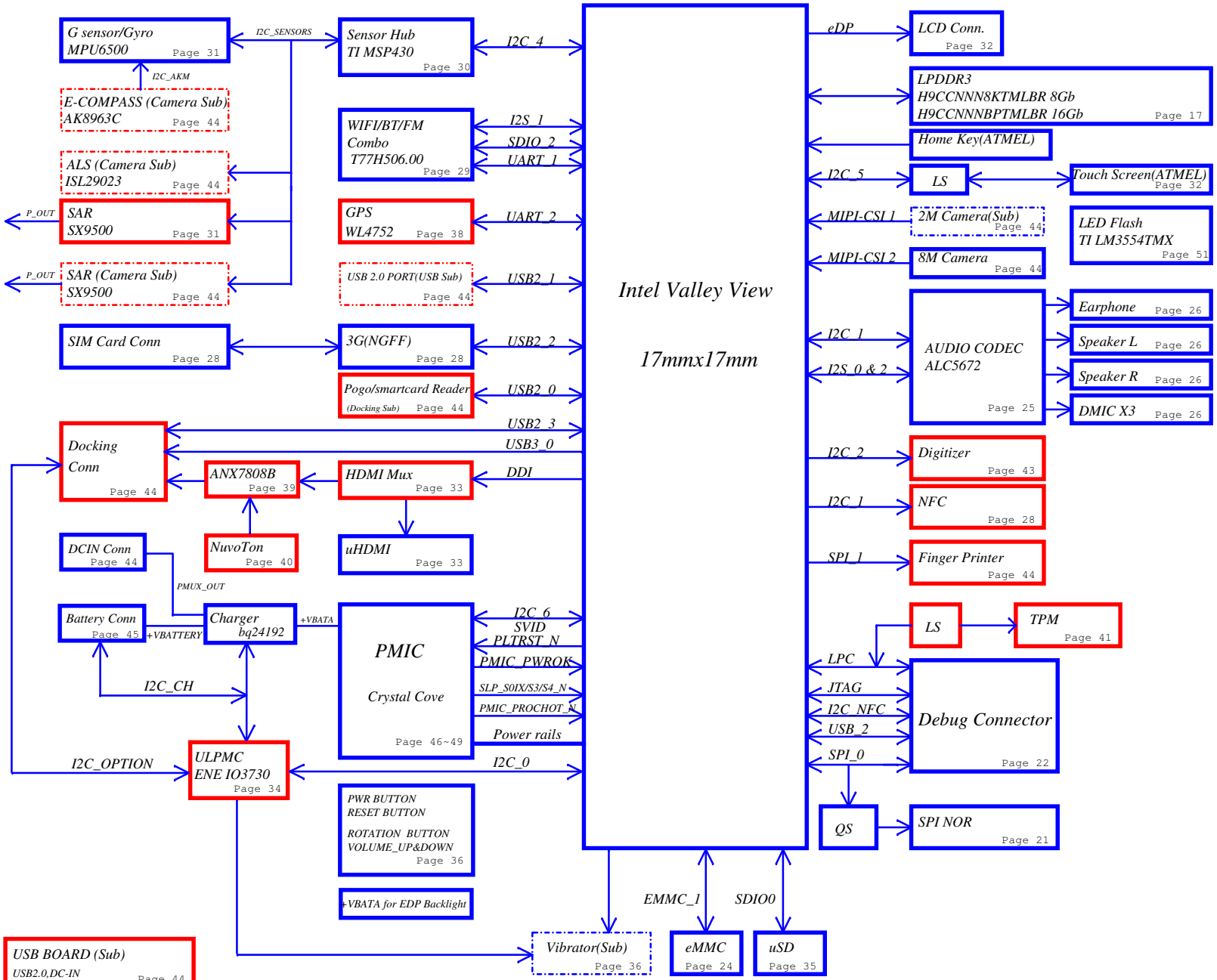
Date: Wednesday, June 04, 2014	Sheet 1 of 54		

Ella Bay Trail-T Block Diagram

LS-----Level Shifter



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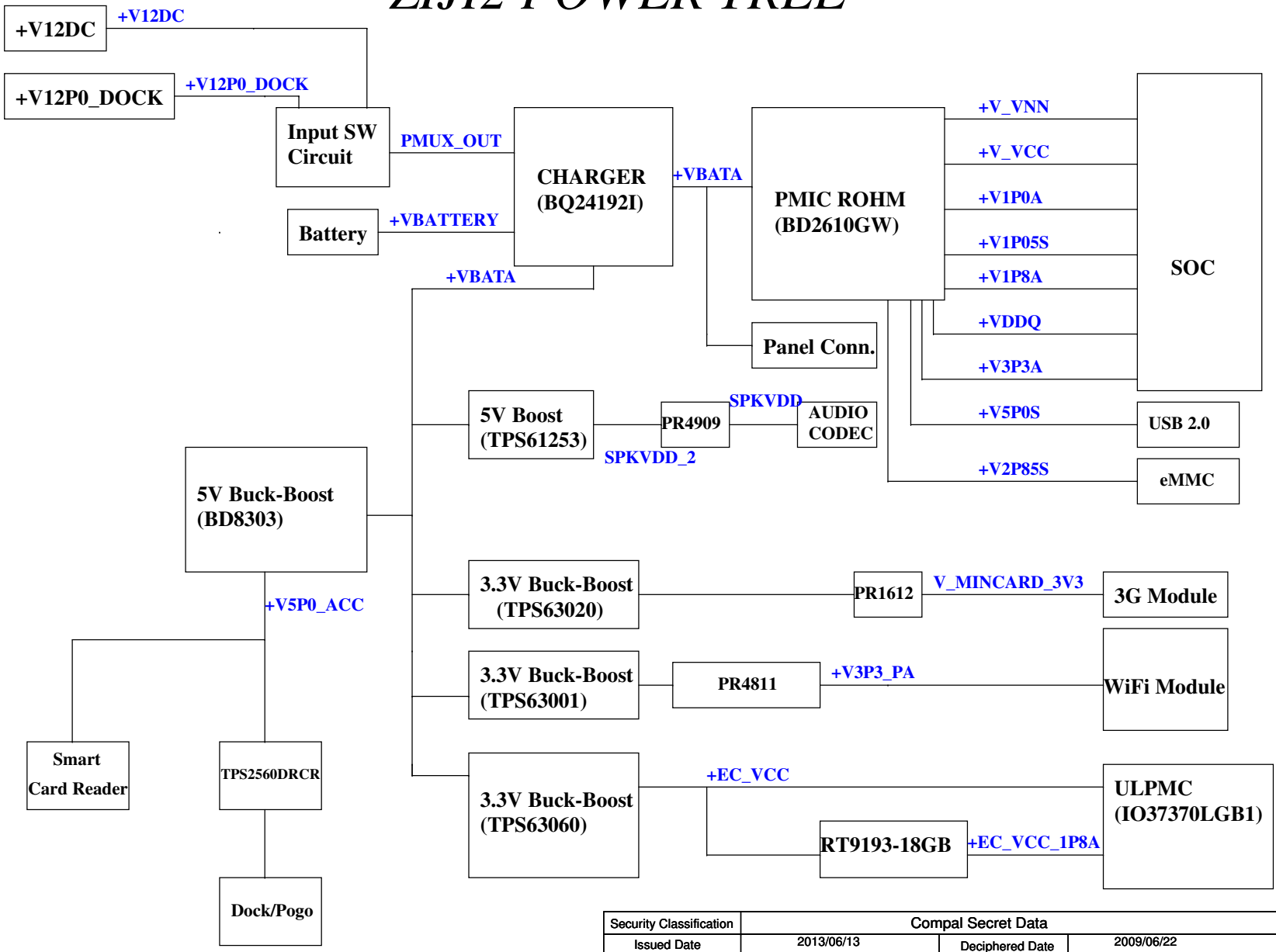
ZIJI2 POWER TREE



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Title: POWER BLOCK DIAGRAM

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Date: Wednesday, June 04, 2014 Sheet 3 of 54

V0JV3 POWER SEQUENCE

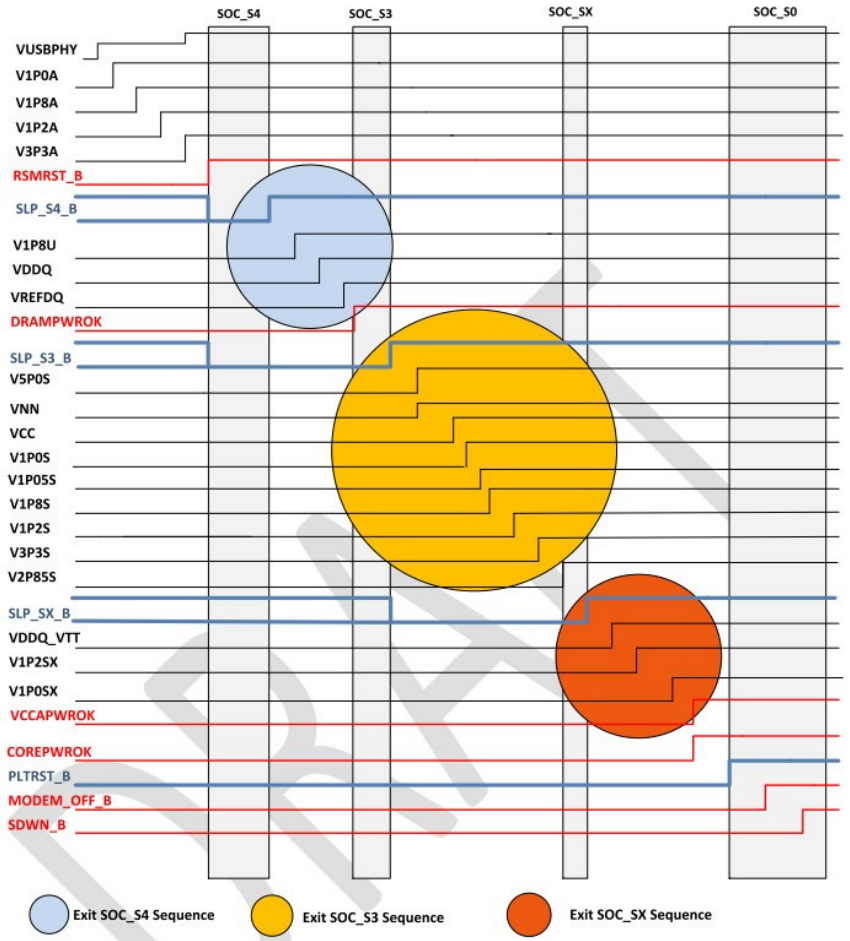

Figure 9: Cold Boot Power Sequencing Diagram

Voltage Rail ON/OFF On Various Power States

Num.	Voltage domain	Supply Module	PWR TYPE	S0	S0ix	S3	S4/5	G3
1	+VCC	VLV2	SVID BUCK	ON	OFF	OFF	OFF	OFF
2	+VNN	VLV2	SVID BUCK	ON	ON	OFF	OFF	OFF
3	+V1P0A	VLV2	BUCK	ON	ON	ON	ON	OFF
4	+V1P05S	VLV2	BUCK	ON	ON	OFF	OFF	OFF
5	+V1P8A	VLV2	BUCK	ON	ON	ON	ON	OFF
6	+VDDQ	VLV2/DDR	BUCK	ON	ON	ON	OFF	OFF
7	+V3P3A	VLV2, Peripheral	BUCK-BOOST	ON	ON	ON	ON	OFF
8	+V2P85S	Peripheral	BUCK-BOOST	ON	ON	OFF	OFF	OFF
9	+V5P0S	Peripheral	BOOST	ON	ON	OFF	OFF	OFF
10	+VSDIO	Peripheral	FET SW	ON/OFF	ON/OFF	OFF	OFF	OFF
11	+VDDQ_VTT	DDR3	LDO	ON	OFF	OFF	OFF	OFF
12	+V1P2A	VLV2	LDO	ON	ON	ON	ON	
13	+VREF		LDO					
14	+VUSBPHY	Peripheral	FET SW	ON	ON	ON	ON	ON
15	+VSYS_U	Peripheral	EXT FET SW	ON/OFF	ON/OFF	ON	OFF	OFF
16	+VSYS_S		FET SW	ON/OFF	ON/OFF	OFF	OFF	OFF
17	+V1P2S		FET SW	ON	ON	OFF	OFF	OFF
18	+V1P2SX		FET SW	ON	OFF	OFF	OFF	OFF
19	+V1P8S	VLV2	FET SW	ON	ON	OFF	OFF	OFF
20	+V1P8SX		FET SW	ON/OFF	ON/OFF	OFF	OFF	OFF
21	+V2P85SX	Peripheral	FET SW	ON/OFF	ON/OFF	OFF	OFF	OFF
22	+V3P3U	Peripheral	EXT FET	ON/OFF	ON/OFF	ON	OFF	OFF
23	+VHDMI	Peripheral	FET SW	ON/OFF	ON/OFF	OFF	OFF	OFF
24	+V1P8U	Peripheral	EXT FET SW	ON	ON	ON	OFF	OFF
25	+V1P0S	VLV2	EXT FET SW	ON	ON	OFF	OFF	OFF
26	+V1P0SX	VLV2	EXT FET SW	ON	OFF	OFF	OFF	OFF
27	+V3P3S	VLV2	EXT FET SW	ON	ON	OFF	OFF	OFF
28	+VBUS	Peripheral	EXT SW	ON/OFF	ON/OFF	OFF	OFF	OFF
29	+VSYS_SX	Peripheral	EXT FET SW	ON/OFF	ON/OFF	OFF	OFF	OFF
30	+VREFDQ0		LDO	ON	ON	OFF	OFF	OFF
31	+VREFDQ1		LDO	ON	ON	ON	OFF	OFF

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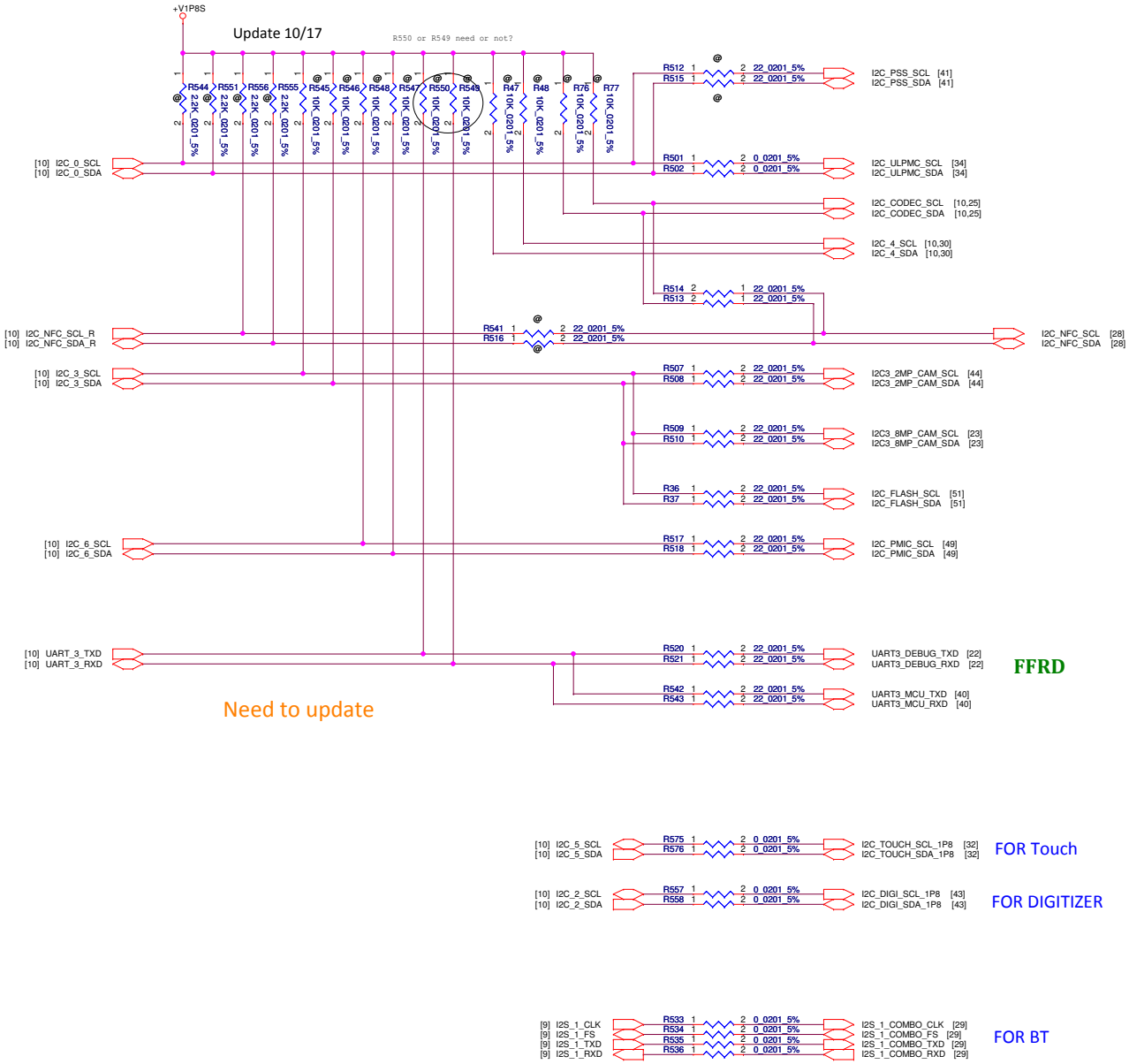
Date: Wednesday, June 04, 2014 | Sheet 4 of 54

I2C & I2S MAP



I2C Mapping	
I2C_0	ULPMC, PSS
I2C_1	Audio Codec
I2C_2	Digitizer
I2C_3	Camera
I2C_4	Sensor Hub
I2C_5	Touch Sensor
I2C_6	PMIC
I2C_NFC	NFC, Debug

	SOC Port#	ZIJI2
USB3.0	0	
	DEV 0	
USB2.0	0	USB2.0 PORT:Cradle
	1	STD USB
	2	NGFF
	3	POGO
SDIO	1	W/L
	2	micro SD
I2S	0	CODEC
	1	BT
	2	CODEC

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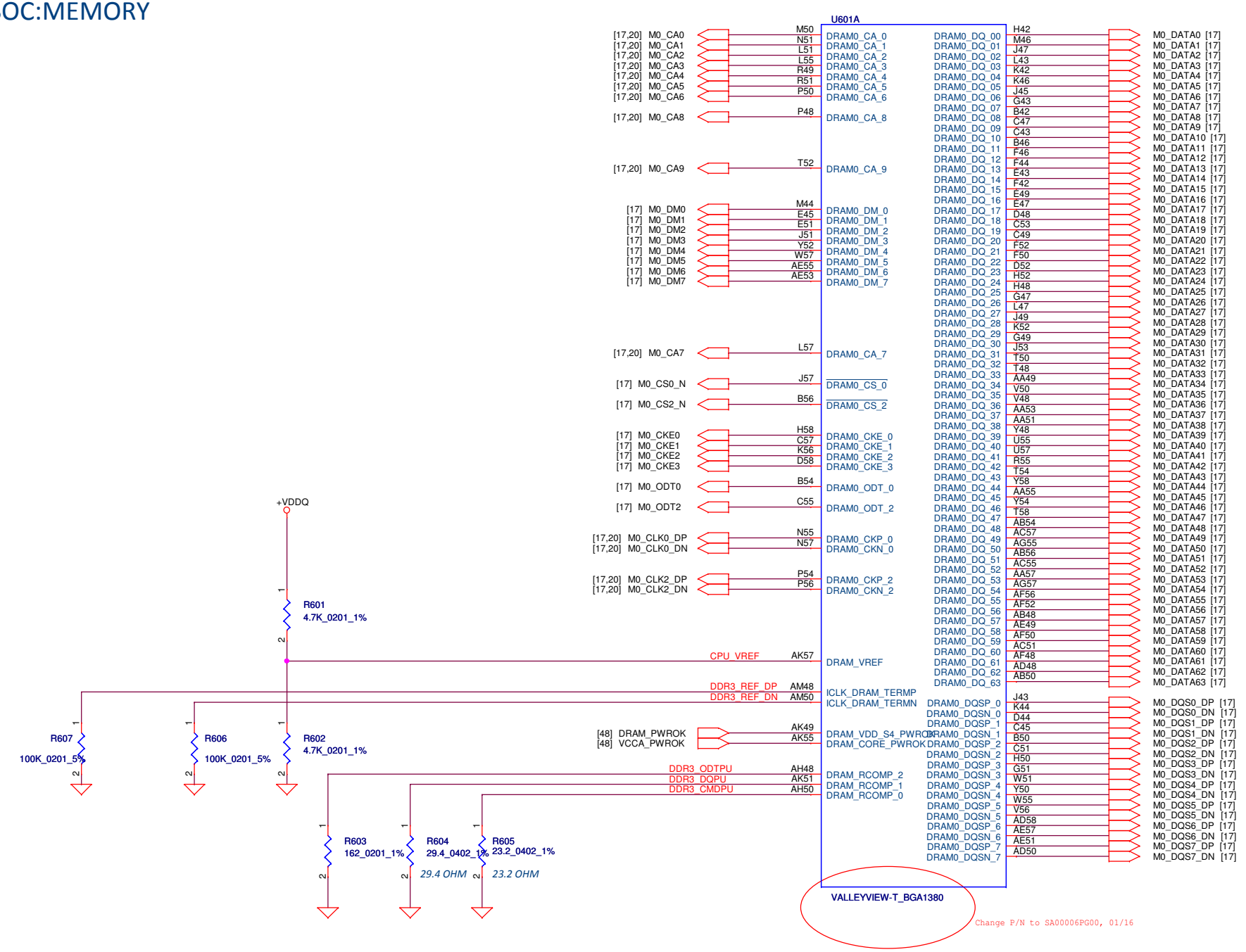
Date: Wednesday, June 04, 2014		Sheet 5 of 54	

SOC:MEMORY



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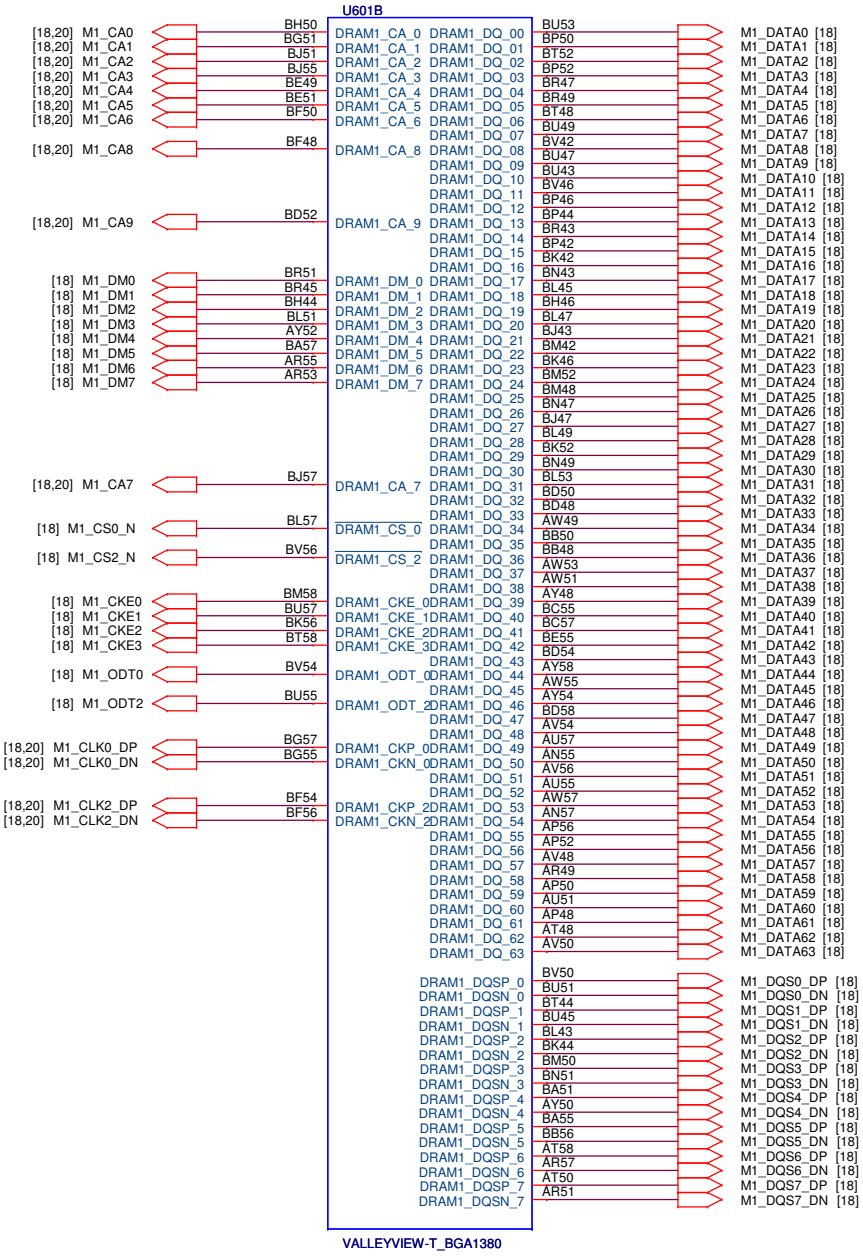
Date: Wednesday, June 04, 2014 | Sheet 6 of 54

SOC:MEMORY



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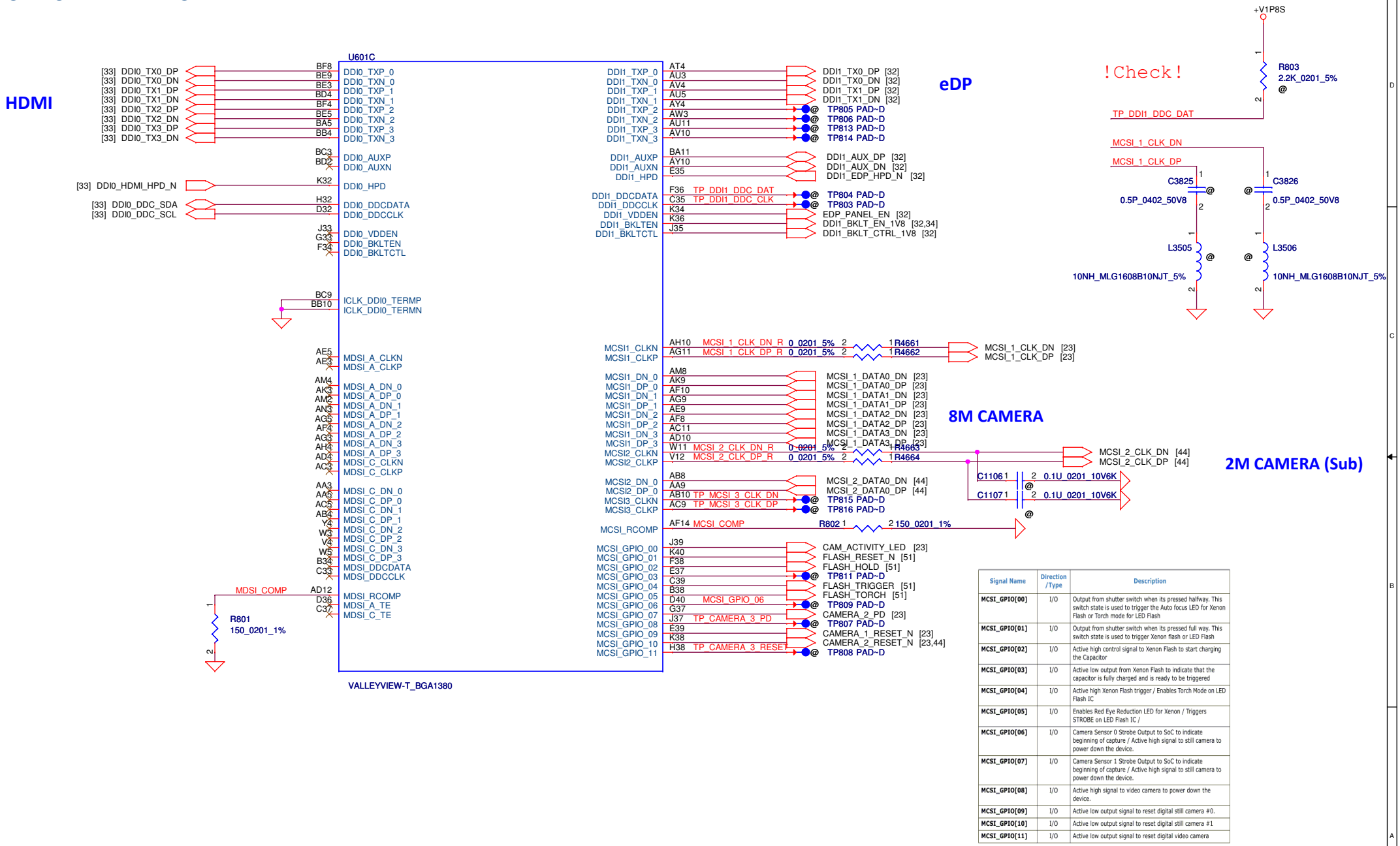
Date: Wednesday, June 04, 2014 | Sheet 7 of 54

SOC: DISPLAY AND CAMERA



Signal Name	Direction /Type	Description
MCSI_GPIO[00]	I/O	Output from shutter switch when its pressed halfway. This switch state is used to trigger the Auto focus LED for Xenon Flash or Torch mode for LED Flash
MCSI_GPIO[01]	I/O	Output from shutter switch when its pressed full way. This switch state is used to trigger Xenon flash or LED Flash
MCSI_GPIO[02]	I/O	Active high control signal to Xenon Flash to start charging the Capacitor
MCSI_GPIO[03]	I/O	Active low output from Xenon Flash to indicate that the capacitor is fully charged and is ready to be triggered
MCSI_GPIO[04]	I/O	Active high Xenon Flash trigger / Enables Torch Mode on LED Flash IC
MCSI_GPIO[05]	I/O	Enables Red Eye Reduction LED for Xenon / Triggers STROBE on LED Flash IC /
MCSI_GPIO[06]	I/O	Camera Sensor 0 Strobe Output to SoC to indicate beginning of capture / Active high signal to still camera to power down the device.
MCSI_GPIO[07]	I/O	Camera Sensor 1 Strobe Output to SoC to indicate beginning of capture / Active high signal to still camera to power down the device.
MCSI_GPIO[08]	I/O	Active high signal to video camera to power down the device.
MCSI_GPIO[09]	I/O	Active low output signal to reset digital still camera #0.
MCSI_GPIO[10]	I/O	Active low output signal to reset digital still camera #1
MCSI_GPIO[11]	I/O	Active low output signal to reset digital video camera

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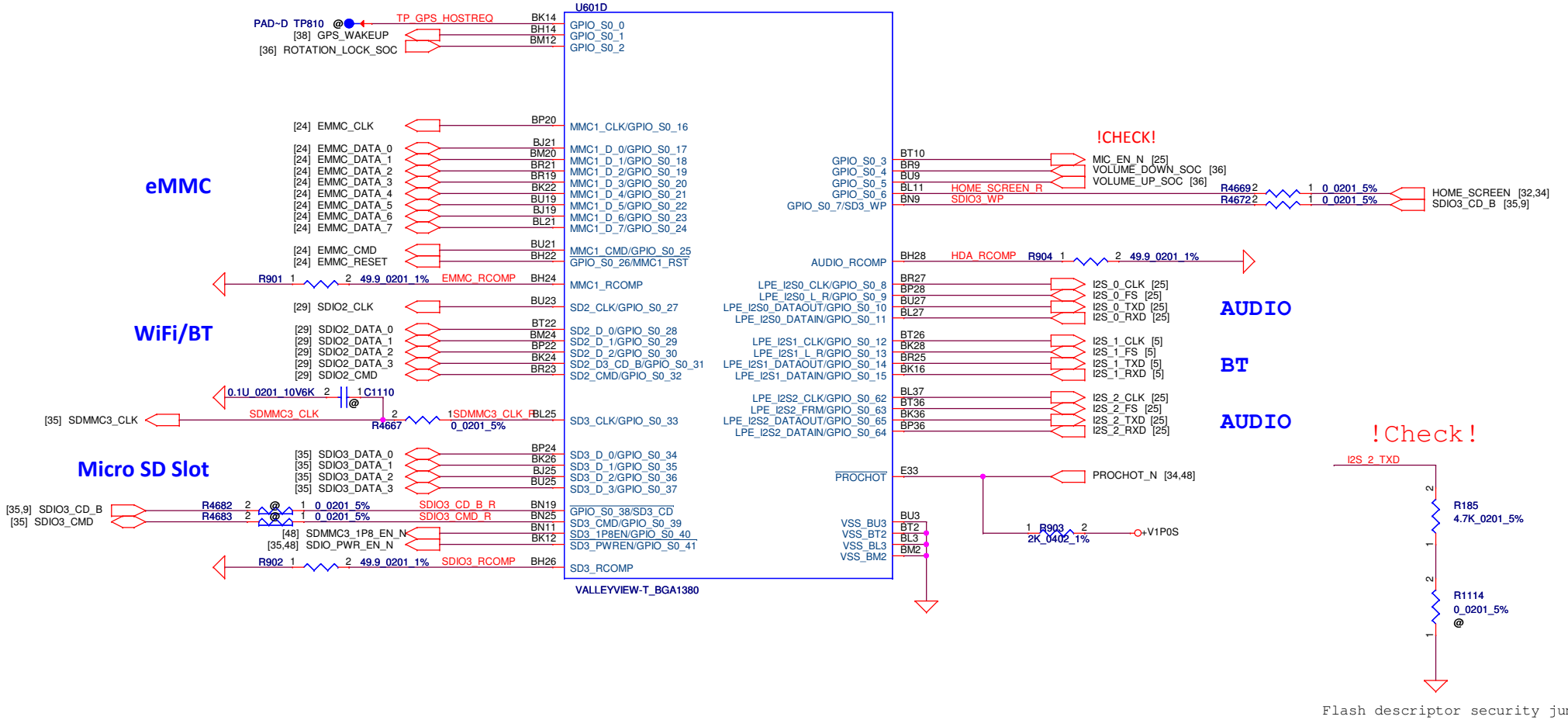
Size: Custom	Document Number: ELLA LA-A811P		Rev: 0.1
Date: Wednesday, June 04, 2014	Sheet 8 of 54		

SOC:STORAGE AND AUDIO



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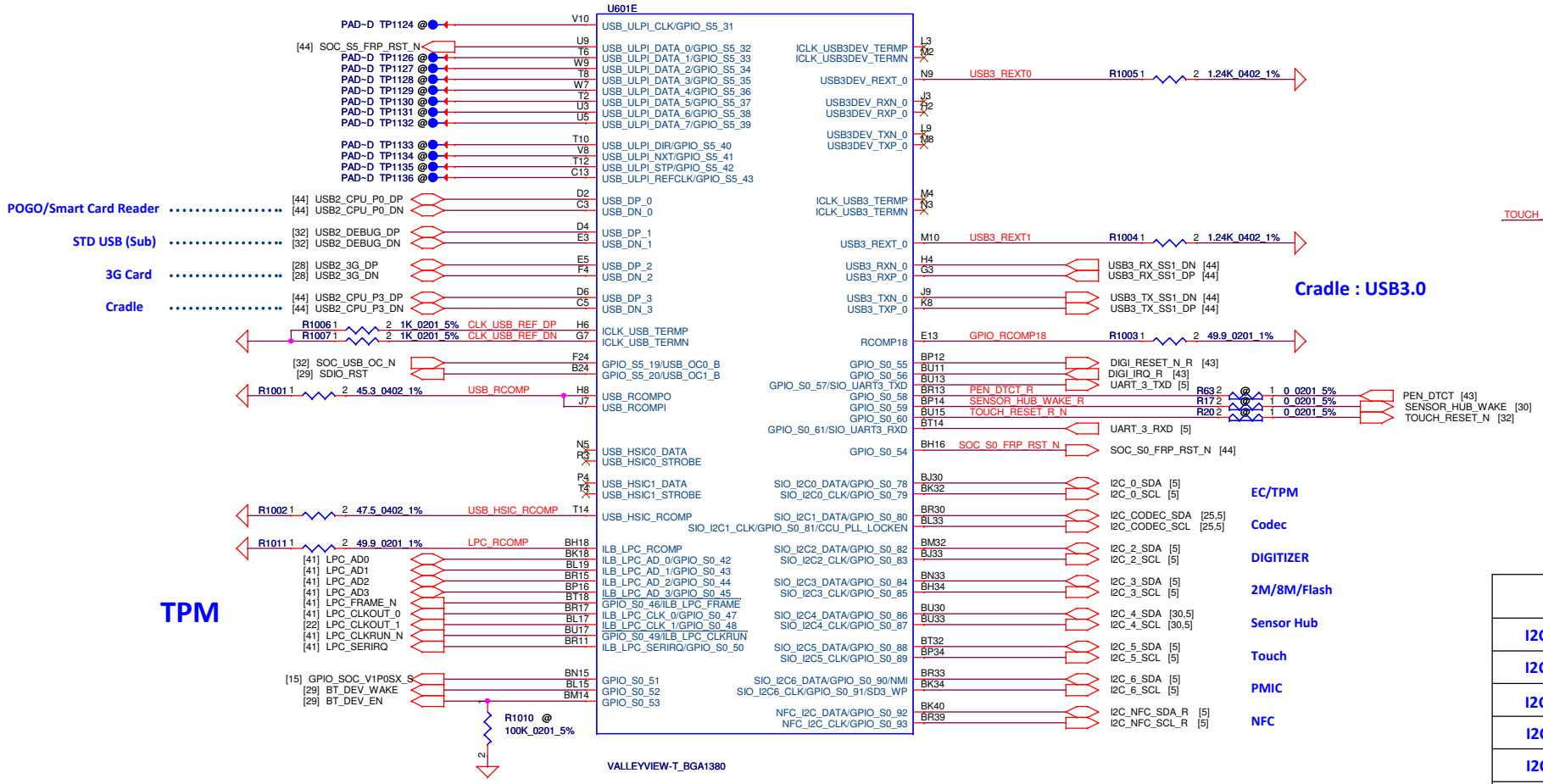
Size	Document Number	Rev
B	ELLA LA-A811P	0.1

Date: Wednesday, June 04, 2014 Sheet 9 of 54

SOC:USB & I2C



I2C Mapping

I2C_0	ULPMC, PSS
I2C_1	Audio Codec
I2C_2	Digitizer
I2C_3	Camera
I2C_4	Sensor Hub
I2C_5	Touch Sensor
I2C_6	PMIC
I2C_NFC	NFC, Debug

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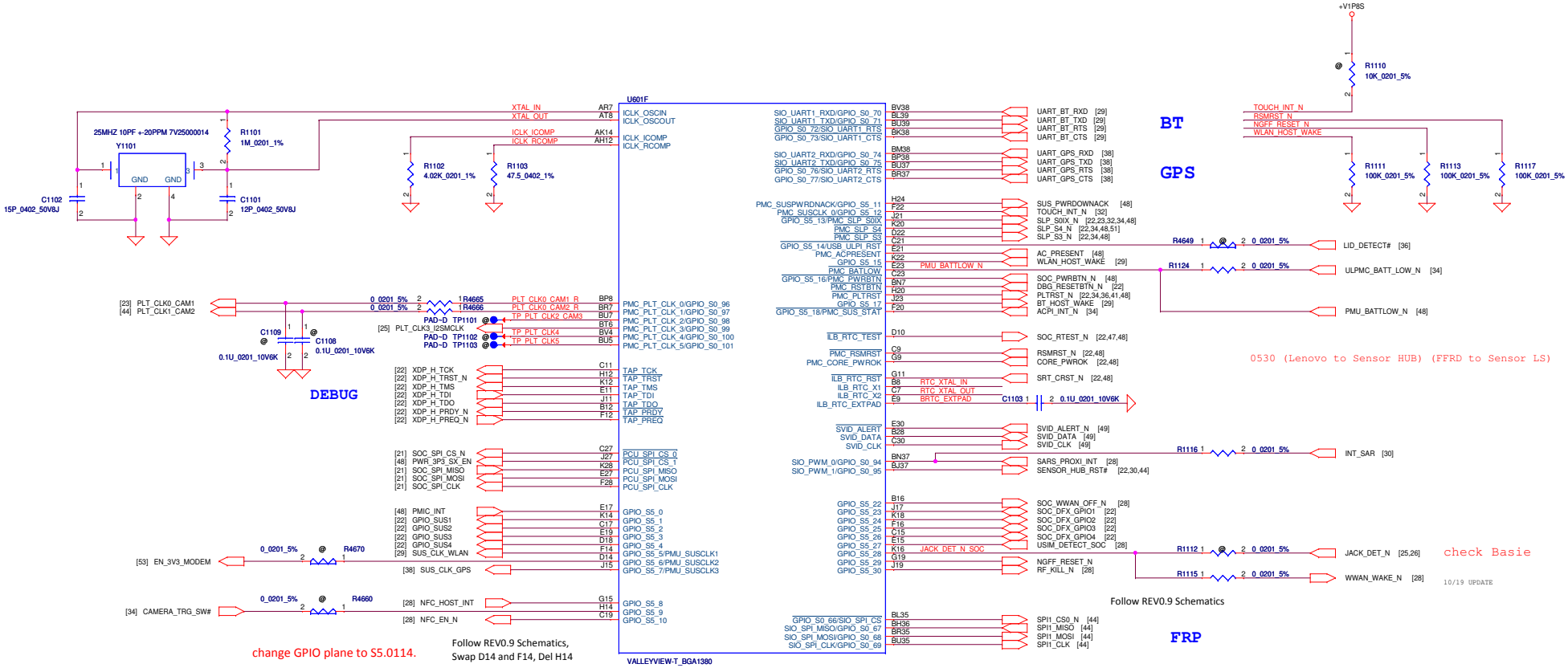
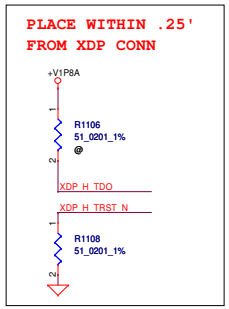
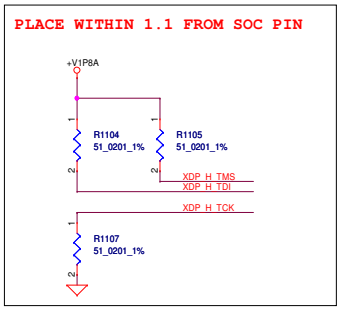
Date: Wednesday, June 04, 2014 | Sheet 10 of 54

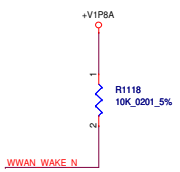
SOC:CLOCK,DEBUG & MISC

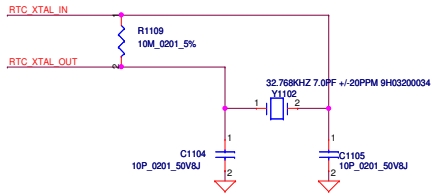


PMC_PLT_CLK Usage
CLK0 CAM 8MP 19.2MHz
CLK1 CAM 2MP 19.2MHz
CLK2 NC
CLK3 Audio 19.2/25MHz
CLK4 NC
CLK5 NC









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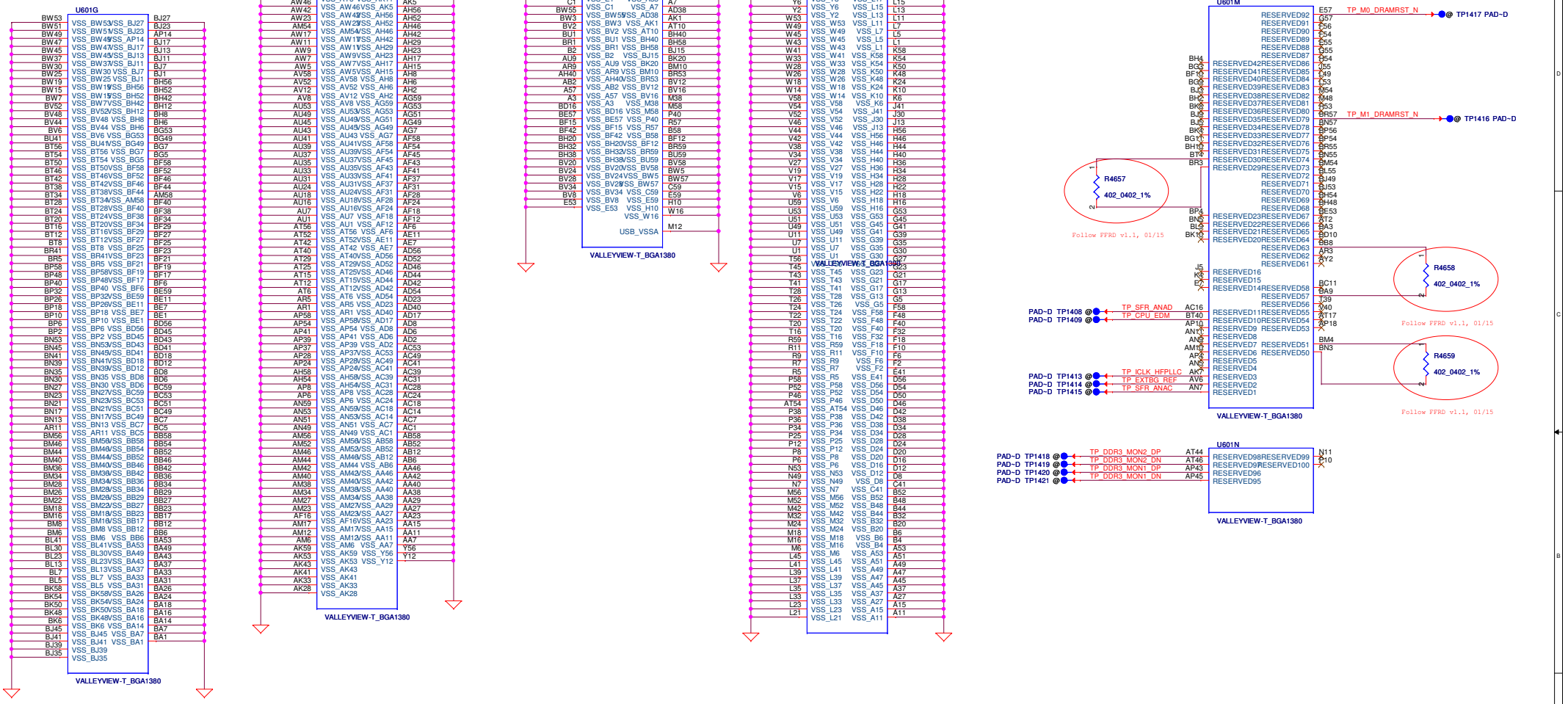
Size	Document Number	Rev
C	ELLA LA-A811P	0.1

Date: Wednesday, June 04, 2014 | Sheet 11 of 54

SOC:GND



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Title: SOC (7 of 9)

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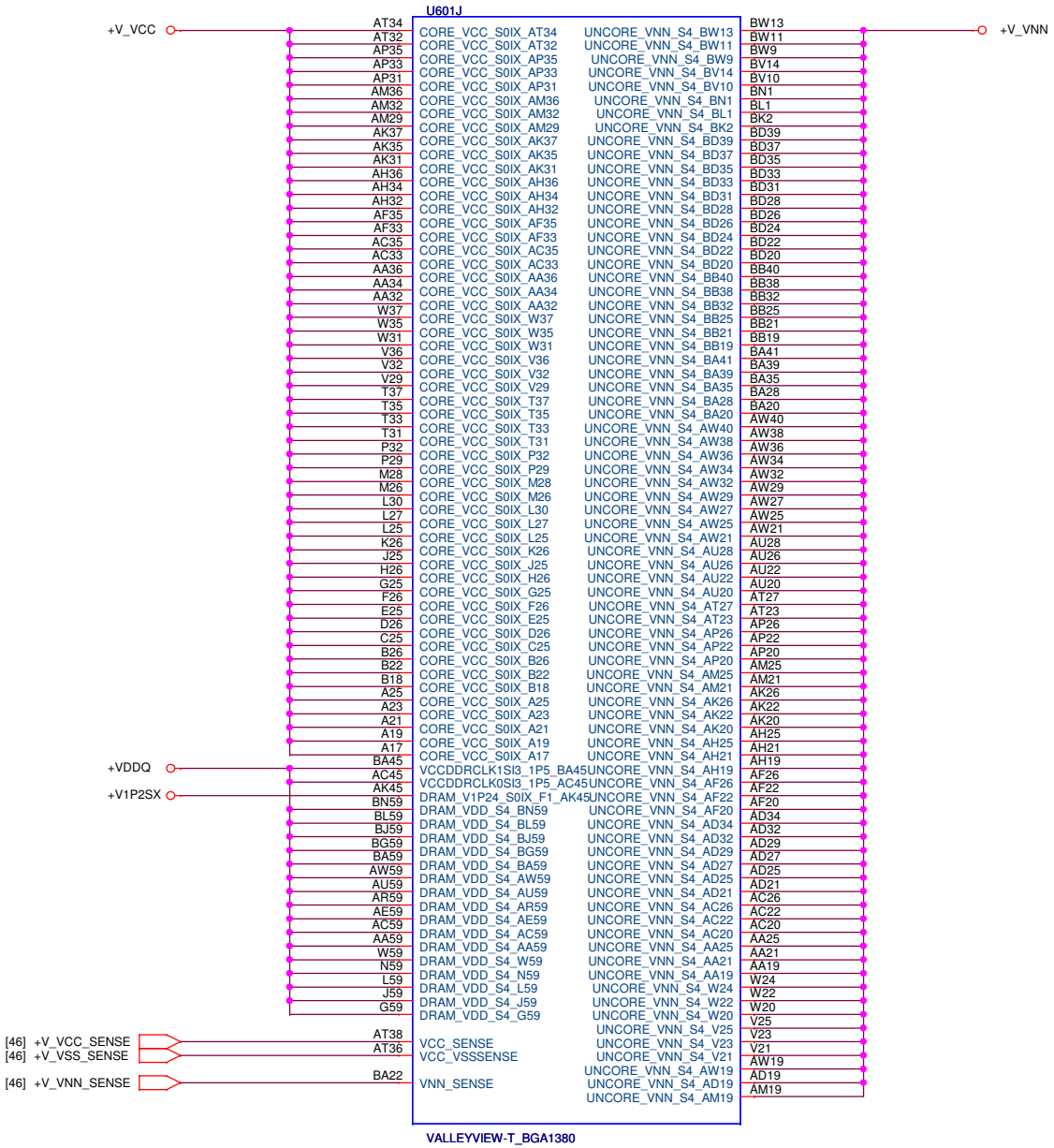
Date: Wednesday, June 04, 2014 | Sheet 12 of 54

SOC:POWER



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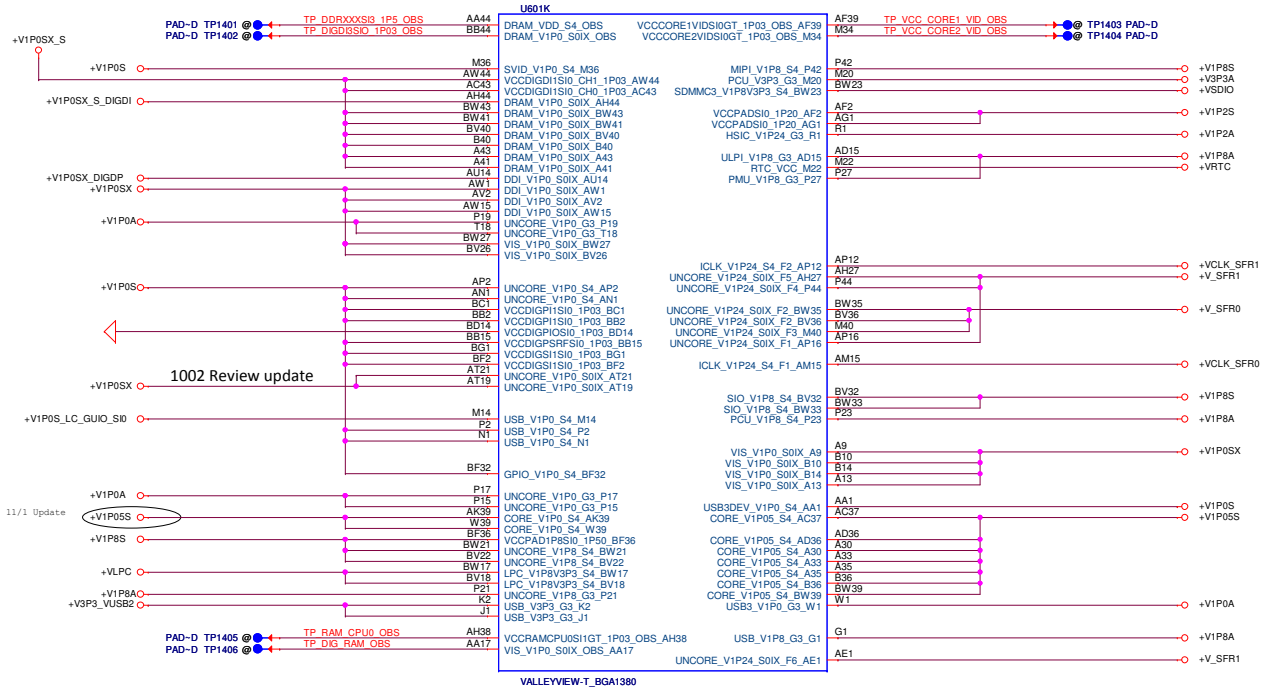
Date: Wednesday, June 04, 2014 | Sheet 13 of 54

SOC: POWER



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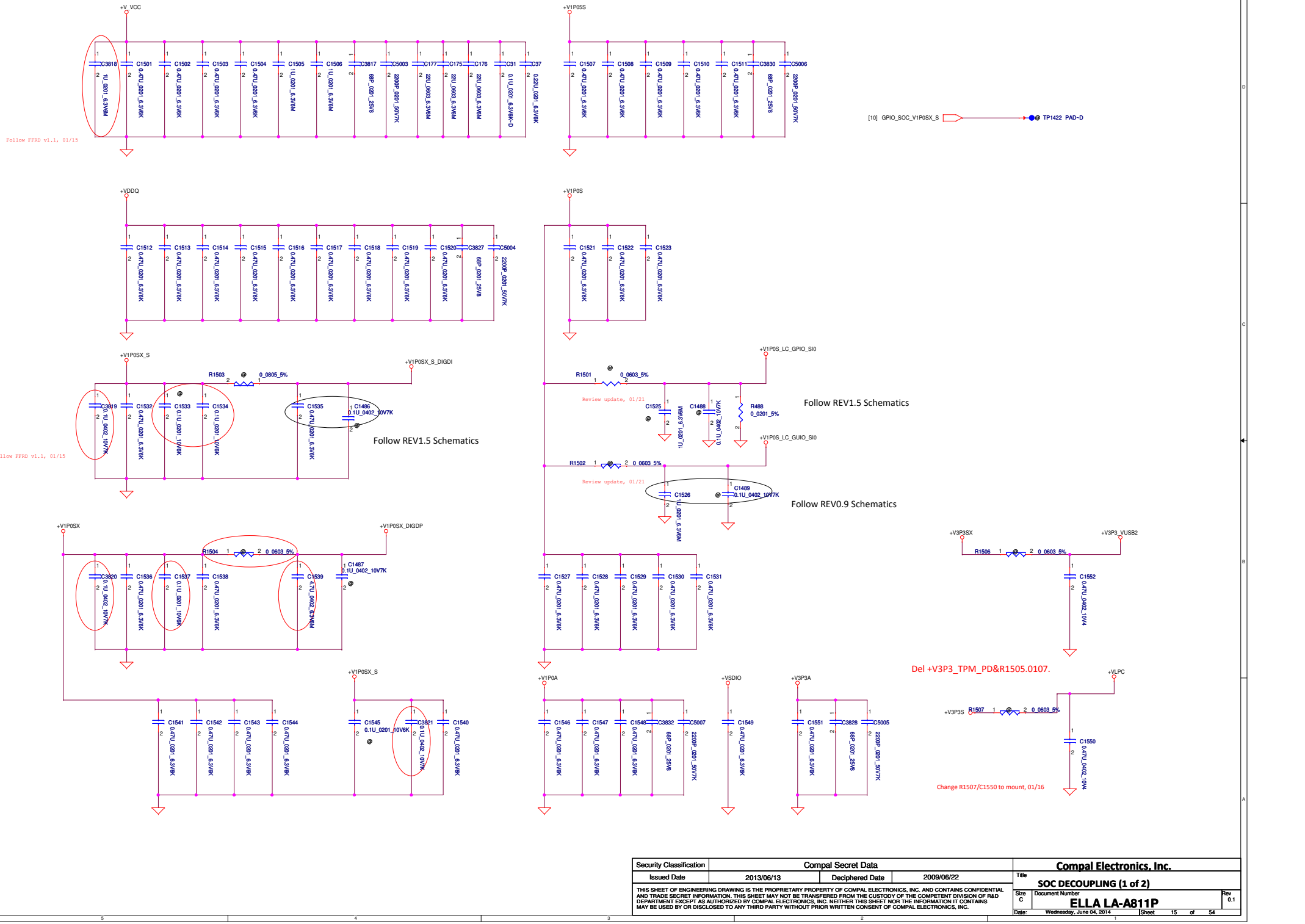
Date: Wednesday, June 04, 2014 | Sheet 14 of 54

SOC DECOUPLING



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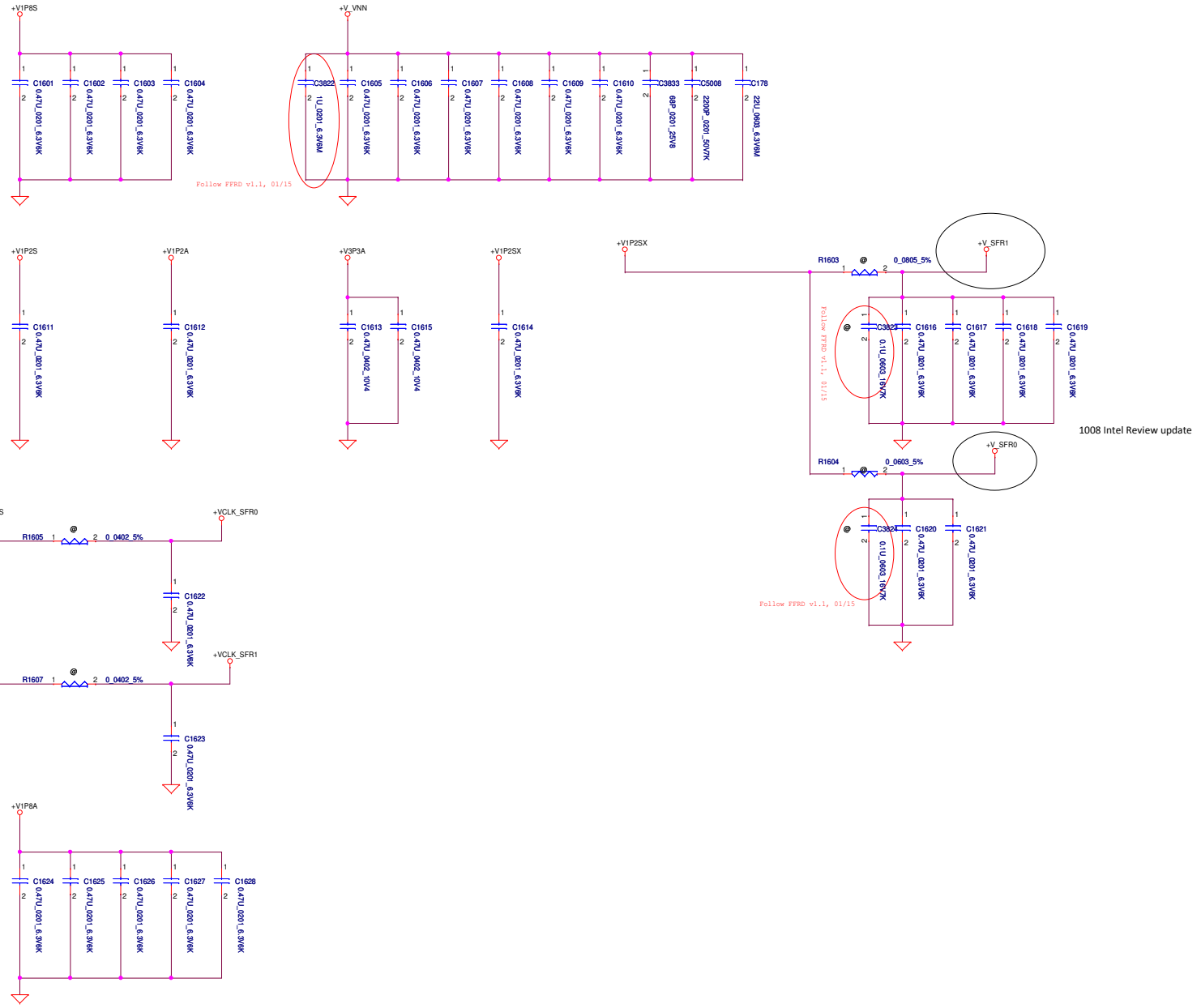
Date: Wednesday, June 04, 2014		Sheet 15 of 54	

SOC DECOUPLING



1008 Intel Review update

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Title: **SOC DECOUPLING (2 of 2)**

Size	Document Number	Rev
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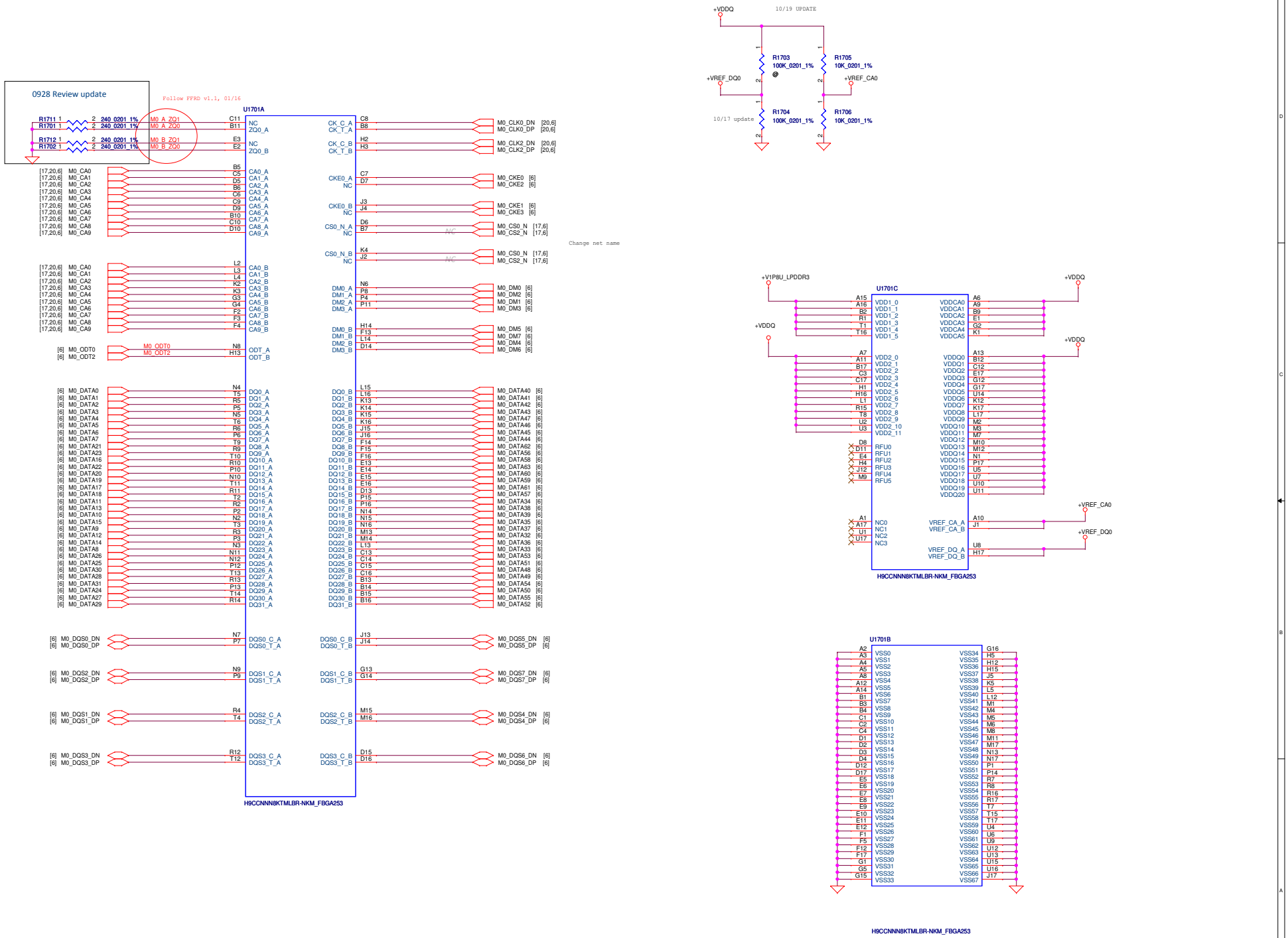

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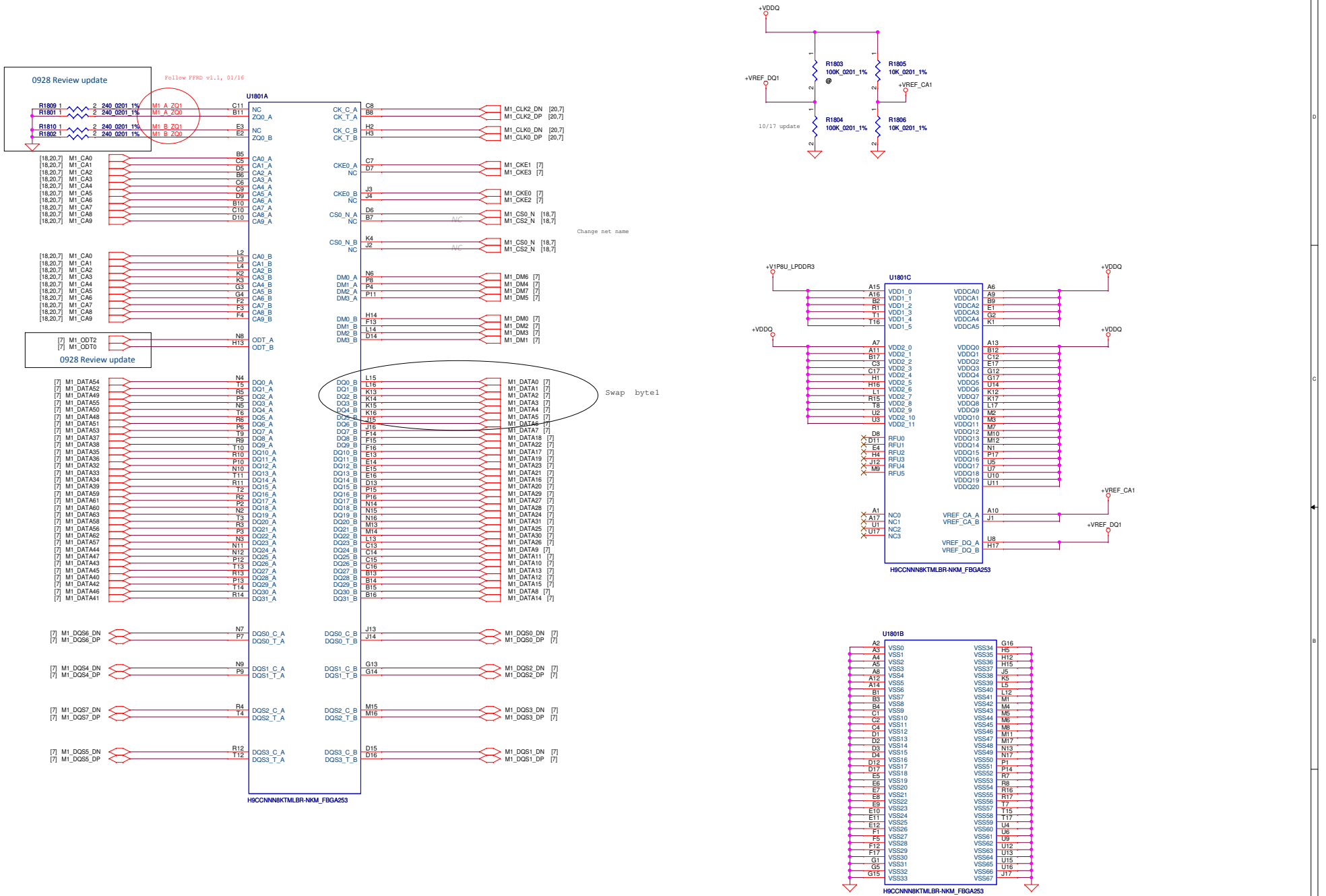

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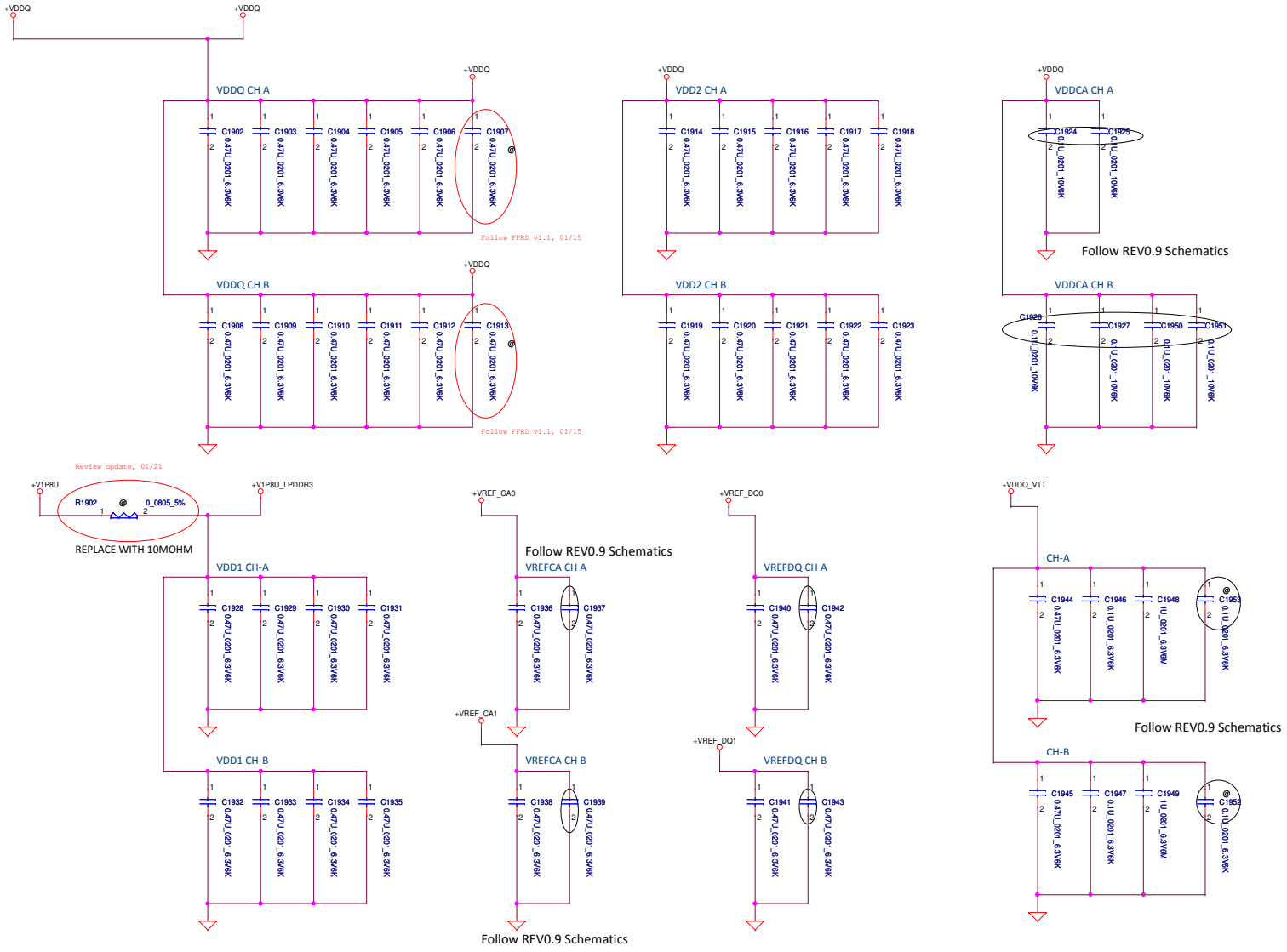
Date: Wednesday, June 04, 2014 Sheet 18 of 54



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Title: **LPDDR3 DECAP**

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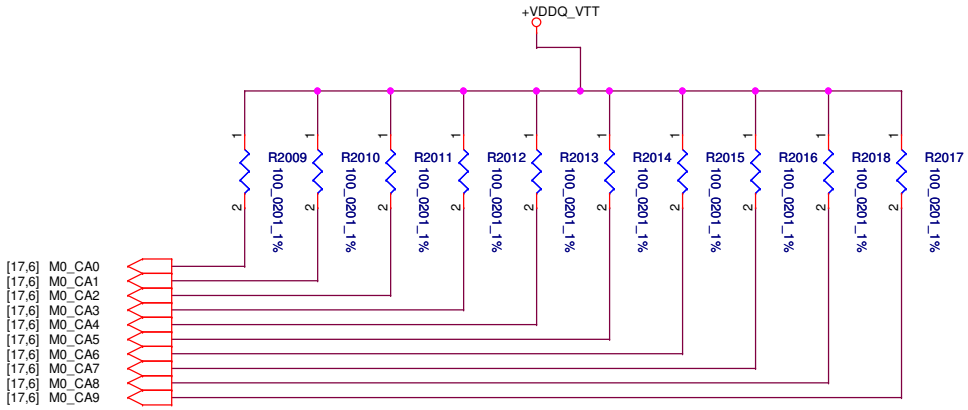
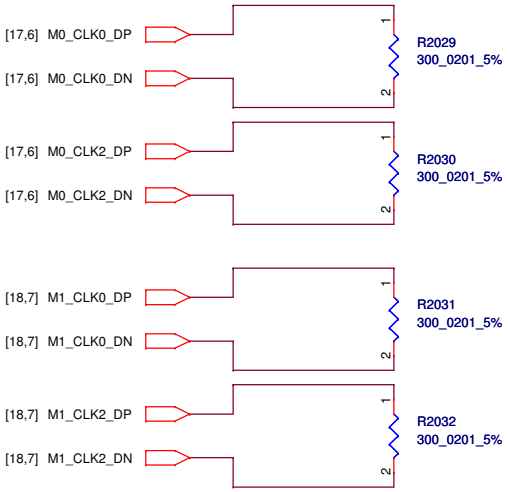
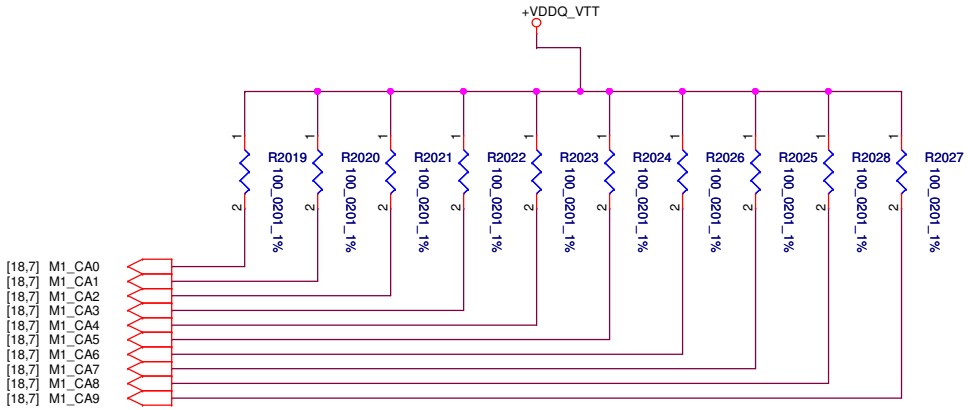
Date: Wednesday, June 04, 2014 Sheet 19 of 54

0928 Review update
Remove seris termination for CKE







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Compal Electronics, Inc.

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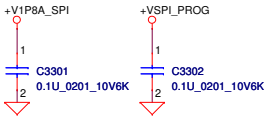
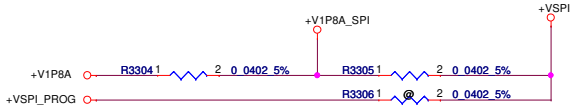
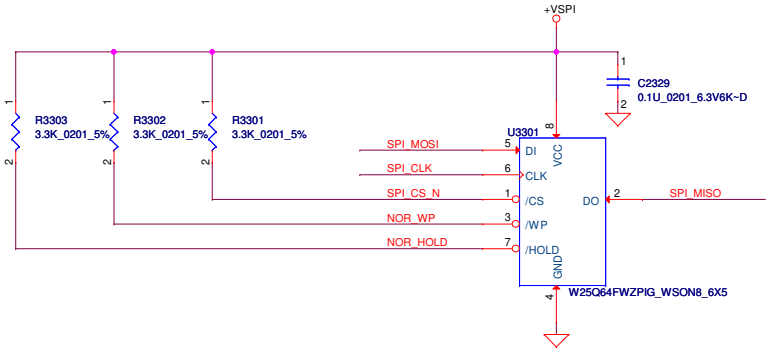
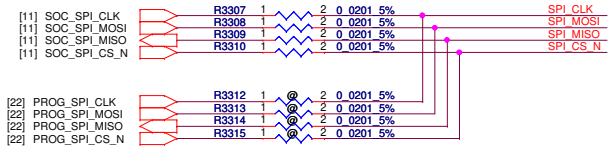
Size: B | Document Number: **ELLA LA-A811P** | Rev: 0.1

Date: Wednesday, June 04, 2014 | Sheet 20 of 54









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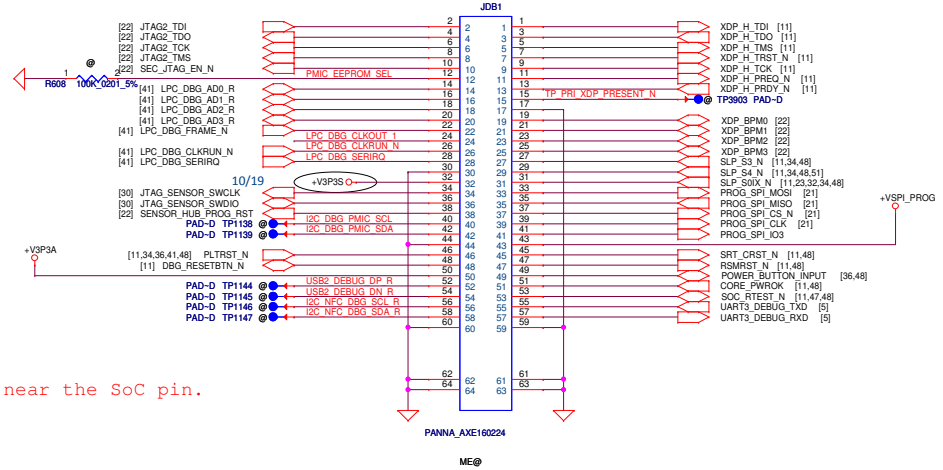
Date: Wednesday, June 04, 2014	Sheet 21 of 54	

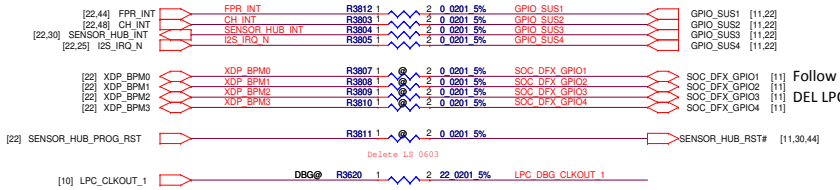
WW39.4 : CHANGED THE CONNECTIONS ON PIN 10 AND 12

Follow REV1.2



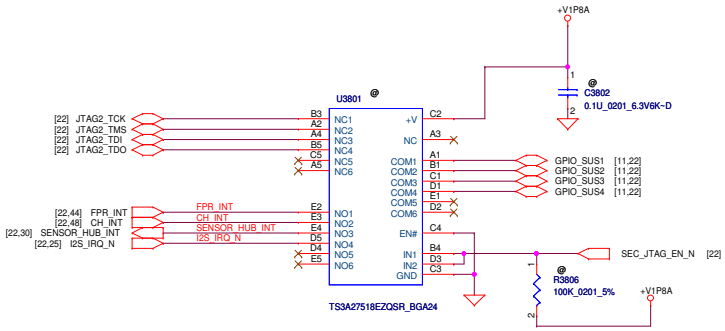
R3819,R3820 ※Places near the SoC pin.

Follow REV0.9 Schematics change net name



Follow REV0.9 Schematics, 0->22, DEL LPC DBG RESET R N

DBG@ NO-ASM for MP



IN1 / IN2	NC TO COM, COM TO NC	NO TO COM, COM TO NO
L	ON	OFF
H	OFF	ON

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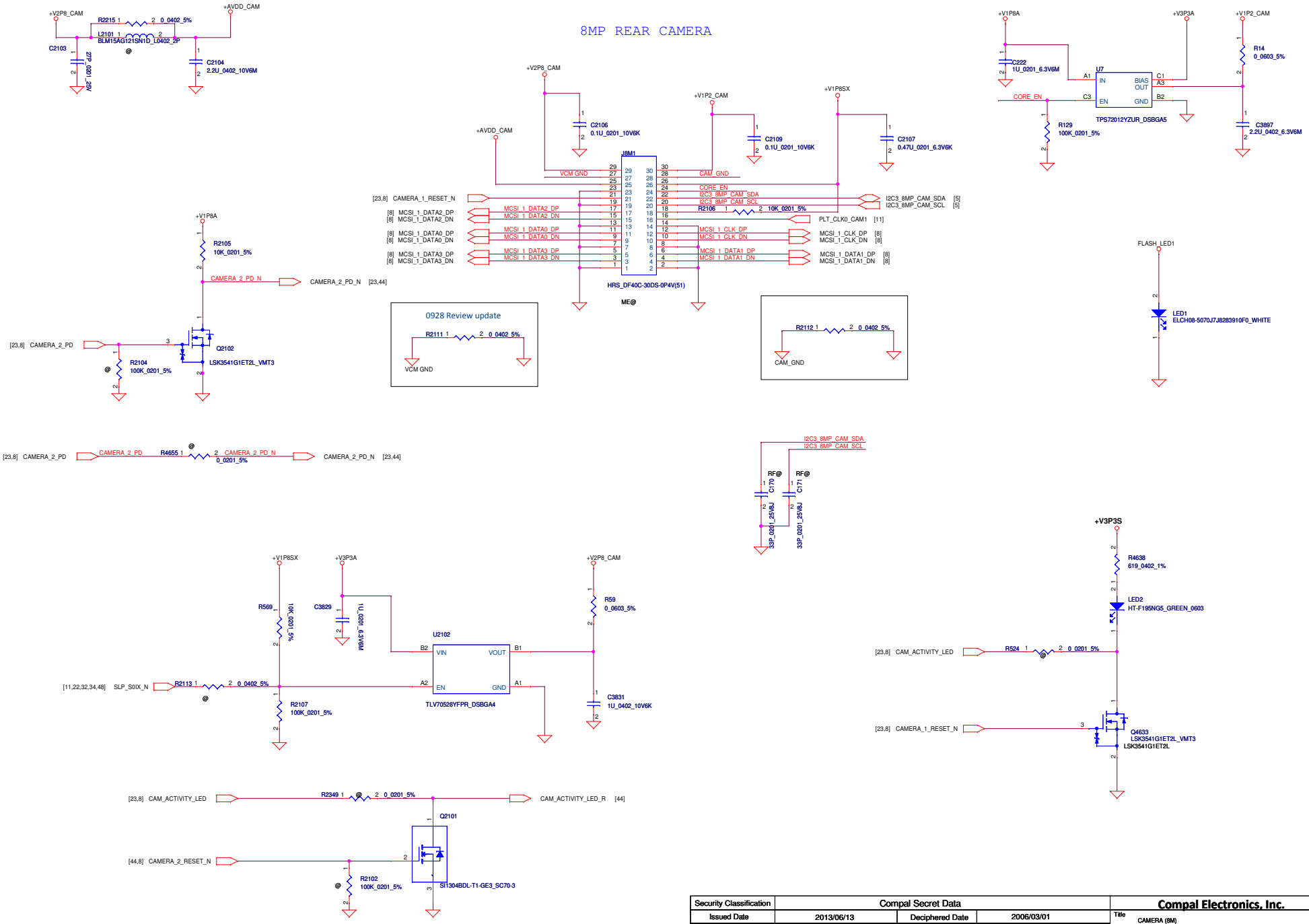
Title	DEBUG		
Size C	Document Number ELLA LA-A811P		Rev 0.1
Date: Wednesday, June 04, 2014		Sheet 22 of 54	

8MP REAR CAMERA



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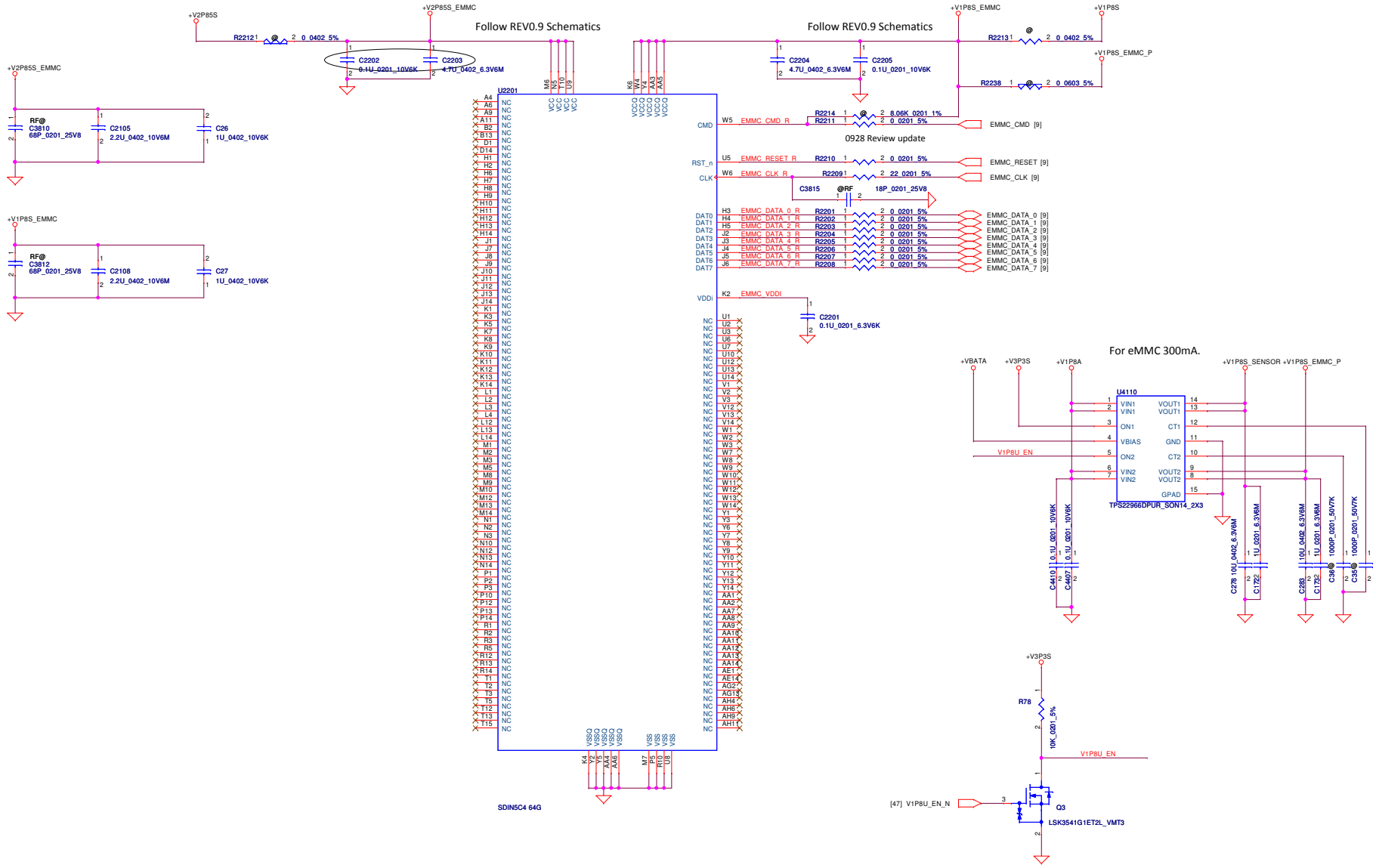
Size C	Document Number ELLA LA-A811P		Rev 0.1
Date: Wednesday, June 04, 2014		Sheet 23 of 54	



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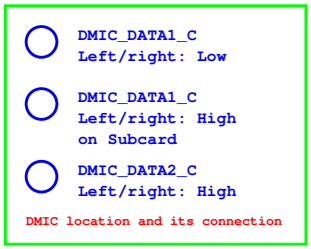
			Date: Wednesday, June 04, 2014; Sheet 24 of 54

Codec

(Lenovo) remove R4671 R4672, ensure impedance on it lower than 0.1ohm

Add layout notice:
put jack as closer as possible to codec
width of HP_MIC_OUT and HP_MIC_OUT2 at least 40mil

(Lenovo) change pin4 to DMIC_DATA1_C and pin5 to DMIC_DATA2_C 0608



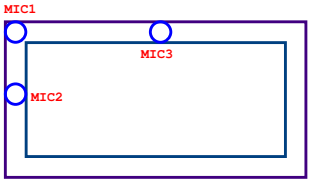


TABLE AUDIO CODEC

U2301	R1
ALC5645	ASM
ALC5672	NO_ASM

(Lenovo) C2318 modify to 4.7u from 10U 0607

Follow REV0.9 Schematics, change R2332 to stuff

Follow REV0.9 Schematics, ADD C2331

Need to check and update to TC7SB66FU

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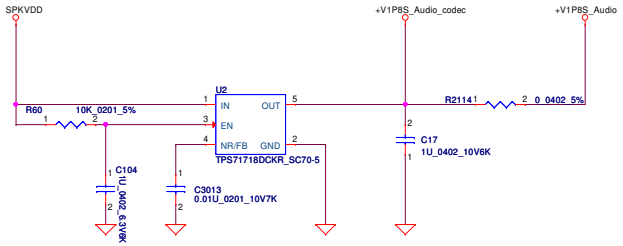
Title: **AUDIO CODEC**

Size	Document Number	Rev
C	ELLA LA-A811P	0.1

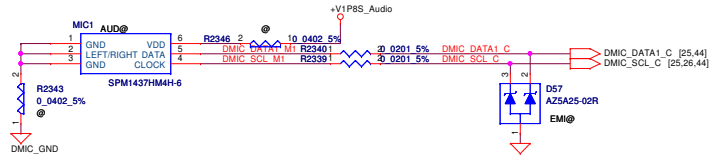
Date: Wednesday, June 04, 2014 Sheet 25 of 54

Digital MIC Conn.

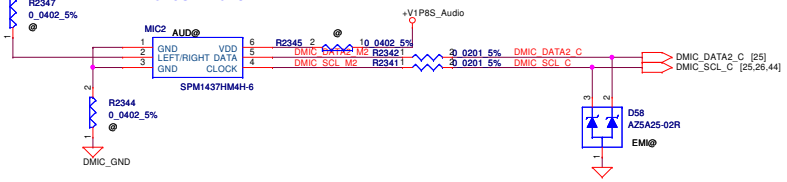


place close to DMIC

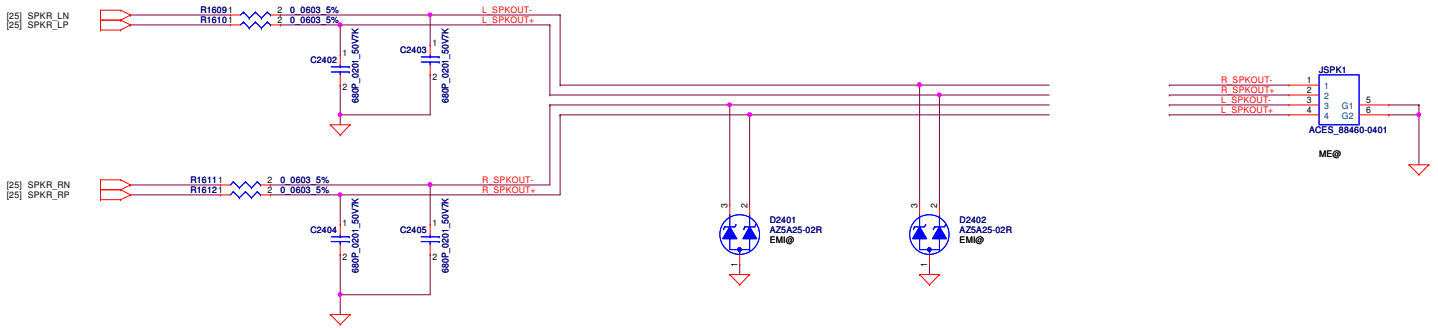
Corner Front



Side Front

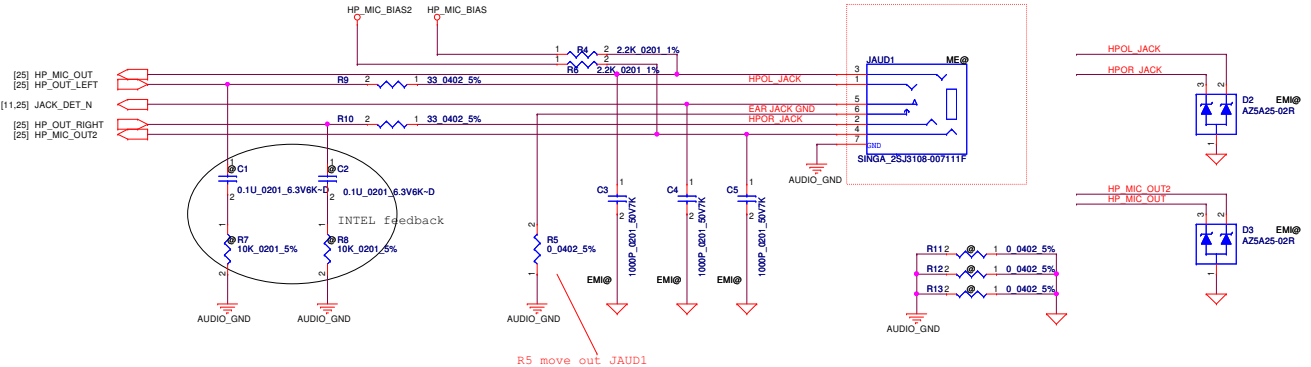


SPEAKERS



Audio Jack

Audio Jack need to confirm normal open



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Title: AUDIO CONNECTORS

Size: Custom | Document Number: ELLA LA-A811P | Rev: 0.1

Date: Wednesday, June 04, 2014 | Sheet 26 of 54

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Issued Date	2013/06/13	Deciphered Date	2006/03/01

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Title: **BLANK**

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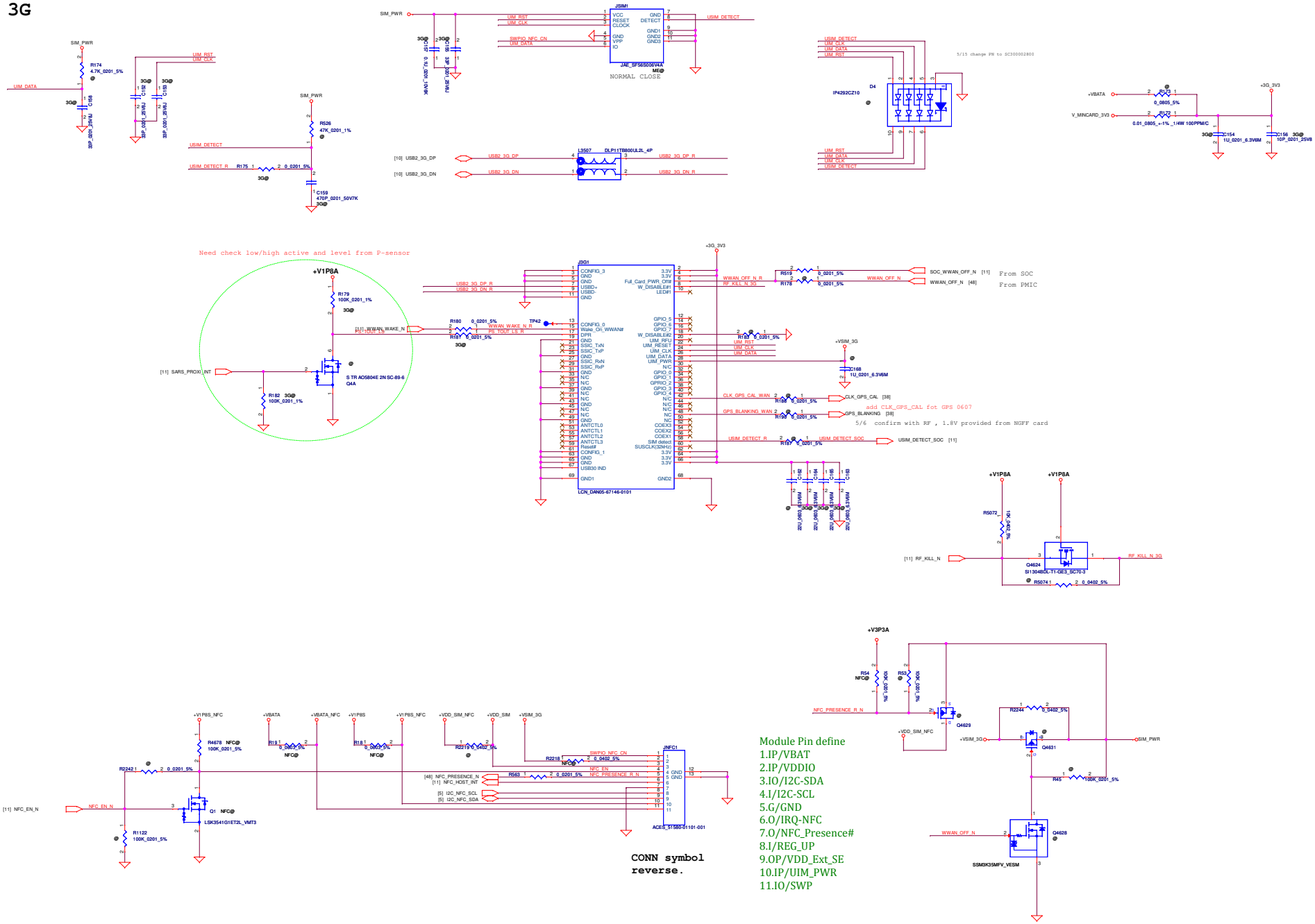
C	ELLA LA-A811P	0.1

Date: Wednesday, June 04, 2014 | Sheet 27 of 54

3G



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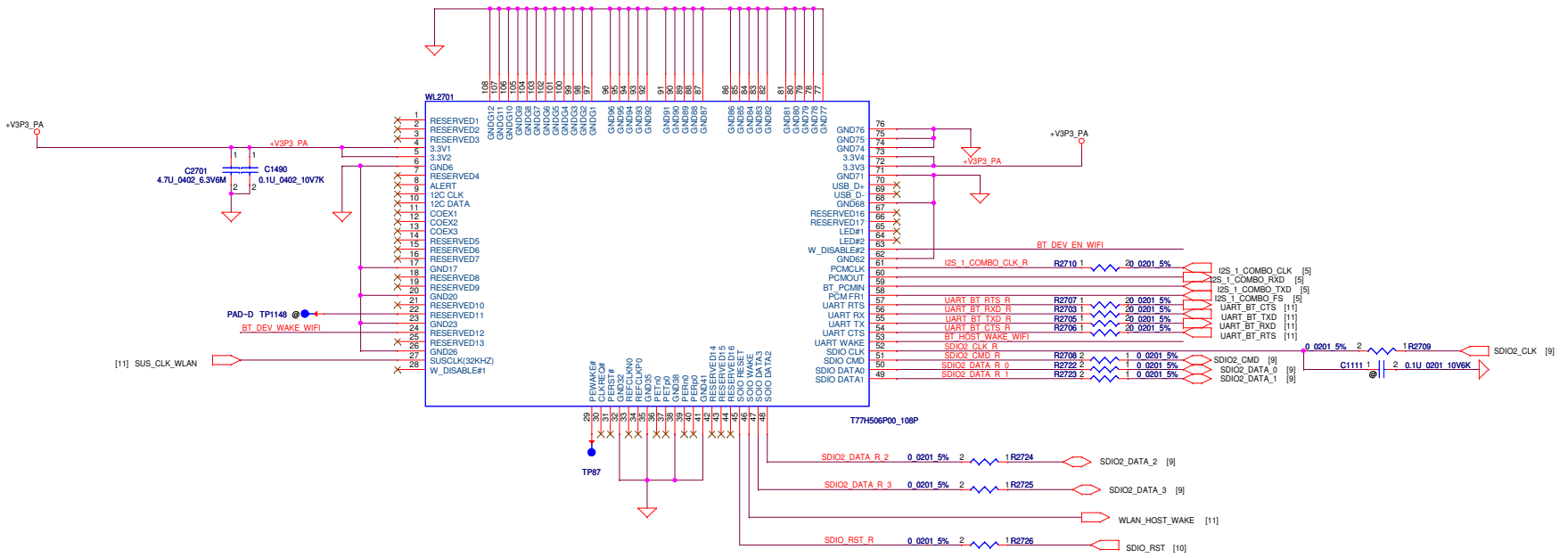
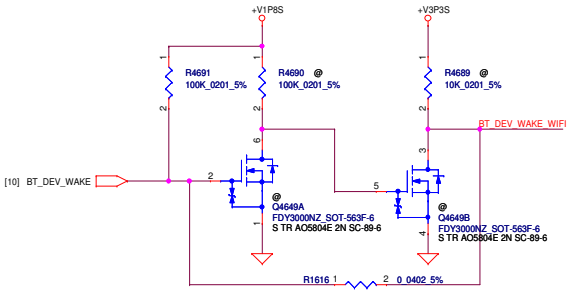
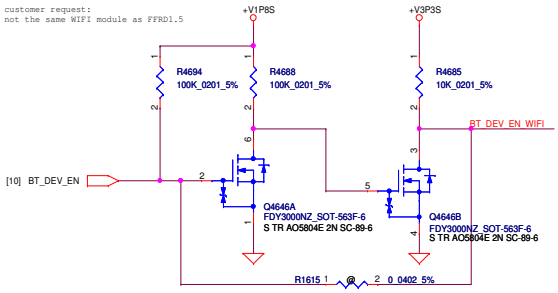
Issued Date	2013/06/13	Deciphered Date	2006/03/01

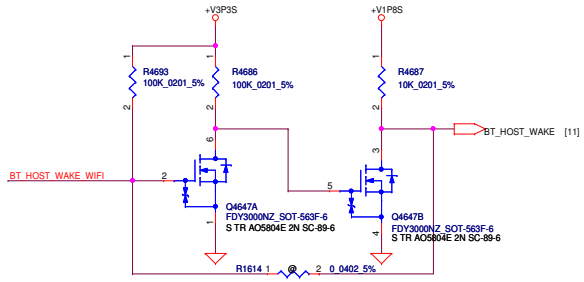
Compal Electronics, Inc.			
Title: 3G			
Size: Custom	Document Number: ELLA LA-A811P		Rev: 0.1
Date: Wednesday, June 04, 2014	Sheet 28 of 54		



0530 FFRD1.5 and Lenovo sch to SOC GPIO_S5_20(WL_DEV_EN)
0602 Check WIFI_PALDO_EN







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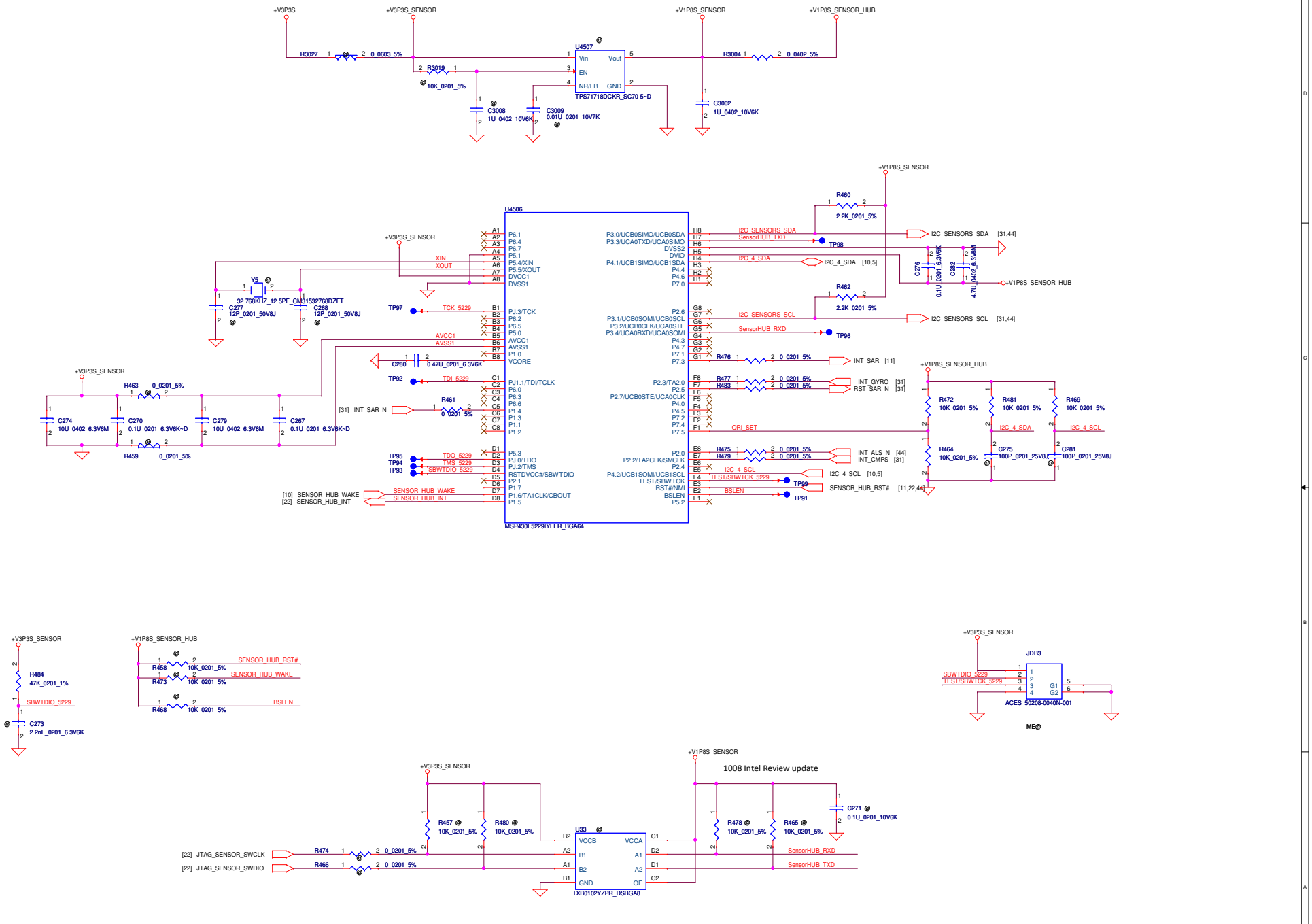
Title: WIFI + BT			
Size: C	Document Number: ELLA LA-A811P		Rev: 0.1
Date: Wednesday, June 04, 2014		Sheet 29 of 54	



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Issued Date	2013/06/13	Deciphered Date	2006/03/01

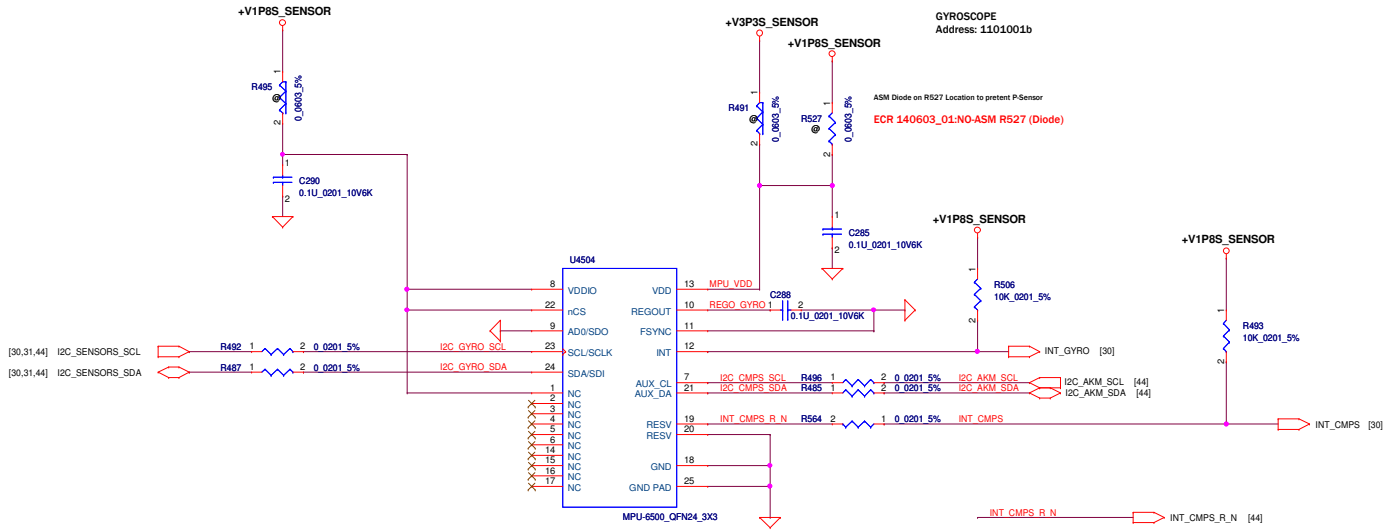
Compal Electronics, Inc.

Title: **SENSOR HUB**

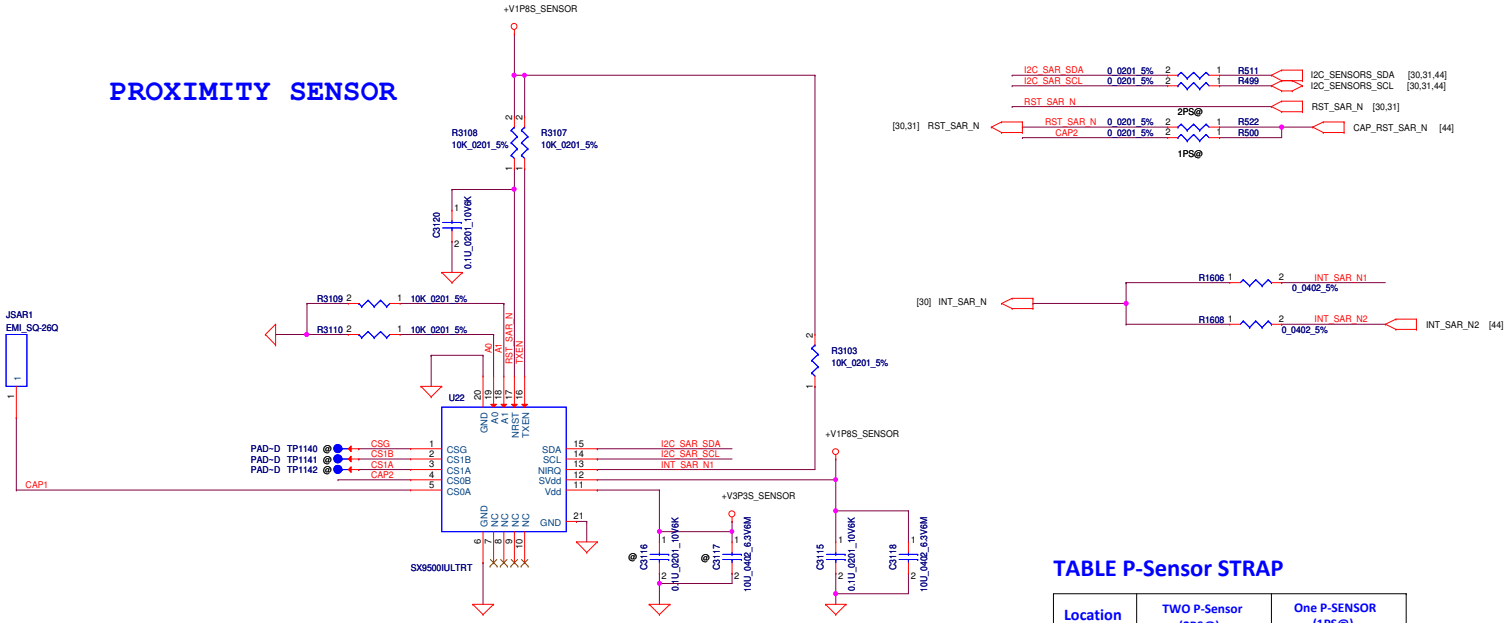
Size	Document Number	Rev
C	ELLA LA-A811P	0.1

Date: Wednesday, June 04, 2014 Sheet 30 of 54

Accelarometer + GYROSCOPE



PROXIMITY SENSOR



SX9500 I2C Address Table

A1	A0	Address	
0	0	0x28	P-Sensor 1(MB)
0	1	0x29	
1	0	0x2A	P-Sensor 2(SUB CAM)
1	1	0x2B	

TABLE P-Sensor STRAP

Location	TWO P-Sensor (2PS@)	One P-SENSOR (1PS@)
R522	ASM	N0 ASM
R500	N0 ASM	ASM

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Issued Date	2013/06/13	Deciphered Date	2006/03/01

Title	SENSORS		
Size C	Document Number ELLA LA-A811P		Rev 0.1
Date: Wednesday, June 04, 2014		Sheet 31 of 54	

7/17 SDV-R SWAP L3508 for routing.

need check EMI

Check BKL

Add to transfer +V3P3S_EDP.0108.

Follow REV0.9 Schematics

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Size: Custom | Document Number: ELLA LA-A811P | Rev: 0.1

Date: Wednesday, June 04, 2014 | Sheet 32 of 54

SLIM_DET	HDMI_SELECT
High	H (DOCK)
Low	L (CONN)

Need to check pull high or down

For PIN control mode

INTEL feedback

Need to check EMI

Need Check
HDMI Conn.

10/19 add de-bounce cap on HDMI_HPD (intel review)
7/17 SDV-R Modify net name HDMI_HPD_CONN

5/13 change PN to SM070002000

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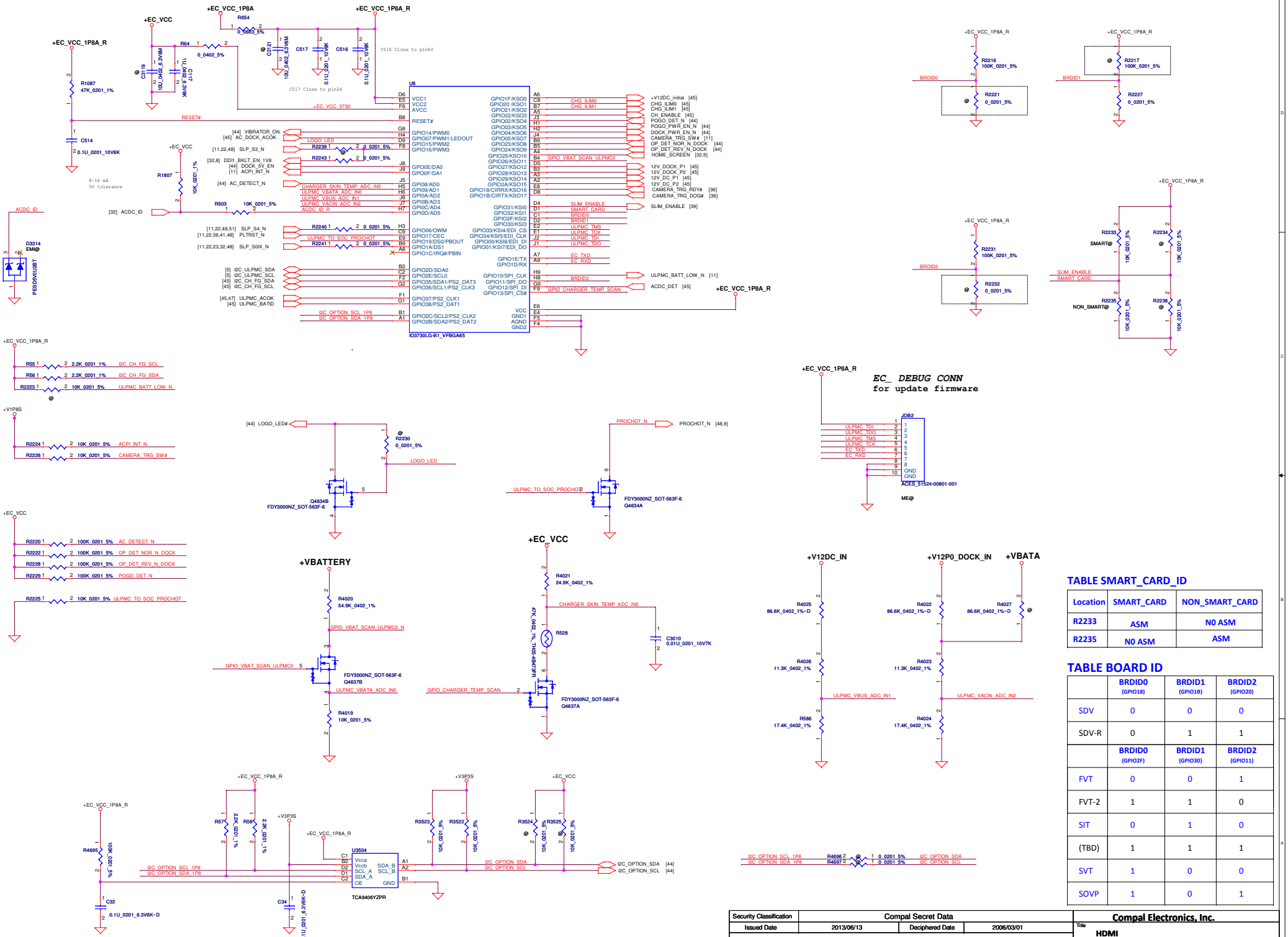
Issued Date	2013/06/13	Deciphered Date	2006/03/01

Compal Electronics, Inc.

Title: HDMI

Size: Custom | Document Number: ELLA LA-A811P | Rev: 0.1

Date: Wednesday, June 04, 2014 | Sheet 33 of 54


+EC_VCC_1P8A_R
+EC_VCC
+EC_VCC_1P8A
R654
0_0603_5%
C517
C516
C516 Close to pin60
C517 Close to pin24
R64
0_0402_5%
R1087
47K_0201_1%
C514
0.1U_0201_10V6K
U6
RESET#
+EC_VCC_3730
VCC1
VCC2
AVCC
RESET#
GPIO14/PWM0
GPIO07/PWM1/LEDOUT
GPIO15/PWM2
GPIO16/PWM3
GPO0E/DA0
GPO0F/DA1
GPI08/AD0
GPI09/AD1
GPI0A/AD2
GPI0B/AD3
GPI0C/AD4
GPI0D/AD5
GPIO06/OWM
GPIO17/CEC
GPIO19/DSO/PBOUT
GPIO1A/DS1
GPIO1C/IRQ#/PBIN
GPIO2D/SDA0
GPIO2E/SCL0
GPIO35/SDA1/PS2_DAT3
GPIO36/SCL1/PS2_CLK3
GPIO37/PS2_CLK1
GPIO38/PS2_DAT1
GPIO2C/SCL2/PS2_CLK2
GPIO2B/SDA2/PS2_DAT2
GPIO1F/KSO0
GPIO20 /KSO1
GPIO21/KSO2
GPIO22/KSO3
GPIO02/KSO4
GPIO03/KSO5
GPIO04/KSO6
GPIO05/KSO7
GPIO23/KSO8
GPIO24/KSO9
GPIO25/KSO10
GPIO26/KSO11
GPIO27/KSO12
GPIO28/KSO13
GPIO29/KSO14
GPIO2A/KSO15
GPIO1B/CIRRX/KSO16
GPIO1B/CIRTX/KSO17
GPIO31/KSI0
GPIO32/KSI1
GPIO2F/KSI2
GPIO30/KSI3
GPIO33/KSI4/EDI_CS
GPIO34/KSI5/EDI_CLK
GPIO00/KSI6/EDI_DI
GPIO01/KSI7/EDI_DO
GPIO1E/TX
GPIO1D/RX
GPIO10/SPI_CLK
GPIO11/SPI_DO
GPIO12/SPI_DI
GPIO13/SPI_CS#
VCC
GND1
AGND
GND2
IO3730LG-B1_VFBGA65
CHG_ILIM0
CHG_ILIM1
GPIO_VBAT_SCAN_ULPMC0
SLIM_ENABLE
BRDID0
BRDID1
BRDID2
ULPMC_TMS
ULPMC_TCK
ULPMC_TDI
ULPMC_TDO
EC_TXD
EC_RXD
BRDID2
GPIO_CHARGER_TEMP_SCAN
+V12DC_initial [45]
CHG_ILIM0 [45]
CHG_ILIM1 [45]
CH_ENABLE [45]
POGO_DET_N [44]
POGO_PWR_EN_N [44]
DOCK_PWR_EN_N [44]
CAMERA_TRG_SW# [11]
OP_DET_NOR_N_DOCK [44]
OP_DET_REV_N_DOCK [44]
HOME_SCREEN [32,8]
12V_DOCK_P1 [45]
12V_DOCK_P2 [45]
12V_DC_P1 [45]
12V_DC_P2 [45]
CAMERA_TRG_RDY# [36]
CAMERA_TRG_DOG# [36]
SLIM_ENABLE [39]
ULPMC_BATT_LOW_N [11]
ACDC_DET [45]
[44] VIBRATOR_ON
[45] AC_DOCK_ACOK
LOGO_LED
[11,22,48] SLP_S3_N
[32,8] DDI1_BKLT_EN_1V8
[44] DOCK_5V_EN
[11] ACPI_INT_N
[44] AC_DETECT_N
CHARGER_SKIN_TEMP_ADC_IN5
ULPMC_VBATA_ADC_IN0
ULPMC_VBUS_ADC_IN1
ULPMC_VACIN_ADC_IN2
ACDC_ID_R
[32] ACDC_ID
[11,22,48,51] SLP_S4_N
[11,22,36,41,48] PLTRST_N
[11,22,23,32,48] SLP_S0IX_N
ULPMC_TO_SOC_PROCHOT
[5] I2C_ULPMC_SDA
[5] I2C_ULPMC_SCL
[45] I2C_CH_FG_SDA
[45] I2C_CH_FG_SCL
[45,47] ULPMC_ACOK
[45] ULPMC_BATID
I2C_OPTION_SCL_1P8
I2C_OPTION_SDA_1P8
R1807
10K_0201_1%
R503
10K_0201_5%
R2239
R2243
R2240
R2241
0_0201_5%
8-16 mA
5V tolerance
ACDC_ID
D3314
EMI@
PESD5V0U2BT
R2216
100K_0201_5%
R2221
R2217
R2227
R2231
R2232
R2233
R2234
R2235
R2236
SMART@
NON_SMART@
SLIM_ENABLE
SMART_CARD
R55
R56
R2223
2.2K_0201_1%
10K_0201_5%
I2C_CH_FG_SCL
I2C_CH_FG_SDA
ULPMC_BATT_LOW_N
+V1P8S
R2224
R2226
ACPI_INT_N
CAMERA_TRG_SW#
R2220
R2222
R2228
R2229
R2225
100K_0201_5%
AC_DETECT_N
OP_DET_NOR_N_DOCK
OP_DET_REV_N_DOCK
POGO_DET_N
ULPMC_TO_SOC_PROCHOT
R2230
LOGO_LED
Q4634B
FDY3000NZ_SOT-563F-6
PROCHOT_N
PROCHOT_N [48,9]
ULPMC_TO_SOC_PROCHOT
Q4634A
EC_ DEBUG CONN
for update firmware
JDB2
ULPMC_TDI
ULPMC_TDO
ULPMC_TMS
ULPMC_TCK
EC_TXD
EC_RXD
GND
ACES_51524-00801-001
ME@
+VBATTERY
R4020
54.9K_0402_1%
GPIO_VBAT_SCAN_ULPMC0_N
GPIO_VBAT_SCAN_ULPMC0
Q4637B
R4019
10K_0201_5%
+EC_VCC
R4021
24.9K_0402_1%
47K_0402_1%_THDB-4847SFR
R528
C3010
0.01U_0201_10V7K
GPIO_CHARGER_TEMP_SCAN
Q4637A
+V12DC_IN
+V12P0_DOCK_IN
+VBATA
R4025
86.6K_0402_1%-D
R4026
11.3K_0402_1%
R586
17.4K_0402_1%
R4022
R4027
R4023
R4024
ULPMC_VBUS_ADC_IN1
ULPMC_VACIN_ADC_IN2
TABLE SMART_CARD_ID
Location
SMART_CARD
NON_SMART_CARD
R2233
ASM
N0 ASM
R2235
N0 ASM
ASM
TABLE BOARD ID
BRDID0 (GPIO18)
BRDID1 (GPIO1B)
BRDID2 (GPIO20)
SDV 0 0 0
SDV-R 0 1 1
BRDID0 (GPIO2F)
BRDID1 (GPIO30)
BRDID2 (GPIO11)
FVT 0 0 1
FVT-2 1 1 0
SIT 0 1 0
(TBD) 1 1 1
SVT 1 0 0
SOVP 1 0 1
R57
R58
2.2K_0201_1%
+V3P3S
R4695
100K_0201_5%
C32
0.1U_0201_6.3V6K-D
C34
U3504
TCA9406YZPR
R3523
R3522
R3524
R3525
10K_0201_5%
I2C_OPTION_SDA
I2C_OPTION_SCL
R4696
R4697
0_0201_5%
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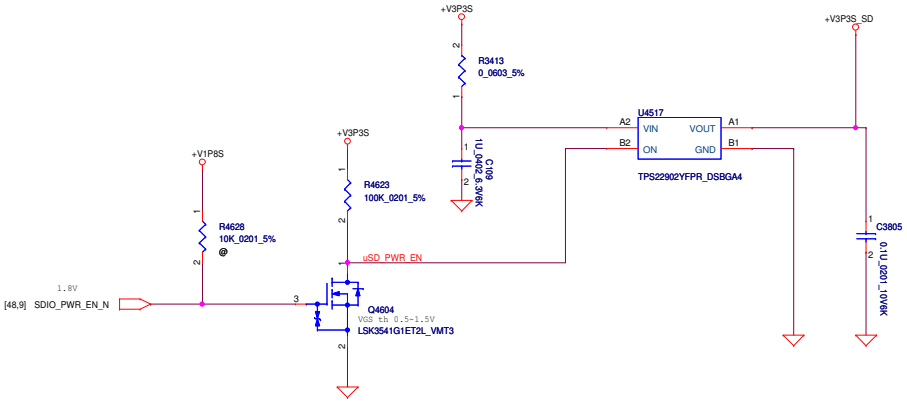
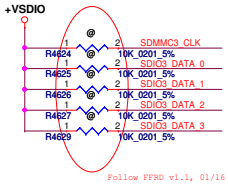
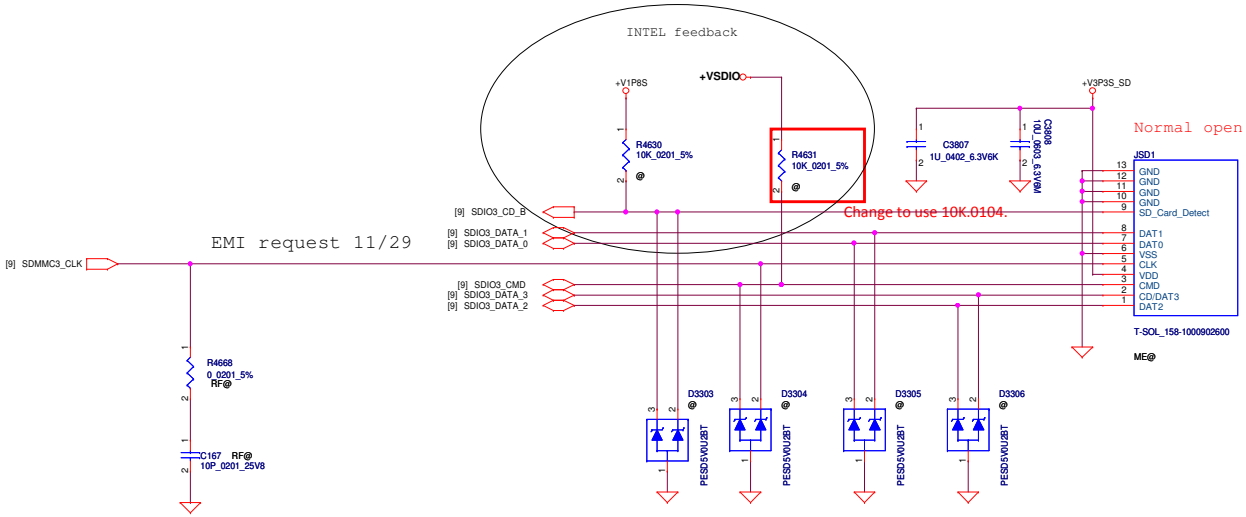
Title
HDMI
Size
Custom
Document Number
ELLA LA-A811P
Rev
0.1
Date:
Wednesday, June 04, 2014
Sheet
34
of
54

Micro SD







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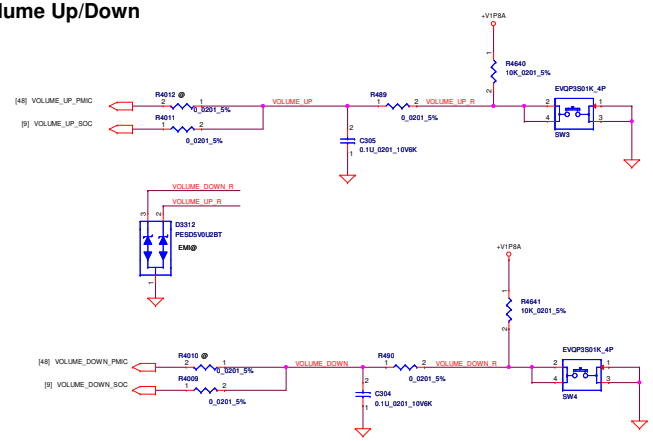
Issued Date	2013/06/13	Deciphered Date	2014/04/01

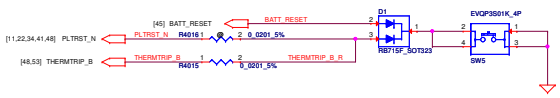
Title	uSD		
Size C	Document Number ELLA LA-A811P		Rev 0.1
Date: Wednesday, June 04, 2014	Sheet 35 of 54		

Volume Up/Down

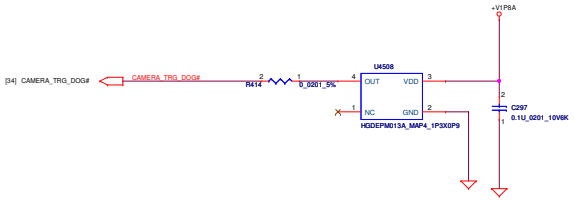


Battery Reset

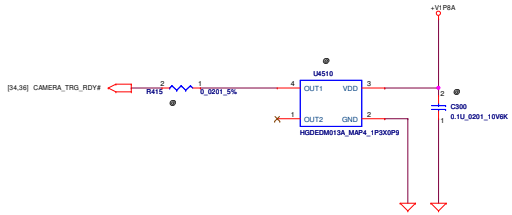
Reset Button check



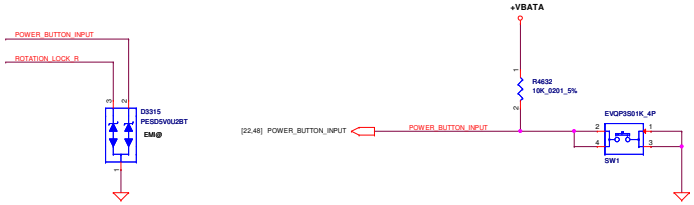
DOG EAR detect



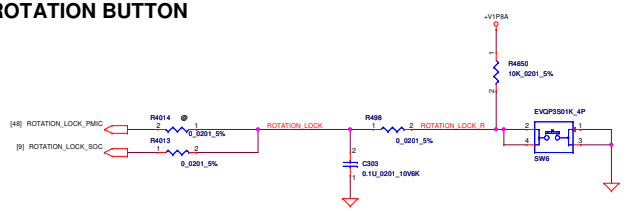
COVER LID SENSOR



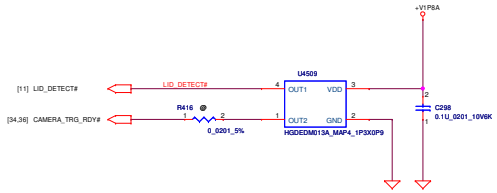
Power SW



ROTATION BUTTON

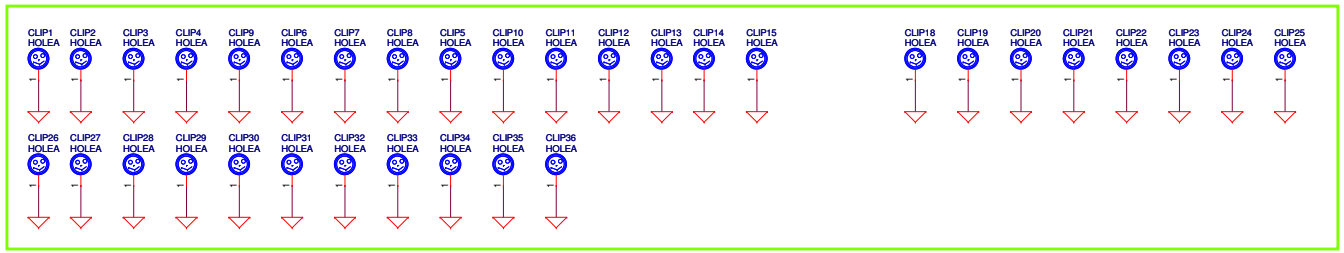


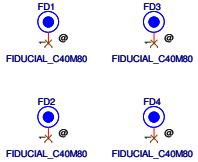
COVER LID SENSOR

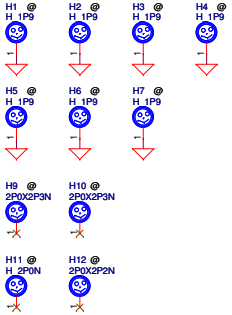


Security Classification	Compal Secret Data		Compal Electronics, Inc.
Issued Date	2012/07/01	Deciphered Date	2014/04/01

Title	BTN/LED/LOCK/HOME Key/Touch Board		
Size	Document Number		Rev
Custom	ELLA LA-A811P		0.1
Date:	Wednesday, June 04, 2014	Sheet	36 of 54







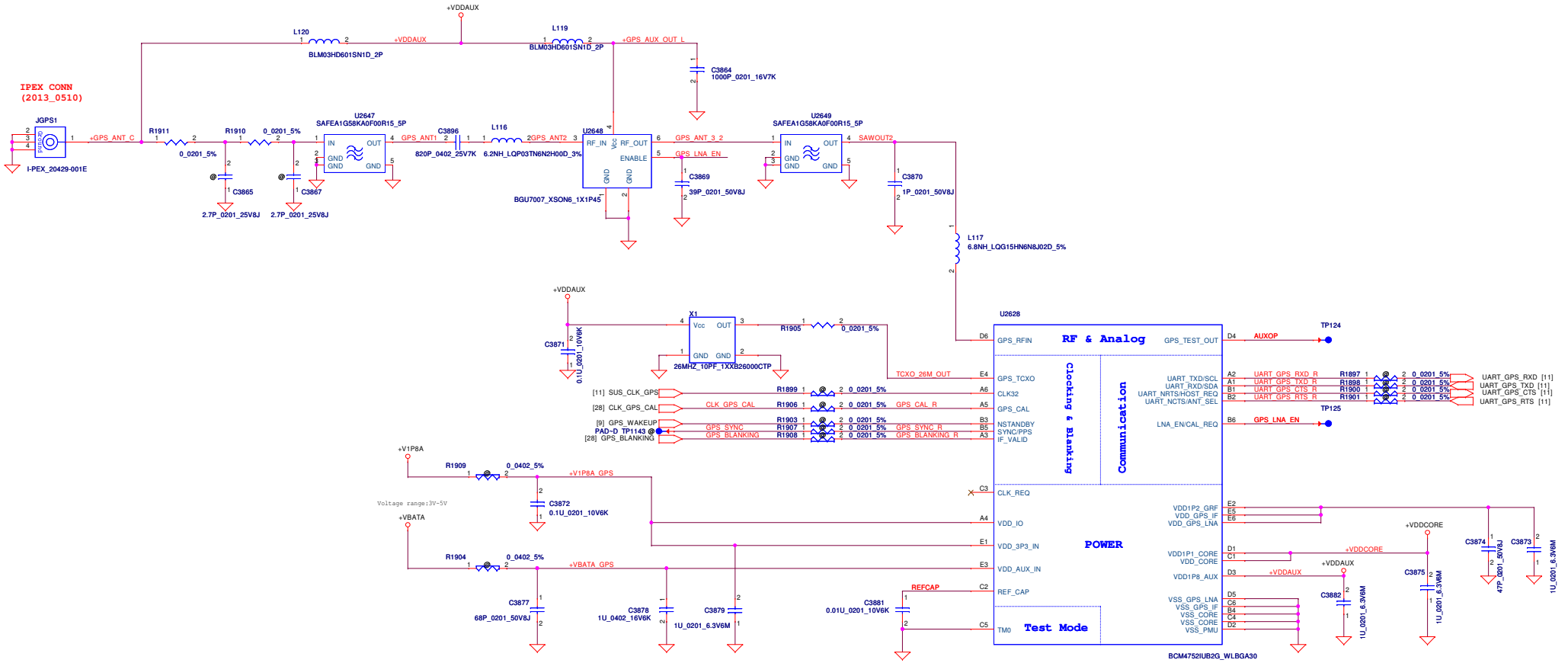
Security Classification	Compal Secret Data		
Issued Date	2013/06/13	Deciphered Date	2014/04/01

Compal Electronics, Inc.

Title: **Screw Hole**

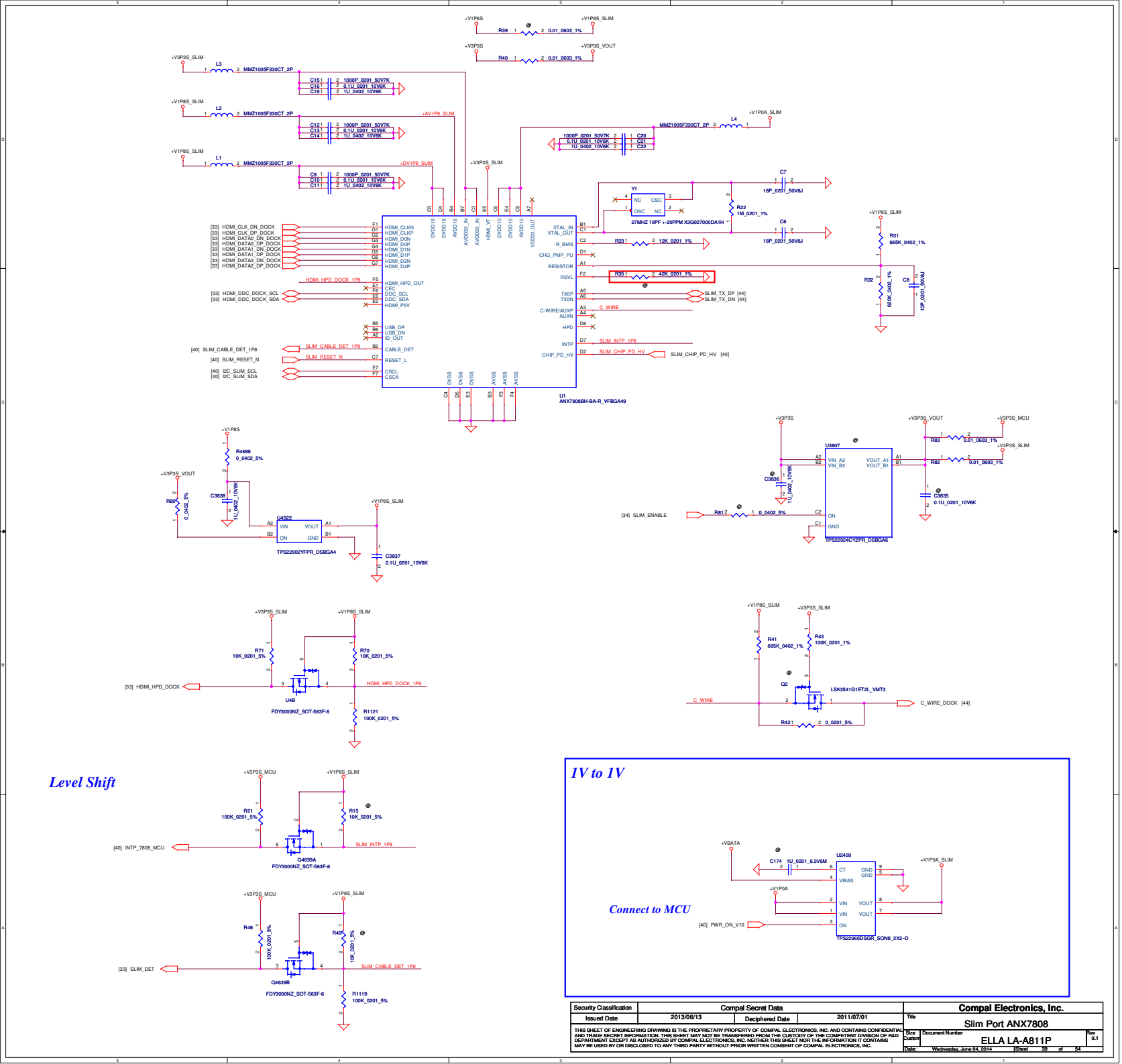
Size	Document Number	Rev
Custom	ELLA LA-A811P	0.1

Date: Wednesday, June 04, 2014 | Sheet 37 of 54

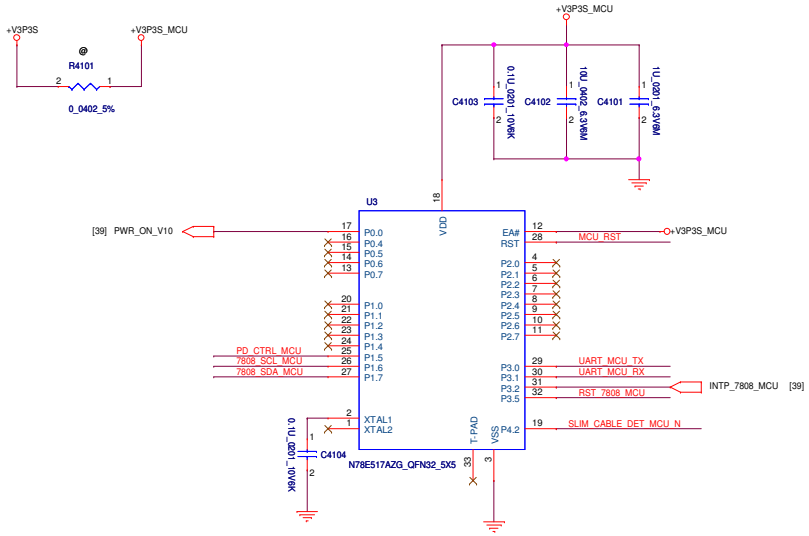


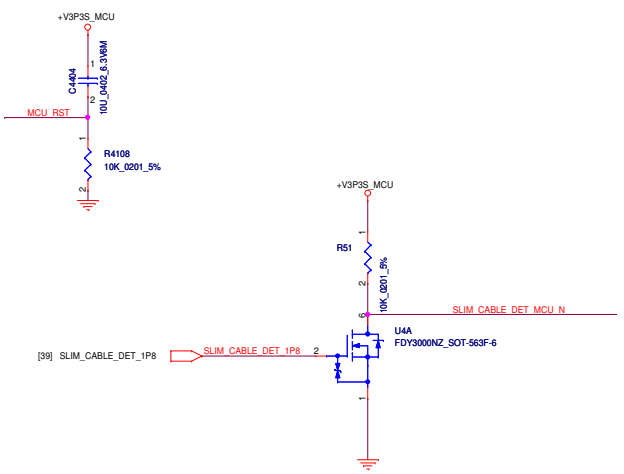
Security Classification	Compal Secret Data		Compal Electronics, Inc.
Issued Date	2013/06/13	Deciphered Date 2006/03/01	Title: GPS

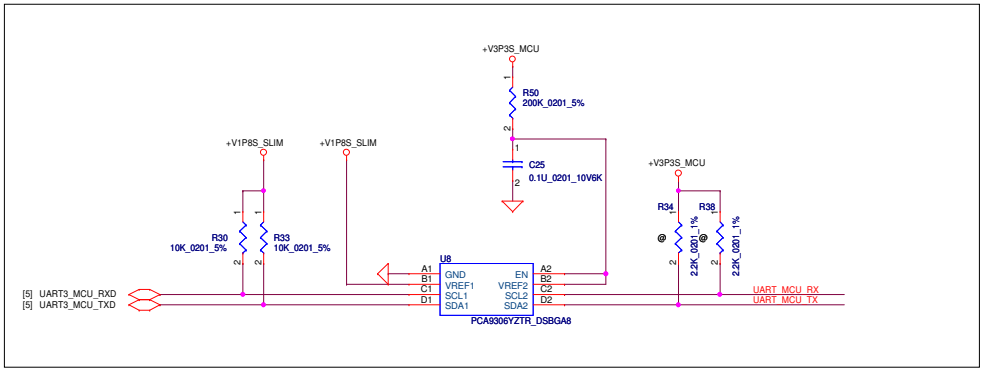
Size: Custom	Document Number: ELLA LA-A811P		Rev: 0.1
Date: Wednesday, June 04, 2014	Sheet 38 of 54		

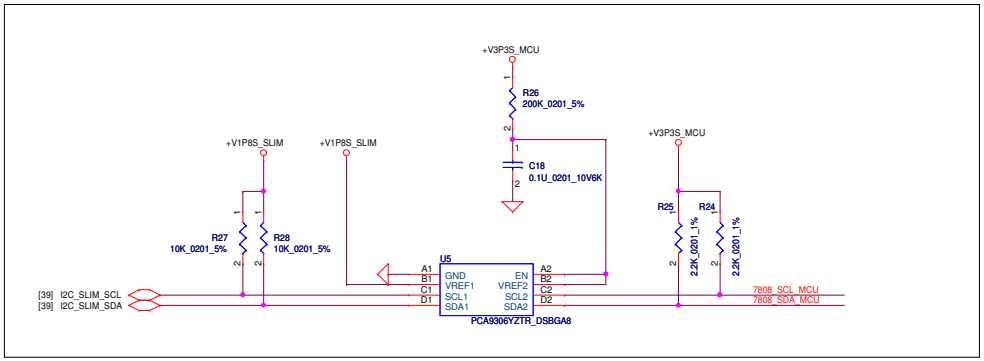


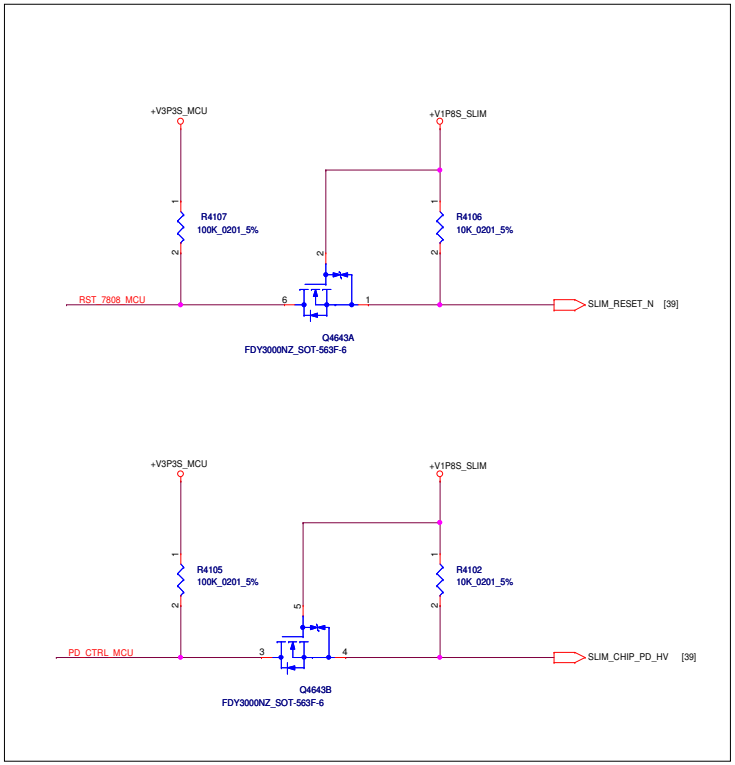
Security Classification	Compal Secret Data		Compal Electronics, Inc.
Issued Date	2013/06/13	Deciphered Date	2011/07/01
Title	Slim Port ANX7808		
Size: Custom	Document Number: ELLA LA-A811P		Rev: 0.1
Date: Wednesday, June 04, 2014	Sheet 39 of 54		











Security Classification	Compal Secret Data		
Issued Date	2013/06/13	Deciphered Date	2006/03/01

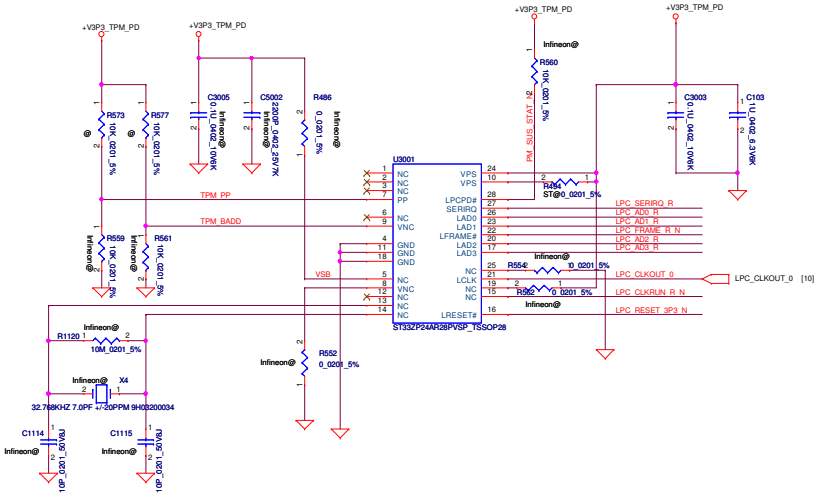
Compal Electronics, Inc.

Title: **BLANK**

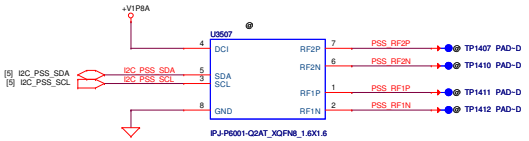
Size	Document Number	Rev
Custom	ELLA LA-A811P	0.1

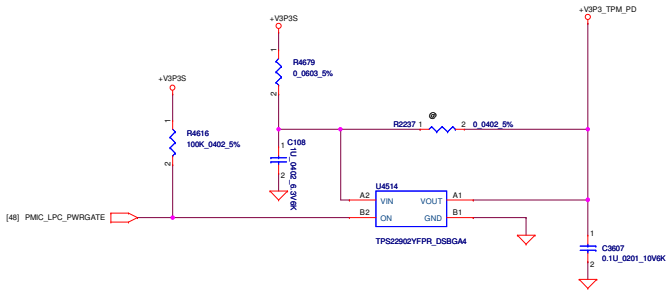
Date: Wednesday, June 04, 2014 | Sheet 40 of 54

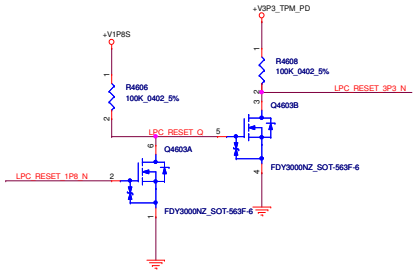
TPM

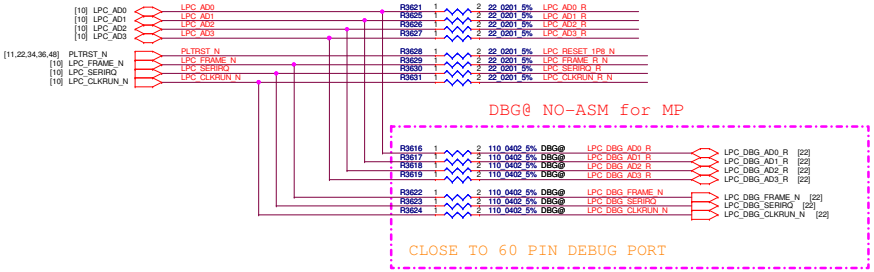


PSS









Follow Lenovo sch

TABLE TPM CONFIGULATION

Location	Infineon	ST Micro	Non dTPM MODEL
C103	ASM	ASM	NO ASM
C108	ASM	ASM	
C1114	ASM	N0 ASM	
C1115	ASM	N0 ASM	
C3003	ASM	ASM	
C3005	ASM	N0 ASM	
C3607	ASM	ASM	
C5002	ASM	NO ASM	
R486	ASM	NO ASM	
R494	NO ASM	ASM	
R552	ASM	NO ASM	
R554	ASM	NO ASM	
R559	ASM	NO ASM	
R560	ASM	NO ASM	
R561	ASM	NO ASM	
R562	ASM	NO ASM	
R573	NO ASM	NO ASM	
R577	NO ASM	NO ASM	
R1120	ASM	NO ASM	
R3621	ASM	ASM	
R3625	ASM	ASM	
R3626	ASM	ASM	
R3627	ASM	ASM	
R3628	ASM	ASM	
R3629	ASM	ASM	
R3630	ASM	ASM	
R3631	ASM	ASM	
R4616	ASM	ASM	
R4606	ASM	ASM	
R4608	ASM	ASM	
R4679	ASM	ASM	
Q4603	ASM	ASM	
X4	ASM	NO ASM	
U4514	ASM	ASM	
U3001	SLB9635	ST33ZP24AR	

Compal Electronics, Inc.			
Title: TPM			
Size: A3	Document Number: LA-A811P		Rev: 0.1
Date: Wednesday, June 04, 2014	Sheet: 41	of: 54	

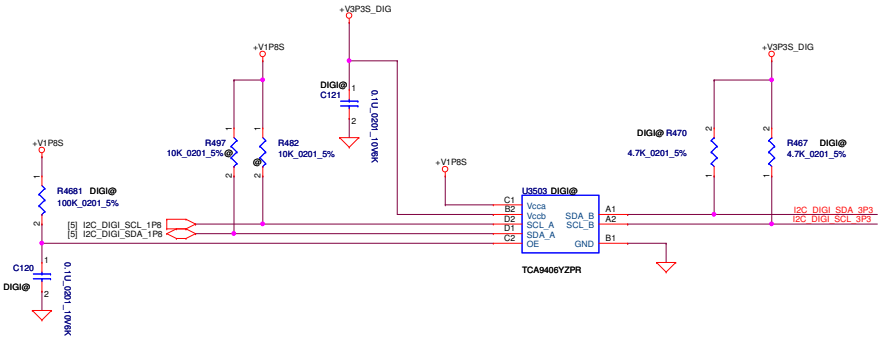
Security Classification	Compal Secret Data		
Issued Date	2013/06/13	Deciphered Date	2006/03/01

Compal Electronics, Inc.

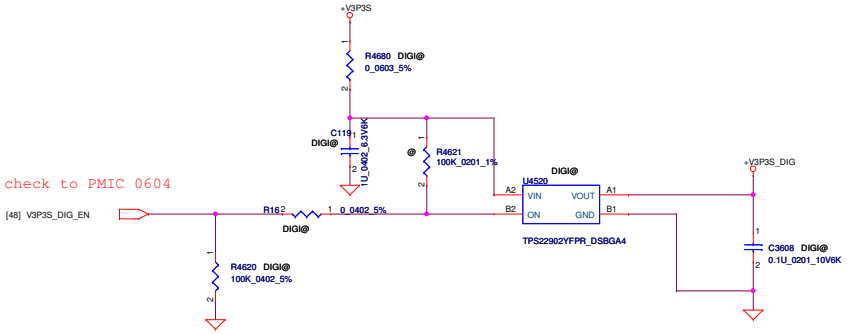
Title: **BLANK**

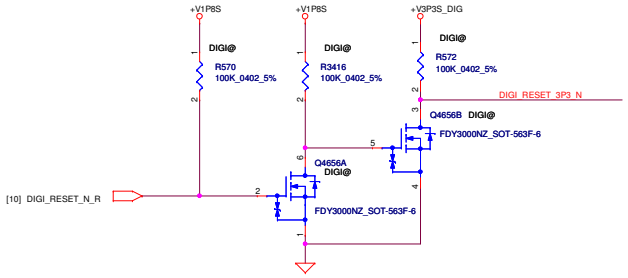
Size	Document Number	Rev
C	ELLA LA-A811P	0.1

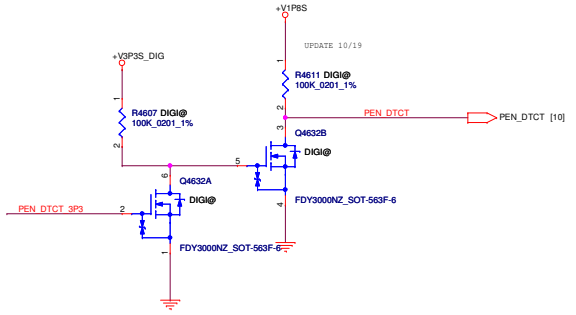
Date: Wednesday, June 04, 2014 | Sheet 42 of 54

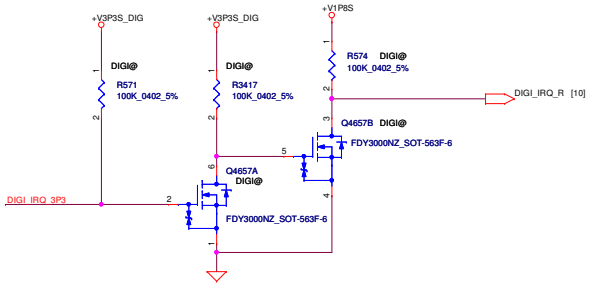


check to PMIC 0604

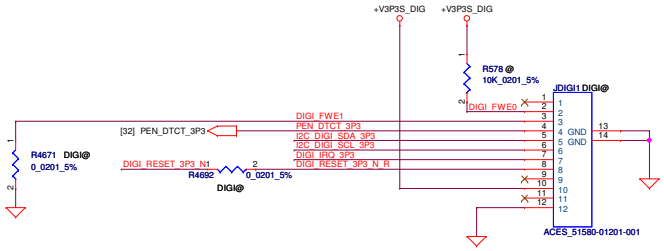








Digitizer board connector



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Issued Date	2013/06/13	Deciphered Date	2014/04/01	Title	Digitizer
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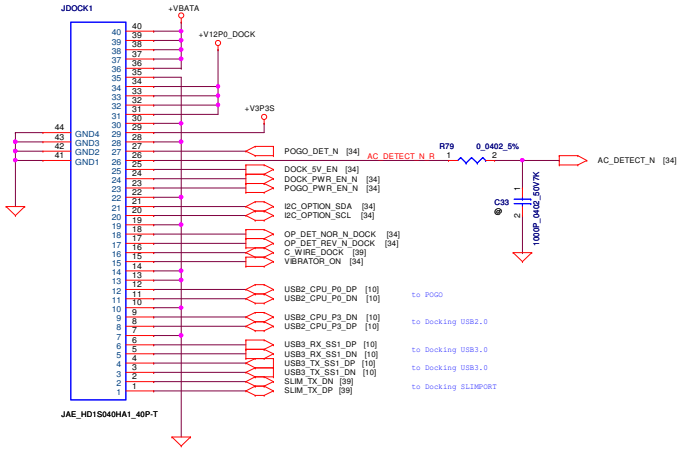
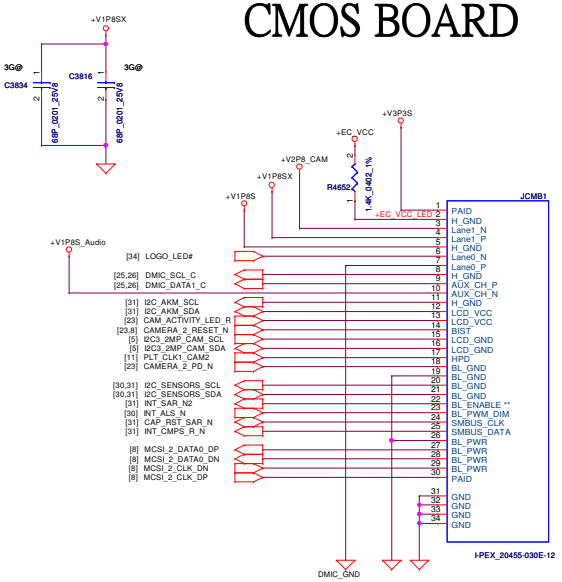
				Date: Wednesday, June 04, 2014	Sheet 43 of 54

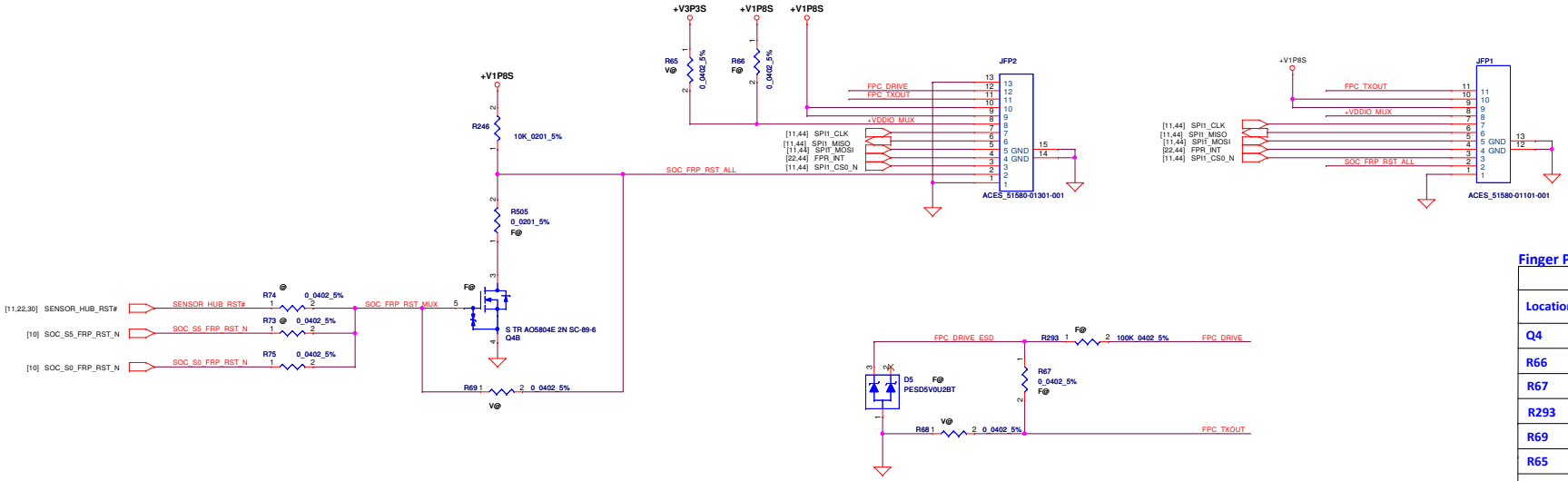
Docking conn



CMOS BOARD



Finger Printer Conn

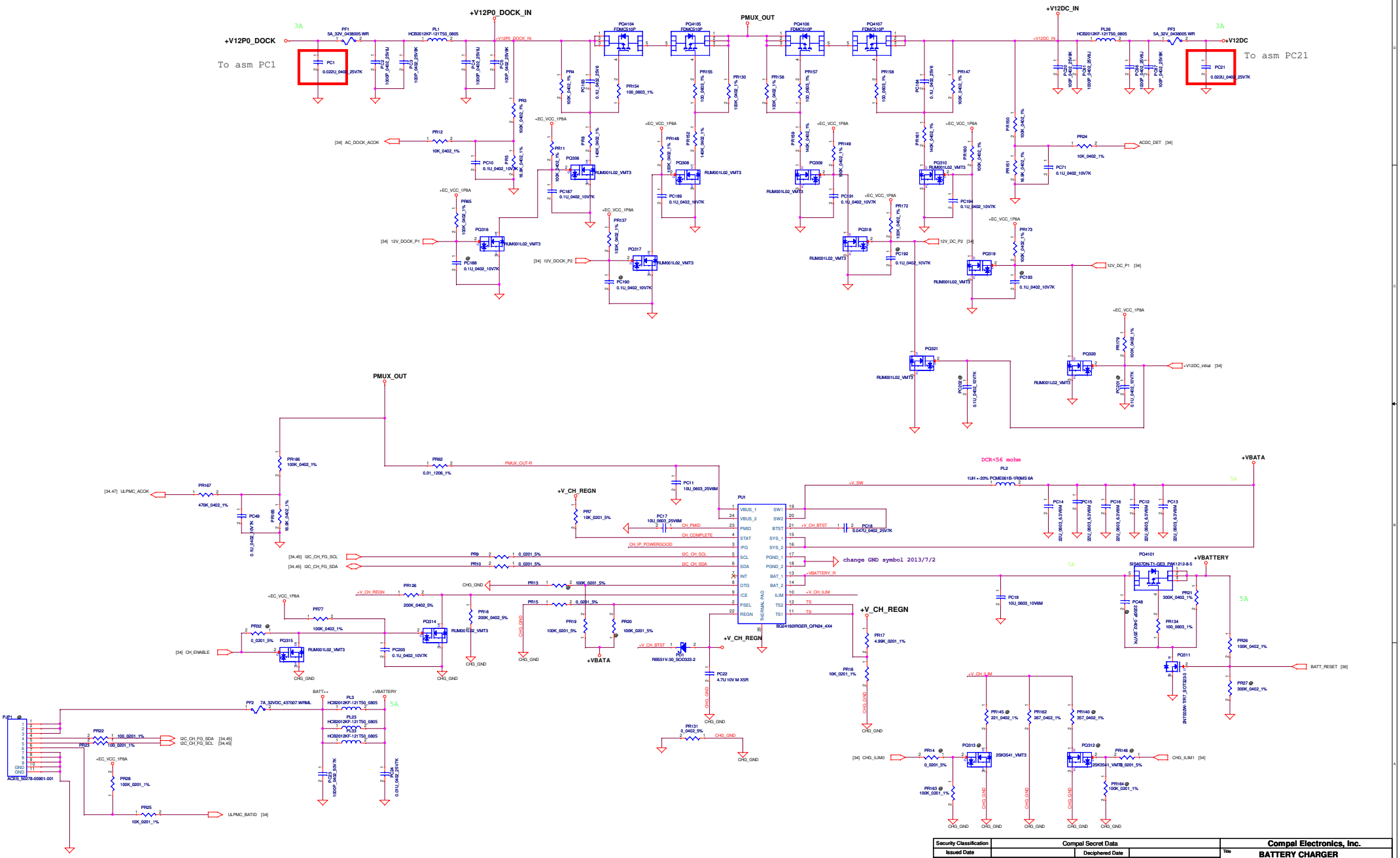


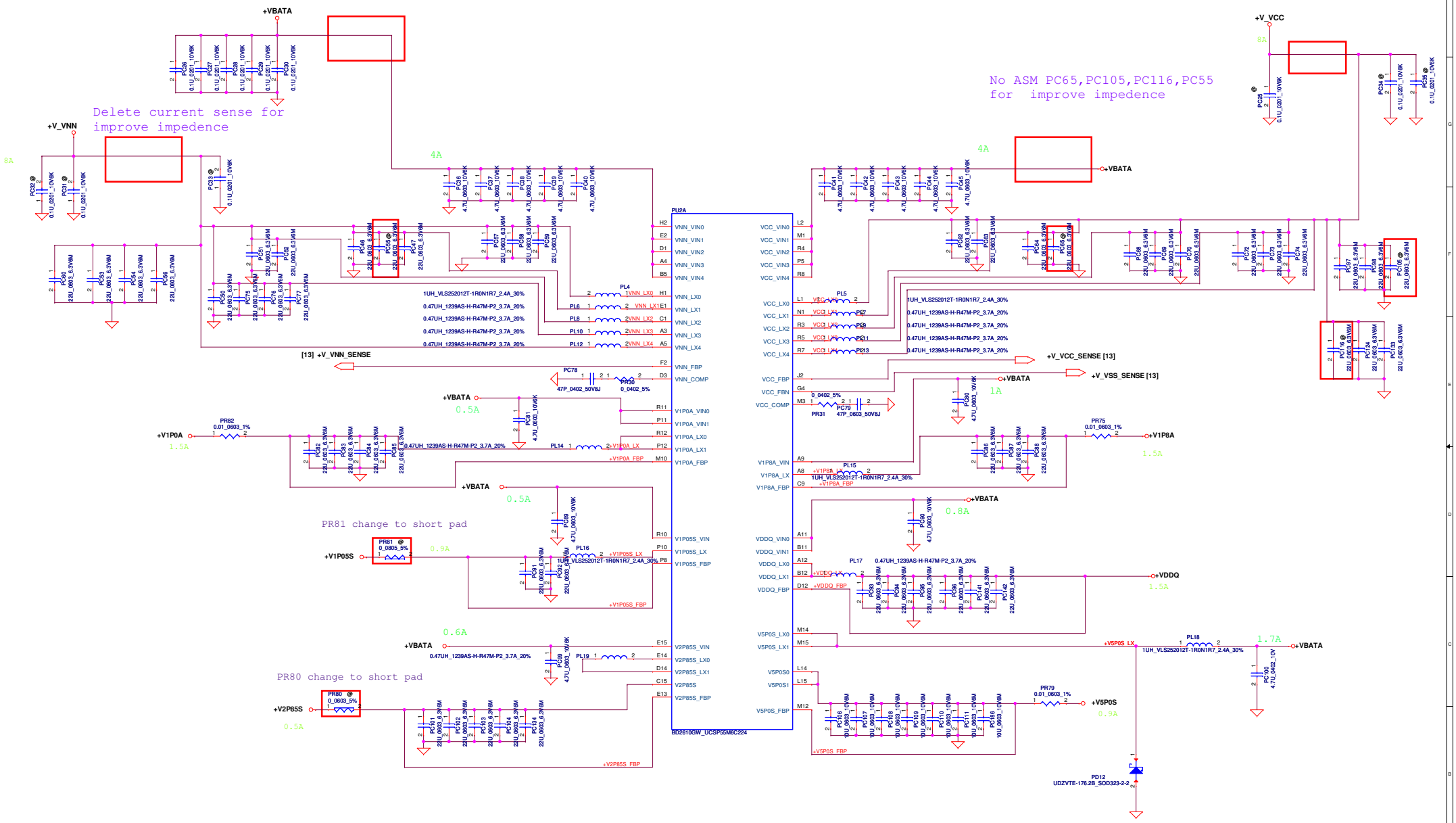
Finger Printer BOM Structure

	JFP1	JFP2
Location	Validityinc (V@) (ACES_51580-01101-001)	Fingerprints (F@) (ACES_51580-01301-001)
Q4	N0 ASM	ASM
R66	N0 ASM	ASM
R67	N0 ASM	ASM
R293	N0 ASM	ASM
R69	ASM	N0 ASM
R65	ASM	N0 ASM
R68	ASM	N0 ASM
D5	N0 ASM	ASM
R505	N0 ASM	ASM

Security Classification	Compal Secret Data		
Issued Date	2013/06/13	Deciphered Date	2014/04/01

Compal Electronics, Inc.	
Title	Docking Conn&DC IN/CMOS
Size: Custom	Document Number: ELLA LA-A811P
Rev	0.1
Date: Wednesday, June 04, 2014	Sheet 44 of 54


+V12P0_DOCK
To asm PC1
+V12P0_DOCK_IN
PMUX_OUT
+V12DC_IN
+V12DC
To asm PC21
PMUX_OUT
+V_CH_REGN
+VBATA
+VBATTERY
change GND symbol 2013/7/2
DCR<56 mohm
BATT_RESET [36]
ACDC_DET [34]
12V_DC_P1 [34]
12V_DC_P2 [34]
+V12DC_initial [34]
CHG_ILIM1 [34]
ULPMC_BATID [34]
CH_ENABLE [34]
ULPMC_ACOK [34,47]
AC_DOCK_ACOK [34]
12V_DOCK_P1 [34]
12V_DOCK_P2 [34]
I2C_CH_FG_SCL [34,45]
I2C_CH_FG_SDA [34,45]
Security Classification
Compal Secret Data
Issued Date
Deciphered Date
Compal Electronics, Inc.
BATTERY CHARGER
Document Number
Rev 6.1
Date: Wednesday, June 04, 2014
Sheet 45 of 54



Security Classification	Compal Secret Data		Compal Electronics, Inc.
Issued Date		Deciphered Date	Title: PMIC-1
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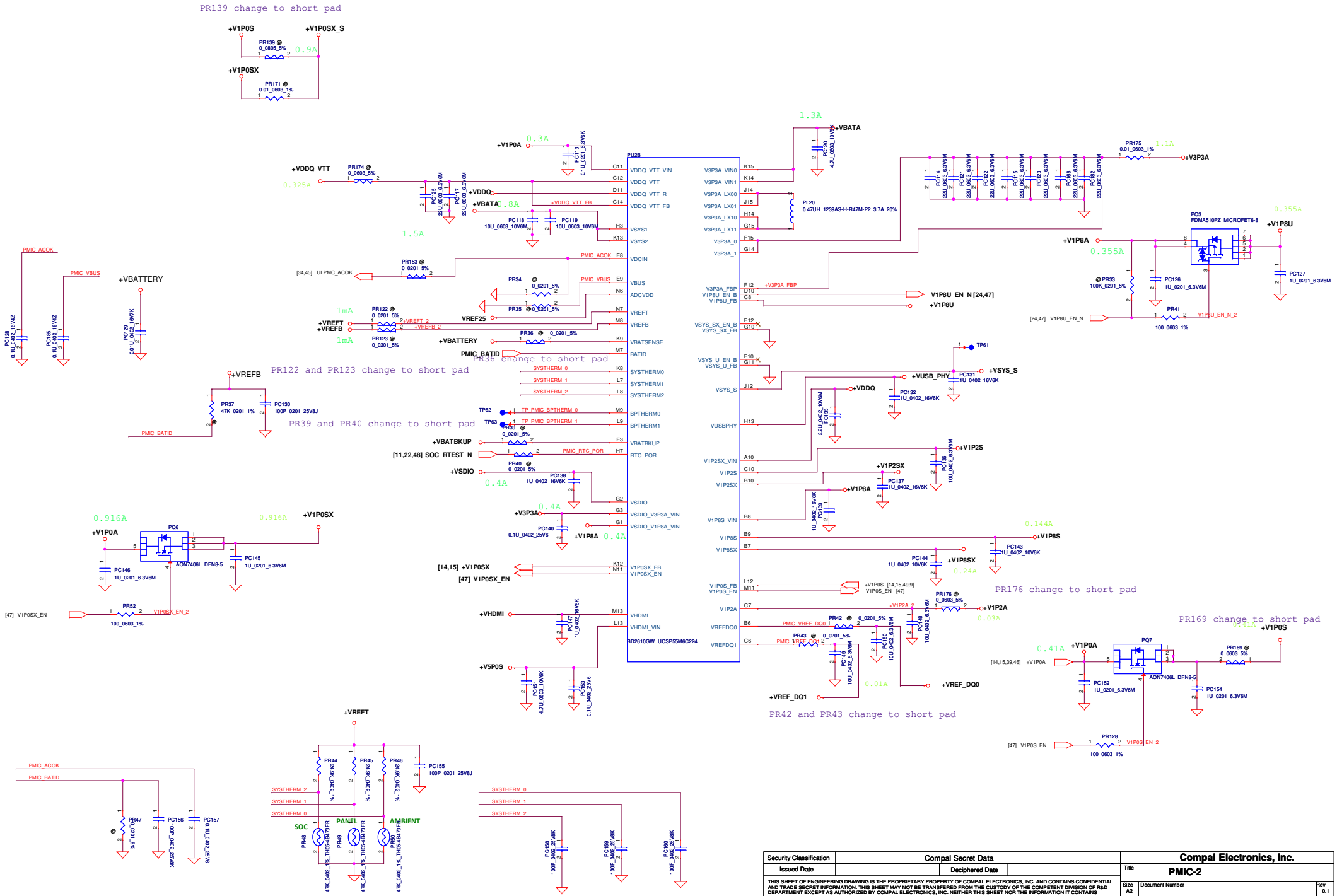
			Date: Wednesday, June 04, 2014 / Sheet 46 of 54



Security Classification	Compal Secret Data		Compal Electronics, Inc.
Issued Date		Deciphered Date	Title: PMIC-2

Size: A2	Document Number		Rev: 0.1
Date: Wednesday, June 04, 2014	Sheet 47 of 54		

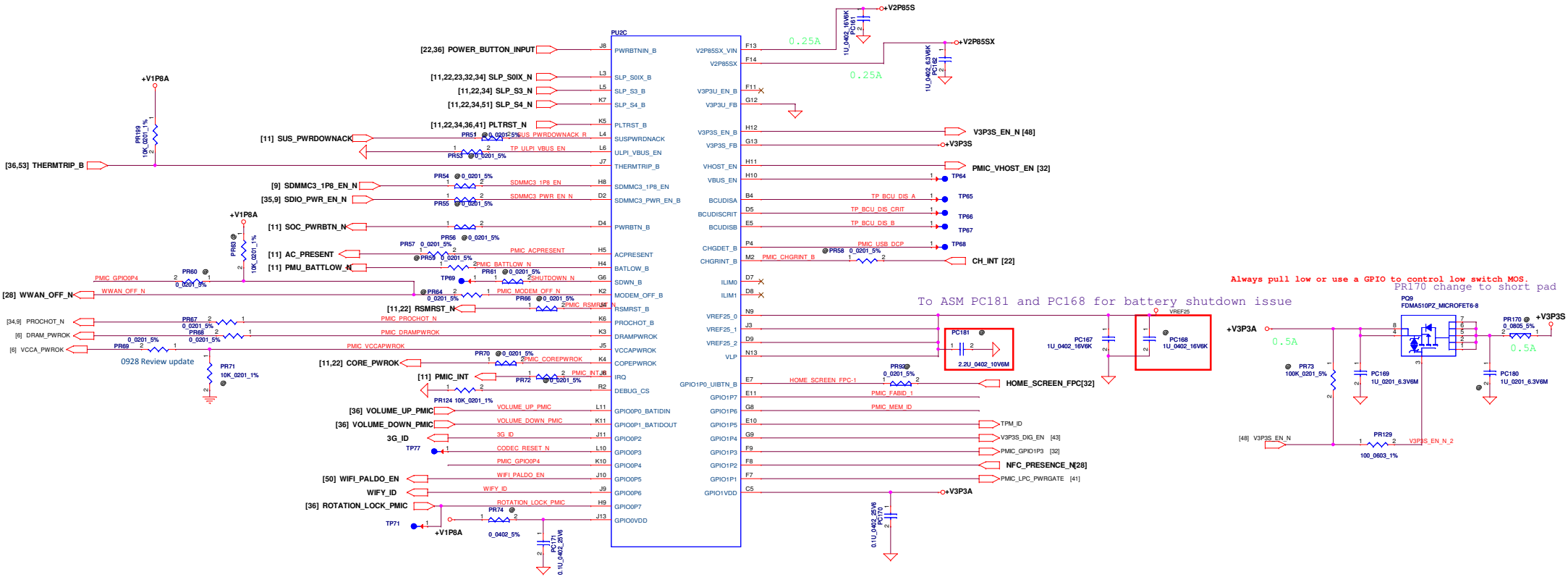
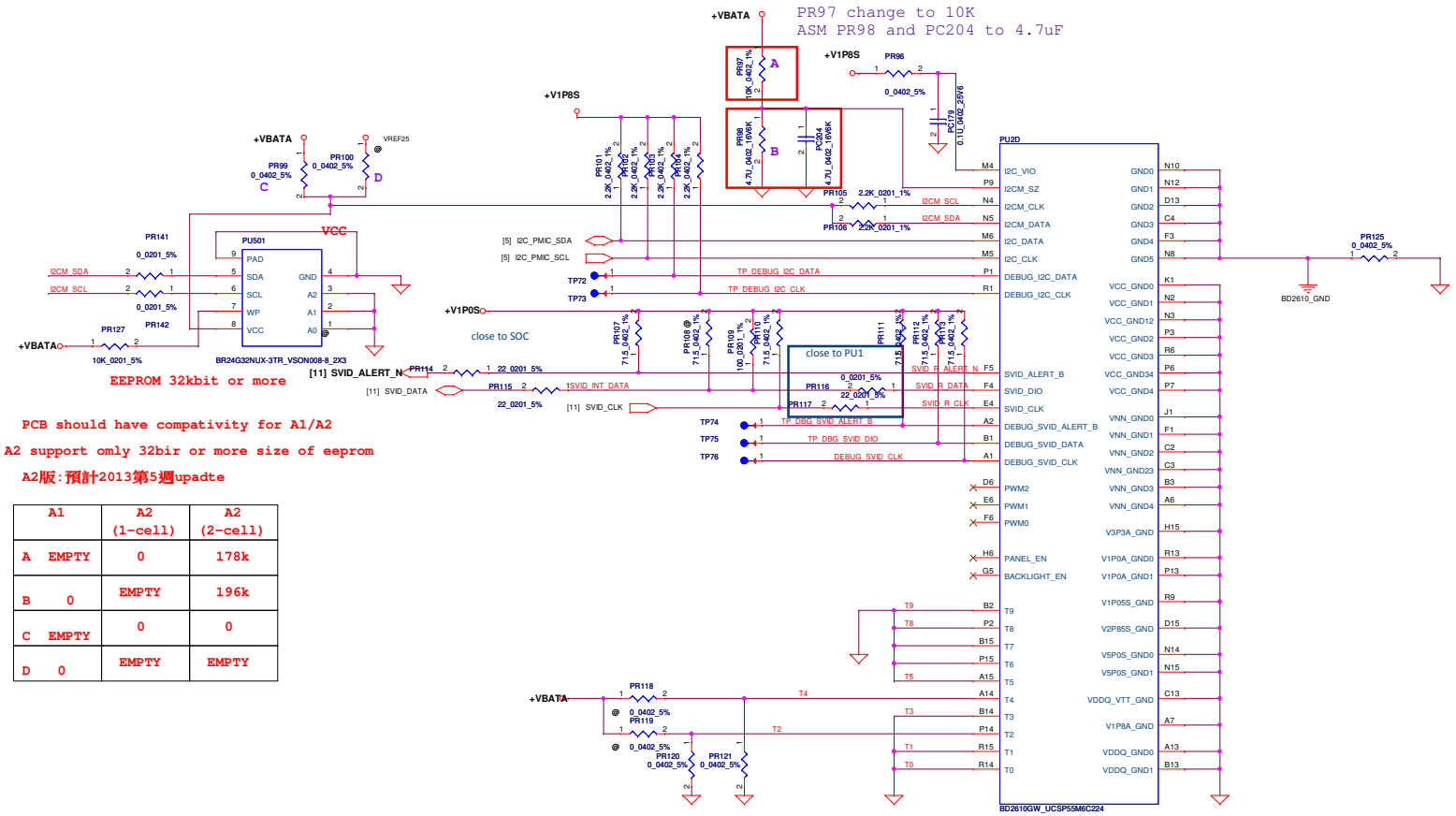


TABLE MEMORY STRAP

Location	2G	4G
R1811	ASM	N0 ASM
R1808	N0 ASM	ASM

Security Classification	Compal Secret Data		Compal Electronics, Inc.
Issued Date	Deciphered Date		Title: PMIC-3
			Size: A2 / Document Number / Rev: 0.1
			Date: Wednesday, June 04, 2014 / Sheet 48 of 54



A1	A2 (1-cell)	A2 (2-cell)
A EMPTY	0	178k
B 0	EMPTY	196k
C EMPTY	0	0
D 0	EMPTY	EMPTY

Security Classification	Compal Secret Data	Compal Electronics, Inc.
Issued Date	Deciphered Date	Title: PMI4
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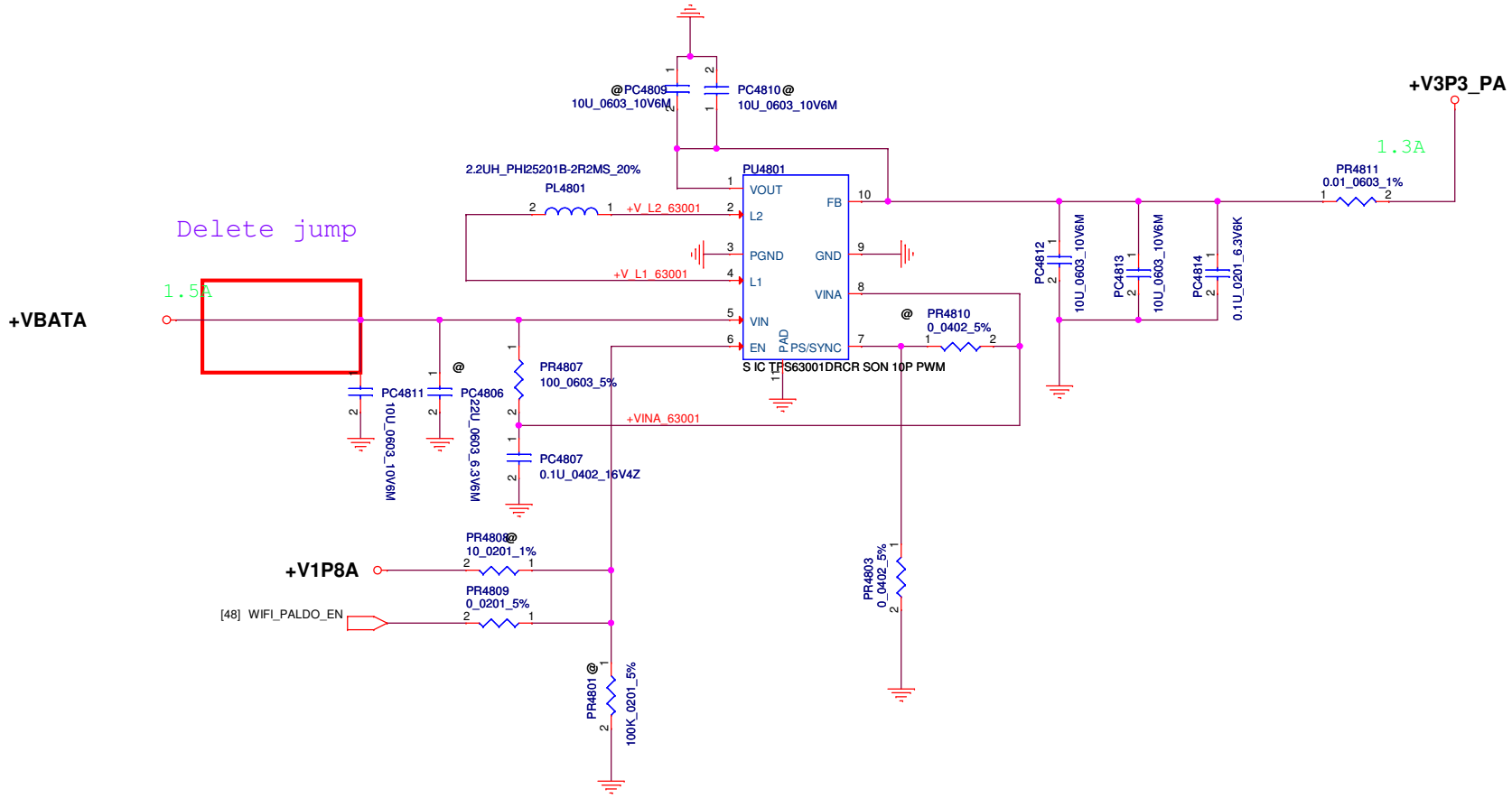
		Date: Wednesday, June 04, 2014 / Sheet 49 of 54



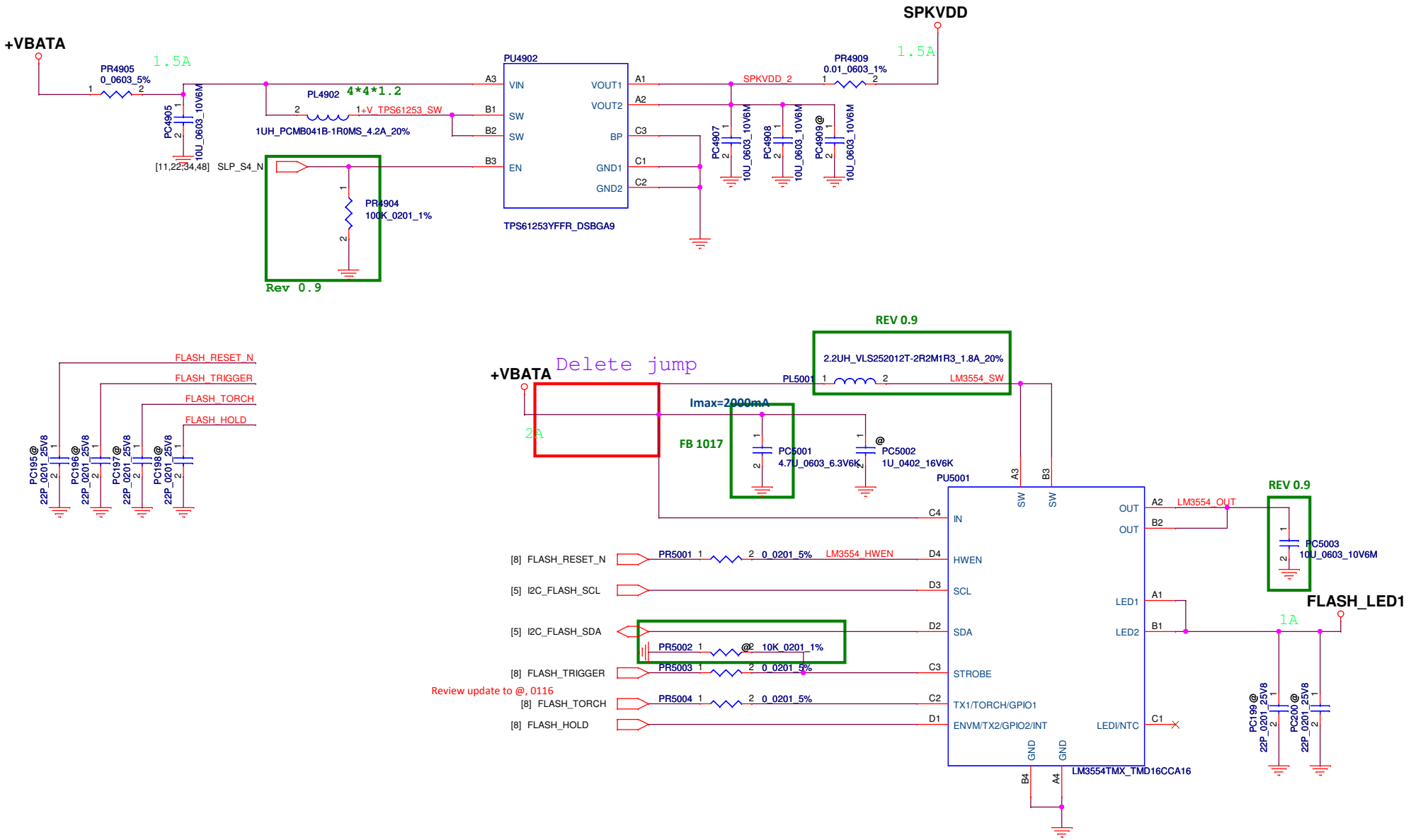
Security Classification	Compal Secret Data		
Issued Date	2013/06/13	Deciphered Date	2009/06/22

Compal Electronics, Inc.

Title: **Wifi (Buck-Boost)**

Size	Document Number	Rev
B		0.1

Date: Wednesday, June 04, 2014 Sheet 50 of 54



Security Classification	Compal Secret Data		
Issued Date	2013/06/13	Deciphered Date	2009/06/22

Compal Electronics, Inc.

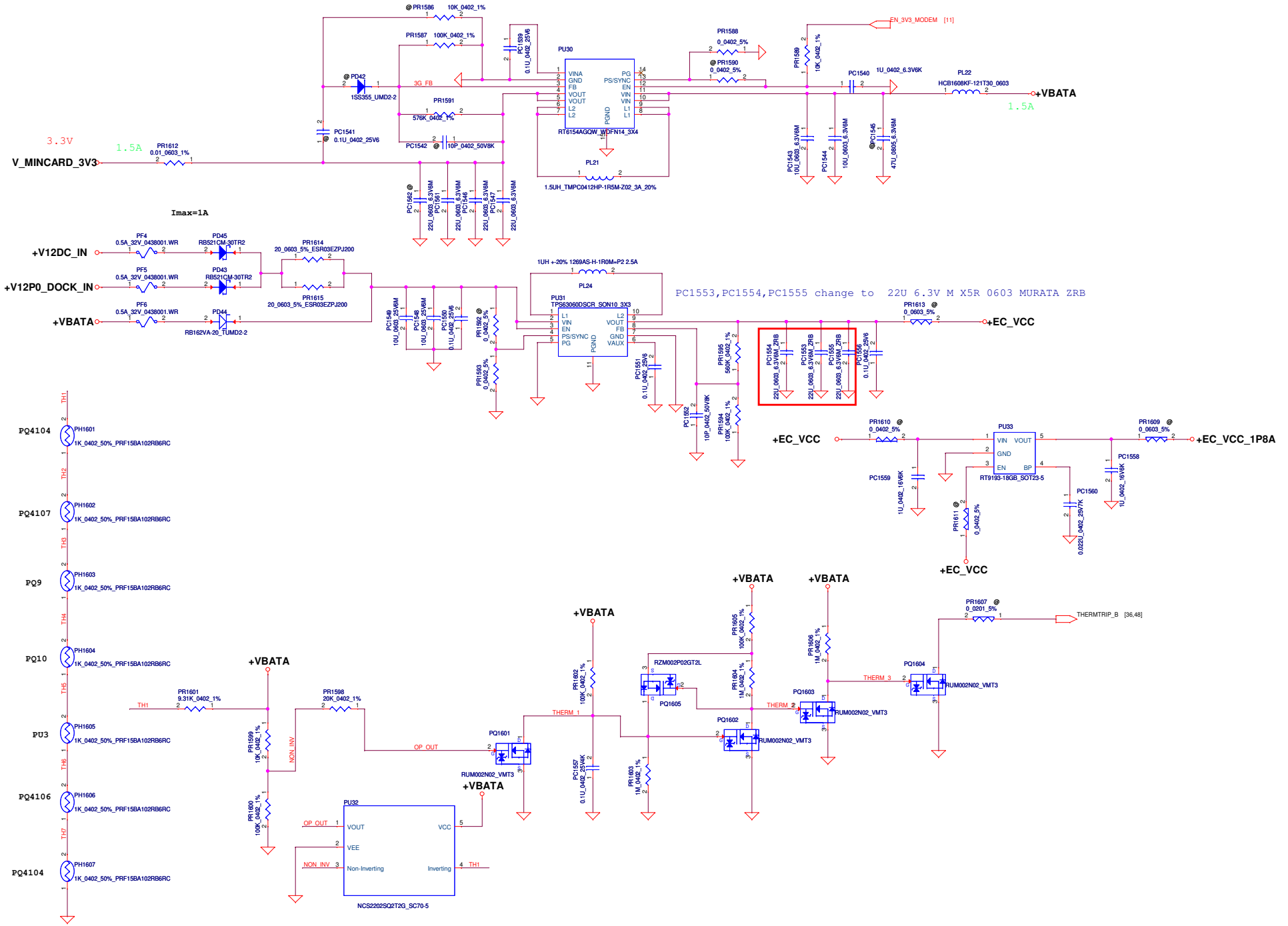
Title: **SPK 5V/Flash LED Driver**

Size	Document Number	Rev
B		0.1

Date: Wednesday, June 04, 2014 Sheet 51 of 54

Security Classification	Compal Secret Data		Compal Electronics, Inc.
Issued Date		Deciphered Date	Title: Backlight

Size: A3	Document Number	Rev: 0.1
Date: Wednesday, June 04, 2014	Sheet 52 of 54	



Security Classification	Compal Secret Data		Compal Electronics, Inc.
Issued Date	2013/06/13	Deciphered Date	2012/06/20
Title	3G Switch/EC Power		
Size C	Document Number ELLA LA-A811P		Rev 0.1
Date: Wednesday, June 04, 2014	Sheet 53 of 54		

Version change list (P.I.R. List)

Item	Reason for change	PG#	Modify List	Date	Phase
1					
2					
3					
4					
5					
6					
7					
8					
9					
10					
11					
12					
13					
14					
15					
16					
17					
18					
19					
20					

Compal Electronics, Inc.

Title: PWR-PIR

Size: A3 | Document Number | Rev: 0.1

Date: Wednesday, June 04, 2014 | Sheet 54 of 54